



Access Identifiers

This chapter describes the access identifiers (AIDs) of TL1 commands and autonomous messages for the Cisco ONS 15454 SDH, Release 7.0.

The AID code directs an input command to its intended physical or data entity inside the network element (NE). Equipment modules and facilities are typical examples of entities addressed by the access code.

25.1 ALL

Table 25-1 ALL

AID	Pattern
AidUnionId	FACILITY VC
AidUnionId1	MS-SPRing
BAND	ALL BAND[-{1-8}]-{1-6,12-17}-{1-4}-ALL BAND[-{1-8}]-{1-6,12-17}-{1-4}-{RX,TX} BAND[-{1-8}]-{1-6,12-17}-{1}-ALL BAND[-{1-8}]-{1-6,12-17}-{1}-{RX,TX}
BANDWL	BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-1530.33 BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-ALL BANDWL-{{1-8}-}{1-6,12-17}-{1-32}-{RX,TX,PT}-<WLEN> BANDWL-{{1-8}-}{1-6,12-17}-{1-32}-{RX,TX,PT}-ALL
BITS	ALL BITS-ALL BITS[<SHELFID>]-ALL BITS[<SHELFID>]-{1,2} SYNC[<SHELFID>]-BITS{1,2}

Table 25-1 ALL (continued)

AID	Pattern	
CHANNEL	ALL	
	CHAN[-{1-8}]-{1-6,12-17}-ALL	
	CHAN[-{1-8}]-{1-6,12-17}-{1-32}-ALL	
	CHAN[-{1-8}]-{1-6,12-17}-{1-32}-{RX,TX}	
	CHAN[-{1-8}]-{1-6,12-17}-{1-4}-ALL	
	CHAN[-{1-8}]-{1-6,12-17}-{1-4}-{RX,TX}	
	CHAN[-{1-8}]-{1-6,12-17}-{2,3}	
	CHAN[-{1-8}]-{1-6,12-17}-{5}	
CHAN[-{1-8}]-{1-6,12-17}-{9,10}		
COM	Common	
CrossConnectId	FACILITY	
CrossConnectId1	VCM	
	FACILITY	
	VC	
ENV	ALL	ENV-IN[-{1-8}]-{1-6}
	ENV-IN-ALL	ENV-OUT-ALL
	ENV-IN[-{1-8}]-ALL	ENV-OUT[-{1-8}]-ALL
	ENV-IN[-{1-8}]-{1-20}	ENV-OUT[-{1-8}]-{1-16}
	ENV-IN[-{1-8}]-{1-32}	ENV-OUT[-{1-8}]-{1-2}
	ENV-IN[-{1-8}]-{1-3}	ENV-OUT[-{1-8}]-{1-4}
	ENV-IN[-{1-8}]-{1-4}	ENV-{IN,OUT}[-{1-8}]-{1-16}
EQPT	ALL	PPM-{1-6,12-17}-{1-12}
	AIP-ALL	PPM[-{1-8}]-{1-6,12-17}-{1-4}
	AIP[-{1-8}]	PPM[-{1-8}]-{1-6,12-17}-{1-8}
	BIC-ALL	PWR-ALL
	BIC[-{1-8}]-ALL	PWR[-{1-8}]-ALL
	BIC[-{1-8}]-{A,B}	PWR[-{1-8}]-{A,B}
	BP-ALL	SLOT-ALL
	BP[-{1-8}]	SLOT[-{1-8}]-ALL
	FAN-ALL	SLOT[-{1-8}]-{1-17}
	FAN[-{1-8}]	SLOT[-{1-8}]-{1-6,12-17}
	PPM-{1-6,12-17}-1	

Table 25-1 ALL (continued)

AID	Pattern	
FACILITY	ALL	FAC[-{1-8}]-{1-6,12-17}-{1-4}
	FAC[-{1-8}]-{1-4,14-17}-{1-8}	FAC[-{1-8}]-{1-6,12-17}-{1-6}
	FAC[-{1-8}]-{1-6,12-17}-1	FAC[-{1-8}]-{1-6,12-17}-{1}
	FAC[-{1-8}]-{1-6,12-17}-ALL	FAC[-{1-8}]-{5,6,12,13}-{1}
	FAC[-{1-8}]-{1-6,12-17}-{0-11}	FAC[-{1-8}]-{8,10}-{1}
	FAC[-{1-8}]-{1-6,12-17}-{0-1}	VFAC[-{1-8}]-{1-6,12-17}-{0-1}
	FAC[-{1-8}]-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	VFAC[-{1-8}]-{1-6,12-17}-{1,2}
	FAC[-{1-8}]-{1-6,12-17}-{1-12,14,16,18,20,22,24}	VFAC[-{1-8}]-{1-6,12-17}-{1,2}-{1,8}
IPADDR	111.222.333.444	

Table 25-1 ALL (continued)

AID	Pattern
LINE	LINE-{{1-8}}-{{1-6,12-17}}-{{1-2}}-ALL LINE-{{1-8}}-{{1-6,12-17}}-{{1-2}}-{{RX,TX}} LINE-{{1-8}}-{{1-6,12-17}}-{{1-3}}-ALL LINE-{{1-8}}-{{1-6,12-17}}-{{1-3}}-{{RX,TX}} LINE-{{1-8}}-{{8,10}}-{{1}}-ALL LINE-{{1-8}}-{{8,10}}-{{1}}-{{RX,TX}} LINE-{{1-8}}-{{1-6,12-17}}-{{1}}-{{RX,TX}}(COM) LINE-{{1-8}}-{{1-6,12-17}}-{{2}}-{{RX,TX}}(OSC) LINE-{{1-8}}-{{1-6,12-17}}-{{3}}-{{RX,TX}}(LINE) LINE-{{1-8}}-{{1-6,12-17}}-{{1-3}}-ALL LINE-{{1-8}}-{{1-6,12-17}}-{{1}}-{{RX,TX}}(LINE) LINE-{{1-8}}-{{1-6,12-17}}-{{2}}-{{RX,TX}}(COM) LINE-{{1-8}}-{{1-6,12-17}}-{{3}}-{{RX,TX}}(OSC) LINE-{{1-8}}-{{1-6,12-17}}-{{4}}-{{RX,TX}}(DC) LINE-{{1-8}}-{{1-6,12-17}}-{{1-4}}-ALL LINE-{{1-8}}-{{1-6,12-17}}-1-RX (For input OTS) LINE-{{1-8}}-{{1-6,12-17}}-1-ALL CHAN-{{1-8}}-{{1-6,12-17}}-{{1-32}}-TX (For drop OCH) CHAN-{{1-8}}-{{1-6,12-17}}-{{1-32}}-ALL LINE-{{1-8}}-{{1-5,12-16}}-{{1}}-{{RX,TX}} (EXP) LINE-{{1-8}}-{{1-5,12-16}}-{{2}}-{{RX,TX}} (COM) LINE-{{1-8}}-{{1-5,12-16}}-{{3}}-{{TX}} (DROP) LINE-{{1-8}}-{{1-5,12-16}}-{{1-3}}-ALL CHAN-{{1-8}}-{{1-5,12-16}}-{{1-32}}-{{RX}} (ADD) CHAN-{{1-8}}-{{1-5,12-16}}-{{1-32}}-{{PT}} (PT) CHAN-{{1-8}}-{{1-5,12-16}}-{{1-32}}-ALL LINE-{{1-8}}-{{1-6,12-17}}-{{1}}-{{RX,TX}} (EXP) LINE-{{1-8}}-{{1-6,12-17}}-{{2}}-{{RX,TX}} (COM) LINE-{{1-8}}-{{1-6,12-17}}-{{3}}-{{RX,TX}} (EXP to other ring) LINE-{{1-8}}-{{1-6,12-17}}-{{1-3}}-ALL
LINEWL	LINEWL-{{1-6,12-17}}-{{1-32}}-{{RX,TX}}-1530.33 LINEWL-{{1-6,12-17}}-{{1-32}}-{{RX,TX}}-ALL LINEWL-{{1-8}}-{{1-6,12-17}}-{{1-32}}-{{RX,TX,PT}}-<WLEN> LINEWL -{{1-8}}-{{1-6,12-17}}-{{1-32}}-{{RX,TX,PT}}-ALL
MS-SPRing	MSSPR-RINGID

Table 25-1 ALL (continued)

AID	Pattern
OPM	ALL OPM[-{1-8}]-{1-5,12-16}-<WLEN> OPM[-{1-8}]-{1-5,12-16}-ALL
OSC	OSC-RINGID
PR SLOT	NULL SLOT-1 SLOT-13 SLOT-15 SLOT-17 SLOT-3 SLOT-5
RFILE	RFILE-DB RFILE-LOG RFILE-PKG
SHELF	SHELF-ALL SHELF-{1-8}
SYN	SYNC-NE
SYN_SRC	BITS-1 FAC-{1-6,12-17}-{1} BITS-2 FAC-{5,6,12,13}-{1} FAC-{1-4,11-14}-{1-16} INTERNAL FAC-{1-4,11-14}-{1-4} NONE FAC-{1-4}-1 STM1-{2}-{1-2}-{1} FAC-{1-4}-{1-4} STM4-{2}-{1-2}-{1} FAC-{1-6,12-17}-{1-4} SYNC-NE
SYNC_REF	SYNC-ALL SYNC-NE SYNC-{BITS1,BITS2}
SYNCSW	INT PRI SEC
UDC	UDC-{F,DCC}-{A,B}

Table 25-1 ALL (continued)

AID	Pattern	
VC	VC11-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-4}	VC4-{1-4,11-14}-{1-4}-{1,17,33,49}
	VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}	VC4-{1-4,11-14}-{1-4}-{1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}
	VC11-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-4}	VC4-{1-4,11-14}-{1-4}-{1,9,17,25,33,41,49,57}
	VC11-{1-6,12-17}-1-1-{1-7}-{1-2}	VC4-{1-4,11-14}-{1-4}-{1-64}
	VC11-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-4}	VC4-{1-4,14-17}-1
	VC11-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-4}	VC4-{1-4,14-17}-1-1
	VC11-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-4}	VC4-{1-4,14-17}-{1-12}-1
	VC12-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-3}	VC4-{1-4,14-17}-{1-4}-1
	VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-3}	VC4-{1-4,14-17}-{1-4}-{1-2}
	VC12-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-3}	VC4-{1-4,14-17}-{1-4}-{1-3}
	VC12-{1-6,12-17}-1-1-{1-7}-{1-2}	VC4-{1-4,14-17}-{1-4}-{1-4}
	VC12-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-3}	VC4-{1-4,14-17}-{1-8}-1
	VC12-{1-6,12-17}-1-{1-2}-{1-7}-{1-3}	VC4-{1-6,12-17}-1
	VC12-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-3}	VC4-{1-6,12-17}-1-1
	VC12-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}	VC4-{1-6,12-17}-1-{1,2,3,5,6,7,9,10,11,13,14,15}
	VC3-{1-14,14-17}-{1-12}-1-{1-3}	VC4-{1-6,12-17}-1-{1,2,4,5,6,7,9,10,13,14}
	VC3-{1-4,14-17}-{1-4}-{1-4}-{1-3}	VC4-{1-6,12-17}-1-{1,5,9,13}
	VC3-{1-4,14-17}-{1-8}-1-{1-3}	VC4-{1-6,12-17}-1-{1-16}
	VC3-{1-6,12-17}-1-{1-16}-{1-3}	VC4-{1-6,12-17}-1-{1-2}
	VC3-{1-6,12-17}-1-{1-4}-{1-3}	VC4-{1-6,12-17}-1-{1-3}
	VC3-{1-6,12-17}-{1-4}-1-{1-3}	VC4-{1-6,12-17}-1-{1-4}
	VC3-{1-6,12-17}-{1-4}-{1-3}	VC4-{1-6,12-17}-1-{1-9}
	VC4-{1-4,11-14}-{1-16}-1	VC4-{1-6,12-17}-{1-4}
	VC4-{1-4,11-14}-{1-16}-{1,5,9,13}	VC4-{1-6,12-17}-{1-4}-1
	VC4-{1-4,11-14}-{1-16}-{1,9}	
	VC4-{1-4,11-14}-{1-4}-1	
	WDMANS	AONS-{E,W}
		WDMANS-{E,W}
	WLEN	WLEN-{E,W}-{ADD,DROP,EXP}-{1530.33,1531.12,1531.90,1532.68,1534.25,1535.04,1535.82,1536.61,1538.19,1538.98,1539.77,1540.56,1542.14,1542.94,1543.73,1544.53,1546.12,1546.92,,1547.72,1548.51,1550.12,1550.92,1551.72,1552.52,1554.13,1554.94,1555.75,1556.55,1558.17,1558.98,1559.79,1560.61}

25.1.1 AidUnionId

Table 25-2 *AidUnionId*

AID	Patterns	
Facility	E1-{2}-{1-21}	FAC-{5,6,12,13}-{1}
	E3-{2}-{1-3}	FAC-{5-6}-{1-28}
	FAC-{1-4,11-14}-ALL	FAC-{5-6}-{1-3}
	FAC-{1-4,11-14}-{1-16}	FAC-{8,10}-{1}
	FAC-{1-4,11-14}-{1-4}	FSSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}	FSSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,14-17}-{1-8}	FSSTE-{1}-{0-7}
	FAC-{1-4}-1	FSSTE-{1}-{1-8}
	FAC-{1-4}-{1-4}	STM1-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-1	STM1-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-ALL	STM4-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-11}	STM4-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-1}	STM16-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	VFAC-{1,2,5,6}-{0-1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24}	VFAC-{1,2,5,6}-{1-8}
	FAC-{1-6,12-17}-{1-12}	VFAC-{1-4,11-14}-{1-4}-{1-4}-1
	FAC-{1-6,12-17}-{1-4}	VFAC-{1-6,12-17}-{0-1}
	FAC-{1-6,12-17}-{1-6}	VFAC-{1-6,12-17}-{1,2}
	FAC-{1-6,12-17}-{1}	VFAC-{1-6,12-17}-{1,2}-{1,8}
	FAC-{1-6}-ALL	VFAC-{1}-{0-1}
		VFAC-{1}-{1-8}

Table 25-2 AidUnionId (continued)

AID	Patterns
VC	VC11-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-4} VC4-{1-4,11-14}-{1-4}-{1,17,33,49}
	VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-4} VC4-{1-4,11-14}-{1-4}- {1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}
	VC11-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-4} VC4-{1-4,11-14}-{1-4}-{1,9,17,25,33,41,49,57}
	VC11-{1-6,12-17}-1-1-{1-7}-{1-2} VC4-{1-4,11-14}-{1-4}-{1-64}
	VC11-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-4} VC4-{1-4,14-17}-1
	VC11-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-4} VC4-{1-4,14-17}-1-1
	VC11-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-4} VC4-{1-4,14-17}-{1-12}-1
	VC12-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-3} VC4-{1-4,14-17}-{1-4}-1
	VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-3} VC4-{1-4,14-17}-{1-4}-{1-2}
	VC12-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-3} VC4-{1-4,14-17}-{1-4}-{1-3}
	VC12-{1-6,12-17}-1-1-{1-7}-{1-2} VC4-{1-4,14-17}-{1-4}-{1-4}
	VC12-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-3} VC4-{1-4,14-17}-{1-8}-1
	VC12-{1-6,12-17}-1-{1-2}-{1-7}-{1-3} VC4-{1-6,12-17}-1
	VC12-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-3} VC4-{1-6,12-17}-1-1
	VC12-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3} VC4-{1-6,12-17}-1- {1,2,3,5,6,7,9,10,11,13,14,15}
	VC3-{1-14,14-17}-{1-12}-1-{1-3} VC4-{1-6,12-17}-1-{1,2,4,5,6,7,9,10,13,14}
	VC3-{1-4,14-17}-{1-4}-{1-4}-{1-3} VC4-{1-6,12-17}-1-{1,5,9,13}
	VC3-{1-4,14-17}-{1-8}-1-{1-3} VC4-{1-6,12-17}-1-{1-16}
	VC3-{1-6,12-17}-1-{1-16}-{1-3} VC4-{1-6,12-17}-1-{1-2}
	VC3-{1-6,12-17}-1-{1-4}-{1-3} VC4-{1-6,12-17}-1-{1-3}
	VC3-{1-6,12-17}-{1-4}-1-{1-3} VC4-{1-6,12-17}-1-{1-4}
	VC3-{1-6,12-17}-{1-4}-{1-3} VC4-{1-6,12-17}-1-{1-9}
	VC4-{1-4,11-14}-{1-16}-1 VC4-{1-6,12-17}-{1-4}
	VC4-{1-4,11-14}-{1-16}-{1,5,9,13} VC4-{1-6,12-17}-{1-4}-1
	VC4-{1-4,11-14}-{1-16}-{1,9} VC4-{1-6,12-17}-{1-4}-1
	VC4-{1-4,11-14}-{1-4}-1 VC4-{1-6,12-17}-{1-4}-1

25.1.2 AidUnionId1

Table 25-3 AidUnionId1

AID	Patterns
MS-SPRing	ALL MSSPR-RINGID

25.1.3 BAND

The BAND AID is used to access Optical Multiplex Section (OMS) layer of optical networking units.

Table 25-4 *BAND*

Pattern	Description
ALL	All of the OMSs of the NE. The ALL AID is applicable for retrieve-only commands.
BAND[-{1-8}]-{1-6,12-17}-{1-4}-ALL	All the channels in a Band OADM (1Bn, 4Bn) unit.
BAND[-{1-8}]-{1-6,12-17}-{1-4}-{RX,TX}	The Receive/Transmit channels in a Band OADM (1Bn, 4Bn) unit.
BAND[-{1-8}]-{1-6,12-17}-{1}-ALL	All the channels in an Optical Multiplexer/Demultiplexer (4Ch) unit.
BAND[-{1-8}]-{1-6,12-17}-{1}-{RX,TX}	The Receive/Transmit channels in an Optical Multiplexer/Demultiplexer (4Ch) unit.

25.1.4 BANDWL

Band wavelength. Identifies a wavelength channel included in any of the lower layer OMS facilities.

Table 25-5 *BANDWL*

Pattern	Description
BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-1530.33	Identifies an allocated wavelength channel included in any of the lower layer OMS facilities.
BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-ALL	Identifies all of the allocated wavelength channels included in any of the lower layer OMS facilities.

Table 25-5 BANDWL (continued)

Pattern	Description
BANDWL-{{1-8}}-{{1-6,12-17}}-{{1-32}}-{{RX,TX,PT}}-<WLEN>	<p>OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies.</p> <p>Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in.</p> <p>The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH] (in the form of 15xx.yy)</p>
BANDWL-{{1-8}}-{{1-6,12-17}}-{{1-32}}-{{RX,TX,PT}}-ALL	<p>OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies.</p> <p>Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in.</p> <p>The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH] (in the form of 15xx.yy)</p>

25.1.5 BITS

AID for building integrated timing supply (BITS).

Table 25-6 BITS

Pattern	Description
ALL	The ALL AID is applicable to RTRV commands only (RTRV-BITS and RTRV-ALM/COND-BITS). The All AID is equivalent to BITS-ALL for these commands. For RTRV-ALM/COND-SYCN, the ALL AID translates to BITS-ALL, SYNC-BITS1, and SYNC-BITS2
BITS-ALL	BITS AIDs of both BITS-1 and BITS-2 in the RTRV-BITS command.
BITS[<SHELFID>]-ALL	BITS shelf ID of both BITS-1 and BITS-2 in the RTRV-BITS command.
BITS[<SHELFID>]-{1,2}	Individual BITS AID.
SYNC[<SHELFID>]-BITS{1,2}	BITS-OUT AIDs of BITS-1 and BITS-2. These AIDs are applicable only in ED/RTRV-BITS commands and are used for setting and retrieving the BITS-OUT parameters.

25.1.6 CHANNEL

Accesses the Optical Channels (OCH) layer of Optical Networking/Client units.

Table 25-7 CHANNEL

CHANNEL Values	Description
ALL	ALL OCHs of the NE. The ALL AID is applicable for retrieve-only commands
CHAN[-{1-8}]-{1-6,12-17}-ALL	All the channels of an Optical Transponder/Muxponder. The format is CHAN-[SLOT]-ALL
CHAN[-{1-8}]-{1-6,12-17}-{1-32}-ALL	All the channels in an Optical Multiplexer/Demultiplexer (32Ch) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL
CHAN[-{1-8}]-{1-6,12-17}-{1-32}-{RX,TX}	The Receive/Transmit channels in an Optical Multiplexer/Demultiplexer (32Ch) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION]
CHAN[-{1-8}]-{1-6,12-17}-{1-32}-{RX,PT}	The Receive/Passthrough channels in an Optical WSS (32Ch) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION]
CHAN[-{1-8}]-{1-6,12-17}-{1-4}-ALL	All the channels in an OADM (1Ch, 2Ch, 4Ch) unit and Optical and Optical Multiplexer/Demultiplexer (4Ch) unit. The format is CHAN-[SLOT]-[PORT]-ALL
CHAN[-{1-8}]-{1-6,12-17}-{1-4}-{RX,TX}	The Receive/Transmit Channels in an OADM (1Ch, 2Ch, 4Ch) unit and Optical Multiplexer/Demultiplexer (4Ch) unit. The format is CHAN-[SLOT]-[PORT]-[DIRECTION]
CHAN[-{1-8}]-{1-6,12-17}-{2,3}	A single channel of an Optical Transponder/Muxponder. The TXP_MR_10G and TXP_MR_2.5G use CHAN-slot-2 for the 1 DWDM Facility. TXPP_MR_2.5G uses CHAN-[SLOT]-{2,3} for the two DWDM facilities. The format is CHAN-[SLOT]-[PORT]
CHAN[-{1-8}]-{1-6,12-17}-{5}}	A single channel of an Optical Transponder/Muxponder. The TXP_MR_10G uses CHAN-[SLOT]-2 for the 1 DWDM facility. MXP_2.5G_10G uses the CHAN-slot-5 for the 1 DWDM facility. The format is CHAN-[SLOT]-[PORT]
CHAN[-{1-8}]-{1-6,12-17}-{9,10}	A single channel of an Optical Muxponder. The MXP_MR_10DME_C and MXP_2.5G_10G use CHAN-[SHELF]-SLOT-9 for the 1 DWDM Facility. The MXPP_2.5G_10G uses CHAN-[SHELF]-SLOT-{9,10} for the 2 DWDM Facilities. The format is CHAN-[SHELF]-[SLOT]-[PORT]

25.1.7 COM

Common

Table 25-8 COM

Pattern	Description
COM	Common

25.1.8 CrossConnectId

Table 25-9 CrossConnectId

AID	Pattern	
FACILITY	E1-{2}-{1-21}	FAC-{5,6,12,13}-{1}
	E3-{2}-{1-3}	FAC-{5-6}-{1-28}
	FAC-{1-4,11-14}-ALL	FAC-{5-6}-{1-3}
	FAC-{1-4,11-14}-{1-16}	FAC-{8,10}-{1}
	FAC-{1-4,11-14}-{1-4}	FSSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}	FSSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,14-17}-{1-8}	FSSTE-{1}-{0-7}
	FAC-{1-4}-1	FSSTE-{1}-{1-8}
	FAC-{1-4}-{1-4}	STM1-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-1	STM1-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-ALL	STM4-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-11}	STM4-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-1}	STM16-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	VFAC-{1,2,5,6}-{0-1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24}	VFAC-{1,2,5,6}-{1-8}
	FAC-{1-6,12-17}-{1-12}	VFAC-{1-4,11-14}-{1-4}-{1-4}-1
	FAC-{1-6,12-17}-{1-4}	VFAC-{1-6,12-17}-{0-1}
	FAC-{1-6,12-17}-{1-6}	VFAC-{1-6,12-17}-{1,2}
	FAC-{1-6,12-17}-{1}	VFAC-{1-6,12-17}-{1,2}-{1,8}
	FAC-{1-6}-ALL	VFAC-{1}-{0-1}
		VFAC-{1}-{1-8}

25.1.9 CrossConnectId1

Table 25-10 CrossConnectId1

AID	Pattern
VCM	VCM-{1-6,12-17}-{0-1}-ALL
	VCM-{1-6,12-17}-{0-1}-{1-256}
	VCM-{1-6,12-17}-{1-4}-ALL
	VCM-{1-6,12-17}-{1-4}-{1-256}

Table 25-10 CrossConnectId1 (continued)

AID	Pattern	
FACILITY	E1-{2}-{1-21}	FAC-{5,6,12,13}-{1}
	E3-{2}-{1-3}	FAC-{5-6}-{1-28}
	FAC-{1-4,11-14}-ALL	FAC-{5-6}-{1-3}
	FAC-{1-4,11-14}-{1-16}	FAC-{8,10}-{1}
	FAC-{1-4,11-14}-{1-4}	FSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}	FSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,14-17}-{1-8}	FSTE-{1}-{0-7}
	FAC-{1-4}-1	FSTE-{1}-{1-8}
	FAC-{1-4}-{1-4}	STM1-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-1	STM1-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-ALL	STM4-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-11}	STM4-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-1}	STM16-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	VFAC-{1,2,5,6}-{0-1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24}	VFAC-{1,2,5,6}-{1-8}
	FAC-{1-6,12-17}-{1-12}	VFAC-{1-4,11-14}-{1-4}-{1-4}-1
	FAC-{1-6,12-17}-{1-4}	VFAC-{1-6,12-17}-{0-1}
	FAC-{1-6,12-17}-{1-6}	VFAC-{1-6,12-17}-{1,2}
	FAC-{1-6,12-17}-{1}	VFAC-{1-6,12-17}-{1,2}-{1,8}
	FAC-{1-6,12-17}-{1}	VFAC-{1}-{0-1}
	FAC-{1-6,12-17}-ALL	VFAC-{1}-{1-8}

Table 25-10 CrossConnectId1 (continued)

AID	Pattern
VC	VC11-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-4}
	VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}
	VC11-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-4}
	VC11-{1-6,12-17}-1-1-{1-7}-{1-2}
	VC11-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-4}
	VC11-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-4}
	VC11-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-4}
	VC12-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-3}
	VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-3}
	VC12-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-3}
	VC12-{1-6,12-17}-1-1-{1-7}-{1-2}
	VC12-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-3}
	VC12-{1-6,12-17}-1-{1-2}-{1-7}-{1-3}
	VC12-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-3}
	VC12-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}
	VC3-{1-14,14-17}-{1-12}-1-{1-3}
	VC3-{1-4,14-17}-{1-4}-{1-4}-{1-3}
	VC3-{1-4,14-17}-{1-8}-1-{1-3}
	VC3-{1-6,12-17}-1-{1-16}-{1-3}
	VC3-{1-6,12-17}-1-{1-4}-{1-3}
	VC3-{1-6,12-17}-{1-4}-1-{1-3}

Table 25-10 *CrossConnectId1 (continued)*

AID	Pattern
VC (continued)	VC3-{1-6,12-17}-{1-4}-{1-3}
	VC4-{1-4,11-14}-{1-16}-1
	VC4-{1-4,11-14}-{1-16}-{1,5,9,13}
	VC4-{1-4,11-14}-{1-16}-{1,9}
	VC4-{1-4,11-14}-{1-4}-1
	VC4-{1-4,11-14}-{1-4}-{1,17,33,49}
	VC4-{1-4,11-14}-{1-4}-{1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}
	VC4-{1-4,11-14}-{1-4}-{1,9,17,25,33,41,49,57}
	VC4-{1-4,11-14}-{1-4}-{1-64}
	VC4-{1-4,14-17}-1
	VC4-{1-4,14-17}-1-1
	VC4-{1-4,14-17}-{1-12}-1
	VC4-{1-4,14-17}-{1-4}-1
	VC4-{1-4,14-17}-{1-4}-{1-2}
	VC4-{1-4,14-17}-{1-4}-{1-3}
	VC4-{1-4,14-17}-{1-4}-{1-4}
	VC4-{1-4,14-17}-{1-8}-1
	VC4-{1-6,12-17}-1
	VC4-{1-6,12-17}-1-1
	VC4-{1-6,12-17}-1-{1,2,3,5,6,7,9,10,11,13,14,15}
	VC4-{1-6,12-17}-1-{1,2,4,5,6,7,9,10,13,14}
	VC4-{1-6,12-17}-1-{1,5,9,13}
	VC4-{1-6,12-17}-1-{1-16}
	VC4-{1-6,12-17}-1-{1-2}
	VC4-{1-6,12-17}-1-{1-3}
	VC4-{1-6,12-17}-1-{1-4}
	VC4-{1-6,12-17}-1-{1-9}
	VC4-{1-6,12-17}-{1-4}
	VC4-{1-6,12-17}-{1-4}-1

25.1.10 ENV

The environmental AID for the AIC-I cards

Table 25-11 ENV

Pattern	Description
ALL	The ALL AID applies to retrieve-only commands: RTRV-ALM/COND-ENV, RTRV-ATTR-CONT, and RTRV-ATTR-ENV
ENV-IN-ALL	Environmental AID for ALL environmental alarms
ENV-IN[-{1-8}]-ALL	Environmental AID for ALL multishelf environmental alarms
ENV-IN[-{1-8}]-{1-4}	Environmental AID for the AIC-I card. "IN" is used for environmental alarms
ENV-IN[-{1-8}]-{1-20}	Environmental AID for the AIC-I card. "IN" is used for environmental alarms
ENV-IN[-{1-8}]-{1-32}	Environmental AID for AIC-I card extensions. "IN" is used for environmental alarms
ENV-IN[-{1-8}]-{1-3}	Environmental AID for the AIC-I card. "IN" is used for environmental alarms
ENV-IN[-{1-8}]-{1-6}	Environmental AID. "IN" is used for environmental alarms
ENV-OUT-ALL	Environmental AID for AIC-I cards. "OUT" is used for environmental controls
ENV-OUT[-{1-8}]-ALL	Environmental AID for AIC-I cards. "OUT" is used for environmental controls
ENV-OUT[-{1-8}]-{1-4}	Environmental AID for AIC-I cards. "OUT" is used for environmental controls
ENV-OUT[-{1-8}]-{1-16}	Environmental AID for AIC-I card extensions. "OUT" is used for environmental controls
ENV-OUT[-{1-8}]-{1-2}	Environmental AID for AIC-I cards. "OUT" is used for environmental controls
ENV-{IN,OUT}[-{1-8}]-{1-16}	ENV-IN[1-16] is used for the environmental alarm AID. ENV-OUT-{1-16} is used for the external control AID

25.1.11 EQPT

Equipment AIDs are used to access specific cards.

Table 25-12 EQPT

Pattern	Description
AIP[-{1-8}]	The AID for the alarm interface panel (AIP). It is used for RTRV-INV output only.
AIP-ALL	The AID for all of the AIPs in any shelf of the node. It is used for RTRV-INV output only.
ALL	Only used for RTRV-INV, RTRV-EQPT, and RTRV-ALM/COND-EQPT commands. RTRV-INV returns all the inventory information for the NE. The ONS 15454 includes the multiservice cards, common control cards, and the AIP, BP, and FAN. The ONS 15327 includes the multiservice cards and common control cards. RTRV-EQPT with ALL AID returns EQPT information on all the slots. RTRV-ALM/COND-EQPT with ALL AID returns EQPT and PWR-A and PWR-B type of alarms and conditions.
BIC-ALL	AIDs for the backplane interface connectors (BICs), BIC-A and BIC-B. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BIC[-{1-8}]-ALL	AIDs for the backplane interface connectors (BICs), BIC-A and BIC-B of a single shelf in a multishelf node. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BIC[-{1-8}]-{A,B}	AIDs for the BICs. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BP[-{1-8}]	The AID for the backplane. It is used for RTRV-INV output only.

Table 25-12 EQPT (continued)

Pattern	Description
BP-ALL	The AID for all of the backplanes in any shelf on the multishelf node. It is used for RTRV-INV output only.
FAN[-{1-8}]	The AID for the fan tray. It is used for RTRV-INV output only.
FAN-ALL	The AID for all of the fan tray in all the shelves of a multishelf node. It is used for RTRV-INV output only.
PPM-{1-6,12-17}-1	PPM for the OC192-XFP card. Format is PPM-[SLOT]-[PPM].
PPM-{1-6,12-17}-{1-12}	PPM for the MRC-12 card. Format is PPM-[SLOT]-[PPM].
PPM[-{1-8}]-{1-6,12-17}-{1-4}	PPM AID for dense wavelength division multiplexing (DWDM) MXP_2.5G_10G, TXP_MR_10G, TXP_MR_2.5G, TXPP_MR_2.5G, MXP_2.5G_10E, and TXP_MR_10E cards. Format of AID is PPM-[SLOT]-[PPM].
PPM[-{1-8}]-{1-6,12-17}-{1-8}	(ONS 15454 only) PPM AID for the MXP_MR_2.5G and MXPP_MR_2.5G cards. Format of AID is PPM-[SLOT]-[PPM].
PWR-ALL	AIDs for the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
PWR[-{1-8}]-{A,B}	AIDs for the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
PWR[-{1-8}]-ALL	AIDs for all of the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
SLOT-ALL	All of the NE equipment AIDs.
SLOT[-{1-8}]-{1-17}	EQPT AID where the format is SLOT-[SLOT].
SLOT[-{1-8}]-{1-6,12-17}	Individual equipment AID of the multiservice card units or slots where the format is SLOT-[SLOT].

25.1.12 FACILITY

Facilities AIDs are used to access specific ports.

Facility AID format:

- Format for DS3i-N-12 electrical facilities: FAC-[SLOT]-[PORT]
- Format for POS ports: VFAC-[SLOT]-[PORT]
- Format for POS port with PIM and PPM: VFAC-[SLOT]-[PIM]-[PPM]-[PORT]

Table 25-13 FACILITY

Pattern	Description
ALL	The ALL AID is applicable for RTRV-only commands (RTRV-rr type of commands), for example: RTRV-STM16 with an ALL AID returns all STM16 facilities on the node
FAC[-{1-8}]-{1-4,14-17}-{1-8}	Facilities for an STM1 card where the format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{1-6,12-17}-1	Facility AID for the 1 Client (CLNT) port on a TXP_MR_10G, TXP_MR_2.5G, TXP_MR_2.5G or TXPP_MR_2.5G card where the format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{1-6,12-17}-ALL	All the facilities of an I/O unit or slot where the format is FAC-[SLOT]-[ALL]

Table 25-13 FACILITY (continued)

Pattern	Description
FAC[-{1-8}]-{1-6,12-17}-{0-11}	Facilities for the Ethernet front-end ports on the ML100T-12 card. Ports are numbered starting with 0 (that is, the first port is FAC-SLOT-0, second port is FAC-SLOT-1, ..., last port is FAC-SLOT-11 for ML100T-12, first port is FAC-SLOT-0 and second port is FAC-SLOT-1 for ML1000-2). The format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{1-6,12-17}-{0-1}	Facilities for the Ethernet back-end Ports on the ML1000-2 card. Ports are 0-based, (for example, the first port is FAC-SLOT-0 and the second port is FAC-SLOT-1). The format is FAC-[SLOT]-[PORT]
FAC-{1-6,12-17}-{1-8}	Facility AID for the Ethernet front-end ports on the CE-100T-8 card The format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}-{0-11}	Facility AID for the Ethernet front-end ports on the MS-ISC-100T The first port is FAC-SLOT-0, the second port is FAC-SLOT-1, and so on. The last port is FAC-SLOT-11. The format is FAC-[SLOT]-[PORT].
FAC[-{1-8}]-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	Facility AIDs for the DS3i-N-12 VC416 Backplane Rate where format is FAC-[SLOT]-[PORT]. Ports 1 through 12 are always available, but only even ports after 12 are available
FAC[-{1-8}]-{1-6,12-17}-{1-12,14,16,18,20,22,24}	Facility AIDs for DS3i-N-12 VC44 Backplane Rate where format is FAC-[SLOT]-[PORT]. Ports 1 through 12 are always available, but only even ports after 12 are available
FAC[-{1-8}]-{1-6,12-17}-{1-4}	Facility AID for the four Client (CLNT) facilities on the MXP_2.5G_10G card. Facility AID for 4-Port G1000/FC_MR-4 card. Facility AID for creating/editing cross-connects (VC3, VC4, VC4-2C, VC4-3C, VC4-4C, and VC4-8C) for the 4-Port G1000/FC_MR-4 card where format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{1-6,12-17}-{1-6}	Facilities for the DS3i-N-12 card where the format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{1-6,12-17}-{1}	Facility AID for the 1-Port STM4, STM16, and STM1 in OSC-CSM cards. Facility AID for the client ports on the MXP/TXP and TXP_MR_2.5G cards where format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{5,6,12,13}-{1}	Facility AID for the STM16/STM64 cards. The STM16/STM64 cards can only use Slot 5, Slot 6, Slot 12, and Slot 13 where format is FAC-[SLOT]-[PORT]
FAC[-{1-8}]-{8,10}-{1}	Facility AID for the OSCM card. The OSCM cards can only use the XC10G slots (Slot 8, Slot 10) where format is FAC-[SLOT]-[PORT]
VFAC[-{1-8}]-{1-6,12-17}-{0-1}	Facilities for the back-end POS ports on the ML-Series card. Port numbering is 0-based (first POS port is VFAC-SLOT-0, second POS port is VFAC-SLOT-1). VC4, VC4-2C, VC4-3C, VC4-4C, and VC4-8C for the ML1000 and ML100T cards. Format is VFAC-[SLOT]-[PORT]
VFAC[-{1-8}]-{1-6,12-17}-{1,2}	GFP facilities on the MXP-MR-2.5G and MXPP-MR-2.5G cards
VFAC[-{1-8}]-{1-6,12-17}-{1,2}-{1,8}	GFP Client facilities for MXP-MR-2.5G and MXPP-MR-2.5G cards

25.1.13 IPADDR

IP Address

Table 25-14 IPADDR

Pattern	Description
111.222.333.444	Standard 4 Part IP Address Notation
ALL	ALL

25.1.14 LINE

The LINE AID is used to access the Optical Transport Section (OTS) layer of optical networking units. Applicable only to ONS 15454 SDH AD-1B-xx.x, AD-4B-xx.x, AD-1C-xx.x, AD-2C-xx.x, AB-4C-xx.x, OSC-CSM, OSCM, OPT-BST, OPT-PRE, 4MD-xx.x, 32MUX-O, and 32DMX-O cards. The format is LINE-[SLOT]-[PORT]-[DIRECTION].

Table 25-15 LINE

Values	Description
ALL	All of the OTSs of the NE. The ALL AID applies to retrieve-only commands
LINE- $\{1-6,12-17\}$ - $\{1-2\}$ -ALL	All the lines in a OPT-PRE, OCS-CSM, AD-1B, AD-4B, AD-1C, AD-2C, and AD-4C units
LINE- $\{1-6,12-17\}$ - $\{1-2\}$ - $\{RX,TX\}$	The receive/transmit Lines in a OPT-PRE, OCS-CSM, AD-1B, AD-4B, AD-1C, AD-2C, and AD-4C units
LINE- $\{1-6,12-17\}$ - $\{1-3\}$ - $\{RX,TX\}$	The receive/transmit lines in a OPT-BST units
LINE- $\{8,10\}$ - $\{1\}$ -ALL	All the lines in a OSCM units
LINE- $\{8,10\}$ - $\{1\}$ - $\{RX,TX\}$	The receive/transmit lines in OSCM units
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{1\}$ - $\{RX,TX\}$	For Booster units, there is an input OTS for LINE(1) and an output OTS (Amplified) for amplification stage COM(2), 2 OTS for input line COM(2) and output LINE(1) and 2 OTS for OSC(3) Add & Drop service channel.
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{2\}$ - $\{RX,TX\}$	
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{3\}$ - $\{RX,TX\}$	
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{1-3\}$ -ALL	
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{1\}$ - $\{RX,TX\}$	
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{2\}$ - $\{RX,TX\}$	Pre-Amplifier unit with 2 stages of amplification; input OTS for LINE(1) and an output OTS (Amplified) for amplification stage COM(2), 2 OTS for input line COM(2) and output LINE(1) and 2 OTS for OSC(3) Add & Drop service channel and output and an input OTS for the DCU(2).
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{3\}$ - $\{RX,TX\}$	
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{4\}$ - $\{RX,TX\}$	
LINE- $\{1-8\}$ - $\{1-6,12-17\}$ - $\{1-4\}$ -ALL	

Table 25-15 LINE (continued)

Values	Description
LINE-{{1-8}}-{{1-6,12-17}}-1-RX For input OTS	For demux units there is an OTS for input line, and up to 32 OCH for drop channel connectors. Demux is a unidirectional unit.
LINE-{{1-8}}-{{1-6,12-17}}-1-ALL	
CHAN-{{1-8}}-{{1-6,12-17}}-{{1-32}}-TX For Drop OCH	
CHAN-{{1-8}}-{{1-6,12-17}}-{{1-32}}-ALL	
LINE-{{1-8}}-{{1-5,12-16}}-{{1}}- {RX,TX} (EXP)	Wavelength switch selector unit has input and output OTS for signal coming from amplifier units COM(2), input and output OTS for signal continuing to next WSS unit in the node EXP(1) and an output PT(3) drop port for the signal continuing to 32-DMX-L card. It also has 32 internal OCH pass-through channels (PT) and 32 external input channels (ADD). This unit is 2 slots sized.
LINE-{{1-8}}-{{1-5,12-16}}-{{2}}- {RX,TX} (COM)	
LINE-{{1-8}}-{{1-5,12-16}}-{{3}}- {TX} (DROP)	
LINE-{{1-8}}-{{1-5,12-16}}-{{1-3}}-ALL	
CHAN-{{1-8}}-{{1-5,12-16}}-{{1-32}}- {RX} (ADD)	
CHAN-{{1-8}}-{{1-5,12-16}}-{{1-32}}- {PT} (PT)	
CHAN-{{1-8}}-{{1-5,12-16}}-{{1-32}}- ALL	
LINE-{{1-8}}-{{1-6,12-17}}-{{1}}- {RX,TX} (EXP)	
LINE-{{1-8}}-{{1-6,12-17}}-{{2}}- {RX,TX} (COM)	
LINE-{{1-8}}-{{1-6,12-17}}-{{3}}- {RX,TX} (EXP to other ring)	
LINE-{{1-8}}-{{1-6,12-17}}-{{1-3}}-ALL	
LINE-{{1-8}}-{{1-6,12-17}}-{{1-3}}-ALL	

25.1.15 LINEWL

Line wavelength. Identifies a wavelength channel included in any of the lower layer OTS facilities.

Table 25-16 LINEWL

Values	Description
LINEWL-{{1-6,12-17}}-{{1-32}}-{{RX,TX}}-1530.33	Identifies an allocated wavelength channel included in any of the lower layer OTS facilities.
LINEWL-{{1-6,12-17}}-{{1-32}}-{{RX,TX}}-ALL	Identifies all of the allocated wavelength channels included in any of the lower layer OTS facilities.

Table 25-16 LINEWL (continued)

Values	Description
LINEWL-{{1-8}-}{1-6,12-17}-{1-32}- {RX,TX,PT}-<WLEN>	<p>OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies.</p> <p>Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in.</p> <p>The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH] (in the form of 15xx.yy).</p>
LINEWL -{{1-8}-}{1-6,12-17}-{1-32}-{RX,TX,PT}-ALL	<p>OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies.</p> <p>Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in.</p> <p>The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH] (in the form of 15xx.yy).</p>

25.1.16 LNKTERM

Link Termination AIDs that are used to access the termination points of a provisionable patchcord.

Table 25-17 LNKTERM

Pattern	Description
ALL	Indicates all the provisionable patchcord terminations on a node. Applicable only for the retrieve commands
LNKTERM-ALL	Indicates all the provisionable patchcord terminations on a node. Applicable only for the retrieve commands
LNKTERM-{1-65535}	Indicates a single provisionable patchcord termination point on a node

25.1.17 MSSPR

MS-SPRing AIDs are used to access the MS-SPRing of the NE.

Table 25-18 MSSPR

Pattern	Description
ALL	ALL AID for all MS-SPRings in the NE. The ALL AID is applicable for retrieve-only commands like RTRV-MSSPR and RTRV-ALM/COND-MSSPR
MSSPR-RINGID	MS-SPRing AID is keyword MS-SPRing followed by RINGID. RINGID is a string of up to 6 characters; valid characters are [A-Z,0-9] (case insensitive)

25.1.18 OPM

OPM AIDs represent the single wavelength inside an optical power monitoring object

Table 25-19 OPM

Values	Description
ALL	The first ID represents the shelf, the second ID represents the slot, and the last ID is the wavelength, represented in the form of 15xx.yy nanometers. The last index of the wavelength is the value of the wavelength as described in OPTICAL_WLEN.
OPM[-{1-8}]-{1-5,12-16}-ALL	
OPM[-{1-8}]-{1-5,12-16}-<wlen>	

25.1.19 OSC

OSC AIDs are used to access the OSC of the NE

Table 25-20 OSC

Values	Description
ALL	All of the OSCs of the NE. The ALL AID applies to the retrieve-only commands
OSC-RINGID	RINGID is a string of up to six characters, valid characters are [A-Z,0-9] (case insensitive)

25.1.20 PRSLOT

Valid protection slots for the electrical cards. Format is SLOT-[SLOT].

Table 25-21 PRSLOT

Pattern	Description
NULL	Indicates there is no protection group. Used when trying to delete a protection group.
SLOT-1	The No. 1 slot of an NE
SLOT-3	The No. 3 slot of an NE
SLOT-5	The No. 5 slot of an NE
SLOT-13	The No. 13 slot of an NE

Table 25-21 *PR SLOT (continued)*

Pattern	Description
SLOT-15	The No. 15 slot of an NE
SLOT-17	The No. 17 slot of an NE

25.1.21 RFILE

File transfer type.

Table 25-22 *RFILE*

Pattern	Description
RFILE-DB	Transferring the system database
RFILE-LOG	Transferring a log file
RFILE-PKG	Transferring a software package

25.1.22 SHELF

Identifies a shelf within a node. SHELF is applicable only to nodes that are set to MULTISHELF or MULTISHELFETH mode.

Table 25-23 *SHELF*

Pattern	Description
SHELF-ALL	All of the shelves in the node.
SHELF-{1-8}	A specific shelf in the node.

25.1.23 SYN

Synchronization AIDs

Table 25-24 *SYN*

Pattern	Description
SYNC-NE	NE synchronization AID

25.1.24 SYN_SRC

Synchronization source

Table 25-25 SYN_SRC

Pattern	Description
BITS-1	Synchronization source is BITS-1. Format is BITS-[PORT]
BITS-2	Synchronization source is BITS-2. Format is BITS-[PORT]
FAC-{1-4,11-14}-{1-4}	Synchronization source is 4-Port STM64. Format is FAC-[SLOT]-[PORT]
FAC-{1-6,12-17}-{1-4}	Synchronization source is the optical card (STM1 and STM4) facility. Format is FAC-[SLOT]-[PORT]
FAC-{1-6,12-17}-{1}	Synchronization source is the optical card (STM1 and STM4) facility. Format is FAC-[SLOT]-[PORT]
FAC-{5,6,12,13}-{1}	Synchronization source is the optical card (STM16 and STM 64) facility. Format is FAC-[SLOT]-[PORT]
INTERNAL	Set the SYN_SRC to be the system default value. The “Internal” value of the SYN_SRC is only applied for the SYNC-NE AID on the ED-SYNCN command
NONE	Set the SYNC_SRC value to the default value for BITS-OUT. The “NONE” value of SYNC_SRC only applies to the BITS-1 and BITS-2 AID of the ED-SYNCN command
SYNC-NE	SYNC-NE source. It is only used in the alarm report or alarm retrieve commands.

25.1.25 SYNC_REF

Synchronization AIDs

Facility AID format for line timing:

- Format for optical facilities without PPM: FAC-[SLOT]-[PORT]
- Format for optical facilities with PPM: FAC-[SLOT]-[PPM]-[PORT]

Table 25-26 SYNC_REF

Pattern	Description
ALL	Equivalent to a combination of SYNC-ALL, BITS-1, and BITS-2. This AID is valid only for the commands RTRV-SYNCN, RTRV-ALM-SYNCN, and RTRV-COND-SYNCN
SYNC-ALL	All synchronization references
SYNC-NE	NE synchronization AID
SYNC-{BITS1,BITS2}	BITS1 and BITS2 sync AIDs

25.1.26 SYNC_SW

Synchronization reference

Table 25-27 SYNCSW

Pattern	Description
INT	Internal clock. The “INT” value of the SYNCSW is only applied for the SYNC-NE AID on the OPR-SYNCSW command.
PRI	Primary timing reference
SEC	Secondary timing reference

25.1.27 UDC

UDC AIDs for F-UDC and DCC-UDC channels on the AIC-I card.

Table 25-28 UDC

Pattern	Description
ALL	ALL AID is applicable to retrieve-only commands, for example: RTRV-ALM/COND-UDCF and RTRV-ALM/COND-UDCDCC. It corresponds to a superset of F-UDC and DCC-UDC AIDs
UDC-{F,DCC}-{A,B}	F-UDC and DCC-UDC AIDs for A and B channels

25.1.28 VC

SDH Frame Level AID Set.

VC4 AID Formats

- For Optical Facilities: VC4-[SLOT]-[PORT]-[VC4]
- For Electrical Facilities: VC4-[SLOT]-[VC4]

VC3 AID Formats

- For Optical Facilities: VC3-[SLOT]-[PORT]-[VC4]-[VC3]
- For Electrical Facilities: VC3-[SLOT]-[VC4]-[VC3]

VC12 AID Formats

- For Optical Facilities: VC12-[SLOT]-[PORT]-[VC4]-[VC3]-[TUG2]-[VC12]
- For Electrical Facilities: VC12-[SLOT]-[VC4]-[VC3]-[TUG2]-[VC12]

VC11 AID Formats

- For Optical Facilities: VC11-[SLOT]-[PORT]-[VC4]-[VC3]-[TUG2]-[VC11]
- For Electrical Facilities: VC11-[SLOT]-[VC4]-[VC3]-[TUG2]-[VC11]

Table 25-29 VC

Pattern	Description
VC11-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-4}	VC11 for the STM1E-12 card
VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}	VC11 for STM4-4
VC11-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-4}	VC11 for STM1-8 card

Table 25-29 VC (continued)

Pattern	Description
VC11-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-4}	VC11 for STM-16 card
VC11-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-4}	VC11 for STM-4 card
VC11-{1-6,12-17}-{1-14}-1-11{1-7}-{1-2}	VC11 for the E1 card
VC11-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}	VC11 for the STM-1 card
VC12-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-3}	VC12 for the STM1E-12 card
VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-3}	VC12 for STM4-4 card
VC12-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-3}	VC12 for STM1-8 card
VC12-{1-6,12-17}-1-1-{1-7}-{1-2}	VC12 AIDs for the E1 card
VC12-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-3}	VC12 for STM-16 card
VC12-{1-6,12-17}-1-{1-2}-{1-7}-{1-3}	VC12 for E1-42 card
VC12-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-3}	VC12 for STM-4 card
VC12-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}	VC12 for STM-1 card
VC3-{1-14,14-17}-{1-12}-1-{1-3}	VC3 for the STM1E-12 card
VC3-{1-4,14-17}-{1-4}-{1-4}-{1-3}	VC3 for the STM4-4 card
VC3-{1-4,14-17}-{1-8}-1-{1-3}	VC3 for the STM1-8 card
VC3-{1-6,12-17}-1-{1-16}-{1-3}	VC3 for the STM-16 card
VC3-{1-6,12-17}-1-{1-4}-{1-3}	VC3 for the STM-4 card
VC3-{1-6,12-17}-{1-4}-1-{1-3}	VC3 for the STM-1 card
VC3-{1-6,12-17}-{1-4}-{1-3}	VC3 AID for DS3i-N-12 and E3 card
VC4-{1-4,14-17}-1	VC4 for E1-42 card
VC4-{1-4,14-17}-1-1	VC4 for STM1-8 card
VC4-{1-4,14-17}-{1-12}-1	VC4 for the STM1E-12 card
VC4-{1-4,14-17}-{1-4}-1	VC4 for the STM4 card and VC4-4c for the STM4-4 card
VC4-{1-4,14-17}-{1-4}-{1-2}	VC4-3c for the STM4-4 card
VC4-{1-4,14-17}-{1-4}-{1-3}	VC4-2c for the STM4-4 card
VC4-{1-4,14-17}-{1-4}-{1-4}	VC4 for the STM4-4 card
VC4-{1-4,14-17}-{1-8}-1	VC4 for the STM1-8 card
VC4-{1-6,12-17}-1	VC4 for E1 card
VC4-{1-6,12-17}-1-1	VC4-4c for the STM-4 card. VC4-16c for the STM-16 card
VC4-{1-6,12-17}-1-{1,2,3,5,6,7,9,10,11,13,14,15}	VC4-2c for the STM-16 card
VC4-{1-6,12-17}-1-{1,2,4,5,6,7,9,10,13,14}	VC4-3c for the STM-16
VC4-{1-6,12-17}-1-{1,5,9,13}	VC4-4c for the STM-16 card
VC4-{1-6,12-17}-1-{1-16}	VC4 for the STM-16 card
VC4-{1-6,12-17}-1-{1-2}	VC4-3c for the STM-4 card
VC4-{1-6,12-17}-1-{1-3}	VC4-2c for the STM-4 card
VC4-{1-6,12-17}-1-{1-4}	VC4 for the STM-4 card

Table 25-29 VC (continued)

Pattern	Description
VC4-{1-6,12-17}-1-{1-9}	VC4-8c for the STM-16 card (454)
VC4-{1-6,12-17}-{1-4}	VC4 for DS3i-N-12, E3, G1000 cards
VC4-{1-6,12-17}-{1-4}-1	VC4 for STM-1 card

25.1.29 WDMANS

This AID is used to access the automatic optical node set up (AONS) application of the NE.

Table 25-30 WDMANS

Pattern	Description
AONS-{E,W}	Automatic Optical Node Setup identifier (per ring direction based)
WDMANS-{E,W}	Automatic Optical Node Setup identifier (per ring direction based)

25.1.30 WLEN

This AID represents the single wavelength inside an external facility. If the facility is of type OTS (line) the wavelengths contained are all the available in the node: currently 32. If the facility is of type OCH (CHAN) the wavelength is just one and it is the same of the correspondent wavelength customized for that channel.

Table 25-31 WLEN

Pattern	Description
WLEN-{E,W}-{ADD,DROP,EXP}-{1530.33,1531.12,1531.90,1532.68,1534.25,1535.04,1535.82,1536.61,1538.19,1538.98,1539.77,1540.56,1542.14,1542.94,1543.73,1544.53,1546.12,1546.92,1547.72,1548.51,1550.12,1550.92,1551.72,1552.52,1554.13,1554.94,1555.75,1556.55,1558.17,1558.98,1559.79,1560.61}	Wavelength identifier

25.2 CTC Port Numbers and TL1 Aids

Table 25-32 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
OSC-CSM	COM-RX	2	LINE-shelf-slot-1-RX
	COM-TX	3	LINE-shelf-slot-1-TX
	LINE-RX	4	LINE-shelf-slot-2-RX
	LINE-TX	5	LINE-shelf-slot-2-TX
	OSC-RX	6	LINE-shelf-slot-3-RX
	OSC-TX	7	LINE-shelf-slot-3-TX
	OC3	1	FAC-shelf-slot-1
32 DMX	CHAN TX	1-32	CHAN-shelf-slot-i-TX (i=1..32)
	COM-RX	33	LINE-shelf-slot-1-RX
OPT-PRE	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	3	LINE-shelf-slot-2-RX
	DC-TX	4	LINE-shelf-slot-2-TX
4 MD	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	CHAN-RX	5	CHAN-shelf-slot-3-RX
	CHAN-TX	6	CHAN-shelf-slot-3-TX
	CHAN-RX	7	CHAN-shelf-slot-4-RX
	CHAN-TX	8	CHAN-shelf-slot-4-TX
	COM-RX	9	LINE-shelf-slot-1-RX
	COM-TX	10	LINE-shelf-slot-1-TX
32 DMX L	CHAN TX	1 - 32	CHAN-shelf-slot-i-TX (i=1..32)
	COM RX	33	LINE-shelf-slot-1-RX
32 WSS L	ADD-RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	PT	33-64	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	69	LINE-shelf-slot-3-TX
	EXP-RX	66	LINE-shelf-slot-2-RX
	EXP-TX	65	LINE-shelf-slot-2-TX
	COM-RX	68	LINE-shelf-slot-1-RX
	COM-TX	67	LINE-shelf-slot-1-TX

Table 25-32 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
OPT-BST	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-BST E	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-BST L	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-2-TX
	OSC-RX	3	LINE-shelf-slot-1-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-1-RX
	LINE-TX	6	LINE-shelf-slot-2-TX
MMU	EXPA-RX	5	LINE-shelf-slot-3-RX
	EXPA-TX	6	LINE-shelf-slot-3-TX
	EXP-RX	1	LINE-shelf-slot-1-RX
	EXP-TX	2	LINE-shelf-slot-1-TX
	COM-RX	3	LINE-shelf-slot-2-RX
	COM-TX	4	LINE-shelf-slot-2-TX
AD 1B	BAND-RX	1	BAND-shelf-slot-1-RX
	BAND-TX	2	BAND-shelf-slot-1-TX
	EXP-RX	3	LINE-shelf-slot-1-RX
	EXP-TX	4	LINE-shelf-slot-1-TX
	COM-RX	5	LINE-shelf-slot-2-RX
	COM-TX	6	LINE-shelf-slot-2-TX

Table 25-32 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
AD 1C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	EXP-RX	3	LINE-shelf-slot-1-RX
	EXP-TX	4	LINE-shelf-slot-1-TX
	COM-RX	5	LINE-shelf-slot-2-RX
	COM-TX	6	LINE-shelf-slot-2-TX
AD 2C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	EXP-RX	5	LINE-shelf-slot-1-RX
	EXP-TX	6	LINE-shelf-slot-1-TX
	COM-RX	7	LINE-shelf-slot-2-RX
	COM-TX	8	LINE-shelf-slot-2-TX
AD 4C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	CHAN-RX	5	CHAN-shelf-slot-3-RX
	CHAN-TX	6	CHAN-shelf-slot-3-TX
	CHAN-RX	7	CHAN-shelf-slot-4-RX
	CHAN-TX	8	CHAN-shelf-slot-4-TX
	EXP-RX	9	LINE-shelf-slot-1-RX
	EXP-TX	10	LINE-shelf-slot-1-TX
	COM-RX	11	LINE-shelf-slot-2-RX
	COM-TX	12	LINE-shelf-slot-2-TX
32 WSS	ADD-RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	PT	33-64	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	69	LINE-shelf-slot-3-TX
	EXP-RX	66	LINE-shelf-slot-2-RX
	RXP-TX	65	LINE-shelf-slot-2-TX
	COM-RX	68	LINE-shelf-slot-1-RX
	COM-TX	67	LINE-shelf-slot-1-TX
32 DMXO	CHAN-TX	1-32	CHAN-shelf-slot-i-TX (i=1..32)
	COM-RX	33	LINE-shelf-slot-1-RX

Table 25-32 *CTC Port Numbers and TL1 Aids*

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
32 MUXO	CHAN RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	COM TX	33	LINE-shelf-slot-1-TX

