



Release Notes for Cisco D9854/D9854-I, D9858, and D9824 Receiver/Decoder for Version 4.75

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Introduction

This document describes the hash digest values, open caveats, and resolved caveats in Version 4.75 for the Cisco D9854/D9854-I Advanced Program Receiver, D9858 Advanced Receiver Transcoder, and D9824 Advanced Multi Decryption Receiver.

Hash Digest Values

You can generate the hash digest to verify the integrity of the software and FPGA versions running on the D9854/D9854-I, D9858, and D9824 units. For more information on generating the hash digest values, see *Cisco D9854/D9854-I Advanced Program Receiver Installation and Configuration Guide*, *Cisco D9858 Advanced Receiver Transcoder Installation and Configuration Guide*, and *D9824 Advanced Multi Decryption Receiver Installation and Configuration Guide*.

The following table lists the official D9854 V4.75 hash digest values.

Code/Processor	Hash Digest	FPGA ID	Version
App/5514	55bd5046 0615c942 3ae98b67 99918e92 b4437018	N/A	R04.75.00DD
App/7109	2d7953b6 9fdc6894 59b9eab0 80e428b4 2caf5b3d	N/A	R04.75.00DD
FPGA FX60/7109	41cd4328 6bc4cb16 cd1fdcd8 eaad78f6 65cfc05f	2	R00.00.03az (MPEG over IP)
FPGA FX60/7109	3bb3523e b4954d1b 382913ce c1fad497 ff5fc115	4	R00.00.02Vu (SFN)
FPGA FX60/7109	ae9d1429 8409e31f 90b6fcfd 77d9101f 4b4de638	1	R00.00.05az (SDI)
FPGA FX100/7109	9842038e 20a58cf3 a41922ee 1770381b c6ab0d0e	1	R00.00.05az (MPEG+SDI)

The following table lists the official D9854-I V4.75 hash digest values.

Code/Processor	Hash Digest	Version
App/5514	55bd5046 0615c942 3ae98b67 99918e92 b4437018	R04.75.00DD

Code/Processor	Hash Digest	Version
App/7109	2d7953b6 9fdc6894 59b9eab0 80e428b4 2caf5b3d	R04.75.00DD
FPGA LX155T/7109	2ffb9d0 095d8aca 36ff301e 39971d5a b2b5ee28	R00.00.01az
FEC FPGA/7109	c9e3fd86 1b8f53e7 a2c67d09 3d0aed88 f9871985	R00.00.01vi

The following table lists the official D9858 V4.75 hash digest values.

Code/Processor	Hash Digest	Version
App/5514	55bd5046 0615c942 3ae98b67 99918e92 b4437018	R04.75.00DD
App/7109	2d7953b6 9fdc6894 59b9eab0 80e428b4 2caf5b3d	R04.75.00DD
APP/SAT	657bf20f 8897424a 72593df0 b98e67a3 1924bb8e	R04.75.00DD
FPGA FX60/7109	edd457b4 03ccaa84 84635a9e 1704999e 733616a4	R00.00.05az

The following table lists the official D9824 V4.75 hash digest values.

Code/Processor	Hash Digest	Version
App/5514	55bd5046 0615c942 3ae98b67 99918e92 b4437018	R04.75.00DD
App/7109	2d7953b6 9fdc6894 59b9eab0 80e428b4 2caf5b3d	R04.75.00DD
FPGA LX110T/7109	41c88170 b0f4fb1b 329093ef 65bccf63 83d34470	R00.00.03az

The table below lists the official D9824 V4.75, with FEC hash digest values. D9824 units purchased after December 2013 may include the IP FEC feature. For more information on identifying the D9824 units, with FEC, see the *Identifying the new Cisco D9854-I Advanced Program Receiver and D9824 Multi Decryption Receiver Hardware Releases Technical Reference*.

Code/Processor	Hash Digest	Version
App/5514	55bd5046 0615c942 3ae98b67 99918e92 b4437018	R04.75.00DD
App/7109	2d7953b6 9fdc6894 59b9eab0 80e428b4 2caf5b3d	R04.75.00DD
FPGA LX155T/7109	2ffb9d0 095d8aca 36ff301e 39971d5a b2b5ee28	R00.00.01az
FEC FPGA/7109	c9e3fd86 1b8f53e7 a2c67d09 3d0aed88 f9871985	R00.00.01vi

If these values do not match the generated digest values, contact Cisco Services.

Caveats

This section includes open and resolved caveats for this release. This section includes the following topics.

- [Open Caveats - Version 4.75, on page 3](#)
- [Resolved Caveats - Version 4.75, on page 3](#)

Open Caveats - Version 4.75

The following lists an open caveat in Version 4.75. The caveat ID can be searched using the Cisco Bug Search Tool (<https://tools.cisco.com/bugsearch>), where you can find details about the caveat.

Bug ID	Description
CSCvd27485 (D9824 only)	Performance limitations with Conditional Access (CA) robustness enhancements. The D9824 receiver may experience issues with descrambling and decoding if more than eight program entries are enabled and each program entry utilizes the maximum number of scrambled services per program entry. If this release is used to enable the CA enhancements on the D9824 receiver, contact Cisco Services for review.
CSCut60159	Possible erroneous ASI muting and unmuting messages, without specific reasons in the upstream.

Resolved Caveats - Version 4.75

The following is a list of resolved caveats in Version 4.75. The caveat ID can be searched using the Cisco Bug Search Tool (<https://tools.cisco.com/bugsearch>), where you can find details about the caveat.

Bug ID	Description
CSCvc44339	Conditional access robustness enhancements.
CSCuy41882 (D9854 and D9854-I only)	OP47 SDP checksum is incorrect.
CSCuy60648 (D9854 and D9854-I only)	The S2031 payload is bit-reversed.

Obtaining Documentation and Submitting a Service Request

For information on obtaining documentation, using the Cisco Bug Search Tool (BST), submitting a service request, and gathering additional information, see [What's New in Cisco Product Documentation](#).

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