

# 5-in-1 EIA/TIA-449 Assembly and Pinouts

Document ID: 10343

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## Introduction

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## Introduction

This document displays the 5-in-1 EIA/TIA-449 cable assembly and describes its cable pin connections.

## Prerequisites

## Requirements

There are no specific requirements for this document.

## Components Used

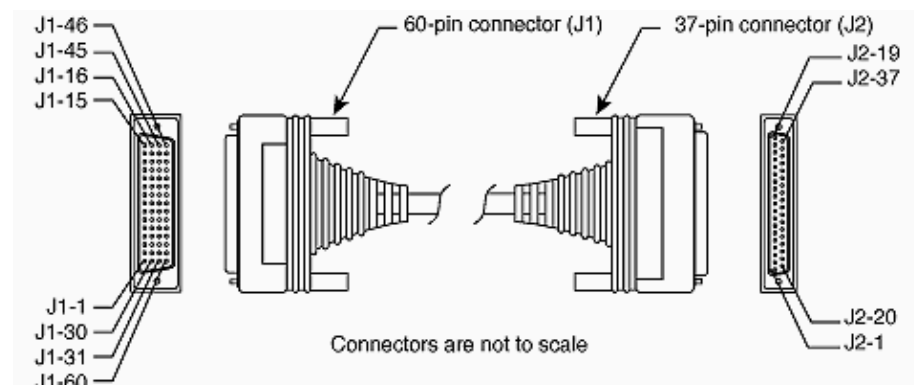
This document is not restricted to specific software and hardware versions.

The information in this document was created from the devices in a specific lab environment. All of the devices used in this document started with a cleared (default) configuration. If your network is live, make sure that you understand the potential impact of any command.

## Conventions

For more information on document conventions, refer to the Cisco Technical Tips Conventions.

## EIA/TIA-449 Cable Assembly



## EIA/TIA-449 DTE Cable Pinouts (DB-60 to DB-37)

60 Pin <sup>1</sup>	Signal	Description	Direction	37 Pin	Signal
J1-49					
J1-48	MODE_1				
J1-51	GND				
J1-52	GND	Shorting			
J1-46	MODE_DCE Shield_GND	Group Single	--	J2-1	Shield GND
J1-11					
J1-12	TxD/RxD+	Twisted pair	==>	J2-4	SD+
J1-24	TxD/RxD--	no. 6	==>	J2-22	SD--
J1-23	TxC/RxC+	Twisted pair	<==	J2-5	ST+
J1-28	TxC/RxC--	no. 9	<==	J2-23	ST--
J1-27	RxD/TxD+	Twisted pair	<==	J2-6	RD+
J1-9	RxD/TxD--	no. 11	<==	J2-24	RD--
J1-10	RTS/CTS+	Twisted pair	==>	J2-7	RS+
J-26	RTS/CTS--	no. 5	==>	J2-25	RS--
J1-25	RxC/TxCE+	Twisted pair	<==	J2-8	RT+
J1-1	RxC/TxCE--	no. 10	<==	J2-26	RT-
J1-2	CTS/RTS+	Twisted pair	<==	J2-9	CS+
J1-44	CTS/RTS--	no. 1	<==	J2-27	CS--
J1-45	LL/DCD	Twisted pair	==>	J2-10	LL
J1-3	Circuit_GND	no. 12	--	J2-37	CS
J1-4	DSR/DTR+	Twisted pair	<==	J2-11	DM+
J1-7	DSR/DTR--	no. 2	<==	J2-29	DM--
J1-8	DTR/DSR+	Twisted pair	==>	J2-12	TR+
J1-5	DTR/DSR--	no. 4	==>	J2-30	TR--
J1-6	DCD/DCD+	Twisted pair	<==	J2-13	RR+
J1-13	DCD/DCD--	no. 3	<==	J2-31	RR-
J1-14	TxCE/TxC+	Twisted pair	==>	J2-17	TT+
J1-15	TxC/TxCND	Twisted pair	==>	J2-39	TT-

J1-16	Circuit_GND	no. 9	--	J2-20	RC
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<sup>1</sup> Any pin not referenced is not connected.

## EIA/TIA-449 DCE Cable Pinouts (DB-60 to DB-37)

60 Pin <sup>1</sup>	Signal	Description	Direction	37 Pin	Signal
J1-49					
J1-48	MODE_1	Shorting			
J1-46	GND Shield_GNC	Group Single	--	J2-1	Shield GND
J1-28					
J1-27	RxD/TxD+	Twisted pair	<==	J2-4	SD+
	RxD/TxD--		<--	J2-22	SD--
J1-13		no. 11			
J1-14	TxCE/TxC+	Twisted pair	==>	J2-5	ST+
	TxCE/TxC--		-->	J2-23	ST--
J1-11		no. 7			
J1-12	TxD/RxD+	Twisted pair	==>	J2-6	RD+
	TxD/RxD--		-->	J2-24	RD--
J1-1		no. 6			
J1-2	CTS/RTS+	Twisted pair	<==	J2-7	RS+
	CTS/RTS--		<--	J2-25	RS--
J1-24		no. 1			
J1-23	TxC/RxC+	Twisted pair	==>	J2-8	RT+
	TxC/RxC--		-->	J2-26	RT--
J1-9		no. 9			
J1-10	RTS/CTS+	Twisted pair	==>	J2-9	CS+
	RTS/CTS--		-->	J2-27	CS--
J1-29		no. 5			
J1-30	NIL/LL	Twisted pair	==>	J2-10	LL
	Circuit_GND			J2-37	SC
J1-3		no. 12			
J1-4	DTR/DSR+	Twisted pair	==>	J2-11	DM+
	DTR/DSR--		-->	J2-29	DM--
J1-3	DSR/DTR+	no. 4 Twisted pair	<==	J2-12	TR+
J1-4	DSR/DTR--		<==	J2-30	TR--

		no. 2			
J1-5					
J1-6	DCD/DCD+	Twisted pair	==>	J2-13	RR+
	DCD/DCD-		-->	J2-31	RR--
J1-26		no. 3			
J1-25	RxC/TxCE+	Twisted pair	<==	J2-17	TT+
	RxC/TxCE-		<--	J2-35	TT-
J1-15		no. 10			
J1-16	Circuit_GND	Twisted pair	--	J2-19	SG
	Circuit_GND		--	J2-20	RC
		no. 8			

<sup>1</sup> Any pin not referenced is not connected.

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Updated: Aug 01, 2006

Document ID: 10343