

分析分級 Fretta 平台依賴 (PD) 問題

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簡介

本文檔介紹如何在Fretta(NCS5500)系列路由器上分析和查詢平台相關問題的根本原因。

要收集的日誌

對於任何與平台相關的診斷，請收集下列基本日誌。根據可疑的元件或功能區域，收集除基本日誌之外的其他日誌，如下面的小節所示。

要收集的基本日誌

- show logging
- show tech ctrace
- show tech card-manager
- show tech shelf-mgr
- show tech os

- show tech obfl

HeartBeat未命中日誌

- show tech heart-beat miss

EOBC/EPC相關日誌

請參閱跟蹤EOBC/EPC特定問題。

- show tech control-ethernet

光纖相關

- show tech fabric from xrvn

切片管理器

- 顯示技術支援sdr_mgr
- show tech-support install
- 顯示技術支援交換矩陣
- show tech-support fpd
- show tech-support cm
- show controller fabric health
- 顯示平台切片

MPA管理器

- show tech-support mpa-mgr

LED

- show tech-support env
- show tech-support alarm_mgr
- 顯示警報
- show led
- 顯示環境

有關LED和狀態的詳細資訊，請參閱以下連結。

PD安裝

請參閱跟蹤PD安裝問題

詳細資料

使用card-mgr日誌進行分析

- 請花一些時間瀏覽卡管理器詳細維基，網址為
- 若要調試任何平台問題，您需要使用show tech ctrace、show tech card-mgr和show tech shelf_mgr日誌來瞭解正在發生的情況。
- show reboot-history card location <> from shelf_mgr logs提供卡的重新啟動歷史記錄。
- show controllers card-mgr event-history brief location <>和show controllers card-mgr event-history detail location <>show tech card-mgr logs提供了有關card-mgr fsm狀態機詳細資訊的詳細資訊。
- 當卡無法啟動時，您需要檢視該卡的事件歷史記錄，並根據卡停滯/發生故障的狀態/事件檢查BIOS、PD安裝或card-mgr視點。每個事件都有一個關聯的郵遞區號，提供有關FSM運動的線索。

 附註：如果線卡無法啟動並進入「失敗/失敗」狀態，則您需要在強制重新載入線卡後連線到線卡控制檯。這有助於您瞭解線卡無法啟動的原因。show tech無法從發生故障的線卡中收集日誌。

請參閱上面提到的card-mgr詳細wiki，瞭解特定狀態、事件和郵遞區號詳細資訊的含義。另請參閱calvados/dc_common_pkg/drivers/card_mgr/card_mgr_fsm.smil 上的card manager smil檔案。此檔案很好地描述了FSM狀態、事件和狀態轉換。

以下是在LC冷重設時的工作案例事件歷史記錄簡要輸出的範例：

```
sysadmin-vm:0_RP0# show controller card-mgr event-history brief location 0/1
Mon Dec 16 14:47:58.974 UTC+00:00
```

```
Card Event History for: 0/1
```

```
Card Event History as seen by Master (0/RP0)
Current State: CARD_READY
```

DATE	TIME (UTC)	STATE	EVENT
12/16	14:46:51.116	WAIT_CARD_INFO	ev_card_info_synced
12/16	14:46:06.990	WAIT_SYSADMIN_VM_READY	ev_sysadmin_vm_booted
12/16	14:45:57.375	HOST_OS_RUNNING	ev_sysadmin_vm_started
12/16	14:45:39.554	BOOTLDR_STARTED	ev_host_os_started
12/16	14:44:22.746	CARD_POWERED_ON	ev_bootldr_started
12/16	14:44:19.142	IOFPGA_BOOTED	ev_dm1_power_up_ok
12/16	14:44:12.825	IOFPGA_RESET_CHECK	ev_inserted
12/16	14:44:12.325	CARD_IN_RESET	ev_removed
12/16	14:44:10.224	PROCESS_PENDING_RESET	if_pending_cold_reset_req
12/16	14:44:10.224	SYSADMIN_VM_GOING_DOWN	ev_host_haltng_os
12/16	14:43:50.258	SYSADMIN_VM_GOING_DOWN	ev_cold_reset_req
12/16	14:43:34.275	CARD_READY	ev_sysadmin_vm_shutdown
12/16	11:11:55.291	OIR_INSERT_NOTIF	if_card_local_init_done
12/16	11:11:55.290	IDLE	ev_card_info_synced

事件歷史記錄詳細輸出示例：

sysadmin-vm:0_RP0# show controller card-mgr event-history detail location 0/1
Mon Dec 16 14:49:20.850 UTC+00:00

Card Event History for: 0/1

Card Event History as seen by Master (0/RP0)

Event buffer info:

Total number of events recorded: 14

Number of events available for display: 14

Current State: CARD_READY

EVENT #: 13 (record index = 13)

TIMESTAMP: 2019/12/16 14:46:51.116090 UTC

STATE: WAIT_CARD_INFO

EVENT: ev_card_info_synced

EVENT DESC: Card info of the remote node has been received

EVENT #: 12 (record index = 12)

TIMESTAMP: 2019/12/16 14:46:06.990465 UTC

STATE: WAIT_SYSADMIN_VM_READY

EVENT: ev_sysadmin_vm_booted

EVENT DESC: SysAdmin VM has booted

EVENT #: 11 (record index = 11)

TIMESTAMP: 2019/12/16 14:45:57.375813 UTC

STATE: HOST_OS_RUNNING

EVENT: ev_sysadmin_vm_started

EVENT DESC: SysAdmin VM has been started from host

EVENT #: 10 (record index = 10)

TIMESTAMP: 2019/12/16 14:45:39.554589 UTC

STATE: BOOTLDR_STARTED

EVENT: ev_host_os_started

EVENT DESC: Host OS has started booting

EVENT #: 9 (record index = 9)

TIMESTAMP: 2019/12/16 14:44:22.746147 UTC

STATE: CARD_POWERED_ON

EVENT: ev_bootldr_started

EVENT DESC: Bootloader on the card has started booting

EVENT #: 8 (record index = 8)

TIMESTAMP: 2019/12/16 14:44:19.142021 UTC

STATE: IOFPGA_BOOTED

EVENT: ev_dm1_power_up_ok

EVENT DESC: I/O FPGA indicating power domain 1 was successfully powered up

EVENT #: 7 (record index = 7)

TIMESTAMP: 2019/12/16 14:44:12.825682 UTC

STATE: IOFPGA_RESET_CHECK

EVENT: ev_inserted

EVENT DESC: Card inserted into the chassis or I/O FPGA booted

EVENT #: 6 (record index = 6)

TIMESTAMP: 2019/12/16 14:44:12.325703 UTC

STATE: CARD_IN_RESET

EVENT: ev_removed

EVENT DESC: Card removed from chassis or I/O FPGA was power cycled

EVENT #: 5 (record index = 5)

TIMESTAMP: 2019/12/16 14:44:10.224354 UTC
STATE: PROCESS_PENDING_RESET
EVENT: if_pending_cold_reset_req

EVENT #: 4 (record index = 4)
TIMESTAMP: 2019/12/16 14:44:10.224343 UTC
STATE: SYSADMIN_VM_GOING_DOWN
EVENT: ev_host_halting_os
EVENT DESC: Host is performing halting of OS

EVENT #: 3 (record index = 3)
TIMESTAMP: 2019/12/16 14:43:50.258016 UTC
STATE: SYSADMIN_VM_GOING_DOWN
EVENT: ev_cold_reset_req
EVENT DESC: Client request to cold reset the card (I/O FPGA is also power-cycled)

EVENT #: 2 (record index = 2)
TIMESTAMP: 2019/12/16 14:43:34.275167 UTC
STATE: CARD_READY
EVENT: ev_sysadmin_vm_shutdown
EVENT DESC: SysAdmin VM shutdown operation has started

EVENT #: 1 (record index = 1)
TIMESTAMP: 2019/12/16 11:11:55.291184 UTC
STATE: OIR_INSERT_NOTIF
EVENT: if_card_local_init_done

EVENT #: 0 (record index = 0)
TIMESTAMP: 2019/12/16 11:11:55.290959 UTC
STATE: IDLE
EVENT: ev_card_info_synced
EVENT DESC: Card info of the remote node has been received

在主SC和主SUP上觀察到的後代碼示例：

從以下輸出中識別相關卡的物理插槽編號以及主SC和SUP:

位置0/1物理插槽編號為2 (ID列)，主SC為0/SC0，主SUP為0/RP0。

```
sysadmin-vm:0_RP0# show controller card-mgr inventory summary
```

Mon Dec 16 14:50:50.810 UTC+00:00

Card Manager Inventory Summary :

BP HW

Location	Card Type	ID	Serial Number	Ver	Card State
----------	-----------	----	---------------	-----	------------

0/1	NC55-32T16Q4H-AT	2	JAE233813G2	0.302	CARD_READY
0/2	NC55-12X100GE-PROT	3	SAL1918EF3S	0.203	CARD_READY
0/FC1	NC55-5504-FC	22	JAE210600VD	0.3	CARD_READY
0/FC3	NC55-5504-FC	24	JAE210600XV	0.3	CARD_READY
0/FC5	NC55-5504-FC	26	JAE210600VX	0.3	CARD_READY
0/RP0	NC55-RP (Master)	27	SAL2044VUZT	1.0	CARD_READY
0/RP1	NC55-RP (Slave)	28	SAL1916DT8B	0.2040	CARD_READY
0/SC0	NC55-SC (Master)	29	SAL2046W07E	1.6	CARD_READY

使用以下命令獲取線路卡0/1的郵遞區號清單：

```
sysadmin-vm:0_RP0# show controller card-mgr trace cmgr_isr location 0/SC0 | inc "slot 2" | inc changed
Mon Dec 16 14:56:27.355 UTC+00:00
2019-12-16:11.14.44.916211712:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.14.44.916268544:[ISR]: POST Code for slot 22 changed to 0x54
2019-12-16:11.14.44.916295168:[ISR]: POST Code for slot 24 changed to 0x54
2019-12-16:11.14.44.916321280:[ISR]: POST Code for slot 26 changed to 0x54
2019-12-16:11.14.44.916347392:[ISR]: POST Code for slot 27 changed to 0xa0
2019-12-16:11.14.44.916373504:[ISR]: POST Code for slot 28 changed to 0xa0
2019-12-16:11.15.03.646569472:[ISR]: POST Code for slot 26 changed to 0xa0
2019-12-16:11.15.04.748022272:[ISR]: POST Code for slot 22 changed to 0xa0
2019-12-16:11.15.14.266484736:[ISR]: POST Code for slot 24 changed to 0xa0
2019-12-16:11.18.11.489846272:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.18.12.491101184:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.22.30.391535104:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.22.31.492875776:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.26.49.407702016:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.26.50.509097472:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.31.08.408430592:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.31.09.409682432:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.35.26.315185152:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.35.27.416556032:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.39.45.310315520:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.39.46.311528448:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.44.04.337517056:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.44.05.338741248:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.48.23.232193024:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.48.24.333538304:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.52.41.234022400:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.52.43.336457728:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:11.57.00.153080320:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:11.57.01.254410752:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.01.19.178457600:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.01.20.179703296:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.05.38.203790336:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.05.39.205028864:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.09.57.103055360:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.09.58.204383232:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.14.15.027237888:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.14.16.128579072:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.18.34.047417856:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.18.35.148794880:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.22.53.047706624:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.22.54.048883200:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.27.12.054199808:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.27.13.055494656:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.31.30.979380224:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.31.32.080705024:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.35.48.888316416:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.35.49.989663744:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.40.07.891782144:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.40.08.993085440:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.44.26.908366848:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.44.27.909621760:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.48.45.918578176:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.48.46.919841792:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.53.03.837281280:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:12.53.04.838517248:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:12.57.22.831639552:[ISR]: POST Code for slot 2 changed to 0x1e
```

2019-12-16:12.57.23.832911360:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.01.41.833031680:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.01.42.834268672:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.06.00.740024320:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.06.01.841394688:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.10.19.768019968:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.10.20.769302528:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.14.37.655355392:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.14.38.756755456:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.18.56.655229952:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.18.57.756587520:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.23.15.658801664:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.23.16.660048384:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.27.34.655034880:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.27.35.656287232:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.31.53.652897792:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.31.54.654104576:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.36.11.558914560:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.36.12.560167424:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.40.30.568370688:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.40.31.569627136:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.44.49.468186112:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.44.50.571635712:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.49.08.482063360:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.49.09.583393280:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.53.26.395422208:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.53.27.496771584:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:13.57.45.399475712:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:13.57.46.500909568:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.02.04.405213184:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.02.05.406433280:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.06.23.417884672:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.06.24.419138048:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.10.42.329566720:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.10.43.430938112:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.15.00.253901824:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.15.01.355243520:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.19.19.247721472:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.19.20.349063680:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.23.38.254869504:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.23.39.256110592:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.27.57.261724160:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.27.58.262965760:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.32.15.158858240:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.32.17.261378560:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.36.34.186439168:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.36.35.187675648:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.40.53.126042624:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.40.54.227419648:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.43.33.504493568:[ISR]: POST Code for slot 2 changed to 0xa1
2019-12-16:14.44.09.450505728:[ISR]: POST Code for slot 2 changed to 0x73
2019-12-16:14.44.18.369435136:[ISR]: POST Code for slot 2 changed to 0x1b
2019-12-16:14.44.21.973499392:[ISR]: POST Code for slot 2 changed to 0xe0
2019-12-16:14.44.45.599875072:[ISR]: POST Code for slot 2 changed to 0xe1
2019-12-16:14.45.26.660646400:[ISR]: POST Code for slot 2 changed to 0xe3
2019-12-16:14.45.28.064965632:[ISR]: POST Code for slot 2 changed to 0xe2
2019-12-16:14.45.30.167515648:[ISR]: POST Code for slot 2 changed to 0xe4
2019-12-16:14.45.33.070848000:[ISR]: POST Code for slot 2 changed to 0xe6
2019-12-16:14.45.38.777229312:[ISR]: POST Code for slot 2 changed to 0x50
2019-12-16:14.45.56.597211648:[ISR]: POST Code for slot 2 changed to 0x54
2019-12-16:14.46.06.211475968:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.46.14.720887296:[ISR]: POST Code for slot 2 changed to 0x17

```
2019-12-16:14.46.15.822237696:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.48.29.977753088:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.48.31.079104512:[ISR]: POST Code for slot 2 changed to 0xa0
2019-12-16:14.52.48.986328576:[ISR]: POST Code for slot 2 changed to 0x1e
2019-12-16:14.52.49.987563520:[ISR]: POST Code for slot 2 changed to 0xa0
```

使用show tech ctrace進行離線分析

在Ads伺服器上離線解碼ctrace

1. 將show tech trace archive複製到ads伺服器上的目錄中。
2. 使用 — tar zxvf showtech-ctrace-admin-2019-Nov-06.174210.UTC.tgz取消歸檔內容
3. cd showtech-ctrace-admin-2019-Nov-06.174210.UTC
4. 使用此命令可以解碼目錄cmgr下的card_mgr進程的跟蹤
： /users/gonaidu/bin/showtech_ct_dec -d cmgr -p card_mgr
5. cd cmgr



附註：ctrace解碼的日誌位於本地時區 — 如果已在班加羅爾ADS伺服器上進行解碼，則ctrace基於IST時區。在比較路由器日誌和離線解碼的ctrace日誌時，請牢記這一點。

確定主SC和主SUP

在cmgr目錄下使用「grep "Master Role" *":

```
card_mgr.0_RP0:299:2019-02-22:07.35.38.709224844:2580:calvados/dc_common_pkg/drivers/card_mgr/src/card_m
card_mgr.0_SC0:96:2019-04-06:19.04.34.500975616:1976:calvados/dc_common_pkg/drivers/card_mgr/src/card_m
```

從主SC檢索後代碼

使用此命令獲取LC 0/1的郵遞區號：grep "slot 2" card_mgr.0_SC0 | grep changed

```
2019-04-06:19.04.34.759844864:1976:calvados/dc_common_pkg/drivers/card_mgr/src/card_mgr_interrupt.c:256
```

主要RP視點中的事件歷史記錄：

grep RAPI_NOTIFY_CARD_POST_CODE_CB card_mgr.0_RP0 | grep "0V1"

```
2019-04-06:19.04.40.665774834:2589:calvados/dc_common_pkg/drivers/card_mgr/src/card_mgr_rack_service.c:
```

分類方案示例

- 卡管理器進程CPU掛起：
- LC重新載入時切片未啟動 — Potenza 10C

如何拉動、編輯和建立Satori產品組合？

可以有多個行同時存在。工程師需要知道他/她應從何處獲取檢視。要瞭解適當的satori組合/devline，請從sysadmin linux提示符運行此命令

```
cat /etc/build-info.txt
```

你得到的輸出是這樣的

```
<#root>
```

```
### Thirdparty Information
```

```
SDK arm /auto/exr-yocto/SDK/WRL7/Fretta/REL0109/arm/kvm-host-arm-sdk.tgz
```

```
SDK x86_64 /auto/exr-yocto/SDK/WRL7/Fretta/REL0109/x86_64/kvm-host-x86_64-sdk.tgz
```

```
Refpoint = thirdparty/opensource/release@tp-main/289
```

```
Hostname      : calcium-99.cisco.com
```

```
Workspace     : /nobackup/hetsoi/satori-wr17.release.20191209/target-n9000-gdb
```

```
Source Base   : ssh://wwwin-git-sjc-2/git/thinstack/satori.git
```

```
Devline      : cisco-xr-wr7
```

```
Devline Ver   : f53915539d9ca49d3dedec0882ee4eb12a408956
```

```
Devline Type  : GIT Repository
```

Here Devline 'cisco-xr-wr7' should be used.

Before pulling the view, setup your environment

步驟2.確保此專案存在於git組態檔中，即； ~/.gitconfig

[應用]

空白= nowarn

ignorewhitespace =更改

步驟3.在執行任何活動之前，使用此命令設定環境

```
source /auto/exr-yocto/tools/scripts/set_yocto_env
```

步驟4.使用此命令獲取檢視。

```
acme pull -sb ios_ena -dev cisco-xr-wr7 -plat none
```

步驟5.在shell中運行以下命令

取消設定CDPATH

用於構建核心的命令

步驟1.導航到satori目錄並運行此命令。

```
scripts/xr/build-release.sh -f
```

步驟2.成功構建核心後，它會將思科特定的修補程式應用到各種.c和.h檔案，並且代碼可在此路徑中用於代碼行走。

```
satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/
```

 附註：您不應在此進行任何永久性更改以在二進位制檔案中反映出來，因為這是tmp位置，將在下一個生成中被覆蓋。此位置的代碼應用於代碼行走和.patch檔案生成。此處討論.patch檔案

從代碼位置角度來看，klm的代碼位於兩條路徑上

- 對於「代碼行走」和「修補程式生成」：

satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/

- 二進制/sdk生成中使用的實際.c和.h檔案

satori/meta-cisco-nxos/recipes-kernel/

在上述路徑上，您將獲得兩個目錄

A.cisco-klm —>這涵蓋模組化和固定式fretta系統中使用的所有klm。

B.cisco-klm-zermatt —>這涵蓋所有klm，它們僅用於固定的fretta系統，例如klm_iofpga。

如何對不同的klms進行更改並進行編譯？

對klm_iofpga進行更改非常簡單。只需轉到此路徑，即可開始對您感興趣的.c或.h檔案進行更改。

satori/target-n9000-gdb/bitbake_build/tmp/work/n900_gdb-wrs-linux/cisco-klm-zermatt/0.1-r0/klm_iofpga

為其他所有klm進行更改會有些棘手。如上所述，您需要轉到tmp位置，進行更改，生成.patch文件(如何生成.patch文件，將在此處介紹)。將.patch檔案復制到特定位置，在.bb檔案中為這個新的.patch檔案建立條目並開始生成。

請找到進行更改的步驟。

步驟1.轉到要在其中進行更改的klm特定目錄。您會在此找到所有KLM。

satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/cisco-klm/0.1-r0

步驟2.使用面組工具進行更改，以便生成.patch檔案。請注意，此處的acme diff不起作用，因此您需要使用面組工具來生成修補程式檔案。

步驟3.設定面組工具別名

cd satori/target-n9000-gdb/bitbake_build/tmp/work/n9000_gdb-wrs-linux/cisco-klm/0.1-r0

alias quilt=/nobackup/rpanday/kernel-wr7/satori/target-n9000-gdb/bitbake_build/tmp/sysroots/x86_64-linux/usr/bin/quilt

quilt new patch_file.patch ->指令面組分配名為patch_file.patch的新.patch檔案。

面組頂部(quilt top)->此命令告知patch_file.patch位於面組堆疊的頂部並可供編輯。

面組編輯klm_obfl/obfl_dc3.c —>指令面組捕獲在klm_obfl/obfl_dc3.c中完成的更改。運行此命令後，obfl_dc3.c將開啟並可供編輯。編輯完成後，寫並退出。

面組刷新 —>此命令獲取已編輯檔案klm_obfl/obfl_dc3.c的差異，並將其放入patch_file.patch中。若要驗證相同內容，請在vi中開啟該檔案。

步驟4.生成修補程式檔案patch_file.patch後，將其複製到此路徑。

```
cp patches/patch_file.patch /nobackup/rpanday/kernel-wr7/satori/meta-cisco-nxos/recipes-  
kernel/cisco-klm/files
```

步驟5.在.bb檔案中為此新的.patch檔案建立一個條目。 .bb檔案位於此位置。

```
/nobackup/rpanday/kernel-wr7/satori/meta-cisco-nxos/recipes-kernel/cisco-klm/cisco-klm_0.1.bb
```

Fretta固定卡硬體和FPGA規格

產品型號	工程名稱	卡型別	硬體規格	IOFPGA規範
NCS-5502-SE	採爾馬特	2RU固定	· EDCS-1515475	· EDCS-1026647 · EDCS-1516467 · EDCS-1193041
NCS-5501-SE	都靈 — MX	1RU固定	· EDCS-1497433	· EDCS-1527505 · EDCS-1527506
NCS-5502-SE-PROTO	採爾馬特 — 普羅托			
NCS-5502	ZERMATT-CR	2RU固定	· EDCS-1515475	EDCS-1516467 CPU IOFPGA FS:EDCS-1026647 MIFPGA - EDCS - 1193041
NCS-5501	太湖	1RU固定	EDCS-1530044	IOFPGA - EDCS - 1541805 MIFPGA - EDCS - 1541804
NCS-5501-A2-SE	臨冬城			

NCS-5501-A1-SE	奧爾德卡斯爾			
NCS-5501-A1	NCS-55A1-36H-S 奧爾德卡斯爾			
NCS-55A1-36H-SE-S	奧爾德卡斯爾 — 塞	1RU固定	· EDCS-1563746	· EDCS-1568105 · EDCS-11402862 · EDCS-11556985
NCS-55A1-36H-S	OLDCASTLE-CR	1RU固定	· EDCS-1563746	· EDCS-1568105 · EDCS-11402862
N540-X-24Z8Q2C-M	托廷共形盒			
N540-24Z8Q2C-M	TORTIN-CR			
N540X-ACC-SYS	托汀-16G-CR			
N540-ACC-SYS	TORTIN-16G			
NCS-5501-HD	N540-24Z8Q2C-M TORTIN-CR			
NCS-5501-A3	NCS-55A1-24H 派克			
NCS-55A1-24H	派克	1RU固定	· EDCS-11415948	· EDCS-1568105 EDCS-1026647 · EDCS-1568940

NCS-55A2-MOD-SE-S	佩托與TCAM	2RU固定	· EDCS-11601538	· EDCS-11632621 · EDCS-11632622
NC55A2-MOD-SE-H-S	TCAM和CC的PEYTO	2RU固定	· EDCS-11601538	· EDCS-11632621 · EDCS-11632622
NCS-55A2-MOD-S	Peyto NonSE溫度(TCAM)	2RU固定	· EDCS-11601538	· EDCS-11632621 · EDCS-11632622
NCS-55A2-MOD-HD-S	無TCAM的PEYTO	2RU固定	· EDCS-11601538	· EDCS-11632621 · EDCS-11632622
NCS-55A2-MOD-HX-S	不帶TCAM的IPETMP CC	2RU固定	· EDCS-11601538	· EDCS-11632621 · EDCS-11632622
NCS-55A1-48Q6H	雙霜 — T	1RU固定	· EDCS-12914104	· EDCS-13259042 · EDCS-15599029 · EDCS-15676955
NCS-55A1-24Q6H-S	都靈 — CR	1RU固定	· EDCS-12909672	· EDCS-13259042 · EDCS-15599029 · EDCS-15676955
NCS-55A1-24Q6H-SS	都靈 — CR			

關於此翻譯

思科已使用電腦和人工技術翻譯本文件，讓全世界的使用者能夠以自己的語言理解支援內容。請注意，即使是最佳機器翻譯，也不如專業譯者翻譯的內容準確。Cisco Systems, Inc. 對這些翻譯的準確度概不負責，並建議一律查看原始英文文件（提供連結）。