

什么导致%PLATFORM-3-PACONFIG 和 %C7200-3-PACONFIG 错误信息？

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简介

本文解释%PLATFORM-3-PACONFIG和%C7200-3-PACONFIG错误消息的原因。本文也描述如何排除故障这些错误。

从错误的端口适配器(PA)配置经常导致这些错误消息，本文从Cisco 7200系列体系结构和PA配置指南的概述开始。跟随的故障排除程序依靠该网络类型的处理引擎(NPE)您插入到7200个机箱。

先决条件

要求

本文档没有任何特定的要求。

使用的组件

本文档中的信息根据此硬件平台：

- Cisco 7200 系列路由器

本文档中的信息都是基于特定实验室环境中的设备编写的。本文档中使用的所有设备最初均采用原始（默认）配置。如果您使用的是真实网络，请确保您已经了解所有命令的潜在影响。

规则

有关文档规则的详细信息，请参阅 [Cisco 技术提示规则](#)。

背景信息

7200系列路由器包括：

- 双槽Cisco 7202
- 四槽Cisco 7204和Cisco 7204VXR
- 六插槽Cisco 7206和Cisco 7206VXR

多协议这些的路由器支援，多媒体路由和桥接与多样化的协议和介质类型。网络接口在提供路由器外围部件互连(PCI) BUS和外部网络之间的一连接的PA驻留。您在所有联机PA slot能安置PA，您希望任意组合的那。

实现内存在NPE和PA之间的通信的7200系列用途PCI BUS。如果PA带宽点限额超过，您从所有PA同时获得内存请求。不管信息包吞吐量，这些同时内存请求发生。不是所有的PA保证在这种情况下获得服务。奇怪的反常现象能结果，例如underrun和PCI总线超时。

在7200系列路由器安装的PA是类型和在其他Cisco 7000系列路由器安装的PA一样。思科7200系列路由器支持在线插拔已安装PA。

在7200系列路由器安装的所有PA和服务适配器连接到在路由器中平面的两PCI BUS。PCI BUS是mb1和mb2。PCI BUS提供一个路径给数据包I/O内存和系统处理器，包括路由和交换。输入输出控制器的可选端口连接对第三PCI总线，mb0。mb0连接到其中一PCI BUS或对两个PCI BUS。此连接取决于哪个NPE或网络服务引擎您的系统安装并且支持。

当您安装在7200VXR路由器时的此NPE NPE-G1与I/O控制器一起使用。您能也使用NPE-G1，不用I/O控制器。NPE-G1每次有三千兆以太网接口，与总共三或六端口联机。(三RJ-45s和三千兆位接口转换器[GBICs]是可用的。)当您安装NPE-G1时，I/O控制器在第三PCI总线连接，mb0。mb0连接直接地到NPE-G1。当您安装与NPE-G1时的I/O控制器，I/O控制器不采取从投入PA的左右PCI BUS的带宽。PG性能限制来自c7200体系结构。PCI总线现在设备能处理至在半双工模式的600mbps。两个方向的最大吞吐量比300mbps是较少(所有端口适配器连接对一给的PCI总线。当您考虑到时PG如何被制作，获得200mbps最大数量在最好的方案的用1500个字节信息包是可能的。

在7200系列路由器中，在奇数slot的PA连接对PCI总线mb1。在偶数插槽的PA连接对PCI总线mb2。遵从一些指南为了均匀地分配PA在两BUS之间。

Cisco7200或7200VXR路由器以NPE-100、NPE-150、NPE-175、NPE-200或者NPE-225使用这些指定为了确定PA分配和配置：

- 高带宽
- 中等带宽
- 低带宽

而不是这些指定、7200VXR路由器有NPE-300的，NPE-400、NPE-G1或者确定PA分配和配置的NSE-1使用带宽点。带宽点是与带宽关连的一个已分配值。然而，值根据使用效率调节PCI总线由硬件。

参考[Cisco 7200系列端口处安装要求](#)关于的详情：

- [配置 指南及要求](#)
- [根据NPE或者NSE安装情况的安装需求](#)
- [带宽及带宽点需求](#)

排除故障

症状

如果您的PA配置超出在本文的指南系统提示您与错误消息。这是错误消息的示例：

- 一个7200系列路由器或7200VXR路由器有NPE-225、NPE-200、NPE-175、NPE-150或者NPE-100的：

```
%C7200-3-PACONFIG:Exceeds 3 high speed port adapters
%C7200-3-PACONFIG:Exceeds 5 high/medium speed port adapters
%C7200-3-PACONFIG:Exceeds 800 aggregate port adapter bandwidth points
```

 这些错误消息显示，当带宽点超过系统限额。
- 有NPE-G1、NPE-400、NPE-300或者NSE-1的一个7200VXR路由器：

```
%C7200-3-PACONFIG:Exceeds 600 bandwidth points for slots 0, 1, 3 & 5
%C7200-3-PACONFIG:Exceeds 600 bandwidth points for slots 2, 4 & 6
%C7200-3-PACONFIG:Exceeds 600 bandwidth points on both odd & even numbered slots
```

 这些错误消息显示，当带宽点在BUS被超出。

在7200系列路由器的启动顺序期间，您能看到事类似于此：

```
System Bootstrap, Version 12.0(19990210:195103) [12.0XE 105], DEVELOPMENT SOFTWARE
Copyright (c) 1994-1999 by cisco Systems, Inc.
C7200 platform with 262144 Kbytes of main memory
```

```
Self decompressing the image : #####
#####
#####
#####
##### [OK]
```

```
%C7200-3-PACONFIG: Exceeds 600 bandwidth points for slots 0, 1, 3 & 5
%C7200-3-PACONFIG: Exceeds 600 bandwidth points on both odd & even numbered slots
```

```
%SYS-6-BOOT_MESSAGES: Messages above this line are from the boot loader.
```

```
Self decompressing the image : #####
#####
#####
#####
#####
#####
#####
#####
```

```
#####  
#####  
##### [OK]
```

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cisco Systems, Inc.
170 West Tasman Drive
San Jose, California 95134-1706

Cisco Internetwork Operating System Software
IOS (tm) 7200 Software (C7200-IS-M), Version 12.1(3.1), MAINTENANCE INTERIM SOFTWARE
Copyright (c) 1986-2000 by cisco Systems, Inc.
Compiled Tue 11-Jul-00 00:59 by cmong
Image text-base: 0x60008950, data-base: 0x6148E000

cisco 7206VXR (NPE300) processor (revision D) with 253952K/40960K bytes of memory.
Processor board ID 23682887
R7000 CPU at 262Mhz, Implementation 39, Rev 2.1, 256KB L2, 2048KB L3 Cache
6 slot VXR midplane, Version 2.1

Last reset from power-on
Bridging software.
X.25 software, Version 3.0.0.
3 FastEthernet/IEEE 802.3 interface(s)
3 ATM network interface(s)

125K bytes of non-volatile configuration memory.
20480K bytes of Flash PCMCIA card at slot 0 (Sector size 128K).
20480K bytes of Flash PCMCIA card at slot 1 (Sector size 128K).
4096K bytes of Flash internal SIMM (Sector size 256K).

--- System Configuration Dialog ---

Would you like to enter the initial configuration dialog? [yes/no]: n

Press RETURN to get started!

```
00:00:08: %PLATFORM-3-PACONFIG: Exceeds 600 bandwidth points for slots 0, 1, 3 & 5  
00:00:08: %PLATFORM-3-PACONFIG: Exceeds 600 bandwidth points on both odd &  
even numbered slots  
00:00:13: %LINK-3-UPDOWN: Interface FastEthernet1/0, changed state to up  
00:00:13: %LINK-3-UPDOWN: Interface FastEthernet2/0, changed state to up  
00:00:13: %LINK-3-UPDOWN: Interface FastEthernet6/0, changed state to up
```

解决方案

您能以超出指南[Cisco 7200系列端口处安装要求](#)提供的PA配置使用7200系列路由器。然而，为了防止反常现象出现，当路由器是在使用中的时，请限制在路由器安装符合指南PA的种类。另外，为了安排[思科技术支持](#)排除故障这些反常现象，您的PA配置必须在指南内。

您能使用目测或[show diagbus](#)命令确定哪PA插入到7200系列路由器。在您收集[show diagbus](#)输出后，请发送输出对diag@external.cisco.com为了使用Show diag口译员外部。在[show diag](#)输出的分析以后，您接收一个自动电子邮件响应。答复提供对应于您的[show diag](#)的不同的部件号。如果想要

在Show diag解释器工具的一个简短的说明，请发送电子邮件对与“帮助的”diag@external.cisco.com在标题栏。

一旦识别在您的7200系列路由器插入的PA，您需要确定是否与指南是兼容的。进行这两操作之一为了做确定：

- 计算带宽点如果有有NPE-G1、NPE-300、NPE-400或者NSE-1的确定一个7200VXR的路由器，使用带宽点PA分配和配置。**注意：**如果安装在7200VXR路由器的I/O控制器有NPE-G1的，请勿计算I/O控制器的带宽点。使用NPE-G1，输入输出控制器不使用带宽或带宽点。另外，在NPE-G1的千兆以太网接口不要求任何带宽点。
- 计数高，中等或者低带宽PA数量如果有有NPE-100、NPE-150、NPE-175、NPE-200或者NPE-225的一个7200或7200VXR路由器，您能由高，中等或者低带宽指定确定PA分配和配置。请使用**show version**命令为了确定哪NPE您有。**注意：**参考[Cisco 7200系列端口处安装要求](#)

最终目标是均匀分配PA在两BUS之间。如果有一个传统7200机箱，并且有许多高带宽PA，请考虑到升级到VXR机箱。计算对应的带宽点为了验证点不超过每总线的限制。如果点不超过限制，支持硬件配置。

如果有许多高带宽PA或，如果600带宽点限制在其中一超过BUS，错误出现。请使用思科[动态配置工具](#)为了检查您的硬件配置。请务必检查**Enable Configuration Guidance**复选框为了发现警告消息

案例研究

此消息发生，每当路由器被重新启动：

```
System Bootstrap, Version 12.0(19990210:195103) [12.0XE 105], DEVELOPMENT SOFTWARE
Copyright (c) 1994-1999 by cisco Systems, Inc.
C7200 platform with 262144 Kbytes of main memory
```

```
Self decompressing the image : #####
#####
#####
#####
##### [OK]
```

```
%C7200-3-PACONFIG: Exceeds 600 bandwidth points for slots 0, 1, 3 & 5
%C7200-3-PACONFIG: Exceeds 600 bandwidth points on both odd & even numbered slots
```

```
%SYS-6-BOOT_MESSAGES: Messages above this line are from the boot loader.
```

```
Self decompressing the image : #####
#####
#####
#####
#####
#####
#####
#####
#####
##### [OK]
```

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subject to restrictions as set forth in subparagraph (c) of the Commercial Computer Software - Restricted Rights clause at FAR sec. 52.227-19 and subparagraph (c) (1) (ii) of the Rights in Technical Data and Computer Software clause at DFARS sec. 252.227-7013.

cisco Systems, Inc.
170 West Tasman Drive
San Jose, California 95134-1706

Cisco Internetwork Operating System Software
IOS (tm) 7200 Software (C7200-IS-M), Version 12.1(3.1), MAINTENANCE INTERIM SOFTWARE
Copyright (c) 1986-2000 by cisco Systems, Inc.
Compiled Tue 11-Jul-00 00:59 by cmong
Image text-base: 0x60008950, data-base: 0x6148E000

cisco 7206VXR (NPE300) processor (revision D) with 253952K/40960K bytes of memory.
Processor board ID 23682887
R7000 CPU at 262Mhz, Implementation 39, Rev 2.1, 256KB L2, 2048KB L3 Cache
6 slot VXR midplane, Version 2.1

Last reset from power-on
Bridging software.
X.25 software, Version 3.0.0.
3 FastEthernet/IEEE 802.3 interface(s)
3 ATM network interface(s)

125K bytes of non-volatile configuration memory.
20480K bytes of Flash PCMCIA card at slot 0 (Sector size 128K).
20480K bytes of Flash PCMCIA card at slot 1 (Sector size 128K).
4096K bytes of Flash internal SIMM (Sector size 256K).

--- System Configuration Dialog ---

Would you like to enter the initial configuration dialog? [yes/no]: n

Press RETURN to get started!

00:00:08: %PLATFORM-3-PACONFIG: Exceeds 600 bandwidth points for slots 0, 1, 3 & 5
00:00:08: %PLATFORM-3-PACONFIG: Exceeds 600 bandwidth points on both odd & even numbered slots
00:00:13: %LINK-3-UPDOWN: Interface FastEthernet1/0, changed state to up
00:00:13: %LINK-3-UPDOWN: Interface FastEthernet2/0, changed state to up
00:00:13: %LINK-3-UPDOWN: Interface FastEthernet6/0, changed state to up

这是输出：

router# show version

Cisco Internetwork Operating System Software
IOS (TM) 7200 Software (C7200-IS-M), Version 12.0(7)T, RELEASE SOFTWARE (fc2)
Copyright (c) 1986-1999 by cisco Systems, Inc.
Compiled Tue 07-Dec-99 16:36 by phanguye
Image text-base: 0x60008900, database: 0x613D8000

ROM: System Bootstrap, Version 12.0(19990210:195103) [12.0XE 105], DEVELOPMENT SOFTWARE
BOOTFLASH: 7200 Software (C7200-BOOT-M), Version 12.0(10)S, EARLY DEPLOYMENT RELEASE SOFTWARE (fc1)

router uptime is 44 minutes
System returned to ROM by processor memory parity error at PC 0x60EC97B8, address 0x0 at 15:08:36 CET Fri Feb 16 2001

System restarted at 15:09:59 CET Fri Feb 16 2001
Running default software

cisco **7206VXR (NPE300)** processor with 122880K/40960K bytes of memory.
R7000 CPU at 262Mhz, Implementation 39, Rev 2.1, 256KB L2, 2048KB L3 Cache
6 slot VXR midplane, Version 2.0

Last reset from power-on
Bridging software.
X.25 software, Version 3.0.0.
8 Ethernet/IEEE 802.3 interface(s)
5 FastEthernet/IEEE 802.3 interface(s)
4 Serial network interface(s)
125K bytes of nonvolatile configuration memory.

46976K bytes of ATA PCMCIA card at slot 0 (Sector size 512 bytes).
4096K bytes of Flash internal SIMM (Sector size 256K).
Configuration register is 0x102

router# **show diag**

Slot 0:
Fast-ethernet on C7200 I/O card with MII or RJ45 Port adapter, 1 port

Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 2.1 Board revision B0
Serial number 21771425 Part number 73-4092-03
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 83 02 01 01 4C 34 A1 49 0F FC 03 00 00 00 00
0x30: 58 00 00 00 00 07 13 00 00 00 FF FF FF FF FF FF

Slot 1:
Fastethernet (TX-ISL) Port adapter, 2 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 2.0 Board revision B0
Serial number 16741556 Part number 73-2618-03
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 6C 02 00 00 FF 74 B4 49 0A 3A 03 00 00 00 00
0x30: 58 00 00 00 00 05 03 00 00 00 FF FF FF FF FF 00

Slot 2:
Ethernet Port adapter, 8 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 1.14 Board revision A0
Serial number 23127697 Part number 73-1391-08
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 01 01 0E 01 60 E6 91 49 05 6F 08 00 00 00 00
0x30: 50 00 00 00 00 09 19 00 FF FF FF FF FF FF FF FF

Slot 3:
Fastethernet (TX-ISL) Port adapter, 2 ports
Port adapter is analyzed

```
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 2.0 Board revision B0
Serial number 16741881 Part number 73-2618-03
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 6C 02 00 00 FF 75 F9 49 0A 3A 03 00 00 00 00
0x30: 58 00 00 00 00 05 03 00 00 00 FF FF FF FF FF 00
```

Slot 4:

```
Mx serial Port adapter, 4 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 1.14 Board revision A0
Serial number 21624236 Part number 73-1577-07
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 0C 01 0E 01 49 F5 AC 49 06 29 07 00 00 00 00
0x30: 50 00 00 00 00 07 03 00 FF FF FF FF FF FF FF FF
```

包含此show diag输出的电子邮件被发送对diag@external.cisco.com。十几秒后，包含此信息的电子邮件接收：

```
router# show version
```

```
Cisco Internetwork Operating System Software
IOS (TM) 7200 Software (C7200-IS-M), Version 12.0(7)T, RELEASE SOFTWARE (fc2)
Copyright (c) 1986-1999 by cisco Systems, Inc.
Compiled Tue 07-Dec-99 16:36 by phanguye
Image text-base: 0x60008900, database: 0x613D8000
```

```
ROM: System Bootstrap, Version 12.0(19990210:195103) [12.0XE 105], DEVELOPMENT
SOFTWARE
BOOTFLASH: 7200 Software (C7200-BOOT-M), Version 12.0(10)S, EARLY DEPLOYMENT
RELEASE SOFTWARE (fc1)
```

```
router uptime is 44 minutes
System returned to ROM by processor memory parity error at PC 0x60EC97B8, address
0x0 at 15:08:36 CET Fri Feb 16 2001
System restarted at 15:09:59 CET Fri Feb 16 2001
Running default software
```

```
cisco 7206VXR (NPE300) processor with 122880K/40960K bytes of memory.
R7000 CPU at 262Mhz, Implementation 39, Rev 2.1, 256KB L2, 2048KB L3 Cache
6 slot VXR midplane, Version 2.0
```

```
Last reset from power-on
Bridging software.
X.25 software, Version 3.0.0.
8 Ethernet/IEEE 802.3 interface(s)
5 FastEthernet/IEEE 802.3 interface(s)
4 Serial network interface(s)
125K bytes of nonvolatile configuration memory.
```

```
46976K bytes of ATA PCMCIA card at slot 0 (Sector size 512 bytes).
4096K bytes of Flash internal SIMM (Sector size 256K).
Configuration register is 0x102
```

```
router# show diag
```

Slot 0:

Fast-ethernet on C7200 I/O card with MII or RJ45 Port adapter, 1 port

Port adapter is analyzed

Port adapter insertion time 00:44:16 ago

EEPROM contents at hardware discovery:

Hardware revision 2.1 Board revision B0

Serial number 21771425 Part number 73-4092-03

Test history 0x0 RMA number 00-00-00

EEPROM format version 1

EEPROM contents (hex):

0x20: 01 83 02 01 01 4C 34 A1 49 0F FC 03 00 00 00 00

0x30: 58 00 00 00 00 07 13 00 00 00 FF FF FF FF FF FF

Slot 1:

Fastethernet (TX-ISL) Port adapter, 2 ports

Port adapter is analyzed

Port adapter insertion time 00:44:16 ago

EEPROM contents at hardware discovery:

Hardware revision 2.0 Board revision B0

Serial number 16741556 Part number 73-2618-03

Test history 0x0 RMA number 00-00-00

EEPROM format version 1

EEPROM contents (hex):

0x20: 01 6C 02 00 00 FF 74 B4 49 0A 3A 03 00 00 00 00

0x30: 58 00 00 00 00 05 03 00 00 00 FF FF FF FF FF 00

Slot 2:

Ethernet Port adapter, 8 ports

Port adapter is analyzed

Port adapter insertion time 00:44:16 ago

EEPROM contents at hardware discovery:

Hardware revision 1.14 Board revision A0

Serial number 23127697 Part number 73-1391-08

Test history 0x0 RMA number 00-00-00

EEPROM format version 1

EEPROM contents (hex):

0x20: 01 01 01 0E 01 60 E6 91 49 05 6F 08 00 00 00 00

0x30: 50 00 00 00 00 09 19 00 FF FF FF FF FF FF FF FF

Slot 3:

Fastethernet (TX-ISL) Port adapter, 2 ports

Port adapter is analyzed

Port adapter insertion time 00:44:16 ago

EEPROM contents at hardware discovery:

Hardware revision 2.0 Board revision B0

Serial number 16741881 Part number 73-2618-03

Test history 0x0 RMA number 00-00-00

EEPROM format version 1

EEPROM contents (hex):

0x20: 01 6C 02 00 00 FF 75 F9 49 0A 3A 03 00 00 00 00

0x30: 58 00 00 00 00 05 03 00 00 00 FF FF FF FF FF 00

Slot 4:

Mx serial Port adapter, 4 ports

Port adapter is analyzed

Port adapter insertion time 00:44:16 ago

EEPROM contents at hardware discovery:

Hardware revision 1.14 Board revision A0

Serial number 21624236 Part number 73-1577-07

Test history 0x0 RMA number 00-00-00

EEPROM format version 1

EEPROM contents (hex):

0x20: 01 0C 01 0E 01 49 F5 AC 49 06 29 07 00 00 00 00

0x30: 50 00 00 00 00 07 03 00 FF FF FF FF FF FF FF FF

由于平台具有NPE - 300的7206VXR，需要计算各PCI总线的带宽点。

```
router# show version
```

```
Cisco Internetwork Operating System Software
IOS (TM) 7200 Software (C7200-IS-M), Version 12.0(7)T, RELEASE SOFTWARE (fc2)
Copyright (c) 1986-1999 by cisco Systems, Inc.
Compiled Tue 07-Dec-99 16:36 by phanguye
Image text-base: 0x60008900, database: 0x613D8000
```

```
ROM: System Bootstrap, Version 12.0(19990210:195103) [12.0XE 105], DEVELOPMENT
SOFTWARE
BOOTFLASH: 7200 Software (C7200-BOOT-M), Version 12.0(10)S, EARLY DEPLOYMENT
RELEASE SOFTWARE (fc1)
```

```
router uptime is 44 minutes
System returned to ROM by processor memory parity error at PC 0x60EC97B8, address
0x0 at 15:08:36 CET Fri Feb 16 2001
System restarted at 15:09:59 CET Fri Feb 16 2001
Running default software
```

```
cisco 7206VXR (NPE300) processor with 122880K/40960K bytes of memory.
R7000 CPU at 262Mhz, Implementation 39, Rev 2.1, 256KB L2, 2048KB L3 Cache
6 slot VXR midplane, Version 2.0
```

```
Last reset from power-on
Bridging software.
X.25 software, Version 3.0.0.
8 Ethernet/IEEE 802.3 interface(s)
5 FastEthernet/IEEE 802.3 interface(s)
4 Serial network interface(s)
125K bytes of nonvolatile configuration memory.
```

```
46976K bytes of ATA PCMCIA card at slot 0 (Sector size 512 bytes).
4096K bytes of Flash internal SIMM (Sector size 256K).
Configuration register is 0x102
```

```
router# show diag
```

```
Slot 0:
Fast-ethernet on C7200 I/O card with MII or RJ45 Port adapter, 1 port
```

```
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 2.1 Board revision B0
Serial number 21771425 Part number 73-4092-03
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 83 02 01 01 4C 34 A1 49 0F FC 03 00 00 00 00
0x30: 58 00 00 00 00 07 13 00 00 00 FF FF FF FF FF FF
```

```
Slot 1:
Fastethernet (TX-ISL) Port adapter, 2 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 2.0 Board revision B0
Serial number 16741556 Part number 73-2618-03
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
```

```
0x20: 01 6C 02 00 00 FF 74 B4 49 0A 3A 03 00 00 00 00
0x30: 58 00 00 00 00 05 03 00 00 00 FF FF FF FF FF 00
```

Slot 2:

```
Ethernet Port adapter, 8 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 1.14 Board revision A0
Serial number 23127697 Part number 73-1391-08
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 01 01 0E 01 60 E6 91 49 05 6F 08 00 00 00 00
0x30: 50 00 00 00 00 09 19 00 FF FF FF FF FF FF FF FF
```

Slot 3:

```
FastEthernet (TX-ISL) Port adapter, 2 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 2.0 Board revision B0
Serial number 16741881 Part number 73-2618-03
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 6C 02 00 00 FF 75 F9 49 0A 3A 03 00 00 00 00
0x30: 58 00 00 00 00 05 03 00 00 00 FF FF FF FF FF 00
```

Slot 4:

```
Mx serial Port adapter, 4 ports
Port adapter is analyzed
Port adapter insertion time 00:44:16 ago
EEPROM contents at hardware discovery:
Hardware revision 1.14 Board revision A0
Serial number 21624236 Part number 73-1577-07
Test history 0x0 RMA number 00-00-00
EEPROM format version 1
EEPROM contents (hex):
0x20: 01 0C 01 0E 01 49 F5 AC 49 06 29 07 00 00 00 00
0x30: 50 00 00 00 00 07 03 00 FF FF FF FF FF FF FF FF
```

第一总线是订购过量的。对于slot 0，1和3，点到达800 (200 + 300 + 300)，比600极大。

解决方案将交换均等slot的slot 3和4为了平衡带宽在两BUS，一和一多的slot的。您也需要更改相关配置。然后，%PLATFORM-3-PACONFIG错误消息消失。

相关信息

- [Cisco 7200系列端口适配器安装要求](#)
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