



Cisco Unified Videoconferencing 3545 MCU Technical Specifications

This section provides technical specifications.

Technical Specifications Table for the 3545 MCU

Table 5-1 Cisco Unified Videoconferencing 3545 Chassis Technical Specifications

Chassis Dimensions	<ul style="list-style-type: none"> • Height: 2U (3.5 inches or 88.9 mm) • Width: 17.25 inches (438.15 mm) • Depth: 10 inches (254 mm) • Weight: 8 kg (17.64 lbs) empty, 11 kg (24.25 lbs) full—may vary according to configuration
Element Board Dimensions	<ul style="list-style-type: none"> • Width: 9.19 inches (233.35 mm) • Depth: 6.3 inches (160 mm)
RTM Board Dimensions	<ul style="list-style-type: none"> • Width: 9.19 inches (233.35 mm) • Depth: 3.15 inches (80 mm)
System Monitoring LED Indicators	<ul style="list-style-type: none"> • POWER • ALARM • FAN • TEMP

Table 5-1 Cisco Unified Videoconferencing 3545 Chassis Technical Specifications (continued)

Board LED Indicators	
Front panel	<ul style="list-style-type: none"> • ETHERNET: <ul style="list-style-type: none"> – Link – Connection Speed • GK Reg • CPU High (MCU only) • CD (gateways only) • ALARM • ACT • MC (EMP only)
Rear panel (Cisco Unified Videoconferencing 3545 PRI Gateway)	<ul style="list-style-type: none"> • PRI 1 or 2: <ul style="list-style-type: none"> – ACT – D-Ch – ALRM
Rear panel (Cisco Unified Videoconferencing 3545 Serial Gateway)	<ul style="list-style-type: none"> • PORT 1 to 4: <ul style="list-style-type: none"> – ACT – ALARM
Push Buttons	<ul style="list-style-type: none"> • RST (front panel)
Communication Interfaces	
Front panel	<ul style="list-style-type: none"> • Ethernet 10/100 Mbps auto-negotiate speed select • Asynchronous serial port RS-232 connected via 9-pin D-type connector
Rear panel (Cisco Unified Videoconferencing 3545 PRI Gateway)	<ul style="list-style-type: none"> • 2 x ISDN E1/T1 PRI port: <ul style="list-style-type: none"> – T1 mode <ul style="list-style-type: none"> Channels: 23B + 1D Clock rate: 1.544 Mbps Framing: F4, F12, ESF no CRC, ESF, F72 Encoding: NRZ, AMI-B7, B8ZS Line impedance: 100Ω – E1 mode <ul style="list-style-type: none"> Channels: 30B + 1D Clock rate: 2.048 Mbps Framing: Double framing, CRC4, Extended CRC4 Encoding: NRZ, CMI, AMI, HDB3 Line impedance: 120Ω

Table 5-1 Cisco Unified Videoconferencing 3545 Chassis Technical Specifications (continued)

Rear panel (Cisco Unified Videoconferencing 3545 Serial Gateway)	<ul style="list-style-type: none"> • 4 x serial ports
Chipset	<ul style="list-style-type: none"> • PowerPC MPC7410 32-bit RISC microprocessor running at 500MHz. • MPC8260 communication processor running at 300/200MHz.
Operating System	<ul style="list-style-type: none"> • RTOS, VxWorks 5.4
Memory	<ul style="list-style-type: none"> • 32 MB on-board flash memory for field upgrades • 2 MB L-2 Cache at 250MHz • 128 MB SDRAM
Failsafe	<ul style="list-style-type: none"> • Watchdog timer built in
Power supply	<ul style="list-style-type: none"> • Dual power supply units • Full redundancy • Power on/alarm LED on each unit. • Input 100-240VAC, 50/60Hz, autoswitched • Output + 3.3VDC, + 5VDC, ± 12VDC • Maximum power load 300W
Ventilation Fans	<ul style="list-style-type: none"> • 2 fan units • Brushless 12V DC motor • Locked rotor detection • Polarity protection • Auto-restart capability

■ Technical Specifications Table for the 3545 MCU