



New and Changed Information

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This section provides information on new feature and changed behavior in Cisco UCS Manager, Release 4.1(3h).

Table 1: New Features and Changed Behavior in Cisco UCS Manager, Release 4.1(3h)

Feature	Description	Where Documented
BIOS tokens	Cisco UCS Manager now has new BIOS tokens and modifications to existing BIOS tokens and values.	Trusted Platform BIOS Settings

This section provides information on new feature and changed behavior in Cisco UCS Manager, Release 4.1(3e).

Table 2: New Features and Changed Behavior in Cisco UCS Manager, Release 4.1(3e)

Feature	Description	Where Documented
New RAS Memory BIOS Setting	Cisco UCS Manager now has the Burst and Postponed Refresh BIOS token added to allow the memory controller defer the refresh intervals and to mitigate Rowhammer-style attacks.	RAS Memory BIOS Settings

This section provides information on new feature and changed behavior in Cisco UCS Manager, Release 4.1(3a).

Table 3: New Features and Changed Behavior in Cisco UCS Manager, Release 4.1(3a)

Feature	Description	Where Documented
New RAS Memory BIOS Setting	<p>Cisco UCS Manager now has the following BIOS tokens to maintain memory temperature, enhance memory test, and to mitigate Rowhammer-style attacks</p> <ul style="list-style-type: none"> • Memory Thermal Disabled Throttling Mode • Advanced Memory Test • Transparent Secure Auto Memory Encryption (TSME) • Secure Encrypted Auto Virtualization (SEV) • DRAMSWThermal servers Throttling 	<ul style="list-style-type: none"> • RAS Memory BIOS Settings
Power Save Mode	<p>Cisco UCS Manager now provides a global Ethernet adapter policy setting to maximize energy efficiency.</p>	<ul style="list-style-type: none"> • Power Save Mode Policy

This section provides information on new feature and changed behavior in Cisco UCS Manager, Release 4.1(2b).

Table 4: New Features and Changed Behavior in Cisco UCS Manager, Release 4.1(2b)

Feature	Description	Where Documented
New RAS Memory BIOS Settings	<p>Cisco UCS Manager now has the following BIOS tokens to mitigate Rowhammer-style attacks:</p> <ul style="list-style-type: none"> • Memory Refresh Rate • Panic and High Watermark 	RAS Memory BIOS Settings

This section provides information on new feature and changed behavior in Cisco UCS Manager, Release 4.1(2a).

Table 5: New Features and Changed Behavior in Cisco UCS Manager, Release 4.1(2a)

Feature	Description	Where Documented
Cisco UCS C240 SD M5 Server	Cisco UCS Manager provides support for rack server power management, FlexFlash FX3S support, and scrub policy on Cisco UCS C240 SD M5 Server.	<ul style="list-style-type: none"> • FlexFlash FX3S Support • Rack Server Power Management
New Processor BIOS Options	<p>Cisco UCS Manager now has BIOS tokens for Configurable TDP Level, External SSC Enable, PCIe PLL SSC, UPI Link Frequency Select, and Uncore Frequency Scaling. It also adds the following DCPMM Memory BIOS tokens:</p> <ul style="list-style-type: none"> • CR QoS • NVM Performance Setting • CR FastGo Config • Snoopy mode for AD • Snoopy mode for 2LM 	<ul style="list-style-type: none"> • Processor BIOS Settings • LOM and PCIe Slots BIOS Settings • Intel® Optane™ DC Persistent Memory (DCPMM) BIOS Tokens
Acoustic Mode	Reduces the fan speed to reduce noise levels in acoustic-sensitive environments in Cisco UCS C220 M5 Server, C240 M5 and C240 SD M5 Rack servers.	<ul style="list-style-type: none"> • Acoustic Mode Fan Profile

This section provides information on new feature and changed behavior in Cisco UCS Manager, Release 4.1(1a).

Table 6: New Features and Changed Behavior in Cisco UCS Manager, Release 4.1(1a)

Feature	Description	Where Documented
RAS Memory Features	<p>Cisco UCS Manager expands on Intel® Xeon® processor support for RAS memory features via the BIOS. These features expand on the capabilities of the processor to increase the performance and reliability of memory DIMMs.</p> <p>These include limiting presented memory to a subset of overall memory, ability to create a mirrored region in memory map with the attributes of a partial mirror copy, rather than the complete mirror copy.</p>	Memory RAS Features