

Micro-Burst Monitoring

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Micro-Burst Monitoring

The micro-burst monitoring feature allows you to monitor traffic to detect unexpected data bursts within a very small time window (microseconds). This allows you to detect traffic in the network that are at risk for data loss and for network congestion.

A micro-burst is detected when the buffer utilization in an egress queue rises above the configured rise-threshold (measured in bytes). The burst for the queue ends when the queue buffer utilization falls below the configured fall-threshold (measured in bytes).

The feature provides timestamp and instantaneous buffer utilization information about the various queues where micro-burst monitoring is enabled.

Guidelines and Limitations for Micro-Burst Monitoring

The following are the guidelines and limitations for micro-burst monitoring:

- Micro-burst monitoring is not supported on Cisco Nexus 9500 platform switches.
- Micro-burst monitoring and detection is supported on the following platforms:

Switches	Minimum Burst Interval	IO FPGA Version
Cisco Nexus 9200	86 µsec	0x16 or later
	96 µsec	0x10 or later
		0x15 or later
		0x6 or later
		0x14 or later
Cisco Nexus 9300	73 µsec	0x8 or later
	78 µsec	0x9 or later
Cisco Nexus 9300-EX		
Cisco Nexus 9300-FX		
Cisco Nexus 9300-FX2		
Cisco Nexus 9332C		
Cisco Nexus 9364C		

On these switches, micro-burst monitoring is supported on both unicast and multicast egress queues.

In addition, early detection of long bursts is supported. For bursts lasting more than 5 seconds, an early burst start record is displayed after 5 seconds from the start of the burst and is updated when the burst actually ends. This is not supported for Cisco Nexus 9300-FX, 9300-FX2, and 9364C platform switches.



Note On these switches, micro-burst duration is not affected by the number of queues configured.

- show commands with the internal keyword are not supported.
- Micro-burst monitoring is available with TOR switches that contain the Network Forwarding Engine (NFE2). The minimum micro-burst that can be detected is 0.64 microseconds for 1 3 queues.

On these switches, micro-burst monitoring is supported on unicast egress queues. It is not supported on multicast, CPU, or span queues.

• On TOR switches that contain a Network Forwarding Engine (NFE2), micro-burst monitoring requires IO FPGA version 0x9 or later.

Beginning with Cisco NX-OS 7.0(3)I5(1), micro-burst monitoring on Cisco Nexus 9200 or 9300-EX TOR switches require the following IO FPGA versions:

TOR Switch	IO FPGA Version
N9K-C92160YC-X	0x16 or later
N9K-C92304QC	0x10 or later
N9K-C9272Q	0x15 or later

TOR Switch	IO FPGA Version
N9K-C9232C	0x6 or later
N9K-C9236C	0x14 or later
N9K-C93180YC-EX	0x8 or later
N9K-C93108TC-EX	0x9 or later

For more information about EPLD programming to upgrade the FPGA, see the *Cisco Nexus 9000 Series FPGA/EPLD Upgrade Release Notes*.

• The following are guidelines for micro-burst duration on TOR switches that contain a Network Forwarding Engine (NFE2):



Note Micro-burst duration is the duration of the burst that can be detected. For example, when micro-burst monitoring is configured for 1 - 3 queues, micro-bursts that exceed 0.64 microseconds are detected. Increasing the number of queues that are configured for micro-burst monitoring increases the duration of the burst that can be detected. This does not apply to Cisco Nexus 9300-FX, 9300-FX2, and 9364C platform switches.

1 - 3 queues	0.64 microsecond duration
8 queues with 10 ports each	9.0 microsecond duration
10 queues with 132 ports each	140 microsecond (0.14 millisecond) duration

• By default, the switch stores a maximum of 1000 burst records. The maximum number of records is configurable within a range of 200 - 2000 records.

- At least, 20 burst records are stored for each queue even when the maximum number of burst records has been reached.
- When the maximum number of burst records has been reached, the oldest record is deleted to allow the storage of a new record.
- You can use the **hardware qos burst-detect max-records** *number-of-records* command to configure the maximum number of burst records to store.
- You can use the **show hardware qos burst-detect max-records** command to display the maximum number of burst records that can be stored.
- Too many back to back burst records while traffic is being drained from queues might result in jitter.

To avoid jitter, configure the fall-threshold to be less than the rise-threshold. As a best practice, configure the fall-threshold to be approximately 20% of the rise-threshold value (bytes).

Configuring Micro-Burst Detection for Cisco Nexus 9200, 9300, 9300-EX, and 9300-FX2 Platform Switches

You can enable micro-burst detection for all interfaces on the device.

For the Cisco Nexus 9300-EX and 9300-FX2, you can enable independent microburst thresholds per queue on these devices. Therefore, those parameters are given under the individual queue(s) in the queuing policy-maps. On the Cisco Nexus 9300-FX, 9364C switches. The thresholds are per switch, and therefore are given globally and apply to any queues where microburst detection is enabled in the queuing policy.



Note

This procedure is for all Cisco Nexus 9000 Series switches except the Cisco Nexus 9300-FX, 9332C, and 9364C switches. For the previously mentioned switches, see Configuring Micro-Burst Detection for Cisco Nexus 9300-FX, 9332C, and 9364C Switches, on page 6.

SUMMARY STEPS

- **1**. configure terminal
- 2. policy-map type queuing policy-map-name
- **3.** class type queuing *class-name*
- 4. burst-detect rise-threshold rise-threshold-bytes bytes fall-threshold fall-threshold-bytes bytes
- 5. exit
- 6. exit
- 7. interface ethernet *slot/port*
- 8. service-policy type queuing output policy-map-name

DETAILED STEPS

	Command or Action	Purpose
Step 1	configure terminal	Enters global configuration mode.
	Example:	
	<pre>switch# configure terminal switch(config)#</pre>	
Step 2	policy-map type queuing <i>policy-map-name</i> Example:	Configures the policy map of type queuing and then enters policy-map mode for the policy-map name you specify.
	<pre>switch(config)# policy-map type queuing xyz switch(config-pmap-que)#</pre>	
Step 3	class type queuing <i>class-name</i> Example:	Configures the class map of type queuing and then enters policy-map class queuing mode.
	<pre>switch(config-pmap-que)# class type queuing</pre>	

	Command or Action	Purpose				
	<pre>c-out-def switch(config-pmap-c-que)#</pre>					
Step 4	burst-detect rise-threshold <i>rise-threshold-bytes</i> bytes fall-threshold <i>fall-threshold-bytes</i> bytes	Specifies the rise-threshold and the fall-threshold for micro-burst detection.				
	Example: switch(config-pmap-c-que)# burst-detect rise-threshold 208 bytes fall-threshold 208 bytes	Note	TOR switches with Network Forwarding Engine (NFE2)	Range for rise-threshold bytes: 208 - 4194304. Range for fall-threshold bytes: 208 - 4194304.		
			Cisco Nexus 9200, 9300-EX, or 9300-FX2 switches.	Range for rise-threshold bytes: 208 - 13319072. Range for fall-threshold bytes: 208 - 13319072.		
Step 5	exit	Exits policy-map queue mode.				
	Example:					
	<pre>switch(config-pmap-c-que)# exit switch(config-pmap-que)#</pre>					
Step 6	exit	Exits poli	cy-map queue mode.			
	Example:					
	<pre>switch(config-pmap-que)# exit switch(config)#</pre>					
Step 7	interface ethernet <i>slot/port</i>	Configures the interface.				
	Example:					
	<pre>switch(config)# interface ethernet 1/1 switch(config-if)#</pre>					
Step 8	service-policy type queuing output policy-map-name	Adds the	policy map to the input of	or output packets of the		
	Example:	system.				
	<pre>switch(config-if)# service-policy type queuing output custom-out-8q-uburst</pre>					

Configuring Micro-Burst Detection for Cisco Nexus 9300-FX, 9332C, and 9364C Switches

You can enable micro-burst detection for all interfaces on the device.

For the Cisco Nexus 9300-EX and 9300-FX2, you can enable independent microburst thresholds per queue on these devices. Therefore, those parameters are given under the individual queue(s) in the queuing policy-maps. On the Cisco Nexus 9300-FX, 9364C switches. The thresholds are per switch, and therefore are given globally and apply to any queues where microburst detection is enabled in the queuing policy.



This procedure is for Cisco Nexus 9300-FX, 9332C, and 9364C switches.

SUMMARY STEPS

- 1. configure terminal
- 2. hardware qos burst-detect rise-threshold rise-threshold-bytes bytes fall-threshold fall-threshold-bytes bytes
- **3.** policy-map type queuing policy-map-name
- 4. class type queuing class-name
- 5. burst-detect enable
- 6. exit
- 7. exit
- 8. interface ethernet *slot/port*
- 9. service-policy type queuing output policy-map-name

DETAILED STEPS

	Command or Action	Purpose			
Step 1	configure terminal	Enters global configuration mode.			
	Example:				
	switch# configure terminal				
Step 2	hardware qos burst-detect rise-threshold rise-threshold-bytes bytes fall-threshold fall-threshold-bytes bytes	Specifies the rise-threshold and the fall-threshold for micro-burst detection.			
	Example:				
	<pre>switch(config)# hardware qos burst-detect rise-threshold 10000 bytes fall-threshold 2000 bytes</pre>				
Step 3	policy-map type queuing <i>policy-map-name</i> Example:	Configures the policy map of type queuing and then enters policy-map mode for the policy-map name you specify.			

	Command or Action	Purpose				
	<pre>switch(config)# policy-map type queuing custom-out-8q-uburst</pre>					
Step 4	class type queuing class-name	Configures the class map of type queuing and then enters				
	Example:	policy-map class queuing mode.				
	<pre>switch(config-pmap-que)# class type queuing c-out-8q-q-default</pre>					
Step 5	burst-detect enable	Enable microburst detection on the queue.				
	Example:					
	<pre>switch(config-pmap-c-que)# burst-detect enable</pre>					
Step 6	exit	Exits policy-map class queue mode.				
	Example:					
	<pre>switch(config-pmap-c-que)# exit</pre>					
Step 7	exit	Exits policy-map queue mode.				
	Example:					
	<pre>switch(config-pmap-que)# exit</pre>					
Step 8	interface ethernet <i>slot/port</i>	Configures the interface.				
	Example:					
	<pre>switch(config)# interface ethernet 1/1 switch(config-if)#</pre>					
Step 9	service-policy type queuing output policy-map-name	Adds the policy map to the input or output packets of the				
	Example:	system.				
	<pre>switch(config-if)# service-policy type queuing output custom-out-8q-uburst</pre>					

Clearing Micro-Burst Detection

You can clear micro-burst detection for all interfaces or a selected interface.

SUMMARY STEPS

1. clear queuing burst-detect [*slot*] [interface *port* [queue *queue-id*]]

DETAILED STEPS

	Command or Action	Purpose
Step 1	clear queuing burst-detect [<i>slot</i>] [interface <i>port</i> [queue <i>queue-id</i>]]	Clears micro-burst information from all interfaces or the specified interface.
	Example:	

Example

• Example for an interface:

clear queuing burst-detect interface Eth1/2

• Example for a queue:

clear queuing burst-detect interface Eth1/2 queue 7

Verifying Micro-Burst Detection

The following displays micro-burst monitoring information:

Command	Purpose
show queuing burst-detect	Displays micro-burst counters information for all interfaces.

• Example for an interface:

show queuing burst-detect interface Eth 1/2

• Example for a queue:

show queuing burst-detect interface Eth 1/2 queue 7

Example of Micro-Burst Detection Output

Example output of TOR switch.

belv0# show queuing burst-detect detail
slot 1
=======

Microburst Statistics

Flags: E - Early start record, U - Unicast, M - Multicast

Ethernet Intfc	Queue	Start Depth (bytes)	Start 	t Time	Peak Depth (bytes)	Peak 	Time 	End Depth (bytes)	En(d Time	Duratio	n
Eth1/36 Eth1/36	U0 U0	310128 311168	2011/01/11 22	2:31:51:081725 2:31:51:181765	310128	2011/01/11 22	:31:51:08172 :31:51:18176	0 2	011/01/11 2: 011/01/11 2:	2:31:51:081918 2:31:51:181959	193.14	us us
Eth1/36 Eth1/36	U0 U0	283712 283712	2011/01/11 22	2:31:51:281825	283712	2011/01/11 22	:31:51:28182 :31:51:38186	0 2	011/01/11 22	2:31:51:282018	193.63	us us
Eth1/36 Eth1/36	U0	312000	2011/01/11 22	2:31:51:481885	312000	2011/01/11 22	:31:51:48188	0 2	011/01/11 2	2:31:51:482080	194.42	us
Eth1/36	U0	291616	2011/01/11 22	2:31:51:681964	291616	2011/01/11 22	:31:51:681964	0 2	011/01/11 2	2:31:51:682157	193.10	us
Eth1/30	00	70512	2011/01/11 22	2:31:51:882167	70512	2011/01/11 22	:31:51:88210	7 0 2	011/01/11 2	2:31:51:882253	85.74	us
Eth1/30	00	245856	2011/01/11 22	2:31:52:182158	245856	2011/01/11 22	:31:52:182158	3 0 2	011/01/11 2:	2:31:52:182352	193.34	us
Eth1/30	00	242112	2011/01/11 22	2:31:52:382284	242112	2011/01/11 22	:31:52:38228	1 0 2	011/01/11 2	2:31:52:382478	193.55	us
Eth1/30 Eth1/30	00	136448 299312	2011/01/11 22	2:31:52:482204 2:31:52:582334	299312	2011/01/11 22 2011/01/11 22	:31:52:48234	1 0 2	011/01/11 2:	2:31:52:482542 2:31:52:582612	278.10	us us
Eth1/36 Eth1/36	00	184912 148304	2011/01/11 22	2:31:52:682432 2:31:52:782387	184912 148304	2011/01/11 22 2011/01/11 22	:31:52:682432 :31:52:78238	2 13312 2	011/01/11 2: 011/01/11 2:	2:31:52:682517 2:31:52:782580	85.42	us us
Etn1/36	00	226512	2011/01/11 22	2:31:52:882492	226512	2011/01/11 22	:31:52:882492	2 0 2	011/01/11 23	2:31:52:882685	193.37	us