



Configuring T3 Interfaces

T3 interface supports 4 ports. The channels on the T3 interface can be configured as either clear channel mode or channelized mode.



Note T3 is supported only on Cisco ASR 900 RSP3 Module.

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Information About T3 Interfaces

The following sections provide information about T3 interfaces.

Overview of T3 Interfaces

The T3 interface supports two modes, clear channel mode and channelized mode.

Benefits of T3 Interfaces

The following are the benefits of T3 interfaces:

- Higher bandwidth
- Flexibility by channelization

Enabling T3 Controller

To enable T3 controller:

```
enable
configure terminal
controller mediatype 0/4/12
mode t3
end
```

Configuring the Controller of Clear Channel T3 Interfaces

When the clear channel T3 interface is used for the first time, the running configuration does not show the T3 controller. You can use the **show platform** command to check whether the chassis recognizes the T3 port and initializes the card correctly. After the port is configured for the slot, the respective controller appears in the running configuration and you can configure the clear channel T3 interface.

Perform this task to configure clear channel controller as T3.

```
enable
configure terminal
controller t3 0/4/12
no channelized
clock source line
no shut
exit
```



Note By default, the T3 controller is in C-Bit framing mode. To configure CEM, the framing mode must be set to unframed.

Verifying Controller Configuration of Clear Channel T3 Interfaces

Use the **show controllers** command to verify the controller configuration of clear channel T3 interface:

```
# show controllers t3 0/4/12
T3 0/4/12 is up.
Hardware is A900-IMA3G-IMSG
Applique type is Clear Channel T3
No alarms detected.
Framing is Unframed, Line Code is B3ZS, Cablelength is 224
Clock Source is internal
Equipment customer loopback
Data in current interval (240 seconds elapsed):
  Near End
    0 Line Code Violations, 0 P-bit Coding Violations
    0 C-bit Coding Violations, 0 P-bit Err Secs
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
    0 Unavailable Secs, 0 Line Errored Secs
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    0 Severely Errored Line Secs, 0 Path Failures
    0 AIS Defect Secs, 0 LOS Defect Secs
  Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
  Near End
    0 Line Code Violations, 0 P-bit Coding Violations
    0 C-bit Coding Violations, 0 P-bit Err Secs
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
    20 Unavailable Secs, 20 Line Errored Secs
```

```

    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    20 Severely Errored Line Secs, 1 Path Failures
    0 AIS Defect Secs, 20 LOS Defect Secs
Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
Near End
    0 Line Code Violations, 0 P-bit Coding Violations,
    0 C-bit Coding Violations, 0 P-bit Err Secs,
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
    20 Unavailable Secs, 20 Line Errored Secs,
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    20 Severely Errored Line Secs, 1 path failures
    0 AIS Defect Secs, 20 LOS Defect Secs
Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
T1 1 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is ESF, Clock Source is Internal
Data in current interval (250 seconds elapsed):
Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    0 Path Failures, 0 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
    0 Line Code Violations,0 Path Code Violations,
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations,0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs, 0 Path Failures

```

Configuring the Controller of Channelized T3/T1 Interfaces

When the channelized T3/T1 interface is used for the first time, the running configuration does not show the T3 controller. You can use the `show platform` command to check if the chassis recognizes the T3 port and initializes the card properly. After the port is configured for the slot, the respective controller appears in the running configuration and you can configure the channelized T3/T1 interface.

Perform this task to configure channelized controller as T3/T1.

```
enable
configure terminal
controller t3 0/4/12
channelized
clock source line
no shut
exit
```



Note The channelized mode is the default mode for T3 interface.

Verifying the Controller Configuration of Channelized T3/T1 Interfaces

Use the `show controllers` command to verify the controller configuration of channelized T3/T1 interfaces:

```
# show controllers t3 0/4/12
T3 0/4/12 is down.
Hardware is A900-IMA3G-IMSG
Applique type is Channelized T3/T1
Receiver has loss of signal.
MDL transmission is disabled
FEAC code received: No code is being received
Framing is C-BIT Parity, Line Code is B3ZS, Cablelength Short less than 225ft
BER thresholds: SF = 10e-10 SD = 10e-10
Clock Source is line
Equipment customer loopback
Data in current interval (240 seconds elapsed):
  Near End
    0 Line Code Violations, 0 P-bit Coding Violations
    0 C-bit Coding Violations, 0 P-bit Err Secs
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
    0 Unavailable Secs, 0 Line Errored Secs
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    0 Severely Errored Line Secs, 0 Path Failures
    0 AIS Defect Secs, 0 LOS Defect Secs
  Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
  Near End
    0 Line Code Violations, 0 P-bit Coding Violations
    0 C-bit Coding Violations, 0 P-bit Err Secs
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
    20 Unavailable Secs, 20 Line Errored Secs
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    20 Severely Errored Line Secs, 1 Path Failures
    0 AIS Defect Secs, 20 LOS Defect Secs
  Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
```

```

    0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
Near End
    0 Line Code Violations, 0 P-bit Coding Violations,
    0 C-bit Coding Violations, 0 P-bit Err Secs,
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
    20 Unavailable Secs, 20 Line Errored Secs,
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    20 Severely Errored Line Secs, 1 path failures
    0 AIS Defect Secs, 20 LOS Defect Secs
Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Sec

T1 1 is down
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is ESF, Clock Source is Internal
Data in current interval (250 seconds elapsed):
Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    0 Path Failures, 0 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
    0 Line Code Violations,0 Path Code Violations,
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations,0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs, 0 Path Failures

```

Configuring SAToP - Clear Channel T3 Interfaces

Before Structure-Agnostic TDM over Packet (SAToP) is configured, the controller of clear channel T3 interface must be configured.

```

enable
configure terminal
controller t3 0/4/12
no channelized
cem-group 0 unframed
interface CEM 0/4/12
cem 0
xconnect 10.10.2.2 204 encapsulation mpls
exit

```

Verifying CEM Configuration of Clear Channel T3 Interfaces for SAToP

Use the **show run interface** command to verify the configuration of xconnect:

```

# show run interface cem 0/4/12
Current configuration : 96 bytes
!
interface CEM 0/4/12
no ip address
cem 0
xconnect 10.10.2.2 204 encapsulation mpls
!
end

```

Use the **show cem circuit interface cem** command to verify the CEM interface configuration of clear channel T3 interface for SAToP:

```

# show cem circuit interface cem 0/4/12
CEM 0/4/12, ID: 0, Line: UP, Admin: UP, Ckt: ACTIVE
Controller state: down, T3 state: up
Configuring SAToP - Clear Channel T3 Interfaces
Idle Pattern: 0xFF, Idle CAS: 0x8
Dejitter: 5 (In use: 0)
Payload Size: 1024
Framing: Unframed
CEM Defects Set
None
Signalling: No CAS
RTP: No RTP
Ingress Pkts: 1321577 Dropped: 0
Egress Pkts: 1321577 Dropped: 0
CEM Counter Details
Input Errors: 0 Output Errors: 0
Pkts Missing: 0 Pkts Reordered: 0
Misorder Drops: 0 JitterBuf Underrun: 0
Error Sec: 0 Severly Errored Sec: 0
Unavailable Sec: 0 Failure Counts: 0
Pkts Malformed: 0 JitterBuf Overrun: 0

```

Configuring SAToP - Channelized T3/T1 Interfaces

Before SAToP is configured, the controller of channelized T3/T1 interface must be configured.

```

enable
configure terminal
controller t3 0/4/12
channelized
t1 1 cem-group 0 unframed
interface CEM 0/4/12
no shut
cem 0

```

```
xconnect 10.10.2.2 204 encapsulation mpls
exit
```

Verifying the CEM Configuration of Channelized T3 or T1 Interfaces

Use the **show run controller** command to verify the CEM configuration of channelized T3 or T1 interface:

```
# show run controller t3 0/4/12
Current configuration : 109 bytes
!
Controller T3 0/4/12
framing c-bit
cablelength short
t1 1 cem-group 0 unframed
end
```

Use the **show cem circuit interface cem** command to verify the CEM configuration of channelized T3 or T1 interface:

```
# show cem circuit interface cem 0/4/12
CEM0/4/12, ID: 1, Line: UP, Admin: UP, Ckt: ACTIVE
Controller state: up, T1 state: up
Idle Pattern: 0xFF, Idle CAS: 0x8
Dejitter: 5 (In use: 0)
Payload Size: 192
Framing: Unframed
CEM Defects Set
None
Signalling: No CAS
RTP: No RTP
Ingress Pkts: 105043259 Dropped: 0
Egress Pkts: 105043387 Dropped: 0
CEM Counter Details
Input Errors: 0 Output Errors: 0
Pkts Missing: 0 Pkts Reordered: 0
Misorder Drops: 0 JitterBuf Underrun: 32
Error Sec: 0 Severly Errored Sec: 0
Unavailable Sec: 0 Failure Counts: 0
Pkts Malformed: 0 JitterBuf Overrun: 0
---
CEM0/4/12, ID: 28, Line: UP, Admin: UP, Ckt: ACTIVE
Controller state: up, T1 state: up
Idle Pattern: 0xFF, Idle CAS: 0x8
Dejitter: 5 (In use: 0)
Payload Size: 192
Framing: Unframed
CEM Defects Set
None
Signalling: No CAS
RTP: No RTP
Ingress Pkts: 136303 Dropped: 0
Egress Pkts: 0 Dropped: 0
CEM Counter Details
Input Errors: 0 Output Errors: 0
Pkts Missing: 135682 Pkts Reordered: 0
Misorder Drops: 0 JitterBuf Underrun: 137649
Error Sec: 0 Severly Errored Sec: 0
Unavailable Sec: 0 Failure Counts: 135
Pkts Malformed: 0 JitterBuf Overrun: 0
```

Configuring Framed SAToP - Channelized T3/T1 Interfaces



Note Framing type should be maintained same in all routers end to end.

To configure the controller of channelized T3/T1 interface for framed SAToP:

```
enable
configure terminal
controller t3 0/4/12
channelized mode
framing c-bit
t1 1 cem-group 0 framed
interface CEM 0/4/12
cem 0
xconnect 10.10.2.2 204 encapsulation mpls
exit
```

Verifying the CEM Configuration of Channelized T3/T1 Interfaces for Framed SAToP

Use the **show run controller** command to verify the CEM configuration of channelized T3/T1 interface for Framed SAToP:

```
# show run controller t3 0/4/12
Current configuration : 109 bytes
!
Controller T3 0/4/12
framing c-bit
cablelength short
t1 1 cem-group 0 framed
end
```

Use the **show cem circuit interface cem** command to verify the CEM configuration of channelized T3/T1 interface for Framed SAToP:

```
# show cem circuit interface cem 0/4/12
CEM0/0/0, ID: 1, Line: UP, Admin: UP, Ckt: ACTIVE
Mode :Channelized-T1, T1: 1, CEM Mode: T1-SAToP
Controller state: down, T1 state: up
Idle Pattern: 0xFF, Idle CAS: 0x8
Dejitter: 5 (In use: 0)
Payload Size: 192
Framing: Framed SAToP
CEM Defects Set
None
```

Performance Monitoring

You can view the statistics or error count generated on the TDM lines for T3 interfaces.

```
enable
configure terminal
```



```

controller MediaType 0/4/12
mode t3
controller t3 0/4/12
framing c-bit
cablelength
long 224-450 ft
short 0-224 ft
controller MediaType 0/4/12
mode t3
controller T3 0/4/12
framing c-bit
cablelength short

```

To view the performance monitoring result, use the **show controller t3** command:

```

Router# show controller t3 0/4/12

T3 0/4/12 is up.
Hardware is A900-IMA3G-IMSG
Applique type is Channelized T3/T1
  No alarms detected.
  MDL transmission is disabled
  FEAC code received: No code is being received
  Framing is C-BIT Parity, Line Code is B3ZS, Cablelength Short less than 225ft
  BER thresholds: SF = 10e-10 SD = 10e-10
  Clock Source is internal
  Equipment customer loopback
  Data in current interval (240 seconds elapsed):
    Near End
      0 Line Code Violations, 0 P-bit Coding Violations
      0 C-bit Coding Violations, 0 P-bit Err Secs
      0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
      0 Unavailable Secs, 0 Line Errored Secs
      0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
      0 Severely Errored Line Secs, 0 Path Failures
      0 AIS Defect Secs, 0 LOS Defect Secs
    Far End
      0 Errored Secs, 0 Severely Errored Secs
      0 C-bit Unavailable Secs, 0 Path Failures
      0 Code Violations, 0 Service Affecting Secs
  Data in Interval 1:
    Near End
      0 Line Code Violations, 0 P-bit Coding Violations
      0 C-bit Coding Violations, 0 P-bit Err Secs
      0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
      20 Unavailable Secs, 20 Line Errored Secs
      0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
      20 Severely Errored Line Secs, 1 Path Failures
      0 AIS Defect Secs, 20 LOS Defect Secs
    Far End
      0 Errored Secs, 0 Severely Errored Secs
      0 C-bit Unavailable Secs, 0 Path Failures
      0 Code Violations, 0 Service Affecting Secs
  Total Data (last 1 15 minute intervals):
    Near End
      0 Line Code Violations, 0 P-bit Coding Violations,
      0 C-bit Coding Violations, 0 P-bit Err Secs,
      0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
      20 Unavailable Secs, 20 Line Errored Secs,
      0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
      20 Severely Errored Line Secs, 1 path failures
      0 AIS Defect Secs, 20 LOS Defect Secs
    Far End
      0 Errored Secs, 0 Severely Errored Secs

```

```

    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs

T1 1 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is ESF, Clock Source is Internal
Data in current interval (250 seconds elapsed):
  Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    0 Path Failures, 0 SEF/AIS Secs
  Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Data in Interval 1:
  Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
  Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
  Near End
    0 Line Code Violations,0 Path Code Violations,
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
  Far End
    0 Line Code Violations,0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs, 0 Path Failures

```

Use Case 1

If your configuration is as follows:

- T1 is up
- No alarms
- Framing is unframed
- Clock Source is Internal

, then the following performance monitoring result is displayed:

```

Router# show controller t3 0/4/12
T3 0/4/12 is up.
Hardware is A900-IMA3G-IMSG
Applique type is Channelized T3/T1

```

```

No alarms detected.
MDL transmission is disabled
FEAC code received: No code is being received
Framing is unframedt
Clock Source is internal
Equipment customer loopback
Data in current interval (240 seconds elapsed):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  0 Unavailable Secs, 0 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  0 Severely Errored Line Secs, 0 Path Failures
  0 AIS Defect Secs, 0 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  20 Unavailable Secs, 20 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 Path Failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations,
  0 C-bit Coding Violations, 0 P-bit Err Secs,
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
  20 Unavailable Secs, 20 Line Errored Secs,
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 path failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
T1 1 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is unframed, Clock Source is Internal
Data in current interval (250 seconds elapsed):
Near End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
  0 Unavailable Secs, 0 Stuffed Secs
  0 Path Failures, 0 SEF/AIS Secs
Far End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
  0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End

```

```

    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
    0 Line Code Violations,0 Path Code Violations,
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations,0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs, 0 Path Failures

```

Use Case 2

If your configuration is as follows:

- T1 28 is up
- No alarm received
- Framing is unframed
- Clock Source is Internal

, then the following performance monitoring result is displayed:

```

Router# show controller t3 0/4/12
T3 0/4/12 is up.
Hardware is A900-IMA3G-IMSG
Applique type is Channelized T3/T1
No alarms detected.
MDL transmission is disabled
FEAC code received: No code is being received
Framing is unframedt
Clock Source is internal
Equipment customer loopback
Data in current interval (240 seconds elapsed):
Near End
    0 Line Code Violations, 0 P-bit Coding Violations
    0 C-bit Coding Violations, 0 P-bit Err Secs
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
    0 Unavailable Secs, 0 Line Errored Secs
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    0 Severely Errored Line Secs, 0 Path Failures
    0 AIS Defect Secs, 0 LOS Defect Secs
Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
Near End
    0 Line Code Violations, 0 P-bit Coding Violations
    0 C-bit Coding Violations, 0 P-bit Err Secs

```

```

    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
    20 Unavailable Secs, 20 Line Errored Secs
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    20 Severely Errored Line Secs, 1 Path Failures
    0 AIS Defect Secs, 20 LOS Defect Secs
  Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
  Near End
    0 Line Code Violations, 0 P-bit Coding Violations,
    0 C-bit Coding Violations, 0 P-bit Err Secs,
    0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
    20 Unavailable Secs, 20 Line Errored Secs,
    0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
    20 Severely Errored Line Secs, 1 path failures
    0 AIS Defect Secs, 20 LOS Defect Secs
  Far End
    0 Errored Secs, 0 Severely Errored Secs
    0 C-bit Unavailable Secs, 0 Path Failures
    0 Code Violations, 0 Service Affecting Secs
T1 28 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is unframed, Clock Source is Internal
Data in current interval (250 seconds elapsed):
  Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    0 Path Failures, 0 SEF/AIS Secs
  Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Data in Interval 1:
  Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
  Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
  Near End
    0 Line Code Violations,0 Path Code Violations,
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
  Far End
    0 Line Code Violations,0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs, 0 Path Failures

```

The complete output for the T3 **show controller** command is:

```
# show controllers t3 0/4/12
T3 0/4/12 is down.
Hardware is A900-IMA3G-IMSG
Applique type is Channelized T3/T1
No alarms detected.
MDL transmission is disabled
FEAC code received: No code is being received
Framing is C-BIT Parity, Line Code is B3ZS, Cablelength Short less than 225ft
BER thresholds: SF = 10e-10 SD = 10e-10
Clock Source is internal
Equipment customer loopback
Data in current interval (240 seconds elapsed):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  0 Unavailable Secs, 0 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  0 Severely Errored Line Secs, 0 Path Failures
  0 AIS Defect Secs, 0 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  20 Unavailable Secs, 20 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 Path Failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations,
  0 C-bit Coding Violations, 0 P-bit Err Secs,
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
  20 Unavailable Secs, 20 Line Errored Secs,
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 path failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs

T1 28 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is unframed, Clock Source is Internal
Data in current interval (250 seconds elapsed):
Near End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
```

```

    0 Unavailable Secs, 0 Stuffed Secs
    0 Path Failures, 0 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations, 0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
    0 Line Code Violations,0 Path Code Violations,
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
    0 Unavailable Secs, 0 Stuffed Secs
    1 Path Failures, 2 SEF/AIS Secs
Far End
    0 Line Code Violations,0 Path Code Violations
    0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
    3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
    0 Unavailable Secs, 0 Path Failures
    
```

Table 1: Feature History

Feature Name	Release Information	Description
GR-820-CORE specific Performance Monitoring	Cisco IOS XE Bengaluru 17.5.1	The show controller tabular enables you to view the performance monitoring details in tabular form as per GR-820-Core standards.

To view the performance monitoring details on T3 interface, use the **show controller t3 tabular** command:

```

Router#show controllers t3 0/7/12 tabular
T3 0/7/12 is down.
Hardware is A900-IMA3G-IMSG
Applique type is Subrate T3
Receiver has loss of signal.
MDL transmission is disabled

FEAC code received: No code is being received
Framing is C-BIT Parity, Line Code is B3ZS, Cablelength Short less than 225ft
BER thresholds: SF = 10e-3 SD = 10e-6
Clock Source is internal
Equipment customer loopback
Near End Data
INTERVAL      CV-L  ES-L  SES-L  LOSS-L  CVP-P  CVCP-P  ESP-P  ESCP-P  SESP-P  SESP-P  SAS-P
AISS-P  FC-P  UASP-P  UASCP-P
11:33-11:46      0  779  779  779      0      0      0      0      0      0      0
              0  0  779  779
Far End Data
INTERVAL      CVCP-PFE  ESCP-PFE  SESP-PFE  UASCP-PFE  FCCP-PFE  SASCP-PFE
11:33-11:46      0      0      0      0      0      0
PE2#
    
```

Troubleshooting T3 Controllers

You can use the following methods to troubleshoot the T3 controllers on the chassis:

Running Bit Error Rate Testing

Bit error rate testing (BERT) is supported on T3 interfaces. You can run 16 BERTs at a time. The test can be either of the T1 or the T3 interface.

The interface contains on board BERT circuitry. With this, the interface software can send and detect a programmable pattern that is compliant with CCITT/ITU O.151, O.152, O.153 pseudo-random, and repetitive test patterns. BERTs allow you to test cables and signal problems in the field.

When running a BERT test, your system must receive the same pattern that it is transmitting. So, ensure the two common options are available:

- Use of a loopback somewhere in the link or network
- Configuration of a remote testing equipment to transmit the same BERT test pattern at the same time

Both the total number of bits and the error bits received are available for analysis. You can select the testing period from 1 minute to 24 hours and you can also retrieve the error statistics anytime during the BERT test.

BERT is supported in two directions:

- Line - supports BERT in TDM direction
- System - supports BERT in PSN direction

BERT Patterns on T3/E3 Interface Module

Bit error rate testing (BERT) is supported on T3/E3 interfaces.

- You can run 16 BERTs at a time.
- The test can be either of the T1/E1 or the T3/E3 interface.

The BERT patterns on the 48-port T3/E3 interface module are:

Table 2: BERT Pattern Descriptions

Keyword	Description
All 0s	Pseudo-random binary test pattern consisting of all 0's that is used for test line coding.
2^15-1 O.151	Pseudo-random O.151 test pattern consisting of a maximum of 14 consecutive zeros and 15 consecutive ones. The length of this pattern is 32,768 bits.
2^20-O.151	Pseudo-random O.151 test pattern consisting of a maximum of 19 consecutive zeros and 20 consecutive ones. The length of this pattern is 1,048,575 bits.

Keyword	Description
2^20-O.153	Pseudo-random O.153 test pattern consisting of a maximum of 19 consecutive zeros and 20 consecutive ones. The length of this pattern is 1,048,575 bits.
2^23-1 O.151	Pseudo-random O.151 test pattern consisting of a maximum of 22 consecutive zeros and 23 consecutive ones. The length of this pattern is 8,388,607 bits.
2^9 1	Pseudo-random binary test pattern consisting of a maximum of eight consecutive zeros and nine consecutive ones. The length of this pattern is 511 bits.
2^11 2	Pseudo-random binary test pattern consisting of a maximum of ten consecutive zeros and eleven consecutive ones. The length of this pattern is 2048 bits.

¹ Starting with Cisco IOS XE Gibraltar 16.12.1, 2^9 is supported on both T3 and T1 modes.

² Starting with Cisco IOS XE Fuji 16.9.5, 2^11 is supported on both T3 and T1 modes.

Configuring BERT for Clear and Channelized T3 Interfaces

Before you configure BERT for clear channel T3 interfaces, ensure that controller and CEM are configured.

To run a BERT on clear channel T3 interface, perform the following tasks in global configuration mode.

```
enable
configure terminal
controller t3 0/4/12
no channelized
bert pattern 0s interval 30 direction line
exit
```



Note To terminate a BERT test during the specified test period, use the **no bert** command.

You can view the results of a BERT test at the following points of time:

- After you terminate the test using the **no bert** command
- After the test runs completely
- Anytime during the test (in real time)

Verifying the BERT Configuration for T3 Interfaces

Use the **show controller** command to verify the BERT configuration for clear channel T3 interfaces:

```
# show controllers t3 0/4/12 | sec BERT
BERT test result (running)
Running Bit Error Rate Testing
Test Pattern : 2^15, Status : Not Sync, Sync Detected : 0
```

```

DSX3 BERT direction : Line
Interval : 5 minute(s), Time Remain : 3 minute(s)
Bit Errors (since BERT started): 0 bits,
Bits Received (since BERT started): 0 Kbits
Bit Errors (since last sync): 0 bits
Bits Received (since last sync): 0 Kbits

```

Use the **show controller** command to verify the BERT configuration of channelized T3/T1 interfaces:

```
# show controllers t3 0/4/12
```

```

Hardware is ASR900-IMA3G-IMSG
Applique type is Channelized T3/T1
No alarms detected.
MDL transmission is disabled
FEAC code received: No code is being received
Framing is C-BIT Parity, Line Code is B3ZS, Cablelength Short less than 225ft
BER thresholds: SF = 10e-10 SD = 10e-10
Clock Source is internal
Equipment customer loopback
Data in current interval (240 seconds elapsed):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  0 Unavailable Secs, 0 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  0 Severely Errored Line Secs, 0 Path Failures
  0 AIS Defect Secs, 0 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  20 Unavailable Secs, 20 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 Path Failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations,
  0 C-bit Coding Violations, 0 P-bit Err Secs,
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
  20 Unavailable Secs, 20 Line Errored Secs,
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 path failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs

T1 28 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.

```

```

Framing is unframed, Clock Source is Internal
BERT test result (running)
Test Pattern : 2^23, Status : Not Sync, Sync Detected : 0
DSX1 BERT direction : Line
Interval : 5 minute(s), Time Remain : 4 minute(s)
Bit Errors (since BERT started): 0 bits,
Bits Received (since BERT started): 0 Kbits
Bit Errors (since last sync): 0 bits
Bits Received (since last sync): 0 Kbits
Data in current interval (250 seconds elapsed):
Near End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
  0 Unavailable Secs, 0 Stuffed Secs
  0 Path Failures, 0 SEF/AIS Secs
Far End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
  0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
  0 Unavailable Secs, 0 Stuffed Secs
  1 Path Failures, 2 SEF/AIS Secs
Far End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
  0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
  0 Line Code Violations,0 Path Code Violations,
  0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
  2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
  0 Unavailable Secs, 0 Stuffed Secs
  1 Path Failures, 2 SEF/AIS Secs
Far End
  0 Line Code Violations,0 Path Code Violations
  0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
  3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
  0 Unavailable Secs, 0 Path Failures

```

Loopback on T1/T3 Interfaces

You can use the following loopback on the clear and channelized T3/T1 interfaces.

Loopback	Description
loopback local	Loops the transmitting signal back to the receiver.
loopback network line	Loops the incoming signal back to the interface using the line loopback mode of the framer. The framer does not reclock or reframe the incoming data. All incoming data is received by the interface driver.

Configuring Loopback for T3 Interfaces

To set a loopback local on the clear channel T3 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/4/12
loopback local
exit
```

To set a loopback network on the clear channel T3 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/4/12
loopback network line
exit
```

To set a loopback local on the channelized channel T3/T1 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/4/12
channelized
t1 1 loopback local
exit
```

To set a loopback network on the channelized channel T3/T1 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/4/12
channelized
t1 1 loopback network line
exit
```



Note To remove a loopback, use the **no loopback** command.



Note Network payload configuration is not supported on SAToP. To configure loopback network payload when SAToP is configured, you need to remove the CEM configuration and then configure the loopback.

Loopback Remote on T1 and T3 Interfaces

The remote loopback configuration attempts to put the far-end T1 or T3 into a loopback.

The remote loopback setting loops back the far-end at line or payload, using IBOC (inband bit-orientated CDE) or the ESF loopback codes to communicate the request to the far-end.

Restrictions for Loopback Remote

E1 and E3 loopback remote are not supported until Cisco IOS XE Fuji 16.9.4 release. Starting from Cisco IOS XE Fuji 16.9.5 release, E1 and E3 loopback remote are supported .

Configuring Loopback Remote on T1 and T3 Interface

To set T3 loopback remote line or payload for T3 or E3 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/0/1
loopback remote {line | payload}
exit
```

To set T1 loopback remote iboc fac1/fac2/csu for T3 or E3 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/0/1
t1 1 loopback remote iboc {fac1 | fac2 | csu}
exit
```

To set T1 loopback remote iboc esf line csu/esf payload for T3 or E3 interface, perform the following tasks in global configuration mode:

```
enable
configure terminal
controller t3 0/0/1
t1 1 loopback remote iboc esf {line csu | payload}
```

Verifying the Loopback Remote Configuration on T1 or T3 Interfaces

Use the following command to check the loopback remote configuration on a T3 interface module:

```
router# show running-config | sec 0/0/1
controller MediaType 0/0/1
mode t3
controller T3 0/0/1
threshold sd-ber 6
threshold sf-ber 3
no channelized
framing c-bit
cablelength short
loopback remote line
```

Use the following command to verify the loopback remote configuration on a T3 interface module:

```
router(config-controller)# do show controller t3 0/0/1
T3 0/0/1 is up. (Configured for Remotely Looped)
  Currently in Remotely Line Looped
  Hardware is A900-48T3E3-CE
  Applique type is Subrate T3
  Receiver has no alarms.
  MDL transmission is disabled
  FEAC code received: No code is being received
  Framing is C-BIT Parity, Line Code is B3ZS, Cablelength Short less than 225ft
```

```

BER thresholds: SF = 10e-10 SD = 10e-10
Clock Source is internal
Equipment customer loopback
Data in current interval (240 seconds elapsed):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  0 Unavailable Secs, 0 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  0 Severely Errored Line Secs, 0 Path Failures
  0 AIS Defect Secs, 0 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Data in Interval 1:
Near End
  0 Line Code Violations, 0 P-bit Coding Violations
  0 C-bit Coding Violations, 0 P-bit Err Secs
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs
  20 Unavailable Secs, 20 Line Errored Secs
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 Path Failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs
Total Data (last 1 15 minute intervals):
Near End
  0 Line Code Violations, 0 P-bit Coding Violations,
  0 C-bit Coding Violations, 0 P-bit Err Secs,
  0 P-bit Severely Err Secs, 0 Severely Err Framing Secs,
  20 Unavailable Secs, 20 Line Errored Secs,
  0 C-bit Errored Secs, 0 C-bit Severely Errored Secs
  20 Severely Errored Line Secs, 1 path failures
  0 AIS Defect Secs, 20 LOS Defect Secs
Far End
  0 Errored Secs, 0 Severely Errored Secs
  0 C-bit Unavailable Secs, 0 Path Failures
  0 Code Violations, 0 Service Affecting Secs

T1 1 is up
timeslots:
FDL per AT&T 54016 spec.
No alarms detected.
Framing is ESF, Clock Source is Internal
Data in current interval (250 seconds elapsed):
Near End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
  0 Unavailable Secs, 0 Stuffed Secs
  0 Path Failures, 0 SEF/AIS Secs
Far End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
  0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
  0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End
  0 Line Code Violations, 0 Path Code Violations
  0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins

```

```

2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
0 Unavailable Secs, 0 Stuffed Secs
1 Path Failures, 2 SEF/AIS Secs
Far End
0 Line Code Violations, 0 Path Code Violations
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
0 Line Code Violations,0 Path Code Violations,
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
0 Unavailable Secs, 0 Stuffed Secs
1 Path Failures, 2 SEF/AIS Secs
Far End
0 Line Code Violations,0 Path Code Violations
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
0 Unavailable Secs, 0 Path Failures

```

Use the following command to check the loopback remote configuration on a T1 interface module:

```

Router#show run | sec 0/4/15
controller MediaType 0/4/15
mode t3
controller T3 0/4/15
threshold sd-ber 6
threshold sf-ber 3
framing c-bit
cablelength short
t1 1 Loopback remote iboc fac1

```

Use the following command to verify the loopback remote configuration on a T1 interface module:

```

Router#show controller t3 0/4/15 | be T1 1
T1 1 is up
timeslots:
FDL per AT&T 54016 spec.
Configured for NIU FAC1 Line Loopback with IBOC
Currently in Inband Remotely Line Looped
Receiver has no alarms.
Framing is ESF, Clock Source is Internal
Data in current interval (250 seconds elapsed):
Near End
0 Line Code Violations, 0 Path Code Violations
0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
0 Unavailable Secs, 0 Stuffed Secs
0 Path Failures, 0 SEF/AIS Secs
Far End
0 Line Code Violations, 0 Path Code Violations
0 Slip Secs, 0 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
0 Errored Secs, 0 Bursty Err Secs, 0 Severely Err Secs
0 Unavailable Secs 0 Path Failures
Data in Interval 1:
Near End
0 Line Code Violations, 0 Path Code Violations
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
0 Unavailable Secs, 0 Stuffed Secs
1 Path Failures, 2 SEF/AIS Secs
Far End

```

```

0 Line Code Violations, 0 Path Code Violations
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins
3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
0 Unavailable Secs 0 Path Failures
Total Data (last 1 15 minute intervals):
Near End
0 Line Code Violations,0 Path Code Violations,
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
2 Errored Secs, 0 Bursty Err Secs, 2 Severely Err Secs
0 Unavailable Secs, 0 Stuffed Secs
1 Path Failures, 2 SEF/AIS Secs
Far End
0 Line Code Violations,0 Path Code Violations
0 Slip Secs, 2 Fr Loss Secs, 0 Line Err Secs, 0 Degraded Mins,
3 Errored Secs, 0 Bursty Err Secs, 3 Severely Err Secs
0 Unavailable Secs, 0 Path Failures

```

Associated Commands

The commands used to configure the interfaces.

Commands	URL
controller mediatype	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c2.html#wp3512725718
mode t3/e3	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-l2.html#wp5688885940
controller t1	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c2.html#wp1472647421
controller t3	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c2.html#wp1921350260
controller e3	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c2.html#wp4240965734
clock source	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c2.html#wp6081785140
channelized	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c1.html#wp7026926390
cem	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c1.html#wp2184138077

Commands	URL
cem-group	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-c1.html#wp2440628600
xconnect	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-t2.html#wp8578094790
t1/e1 cem-group	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-t1.html#wp8472041760
payload-size dejitter-buffer	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-o1.html#wp3946673156
bert pattern	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-a1.html#wp3620978929
loopback	http://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-l2.html#wp2513399572
t1/e1 loopback	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-t1.html#wp3852360411
show controllers t3	https://www.cisco.com/c/en/us/td/docs/ios-xml/ios/interface/command/ir-cr-book/ir-s3.html#wp1987423547

