



# CHAPTER 25

## Access Identifiers

This chapter describes the access identifiers (AIDs) of TL1 commands and autonomous messages for the Cisco ONS 15310-MA SDH, Cisco ONS 15454 SDH, Cisco ONS 15454 M2, and Cisco ONS 15454 M6.



**Note**

Access identifiers supported on the Cisco ONS 15454 SDH platform are also supported on Cisco ONS 15454 M2 and Cisco ONS 15454 M6 platforms.

The AID code directs an input command to its intended physical or data entity inside the network element (NE). Equipment modules and facilities are typical examples of entities addressed by the access code.

## 25.1 ALL

The table lists the ALL AIDs and patterns.

**Table 25-1** ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
AidUnionId	FACILITY VC	Y	Y
AidUnionId1	MS-SPRing	Y	N
BAND	ALL BAND[-{1-30}]{1-6,12-17}{1-4}-ALL BAND[-{1-30}]{1-6,12-17}{1-4}{RX, TX} BAND[-{1-30}]{1-6,12-17}{1}-ALL BAND[-{1-30}]{1-6,12-17}{1}{RX, TX}	Y	N

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
BAND WL	BANDWL- $\{1-6,12-17\}$ - $\{1-32\}$ - $\{RX,TX\}$ -1530.33 BANDWL- $\{1-6,12-17\}$ - $\{1-32\}$ - $\{RX,TX\}$ -ALL BANDWL- $\{1-30\}$ - $\{1-6,12-17\}$ - $\{1-32\}$ - $\{RX,TX,PT\}$ - $\langle WLEN \rangle$ BANDWL- $\{1-30\}$ - $\{1-6,12-17\}$ - $\{1-32\}$ - $\{RX,TX,PT\}$ -ALL	Y	N
BITS	ALL BITS-ALL BITS[ $\langle SHELFID \rangle$ ]-ALL BITS[ $\langle SHELFID \rangle$ ]- $\{1,2\}$ SYNC[ $\langle SHELFID \rangle$ ]-BITS $\{1,2\}$	Y	Y

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
CHANNEL	ALL CHAN[-{1-30}]{1-6,12-17}-ALL CHAN[-{1-30}]{1-6,12-17}{1-32}-ALL CHAN[-{1-30}]{1-6,12-17}{1-32}{RX,TX} CHAN[-{1-30}]{1-6,12-17}{1-32}{RX,PT} CHAN[-{1-30}]{1-5,12-16}{1-40}-ALL CHAN[-{1-30}]{1-5,12-16}{1-40}{RX,TX} CHAN[-{1-30}]{1-5,12-16}{1-40}{RX,PT} CHAN[-{1-30}]{1-6,12-17}{1-4}-ALL CHAN[-{1-30}]{1-6,12-17}{1-4}{RX,TX} CHAN[-{1-30}]{1-6,12-17}{2,3} CHAN[-{1-30}]{1-6,12-17}{5} CHAN[-{1-30}]{1-6,12-17}{9,10} CHAN[-{1-30}]{1-6,12-17}{3,4}{1} CHAN[-{1-30}]{1-6,12-17}{21,22}{1} CHAN[-{1-30}]{1-6,12-17}{1-32}{RX,TX,PT} CHAN[-{1-30}]{1-6,12-17}{1-32}-ALL CHAN[-{1-30}]{1-6,12-17}{19}{1} CHAN[-{1-30}]{1-6,12-17}{17,18}{1}	Y	N
COM	Common	Y	N
Cross ConnectId	FACILITY	Y	Y
Cross ConnectId1	VCM FACILITY VC	Y	Y

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
ENV	ALL ENV-IN-ALL ENV-IN[-{1-30}]-ALL ENV-IN[-{1-30}]-{1-20} ENV-IN[-{1-30}]-{1-32} ENV-IN[-{1-30}]-{1-3} ENV-IN[-{1-30}]-{1-4}	ENV-IN[-{1-30}]-{1-6} } ENV-OUT-ALL ENV-OUT[-{1-30}]-ALL ENV-OUT[-{1-30}]-{1-16} ENV-OUT[-{1-30}]-{1-2} ENV-OUT[-{1-30}]-{1-4} ENV-{IN,OUT}[-{1-30}]-{1-16}	ENV-IN[-{1-32} ENV-OUT-{1-8}
EQPT	ALL AIP-ALL AIP[-{1-30}] BIC-ALL BIC[-{1-30}]-ALL BIC[-{1-30}]-{A,B} BP-ALL BP[-{1-30}] FAN-ALL FAN[-{1-30}] PPM-{1-6,12-17}-1	PPM-{1-6,12-17}-{1-30} PPM[-{1-30}]-{1-6,12-17}-{1-4} PPM[-{1-30}]-{1-6,12-17}-{1-30} PWR-ALL PWR[-{1-30}]-ALL PWR[-{1-30}]-{A,B} SLOT-ALL SLOT[-{1-30}]-ALL SLOT[-{1-30}]-{1-17} SLOT[-{1-30}]-{1-6,12-17}	PPM-{3-4}-{1-2} PWR-ALL SLOT-{1-6}

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
FACILITY	ALL FAC[-{1-30}]-{1-4,14-17}-{1-12} FAC[-{1-30}]-{1-6,12-17}-1 FAC[-{1-30}]-{1-6,12-17}-ALL FAC[-{1-30}]-{1-6,12-17}-{0-11} FAC[-{1-30}]-{1-6,12-17}-{0-1} FAC[-{1-30}]-{1-6,12-17}-{1-30,14,16,18,20,22,24,26,28,30,32,34,36} FAC[-{1-30}]-{1-6,12-17}-{1-30,14,16,18,20,22,24}	FAC[-{1-30}]-{1-6,12-17}-{1-4} FAC[-{1-30}]-{1-6,12-17}-{1-6} FAC[-{1-30}]-{1-6,12-17}-{1} FAC[-{1-30}]-{5,6,12,13}-{1} FAC[-{1-30}]-{8,10}-{1} VFAC[-{1-30}]-{1-6,12-17}-{0-1} VFAC[-{1-30}]-{1-6,12-17}-{1,2} VFAC[-{1-30}]-{1-6,12-17}-{1,2}-{1,8}	ETH-{1-2,5-6}-1-6} STMN-{3-4}-{1-2}-1 E1-{1-2,5-6}-{1-21} for WBE-21 card E1-{1-2,5-6}-{1-63} for WBE-63 card E3-{1-2,5-6}-{1-3} T3-{1-2,5-6}-{1-3}
IPADDR	111.222.333.444	Y	N

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
LINE	LINE-{{1-30}}-{{1-6,12-17}}-{{1-2}}-ALL LINE-{{1-30}}-{{1-6,12-17}}-{{1-2}}-{{RX,TX}}	CHAN-{{1-30}}-{{1-6,12-17}}-{{1-32}}-TX (For drop OCH)	N
	LINE-{{1-30}}-{{1-6,12-17}}-{{1-3}}-ALL LINE-{{1-30}}-{{1-6,12-17}}-{{1-3}}-{{RX,TX}}	CHAN-{{1-30}}-{{1-6,12-17}}-{{1-32}}-ALL LINE-{{1-30}}-{{1-5,12-16}}-{{1}}-{{RX,TX}} (EXP)	
	LINE-{{1-30}}-{{8,10}}-{{1}}-ALL LINE-{{1-30}}-{{8,10}}-{{1}}-{{RX,TX}}	LINE-{{1-30}}-{{1-5,12-16}}-{{2}}-{{RX,TX}} (COM)	
	LINE-{{1-30}}-{{1-6,12-17}}-{{1}}-{{RX,TX}} (COM)	LINE-{{1-30}}-{{1-5,12-16}}-{{3}}-{{TX}} (DROP)	
	LINE-{{1-30}}-{{1-6,12-17}}-{{2}}-{{RX,TX}} (OSC)	LINE-{{1-30}}-{{1-5,12-16}}-{{1-3}}-ALL	
	LINE-{{1-30}}-{{1-6,12-17}}-{{3}}-{{RX,TX}} (LINE)	CHAN-{{1-30}}-{{1-5,12-16}}-{{1-32}}-{{RX}} (ADD)	
	LINE-{{1-30}}-{{1-6,12-17}}-{{1-3}}-ALL LINE-{{1-30}}-{{1-6,12-17}}-{{1}}-{{RX,TX}} (LINE)	CHAN-{{1-30}}-{{1-5,12-16}}-{{1-32}}-{{PT}} (PT)	
	LINE-{{1-30}}-{{1-6,12-17}}-{{2}}-{{RX,TX}} (COM)	CHAN-{{1-30}}-{{1-5,12-16}}-{{1-32}}-ALL	
	LINE-{{1-30}}-{{1-6,12-17}}-{{3}}-{{RX,TX}} (OSC)	LINE-{{1-30}}-{{1-6,12-17}}-{{1}}-{{RX,TX}} (EXP)	
	LINE-{{1-30}}-{{1-6,12-17}}-{{4}}-{{RX,TX}} (DC)	LINE-{{1-30}}-{{1-6,12-17}}-{{2}}-{{RX,TX}} (COM)	
	LINE-{{1-30}}-{{1-6,12-17}}-{{1-4}}-ALL LINE-{{1-30}}-{{1-6,12-17}}-1-RX (For input OTS)	LINE-{{1-30}}-{{1-6,12-17}}-{{3}}-{{RX,TX}} (EXP to other ring)	
	LINE-{{1-30}}-{{1-6,12-17}}-1-ALL	LINE-{{1-30}}-{{1-6,12-17}}-{{1-3}}-ALL (Only Cisco ONS 15454 SDH)	

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
LINE WL	LINEWL[-{1-30}]-{1-6,8,10,12-17}-ALL L LINEWL[-{1-30}]-{1-6,12-17}-{1}-{RX ,TX}-ALL LINEWL[-{1-30}]-{1-6,12-17}-{1}-{RX ,TX}-<WLEN> LINEWL[-{1-30}]-{1-6,12-17}-{1-3}-{R X,TX}-ALL LINEWL[-{1-30}]-{1-6,12-17}-{1-3}-{R X,TX}-<WLEN> LINEWL[-{1-30}]-{1-6,12-17}-{1-2}-{R X,TX}-ALL LINEWL[-{1-30}]-{1-6,12-17}-{1-2}-{R X,TX}-<WLEN> LINEWL[-{1-30}]-{1-6,12-17}-{1-2}-{R X,TX}-ALL LINEWL[-{1-30}]-{1-6,12-17}-{1-2}-{R X,TX}- <WLEN> LINEWL[-{1-30}]-{1-6,12-17}-{1,3,4}-{ RX,TX} -ALL LINEWL[-{1-30}]-{1-6,12-17}-{1,3,4}-{ RX,TX} -<WLEN> LINEWL-{{1-30}}-{{1-5,12-16}}-{{1-8}}-{{R X}}-<WLEN> LINEWL-{{1-30}}-{{1-5,12-16}}-{{9}}-{{RX ,TX}}-<WLEN> LINEWL-{{1-30}}-{{1-5,12-16}}-{{10}}-{{T X}}-<WLEN> LINEWL-{{1-30}}-{{1-5,12-16}}-{{11}}-{{R X,TX}}-<WLEN>	Y	N
MS-S PRing	MSSPR-RINGID	Y	N
OPM	ALL OPM[-{1-30}]-{1-5,12-16}-<WLEN> OPM[-{1-30}]-{1-5,12-16}-ALL	Y	N
OSC	OSC-RINGID	Y	N

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
PRSL OT	NULL SLOT-1 SLOT-13 SLOT-15 SLOT-17 SLOT-3 SLOT-5	Y	N
RPRIF	ALL RPRIF-{1-6,12-17}-0	PPM[-{1-30}]-{1-4,11-14}-{1-4}-{1-4} RPRIF-<SLOT>-<RPR INTERFACE>. Currently, only interface 0 is supported.	PPM[-{1-30}]-{1-4,11-14}-{1-4}-{1-4} RPRIF-<SLOT>-<RPR INTERFACE>. Currently, only interface 0 is supported.
RFILE	RFILE-DB RFILE-LOG RFILE-PKG RFILE-COMPDB	Y	Y
SHELF F	SHELF-ALL SHELF-{1-30}	Y	N
SYN	SYNC-NE	Y	Y
SYN_ SRC	BITS-1 BITS-2 FAC-{1-4,11-14}-{1-16} FAC-{1-4,11-14}-{1-4} FAC-{1-4}-1 FAC-{1-4}-{1-4} FAC-{1-6,12-17}-{1-4}	FAC-{1-6,12-17}-{1} FAC-{5,6,12,13}-{1} INTERNAL NONE STM1-{2}-{1-2}-{1} STM4-{2}-{1-2}-{1} SYNC-NE	FAC-{1-6,12-17}-{1} FAC-{5,6,12,13}-{1} INTERNAL NONE STM1-{2}-{1-2}-{1} STM4-{2}-{1-2}-{1} SYNC-NE
SYNC_ REF	SYNC-ALL SYNC-NE SYNC-{BITS1,BITS2}	Y	Y
SYNC SW	INT PRI SEC	Y	Y
UDC	UDC-{F,DCC}-{A,B}	Y	Y



Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
VC	VC11-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}- -1-4}VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}- -1-7}-{1-4}VC11-{1-4,14-17}-{1-8}- -1-1-1-3}-{1-7}-{1-4}VC11-{1-6,12-17}- -1-1-1-7}-{1-2}VC11-{1-6,12-17}-1-1-1-16}- -1-3}-{1-7}-{1-4}VC11-{1-6,12-17}- -1-1-1-4}-{1-3}-{1-7}-{1-4}VC11-{1-6,1 2-17}-{1-4}-1-1-3-3}-{1-7}-{1-4}VC12- -1-4,14-17}-{1-12}-1-1-3-3}-{1-7}-{1-3} VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}- -1-3}VC12-{1-4,14-17}-{1-8}-1-1-1-3-3}- -1-7}-{1-3}VC12-{1-6,12-17}-1-1-1-1-7}- -1-2}VC12-{1-6,12-17}-1-1-1-16}-1-1-3-3}- -1-7}-{1-3}VC12-{1-6,12-17}-1-1-1-2-2}- -1-7}-{1-3}VC12-{1-6,12-17}-1-1-1-4-4}- -1-3-3}-{1-7}-{1-3}VC12-{1-6,12-17}-1- -1-4-4}-1-1-3-3}-{1-7}-{1-3}VC3-{1-14,14- 17}-{1-12}-1-1-3}VC3-{1-4,14-17}-{1-4-4}- -1-4-4}-{1-3}VC3-{1-4,14-17}-{1-8}-1-1-3-3}- -1-6,12-17}-1-1-16-16}-{1-3}VC3-{1-6,12-17}- -1-1-4-4}-{1-3}VC3-{1-6,12-17}-1-1-4-4}- -1-3}VC4-{1-4,11-14}-{1-16}-1}VC4-{1-4,11-14}- -1-16}-{1,5,9,13}VC4-{1-4,11-14}-1-16}- -1-9}VC4-{1-4,11-14}-1-4}-1	VC4-{1-4,11-14}-{1-4-4-4}-1-1,17,33,49}VC4-{1-4,11-14}- -1-4-4-4}-{1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}VC4- -1-4,11-14}-{1-4-4-4}-{1,9,17,25,33,41,49,57}VC 4-{1-4,11-14}-{1-4-4-4}-{1-64}VC4-{1-4,14-17}- -1}VC4-{1-4,14-17}-1-1}VC4-{1-4,14-17}-1-1-2}- -1}VC4-{1-4,14-17}-1-1-2}-1}VC4-{1-4,14-17}- -1-1-4}-1}VC4-{1-4,14-17}-1-1-7}-1-4}-1-2}VC4-1- -4,14-17}-{1-4-4-4}-{1-3}VC4-{1-4,14-17}-1-4-4-4}- -1-4}VC4-1-4,14-17-1-7-1-8}-1}VC4-1-6,12-17}- -1}VC4-1-6,12-17-1-1-1-1}VC4-1-1-1,2,3,5,6,7,9,10,11,13,14, 15}VC4-1-6,12-17-1-1-1-2,4,5,6,7,9,10,13,14}VC4-1-6,12-17-1-1-1-5,9,13}VC4-1-6,12-17-1-1-1-16}VC4-1-6,12-17-1-1-1-12-17-1-1-2}VC4-1-6,12-17-1-1-3}VC4-1-6,12-17-1-1-4}VC4-1-6,12-17-1-1-9}VC4-1-6,12-17-1-4}VC4-1-6,12-17-1-4}-1	VC4-{1-4,11-14}-{1-4-4-4}-1-1,17,33,49}VC4-{1-4,11-14}- -1-4-4-4}-{1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}VC4- -1-4,11-14}-{1-4-4-4}-{1,9,17,25,33,41,49,57}VC 4-{1-4,11-14}-{1-4-4-4}-{1-64}VC4-{1-4,14-17}- -1}VC4-{1-4,14-17}-1-1}VC4-{1-4,14-17}-1-1-2}- -1}VC4-{1-4,14-17}-1-1-2}-1}VC4-{1-4,14-17}- -1-1-4}-1}VC4-{1-4,14-17}-1-1-7}-1-4}-1-2}VC4-1- -4,14-17}-{1-4-4-4}-{1-3}VC4-{1-4,14-17}-1-4-4-4}- -1-4}VC4-1-4,14-17-1-7-1-8}-1}VC4-1-6,12-17}- -1}VC4-1-6,12-17-1-1-1-1}VC4-1-1-1,2,3,5,6,7,9,10,11,13,14, 15}VC4-1-6,12-17-1-1-1-2,4,5,6,7,9,10,13,14}VC4-1-6,12-17-1-1-1-5,9,13}VC4-1-6,12-17-1-1-1-16}VC4-1-6,12-17-1-1-1-12-17-1-1-2}VC4-1-6,12-17-1-1-3}VC4-1-6,12-17-1-1-4}VC4-1-6,12-17-1-1-9}VC4-1-6,12-17-1-4}VC4-1-6,12-17-1-4}-1

Table 25-1 ALL

AID	Pattern	Cisco ONS 15454 SDH	Cisco ONS 15310 MA SDH
WDM ANS	WDMSIDE-{UNKNOWN,A,B,C,D,E,F, G,H}  WDMNODE	Y	N
WLE N	WLEN-{E,W}-{ADD,DROP,EXP}-{153 0.33,1531.12,1531.90,1532.68,1534.25,1 535.04,1535.82, 1536.61,1538.19,1538.98,1539.77,1540.5 6,1542.14,1542.94,1543.73,1544.53,1546 .12, 1546.92,1547.72,1548.51,1550.12,1550.9 2,1551.72,1552.52,1554.13,1554.94,1555 .75,1556.55, 1558.17,1558.98,1559.79,1560.61}	Y	N

## 25.2 AidUnionId

[Table 25-2](#) lists the AidUnionId AIDs and patterns.

Table 25-2 AidUnionId

AID	Patterns	
Facility	E1-{2}-{1-21}	FAC-{5,6,12,13}-{1}
	E3-{2}-{1-3}	FAC-{5-6}-{1-28}
	FAC-{1-4,11-14}-ALL	FAC-{5-6}-{1-3}
	FAC-{1-4,11-14}-{1-16}	FAC-{8,10}-{1}
	FAC-{1-4,11-14}-{1-4}	FSSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}	FSSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,14-17}-{1-8}	FSSTE-{1}-{0-7}
	FAC-{1-4}-1	FSSTE-{1}-{1-8}
	FAC-{1-4}-{1-4}	STM1-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-1	STM1-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-ALL	STM4-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-11}	STM4-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-1}	STM16-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	VFAC-{1,2,5,6}-{0-1}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24}	VFAC-{1,2,5,6}-{1-8}
	FAC-{1-6,12-17}-{1-12}	VFAC-{1-4,11-14}-{1-4}-{1-4}-1
	FAC-{1-6,12-17}-{1-4}	VFAC-{1-6,12-17}-{0-1}
	FAC-{1-6,12-17}-{1-6}	VFAC-{1-6,12-17}-{1,2}
	FAC-{1-6,12-17}-{1}	VFAC-{1-6,12-17}-{1,2}-{1,8}
	FAC-{1-6}-ALL	VFAC-{1}-{0-1}
		VFAC-{1}-{1-8}

Table 25-2 AidUnionId (continued)

AID	Patterns
VC	VC11-1-7-1-4-17-1-12-1-1-3-1-7-1-4} VC4-1-4,11-14-1-4-1,17,33,49}
	VC11-1-7-1-4-17-1-4-1-4-1-3-1-7-1-4} VC4-1-4,11-14-1-4-1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}
	VC11-1-7-1-4-17-1-8-1-1-3-1-7-1-4} VC4-1-4,11-14-1-4-1,9,17,25,33,41,49,57}
	VC11-1-6,12-17-1-1-1-7-1-2} VC4-1-4,11-14-1-4-1-64}
	VC11-1-6,12-17-1-1-16-1-3-1-7-1-4} VC4-1-4,14-17-1}
	VC11-1-6,12-17-1-1-4-1-3-1-7-1-4} VC4-1-4,14-17-1-1}
	VC11-1-6,12-17-1-4-1-3-1-7-1-4} VC4-1-4,14-17-1-12-1}
	VC12-1-7-1-3} VC4-1-4,14-17-1-4-1}
	VC12-1-7-1-3} VC4-1-4,14-17-1-4-1-2}
	VC12-1-7-1-3} VC4-1-4,14-17-1-4-1-3}
	VC12-1-7-1-3} VC4-1-4,14-17-1-4-1-4}
	VC12-1-7-1-3} VC4-1-4,14-17-1-8-1}
	VC12-1-7-1-3} VC4-1-6,12-17-1}
	VC12-1-7-1-3} VC4-1-6,12-17-1-1}
	VC12-1-7-1-3} VC4-1-6,12-17-1-1-1}
	VC12-1-6,12-17-1-1-1-7-1-2} VC4-1-6,12-17-1-1-1,2,3,5,6,7,9,10,11,13,14,15}
	VC12-1-6,12-17-1-1-16-1-3-1-7-1-3} VC4-1-6,12-17-1-1-1,2,4,5,6,7,9,10,13,14}
	VC12-1-6,12-17-1-1-2-1-7-1-3} VC4-1-6,12-17-1-1-1,5,9,13}
	VC12-1-6,12-17-1-1-4-1-3-1-7-1-3} VC4-1-6,12-17-1-1-16}
	VC12-1-6,12-17-1-1-4-1-3-1-7-1-3} VC4-1-6,12-17-1-1-2}
	VC12-1-6,12-17-1-1-4-1-3-1-7-1-3} VC4-1-6,12-17-1-1-3}
	VC12-1-6,12-17-1-1-4-1-3-1-7-1-3} VC4-1-6,12-17-1-1-4}
	VC3-1-14,14-17-1-12-1-1-3} VC4-1-6,12-17-1-1-9}
	VC3-1-4,14-17-1-4-1-4-1-3} VC4-1-6,12-17-1-1-4}
	VC3-1-4,14-17-1-8-1-1-3} VC4-1-6,12-17-1-1-4-1}
	VC3-1-6,12-17-1-1-16-1-3} VC4-1-6,12-17-1-1-4-1}
	VC3-1-6,12-17-1-1-4-1-3} VC4-1-6,12-17-1-1-4-1}
	VC3-1-6,12-17-1-1-4-1-3} VC4-1-6,12-17-1-1-4-1}
	VC4-1-4,11-14-1-16-1} VC4-1-4,11-14-1-16-1,5,9,13}
	VC4-1-4,11-14-1-16-1} VC4-1-4,11-14-1-16-1,9}
	VC4-1-4,11-14-1-4-1} VC4-1-4,11-14-1-4-1}

## 25.3 AidUnionId1

Table 25-3 lists the AidUnionId1 AIDs and patterns.

**Table 25-3** AidUnionId1

AID	Patterns
MS-SPRing	ALL MSSPR-RINGID

## 25.4 BAND



**Note**

The BAND AID does not apply to the Cisco ONS 15310 MA SDH platform.

The BAND AID is used to access Optical Multiplex Section (OMS) layer of optical networking units. Table 25-4 lists the BAND AIDs and their descriptions.

**Table 25-4** BAND

Pattern	Description
ALL	All of the OMSs of the NE. The ALL AID is applicable for retrieve-only commands.
BAND[-{1-30}]-{1-6,12-17}-{1-4}-ALL	All the channels in a Band OADM (AD-1B-xx.x, AD-4B-xx.x) unit.
BAND[-{1-30}]-{1-6,12-17}-{1-4}-{RX,TX}	The Receive/Transmit channels in a Band OADM (AD-1B-xx.x, AD-4B-xx.x) unit.
BAND[-{1-30}]-{1-6,12-17}-{1}-ALL	All the channels in an Optical Multiplexer/Demultiplexer (4MD-xx.x) unit.
BAND[-{1-30}]-{1-6,12-17}-{1}-{RX,TX}	The Receive/Transmit channels in an Optical Multiplexer/Demultiplexer (4MD-xx.x) unit.

## 25.5 BANDWL



**Note**

The BANDWL AID does not apply to the Cisco ONS 15310 MA SDH platform.

Band wavelength. Identifies a wavelength channel included in any of the lower layer OMS facilities. Table 25-5 lists the BANDWL AIDs and their descriptions.

Table 25-5 BANDWL

Pattern	Description
BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-1530.33	Identifies an allocated wavelength channel included in any of the lower layer OMS facilities.
BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-ALL	Identifies all of the allocated wavelength channels included in any of the lower layer OMS facilities.
BANDWL-[[1-30]-][1-6,12-17]-[1-32]-{RX,TX,PT}-<WLEN>	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the endpoints of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH]. WAVELENGTH is in the form of 15xx.yy.
BANDWL-[[1-30]-][1-6,12-17]-[1-32]-{RX,TX,PT}-ALL	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the endpoints of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH]. WAVELENGTH is in the form of 15xx.yy.

## 25.6 BITS

AID for building integrated timing supply (BITS). [Table 25-6](#) lists the BITS AIDs and their descriptions.

Table 25-6 BITS

Pattern	Description
ALL	The ALL AID is applicable to RTRV commands only (RTRV-BITS, RTRV-ALM-BITS, and RTRV-COND-BITS). The All AID is equivalent to BITS-ALL for these commands. For RTRV-ALM-SYNCN and RTRV-COND-SYCN, the ALL AID translates to BITS-ALL, SYNC-BITS1, and SYNC-BITS2.
BITS-ALL	BITS AIDs of both BITS-1 and BITS-2 in the RTRV-BITS command.
BITS[<SHELFID>]-ALL	BITS shelf ID of both BITS-1 and BITS-2 in the RTRV-BITS command.

Table 25-6 BITS (continued)

Pattern	Description
BITS[<SHELFID>]-{1,2}	Individual BITS AID.
SYNC[<SHELFID>]-BITS{1,2}	BITS-OUT AIDs of BITS-1 and BITS-2. These AIDs are applicable only in ED-BITS and RTRV-BITS commands and are used for setting and retrieving the BITS-OUT parameters.

## 25.7 CHANNEL


**Note**

The CHANNEL AID does not apply to the Cisco ONS 15310 MA SDH platform.

Accesses the Optical Channel (OCH) layer of optical networking/client units. [Table 25-9](#) lists the CHANNEL AIDs and their descriptions.

Table 25-7 CHANNEL

CHANNEL Values	Description
ALL	ALL OCHs of the NE. The ALL AID is applicable for retrieve-only commands.
CHAN[-{1-30}]-{1-6,12-17}-ALL	All the channels of an Optical Transponder/Muxponder. The format is CHAN-[SLOT]-ALL
CHAN[-{1-30}]-{1-6,12-17}-{1-32}-ALL	All the channels in an Optical Multiplexer/Demultiplexer 32WSS unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL.
CHAN[-{1-30}]-{1-6,12-17}-{1-32}-{RX,TX}	The Receive/Transmit channels in an Optical Multiplexer/Demultiplexer (32DMX, 32MUX) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-30}]-{1-6,12-17}-{1-32}-{RX,PT}	The Receive/Pass-through channels in an Optical 32WSS unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-30}]-{1-5,12-16}-{1-40}-ALL	All the channels in Optical Multiplexer/Demultiplexer/WSS (40Ch) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL.
CHAN[-{1-30}]-{1-5,12-16}-{1-40}-{RX,TX}	The receive/transmit channels in an Optical Multiplexer/Demultiplexer (40Ch) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-30}]-{1-5,12-16}-{1-40}-{RX,PT}	The Receive/Pass-through channels in an Optical WSS (40Ch) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-30}]-{1-6,12-17}-{1-4}-ALL	All the Channels in an OADM (AD-1C-xx.x, AD-2C-xx.x, AD-4C-xx.x) unit or Optical Multiplexer/Demultiplexer (4MD-xx.x) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL.



Table 25-7 CHANNEL (continued)

CHANNEL Values	Description
CHAN[-{1-30}]-{1-6,12-17}-{1-4}-{RX,TX}	The Receive/Transmit Channels in an OADM (AD-1C-xx.x, AD-2C-xx.x, AD-4C-xx.x) unit or Optical Multiplexer/Demultiplexer (4MD-xx.x) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-30}]-{1-6,12-17}-{2,3}	A single channel of an Optical Transponder/Muxponder. The TXP_MR_10G, TXP_MR_10E, and TXP_MR_2.5G cards use CHAN-[SHELF]-SLOT-2 for the one dense wavelength division multiplexing (DWDM) Facility. The TXPP_MR_2.5G card uses CHAN-[SHELF]-SLOT-{2,3} for the two DWDM Facilities. The format is CHAN-[SHELF]-[SLOT]-[PORT].
CHAN[-{1-30}]-{1-6,12-17}-{5}	A single channel of an Optical Muxponder. The TXP_MR_10G and TXP_MR_10E cards use CHAN-[SHELF]-SLOT-5 for the one DWDM facility. The TXP_MR_10G uses the CHAN-[SHELF]-SLOT-5 for the one DWDM facility. The format is CHAN-[SHELF]-[SLOT]-[PORT].
CHAN[-{1-30}]-{1-6,12-17}-{9,10}	A single channel of an Optical Muxponder. The MXP_MR_10DME_C and MXP_2.5G_10G cards use CHAN-[SHELF]-SLOT-9 for the one DWDM Facility. The MXPP_2.5G_10G card uses CHAN-[SHELF]-SLOT-{9,10} for the two DWDM Facilities. The format is CHAN-[SHELF]-[SLOT]-[PORT].
CHAN[-{1-30}]-{1-6,12-17}-{3,4}-{1}	A single channel of an Optical Muxponder. The 10GE-XP uses CHAN-[SHELF]-SLOT-{3,4}-1 for the two DWDM facilities. The format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN[-{1-30}]-{1-6,12-17}-{21,22}-{1}	A single channel of an Optical Muxponder. The GE-XP uses CHAN-[SHELF]-SLOT-{11,12}-1 for the two DWDM facilities. The format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN-[[{1-30}]-{1-6,12-17}-{1-32}]-{RX,TX,PT}	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION].
CHAN-[[{1-30}]-{1-6,12-17}-{1-32}]-ALL	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH (in the form of 15xx.yy)].
CHAN[-{1-30}]-{1-6,12-17}-{19}-{1}	A single channel of an ADM-10G card. ADM-10G uses CHAN-[SHELF]-SLOT-19 for one DWDM Facility. Format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN[-{1-30}]-{1-6,12-17}-{17,18}-{1}	The DWDM interlinks between two peer ADM-10G cards. ADM-10G uses CHAN-[SHELF]-SLOT-{17,18} for the two DWDM interlinks Facilities. Format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].

Table 25-7 CHANNEL (continued)

CHANNEL Values	Description
CHAN[-{1-30}]-{1-6,12-17}-{19}-{1}	A single channel of an ADM-10G card. ADM-10G uses CHAN-[shelf]-slot-19 for the 1 DWDM Facility. Format is CHAN-[shelf]-[slot]-[ppm]-[port].
CHAN[-{1-0}]-{1-6,12-17}-{17,18}-{1}	The DWDM Interlinks between 2 peer ADM-10G cards. ADM-10G uses CHAN-[shelf]-slot-{17,18} for the 2 DWDM Interlinks Facilities. Where format is CHAN-[shelf]-[slot]-[ppm]-[port]. In Release 9.0 or later, Port Number 18 can be used as an optical channel in the single card mode, the AID and the format remain the same.

## 25.8 CHGRP

AID for the Channel Group (CHGRP). [Table 25-8](#) lists the CHGRP AIDs and their descriptions.

Table 25-8 CHGRP

Pattern	Description
ALL	The ALL aid is applicable for RTRV-only commands, basically the RTRV-rr type of commands. For example, RTRV-CHGRP with ALL aid returns all CHGRP ports on the GE_XP and 10GE_XP cards.
CHGRP-[[{1-30}]-{1-6,12-17}]-{1-11}	Channel group AID on GE_XP and 10GE_XP cards. The format is CHGRP-[SHELF]-[SLOT]-[CHGRP]

## 25.9 COM



### Note

The COM AID does not apply to the Cisco ONS 15310 MA SDH platform.

[Table 25-9](#) lists the common (COM) AIDs and their descriptions.

Table 25-9 COM

Pattern	Description
COM	Common

## 25.10 CrossConnectId

[Table 25-10](#) lists the CrossConnectId AIDs and their descriptions.

Table 25-10 CrossConnectId

AID	Pattern
FACILITY	E1-{2}-{1-21} FAC-{5,6,12,13}-{1}
	E3-{2}-{1-3} FAC-{5-6}-{1-28}
	FAC-{1-4,11-14}-ALL FAC-{5-6}-{1-3}
	FAC-{1-4,11-14}-{1-16} FAC-{8,10}-{1}
	FAC-{1-4,11-14}-{1-4} FSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1} FSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,14-17}-{1-8} FSTE-{1}-{0-7}
	FAC-{1-4}-1 FSTE-{1}-{1-8}
	FAC-{1-4}-{1-4} STM1-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-1 STM1-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-ALL STM4-{2}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-11} STM4-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{0-1} STM16-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-{1-30,14,16,18,2 0,22,24,26,28, 30,32,34,36} VFAC-{1,2,5,6}-{0-1}
	FACILITY (cont.)
FAC-{1-6,12-17}-{1-30} VFAC-{1-4,11-14}-{1-4}-{1-4}-1	
FAC-{1-6,12-17}-{1-4} VFAC-{1-6,12-17}-{0-1}	
FAC-{1-6,12-17}-{1-4} VFAC-{1-6,12-17}-{1,2}	
FAC-{1-6,12-17}-{1-6} VFAC-{1-6,12-17}-{1,2}-{1,8}	
FAC-{1-6,12-17}-{1} VFAC-{1}-{0-1}	
FAC-{1-6}-ALL VFAC-{1}-{1-8}	

## 25.11 CrossConnectId1

Table 25-11 lists the CrossConnectId1 AIDs and their descriptions.

Table 25-11 CrossConnectId1

AID	Pattern	
EQPT	ALL PPM[-{1-30}]-{1-4,11-14}-{1-4}-{1-4}	
	AIP-ALL PPM[-{1-30}]-{1-6,12-17}-{1-4}	
	AIP[-{1-30}] PPM[-{1-30}]-{1-6,12-17}-{1-8}	
	BIC-ALL PWR-ALL	
	BIC[-{1-30}]-ALL PWR[-{1-30}]-ALL	
	BIC[-{1-30}]-{A,B} PWR[-{1-30}]-{A,B}	
	BP-ALL SLOT-ALL	
	BP[-{1-30}] SLOT[-{1-30}]-ALL	
	FAN-ALL SLOT[-{1-30}]-{1-14}	
	FAN[-{1-30}] SLOT[-{1-30}]-{1-17}	
	PIM[-{1-30}]-{1-4,11-14}-ALL SLOT[-{1-30}]-{1-2}	
	PIM[-{1-30}]-{1-4,11-14}-{1-4 } SLOT[-{1-30}]-{1-4,11-14}	
	PPM[-{1-30}]-1-{1,2} SLOT[-{1-30}]-{1-6,12-17}	
	PPM[-{1-30}]-2-{1,2} SLOT[-{1-30}]-{1-8}	
	PPM[-{1-30}]-{1-4,11-14}-{1-4 }-ALL	
	VCM	VCM-{1-6,12-17}-{0-1}-ALL
		VCM-{1-6,12-17}-{0-1}-{1-256}
VCM-{1-6,12-17}-{1-4}-ALL		
VCM-{1-6,12-17}-{1-4}-{1-256}		

Table 25-11 CrossConnectId1 (continued)

AID	Pattern		
FACILITY	E1-{2}-{1-21}	FAC-{5,6,12,13}-{1}	
	E3-{2}-{1-3}	FAC-{5-6}-{1-28}	
	FAC-{1-4,11-14}-ALL	FAC-{5-6}-{1-3}	
	FAC-{1-4,11-14}-{1-16}	FAC-{8,10}-{1}	
	FAC-{1-4,11-14}-{1-4}	FSSTE-{1,2,5,6}-{0-7}	
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}	FSSTE-{1,2,5,6}-{1-8}	
	FAC-{1-4,14-17}-{1-8}	FSSTE-{1}-{0-7}	
	FAC-{1-4}-1	FSSTE-{1}-{1-8}	
	FAC-{1-4}-{1-4}	STM1-{2}-{1-2}-{1}	
	FAC-{1-6,12-17}-1	STM1-{3,4}-{1-2}-{1}	
	FAC-{1-6,12-17}-ALL	STM4-{2}-{1-2}-{1}	
	FAC-{1-6,12-17}-{0-11}	STM4-{3,4}-{1-2}-{1}	
	FAC-{1-6,12-17}-{0-1}	STM16-{3,4}-{1-2}-{1}	
	FAC-{1-6,12-17}-{1-30,14,16,18,20,22,24,26,28,30,32,34,36}	VFAC-{1,2,5,6}-{0-1}	
		VFAC-{1,2,5,6}-{1-8}	
	FACILITY (cont.)	FAC-{1-6,12-17}-{1-30,14,16,18,20,22,24}	VFAC-{1-4,11-14}-{1-4}-{1-4}-1
		FAC-{1-6,12-17}-{1-30}	VFAC-{1-6,12-17}-{0-1}
FAC-{1-6,12-17}-{1-4}		VFAC-{1-6,12-17}-{1,2}	
FAC-{1-6,12-17}-{1-6}		VFAC-{1-6,12-17}-{1,2}-{1,8}	
FAC-{1-6,12-17}-{1}		VFAC-{1}-{0-1}	
FAC-{1-6}-ALL		VFAC-{1}-{1-8}	

Table 25-11 CrossConnectId1 (continued)

AID	Pattern
VC	VC11-{1-4,14-17}-{1-30}-1-{1-3}-{1-7}-{1-4}
	VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}
	VC11-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-4}
	VC11-{1-6,12-17}-1-1-{1-7}-{1-2}
	VC11-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-4}
	VC11-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-4}
	VC11-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-4}
	VC12-{1-4,14-17}-{1-30}-1-{1-3}-{1-7}-{1-3}
	VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-3}
	VC12-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-3}
	VC12-{1-6,12-17}-1-1-{1-7}-{1-2}
	VC12-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-3}
	VC12-{1-6,12-17}-1-{1-2}-{1-7}-{1-3}
	VC12-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-3}
	VC12-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}
	VC3-{1-14,14-17}-{1-30}-1-{1-3}
	VC3-{1-4,14-17}-{1-4}-{1-4}-{1-3}
	VC3-{1-4,14-17}-{1-8}-1-{1-3}
	VC3-{1-6,12-17}-1-{1-16}-{1-3}
	VC3-{1-6,12-17}-1-{1-4}-{1-3}
	VC3-{1-6,12-17}-{1-4}-1-{1-3}
	VC3-{1-6,12-17}-{1-4}-{1-3}
	VC4-{1-4,11-14}-{1-16}-1
	VC4-{1-4,11-14}-{1-16}-{1,5,9,13}
	VC4-{1-4,11-14}-{1-16}-{1,9}
	VC4-{1-4,11-14}-{1-4}-1
	VC4-{1-4,11-14}-{1-4}-{1,17,33,49}
	VC4-{1-4,11-14}-{1-4}-{1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61}

Table 25-11 CrossConnectId1 (continued)

AID	Pattern
VC (continued)	VC4-{1-4,11-14}-{1-4}-{1,9,17,25,33,41,49,57}
	VC4-{1-4,11-14}-{1-4}-{1-64}
	VC4-{1-4,14-17}-1
	VC4-{1-4,14-17}-1-1
	VC4-{1-4,14-17}-{1-30}-1
	VC4-{1-4,14-17}-{1-4}-1
	VC4-{1-4,14-17}-{1-4}-{1-2}
	VC4-{1-4,14-17}-{1-4}-{1-3}
	VC4-{1-4,14-17}-{1-4}-{1-4}
	VC4-{1-4,14-17}-{1-8}-1
	VC4-{1-6,12-17}-1
	VC4-{1-6,12-17}-1-1
	VC4-{1-6,12-17}-1-{1,2,3,5,6,7,9,10,11,13,14,15}
	VC4-{1-6,12-17}-1-{1,2,4,5,6,7,9,10,13,14}
	VC4-{1-6,12-17}-1-{1,5,9,13}
	VC4-{1-6,12-17}-1-{1-16}
	VC4-{1-6,12-17}-1-{1-2}
	VC4-{1-6,12-17}-1-{1-3}
	VC4-{1-6,12-17}-1-{1-4}
	VC4-{1-6,12-17}-1-{1-9}
	VC4-{1-6,12-17}-{1-4}
	VC4-{1-6,12-17}-{1-4}-1

## 25.12 ENV

Table 25-12 lists the environmental (ENV) AIDs for the AIC-I cards and their descriptions.

Table 25-12 ENV

Pattern	Description
ALL	The ALL AID applies to retrieve-only commands: RTRV-ALM-ENV, RTRV-COND-ENV, RTRV-ATTR-CONT, and RTRV-ATTR-ENV.
ENV-IN-ALL	Environmental AID for ALL environmental alarms.
ENV-IN[-{1-30}]-ALL	Environmental AID for ALL multishelf environmental alarms.
ENV-IN[-{1-30}]-{1-4}	Environmental AID for the AIC-I card. "IN" is used for environmental alarms.

Table 25-12 ENV (continued)

Pattern	Description
ENV-IN[-{1-30}]-{1-20}	Environmental AID for the AIC-I card. “IN” is used for environmental alarms.
ENV-IN[-{1-30}]-{1-32}	Environmental AID for AIC-I card extensions. “IN” is used for environmental alarms.
ENV-IN[-{1-30}]-{1-3}	Environmental AID for the AIC-I card. “IN” is used for environmental alarms.
ENV-IN[-{1-30}]-{1-6}	Environmental AID. “IN” is used for environmental alarms.
ENV-OUT-ALL	Environmental AID for AIC-I cards. “OUT” is used for environmental controls.
ENV-OUT[-{1-30}]-ALL	Environmental AID for AIC-I cards. “OUT” is used for environmental controls.
ENV-OUT[-{1-30}]-{1-4}	Environmental AID for AIC-I cards. “OUT” is used for environmental controls.
ENV-OUT[-{1-30}]-{1-16}	Environmental AID for AIC-I card extensions. “OUT” is used for environmental controls.
ENV-OUT[-{1-30}]-{1-2}	Environmental AID for AIC-I cards. “OUT” is used for environmental controls.
ENV-{IN,OUT}[-{1-30}]-{1-16}	ENV-IN[1-16] is used for the environmental alarm AID. ENV-OUT-{1-16} is used for the external control AID.

## 25.13 EQPT

Table 25-13 lists the equipment (EQPT) AIDs for the AIC-I cards and their descriptions. EQPT AIDs are used to access specific cards.

Table 25-13 EQPT

Pattern	Description
AIP[-{1-30}]	The AID for the alarm interface panel (AIP). It is used for RTRV-INV output only.
AIP-ALL	The AID for all of the AIPs in any shelf of the node. It is used for RTRV-INV output only.
ALL	Only used for RTRV-INV, RTRV-EQPT, RTRV-ALM-EQPT, and RTRV-COND-EQPT commands. RTRV-INV returns all the inventory information for the NE. The ONS 15454 SDH includes the multiservice cards, common control cards, and the AIP, BP, and FAN. RTRV-ALM-EQPT and RTRV-COND-EQPT with the ALL AID return EQPT and PWR-A and PWR-B type of alarms and conditions.
BIC-ALL	AIDs for the backplane interface connectors (BICs): BIC-A and BIC-B. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.



Table 25-13 EQPT (continued)

Pattern	Description
BIC[-{1-30}] ]-ALL	AIDs for the BICs: BIC-A and BIC-B of a single shelf in a multishelf node. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BIC[-{1-30}] -{A,B}	AIDs for the BICs. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BP[-{1-30}]	The AID for the backplane. It is used for RTRV-INV output only.
BP-ALL	The AID for all of the backplanes in any shelf on the multishelf node. It is used for RTRV-INV output only.
FAN[-{1-30}]	The AID for the fan tray. It is used for RTRV-INV output only.
FAN-ALL	The AID for all of the fan tray in all the shelves of a multishelf node. It is used for RTRV-INV output only.
PPM-{1-6,12-17}-1	PPM for the OC192-XFP card. Format is PPM-[SLOT]-[PPM].
PPM-{1-6,12-17}-{1-4}	Pluggable Port Module for MRC-4. Format is PPM-[SLOT]-[PPM]. It is not applied on CHG-EQPT, and RMV/RST/ED-EQPT commands.
PPM-{1-6,12-17}-{1-30}	Pluggable Port Module for MRC-12 card. Format is PPM-[SLOT]-[PPM]. It is not applied on CHG-EQPT, and RMV/RST/ED-EQPT commands.
PPM[-{1-30}] ]-{1-6,12-17} - {1-4}	PPM AID for DWDM MXP_2.5G_10G, TXP_MR_10G, TXP_MR_2.5G, TXPP_MR_2.5G, MXP_2.5G_10E, and TXP_MR_10E cards. Format of AID is PPM-[SLOT]-[PPM].
PPM[-{1-30}] ]-{1-6,12-17} - {1-8}	(ONS 15454 only) PPM AID for the MXP_MR_2.5G and MXPP_MR_2.5G cards. Format of AID is PPM-[SLOT]-[PPM].
PWR-ALL	AIDs for the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
PWR[-{1-30}] ]-{A,B}	AIDs for the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
PWR[-{1-30}] ]-ALL	AIDs for all of the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
SLOT-ALL	All of the NE equipment AIDs.
SLOT[-{1-30}] ]-{1-17}	EQPT AID where the format is SLOT-[SLOT].
SLOT[-{1-30}] ]-{1-6,12-17} }	Individual equipment AID of the multiservice card units or slots where the format is SLOT-[SLOT].

## 25.14 ETH

Ethernet (ETH) AIDs are used to access the ethernet port. [Table 25-8](#) lists the ETH AIDs and their descriptions.

**Table 25-14** ETH

Pattern	Description
ETH-{1,2,5,6}-{1-6}	ETH aids for front end ports of CE-MR-6 (GT3) card on 15310MA. Format is ETH-slot-port. Port numbering is 1-based.
ETH[-{1-30}]{-}{1-6,12-17}{-}{1-22}{-}{1}	Ethernet AID for ethernet port on GE_XP and 10GE_XP cards. The format is ETH-[SHELF]-[SLOT]-[PPM]-[PORT].
ETH[-{1-30}]{-}{1-6,12-17}{-}{1-22}{-}{1}{-}{1-4096}	Ethernet CEPID AID for ethernet port CEP on GE_XP and 10GE_XP cards. The format is ETH-[SHELF]-[SLOT]-[PPM]-[PORT]-[CEPID], where [CEPID] is the connection end point identifier, that identifies a single 1 Gbit/sec circuit inside an ethernet port.

## 25.15 FACILITY

Facilities AIDs are used to access specific ports. [Table 25-15](#) lists the FACILITY AIDs and their descriptions. The FACILITY AID formats are as follows:

- Format for DS3i-N-12 electrical facilities: FAC-[SLOT]-[PORT]
- Format for POS ports: VFAC-[SLOT]-[PORT]
- Format for POS port with PIM and PPM: VFAC-[SLOT]-[PIM]-[PPM]-[PORT]

**Table 25-15** FACILITY

Pattern	Description
ALL	The ALL AID is applicable for retrieve-only commands (RTRV-xx type of commands), for example: RTRV-STM16 with an ALL AID returns all STM16 facilities on the node.
FAC[-{1-30}]{-}{1-4,14-17}{-}{1-8}	Facilities for an STM1 card where the format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-}{1-6,12-17}-1	Facility AID for the one client (CLNT) port on a TXP_MR_10G, TXP_MR_2.5G, TXP_MR_2.5G, or TXPP_MR_2.5G card where the format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-}{1-6,12-17}-ALL	All the facilities of an input/output (I/O) unit or slot where the format is FAC-[SLOT]-[ALL].
FAC[-{1-30}]{-}{1-6,12-17}{-}{0-11}	Facilities for the Ethernet front-end ports on the ML100T-12 card. Ports are numbered starting with 0 (for the ML100T-12, the first port is FAC-SLOT-0, second port is FAC-SLOT-1, etc., and the last port is FAC-SLOT-11; for the ML1000-2, the first port is FAC-SLOT-0 and second port is FAC-SLOT-1). The format is FAC-[SLOT]-[PORT].

Table 25-15 FACILITY (continued)

Pattern	Description
FAC[-{1-30}]{-1-6,12-17}{-0-1}	Facilities for the Ethernet back-end ports on the ML1000-2 card. Ports are 0-based, (for example, the first port is FAC-SLOT-0 and the second port is FAC-SLOT-1). The format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}{-1-8}	Facility AID for the Ethernet front-end ports on the CE-100T-8 card The format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}{-0-11}	Facility AID for the Ethernet front-end ports on the MS-ISC-100T The first port is FAC-SLOT-0, the second port is FAC-SLOT-1, and so on. The last port is FAC-SLOT-11. The format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-1-6,12-17}{-1-12,14,16,18,20,22,24,26,28,30,32,34,36}	Facility AIDs for the DS3i-N-12 VC416 backplane rate where format is FAC-[SLOT]-[PORT]. Ports 1 through 12 are always available, but only even ports after 12 are available.
FAC[-{1-30}]{-1-6,12-17}{-1-12,14,16,18,20,22,24}	Facility AIDs for DS3i-N-12 VC44 backplane rate where format is FAC-[SLOT]-[PORT]. Ports 1 through 12 are always available, but only even ports after 12 are available.
FAC[-{1-30}]{-1-6,12-17}{-1-4}	Facility AID for the four CLNT facilities on the MXP_2.5G_10G card. Facility AID for 4-Port G1000/FC_MR-4 card. Facility AID for creating and editing cross-connects (VC3, VC4, VC4-2C, VC4-3C, VC4-4C, and VC4-8C) for the 4-Port G1000/FC_MR-4 card where format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-1-6,12-17}{-1-6}	Facilities for the DS3i-N-12 card where the format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-1-6,12-17}{-1}	Facility AID for the 1-Port STM4, STM16, and STM1 in OSC-CSM cards. Facility AID for the client ports on the MXP/TXP and TXP_MR_2.5G cards where format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-5,6,12,13}{-1}	Facility AID for the STM16/STM64 cards. The STM16/STM64 cards can only use Slot 5, Slot 6, Slot 12, and Slot 13 where format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-8,10}{-1}	Facility AID for the OSCM card where format is FAC-[SLOT]-[PORT].
FAC[-{1-30}]{-1-6,12-17}-1-1	Facility AID for the TXP_MR_2.5G, TXPP_MR_2.5G, TXP_MR_10E, TXP_MR_10EX and TXPP_MR_10EX cards in the format of:FAC-[shelf]-[slot]-[ppm]-[port].
VFAC[-{1-30}]{-1-6,12-17}{-0-1}	Facilities for the back-end packet-over-SDH (POS) ports on the ML-Series card. Port numbering is 0-based (first POS port is VFAC-SLOT-0, second POS port is VFAC-SLOT-1). VC4, VC4-2C, VC4-3C, VC4-4C, and VC4-8C for the ML1000 and ML100T cards. Format is VFAC-[SLOT]-[PORT].

Table 25-15 FACILITY (continued)

Pattern	Description
VFAC-{1-6,12-17}-{0-15}	Facilities for ML-MR-10 back-end packet-over-SDH (POS) ports in MLMR-AUTO Mode. ML-MR-10 has 16 POS Ports. POS Ports are 0-Based. Format is VFAC-[slot]-[port]
VFAC[-{1-30}]-{1-6,12-17}-{1,2}	GFP facilities on the MXP-MR-2.5G and MXPP-MR-2.5G cards.
VFAC[-{1-30}]-{1-6,12-17}-{1,2}-{1,8}	GFP client facilities for MXP-MR-2.5G and MXPP-MR-2.5G cards
FAC[-{1-30}]-{1-4,14-17}-{1-4}	Facility AID for four Ether Ports on the CE1000 card.
VFAC[-{1-30}]-{1-4,14-17}-{1-4}	Facility AID for the back-end ports on the CE1000 card.

## 25.16 IPADDR

Table 25-16 lists the IP address (IPADDR) AIDs and their descriptions.

Table 25-16 IPADDR

Pattern	Description
111.222.333.444	Standard 4-part IP address notation
ALL	ALL

## 25.17 LINE



### Note

The LINE AID does not apply to the Cisco ONS 15310 MA SDH platform.

The LINE AID is used to access the Optical Transport Section (OTS) layer of optical networking units. Applicable only to ONS 15454 SDH AD-1B-xx.x, AD-4B-xx.x, AD-1C-xx.x, AD-2C-xx.x, AB-4C-xx.x, OSC-CSM, OSCM, OPT-BST, OPT-PRE, 4MD-xx.x, 32MUX-O, and 32DMX-O cards. The format is LINE-[SLOT]-[PORT]-[DIRECTION]. Table 25-17 lists the LINE AIDs and their descriptions.

Table 25-17 LINE

Values	Description
ALL	All of the OTSs of the NE. The ALL AID applies to retrieve-only commands.
LINE-{1-6,12-17}-{1-2}-ALL	All the lines in a OPT-PRE, OCS-CSM, AD-1B-xx.x, AD-4B-xx.x, AD-1C-xx.x, AD-2C-xx.x, and AD-4C-xx.x cards.
LINE-{1-6,12-17}-{1-2}-{RX,TX}	The receive/transmit lines in a OPT-PRE, AD-1B-xx.x, AD-4B-xx.x, AD-1C-xx.x, AD-2C-xx.x, and AD-4C-xx.x cards.

Table 25-17 LINE (continued)

Values	Description
LINE-{{1-6,12-17}}-{{1-3}}-{{RX,TX}}	The receive/transmit lines in a OPT-BST cards.
LINE-{{8,10}}-{{1}}-ALL	All the lines in a OSCM cards.
LINE-{{8,10}}-{{1}}-{{RX,TX}}	The receive/transmit lines in OSCM car
LINE-{{1-30}}-{{1-6,12-17}}-{{1}}-{{RX,TX}}	For booster cards, there is an input OTS for LINE(1) and an output OTS (amplified) for amplification stage COM(2), two OTS for input line COM(2) and output LINE(1), and 2 OTS for OSC(3) add and drop service channel.
LINE-{{1-30}}-{{1-6,12-17}}-{{2}}-{{RX,TX}}	
LINE-{{1-30}}-{{1-6,12-17}}-{{3}}-{{RX,TX}}	
LINE-{{1-30}}-{{1-6,12-17}}-{{1-3}}-ALL	
LINE-{{1-30}}-{{1-6,12-17}}-{{1}}-{{RX,TX}}	
LINE-{{1-30}}-{{1-6,12-17}}-{{2}}-{{RX,TX}}	
LINE-{{1-30}}-{{1-6,12-17}}-{{3}}-{{RX,TX}}	
LINE-{{1-30}}-{{1-6,12-17}}-{{4}}-{{RX,TX}}	Preamplifier card with 2 stages of amplification; input OTS for LINE(1) and an output OTS (amplified) for amplification stage COM(2), two OTS for input line COM(2) and output LINE(1), and two OTS for OSC(3) add and drop service channel and output and an input OTS for the DCU(2).
LINE-{{1-30}}-{{1-6,12-17}}-{{1-4}}-ALL	
LINE-{{1-30}}-{{1-6,12-17}}-1-RX For input OTS	
LINE-{{1-30}}-{{1-6,12-17}}-1-ALL	
CHAN-{{1-30}}-{{1-6,12-17}}-{{1-32}}-TX for Drop OCH	
CHAN-{{1-30}}-{{1-6,12-17}}-{{1-32}}-ALL	

Table 25-17 LINE (continued)

Values	Description
LINE-{{1-30}}-{{1-5,12-16}}-{{1}}- {RX,TX} (EXP)	Wavelength switch selector card has input and output OTS for signals coming from amplifier units COM(2), input and output OTS for signal continuing to next WSS unit in the node EXP(1), and an output PT(3) drop port for the signal continuing to 32DMX-L card. It also has 32 internal OCH pass-through channels (PT) and 32 external input channels (ADD). This unit is 2 slots wide.
LINE-{{1-30}}-{{1-5,12-16}}-{{2}}- {RX,TX} (COM)	
LINE-{{1-30}}-{{1-5,12-16}}-{{3}}-{{TX}} } (DROP)	
LINE-{{1-30}}-{{1-5,12-16}}-{{1-3}}-A LL	
CHAN-{{1-30}}-{{1-5,12-16}}-{{1-32}}- {RX} (ADD)	
CHAN-{{1-30}}-{{1-5,12-16}}-{{1-32}}- {PT} (PT)	
CHAN-{{1-30}}-{{1-5,12-16}}-{{1-32}}- ALL	
LINE-{{1-30}}-{{1-6,12-17}}-{{1}}- {RX,TX} (EXP)	Multiring/mesh unit has 6 OTS connectors: two are the input and output OTS for signal coming from amplifier units COM(1), the other four are for the split 50/50 signals continuing to AD, MUX/DMX, and WSS units. Because the incoming signal is split into two separate signals, there are two sets of input and output EXP ports (2 and 3).
LINE-{{1-30}}-{{1-6,12-17}}-{{2}}- {RX,TX} (COM)	
LINE-{{1-30}}-{{1-6,12-17}}-{{3}}- {RX,TX} (EXP to other ring)	
LINE-{{1-30}}-{{1-6,12-17}}-{{1-3}}-A LL	

## 25.18 LINEWL



### Note

The LINEWL AID does not apply to the Cisco ONS 15310 MA SDH platform.

The line wavelength (LINEWL) AID identifies a wavelength channel included in any of the lower layer OTS facilities. [Table 25-19](#) lists the LINEWL AIDs and their descriptions.

**Table 1:**

Values	Description
LINEWL[-{{1-30}}]-{{1-6,8,10,12-17}}- ALL	All the Optical Channels representing single wavelength inside OTS facility of a card configured in specified slot. The format is LINE-[SHELF]-[SLOT]-ALL
LINEWL[-{{1-30}}]-{{1-6,12-17}}-{{1}}- {RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS facility in a 32-DMX-O, 32-DMX, 32-DMX-L, 32-MUX-O, 40-DMX-C, 40-MUX-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.

Table 1:

Values	Description
LINEWL[-{1-30}]-{1-6,12-17}-{1}- {RX,TX}-<wlen>	The Optical Channel representing single wavelength inside OTS facility in a 32-DMX-O, 32-DMX, 32-DMX-L, 32-MUX-O, 40-DMX-C, 40-MUX-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-30}]-{1-6,12-17}-{1-3}- {RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS facility in a 32-WSS, 40-WSS-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-30}]-{1-6,12-17}-{1-3}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS facility in a 32-WSS, 40-WSS-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-30}]-{1-6,12-17}-{1-2}- {RX,TX}-ALL	All the Optical Channel representing single wavelength inside OTS facility in a AD-1B, AD-4B, AD-1C, AD-2C, AD-4C units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL
LINEWL[-{1-30}]-{1-6,12-17}-{1-2}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS facility in a AD-1B, AD-4B, AD-1C, AD-2C, AD-4C units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN]
LINEWL[-{1-30}]-{1-6,12-17}-{1-2}- {RX,TX}-ALL	All the Optical Channel representing single wavelength inside OTS (COM=1, DC=2) facility in an OPT-PRE unit. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL
LINEWL[-{1-30}]-{1-6,12-17}-{1-2}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, DC=2) facility in a OPT-PRE units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-<WLEN>
LINEWL[-{1-30}]-{1-6,12-17}-{1,3}- {RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C, and OPT-AMP-23-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-30}]-{1-6,12-17}-{1,3}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C, OPT-AMP-23-C, and OPT-AMP-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-30}]-{1-6,12-17}-{1,3,4}- {RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3, DC=4) facility in a OPT-AMP-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-30}]-{1-6,12-17}-{1,3,4}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3, DC=4) facility in a OPT-AMP-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].

Table 1:

Values	Description
LINEWL[-{1-30}]-{1-6,12-17}-{1}- {RX,TX}-ALL	All the Optical Channel representing single wavelength inside OTS (COM) facility in a OPT-PRE units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL
LINEWL[-{1-30}]-{1-6,12-17}-{1}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM) facility in a OPT-PRE units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN]
LINEWL[-{1-30}]-{1-6,12-17}-{1,3}- {RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C and OPT-AMP-23-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-30}]-{1-6,12-17}-{1,3}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C and OPT-AMP-23-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-30}]-{1-6,12-17}-{1-2}- {RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=2) facility in a OSC-CSM cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-30}]-{1-6,12-17}-{1-2}- {RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=2) facility in a OSC-CSM cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL- [{1-30}]-{1-5,12-16}-{1-8}-{RX}-< WLEN>	The Optical Channel representing single wavelength inside EXP lines from all the other WXC cards in a 40-WX-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL- [{1-30}]-{1-5,12-16}-{9}-{RX,TX}- <WLEN>	The Optical Channel representing single wavelength inside ADD/DROP line to/from MUX/DMX/WSS cards in a 40-WXC-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL- [{1-30}]-{1-5,12-16}-{10}-{TX}-< WLEN>	The Optical Channel representing single wavelength inside EXP line in a 40-WXC-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL- [{1-30}]-{1-5,12-16}-{11}-{RX,TX}- <WLEN>	The Optical Channel representing single wavelength inside COM line in a 40-WXC-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].

## 25.19 LNKTERM



### Note

The LNKTERM AID does not apply to the Cisco ONS 15310 MA SDH platform.



Link Termination (LNKTERM) AIDs are used to access the termination points of a provisionable patchcord. [Table 25-18](#) lists the LINKTERM AIDs and their descriptions.

**Table 25-18** LNKTERM

Pattern	Description
ALL	Indicates all the provisionable patchcord terminations on a node. Applicable only for the retrieve commands.
LNKTERM-ALL	Indicates all the provisionable patchcord terminations on a node. Applicable only for the retrieve commands.
LNKTERM-{1-65535}	Indicates a single provisionable patchcord termination point on a node.

## 25.20 MSSPR



**Note**

The MSSPR AID does not apply to the Cisco ONS 15310 MA SDH platform.

Multiplex section-shared protection ring (MSSPR) AIDs are used to access the MS-SPRing of the NE. [Table 25-19](#) lists the MSSPR AIDs and their descriptions.

**Table 25-19** MSSPR

Pattern	Description
ALL	ALL AID for all MS-SPRings in the NE. The ALL AID is applicable for retrieve-only commands like RTRV-MSSPR, RTRV-ALM-MSSPR, and RTRV-COND-MSSPR.
MSSPR-RINGID	MS-SPRing AID is keyword MS-SPRing followed by RINGID. RINGID is a string of up to 6 characters; valid characters are [A-Z,0-9] (case insensitive).

## 25.21 OPM



**Note**

The OPM AID does not apply to the Cisco ONS 15310 MA SDH platform.

Optical power monitoring (OPM) AIDs represent the single wavelength inside an optical power monitoring object. [Table 25-20](#) lists the OPM AIDs and their descriptions.

**Table 25-20** OPM

Values	Description
ALL	The first ID represents the shelf, the second ID represents the slot, and the last ID is the wavelength, represented in the form of 15xx.yy nanometers. The last index of the wavelength is the value of the wavelength as described in OPTICAL_WLEN.
OPM[-{1-30}]-{1-5,12-16}-ALL	
OPM[-{1-30}]-{1-5,12-16}-<WLEN>	

## 25.22 OSC


**Note**

The OSC AID does not apply to the Cisco ONS 15310 MA SDH platform.

Optical service channel (OSC) AIDs are used to access the OSC of the NE. [Table 25-21](#) lists the OSC AIDs and their descriptions.

**Table 25-21** OSC

Values	Description
ALL	All of the OSCs of the NE. The ALL AID applies to the retrieve-only commands.
OSC-RINGID	RINGID is a string of up to six characters, valid characters are [A–Z,0–9] (case insensitive).

## 25.23 PRSLOT

Valid protection slots for the electrical cards. Format is SLOT-[SLOT]. [Table 25-22](#) lists the PRSLPT AIDs and their descriptions.

**Table 25-22** PRSLOT

Pattern	Description
NULL	Indicates there is no protection group. Used when trying to delete a protection group.
SLOT-1	Slot 1 of an NE. (Cisco ONS 15310 MA SDH)
SLOT-2	Slot 2 of an NE. (Cisco ONS 15310 MA SDH)
SLOT-3	Slot 3 of an NE.
SLOT-5	Slot 5 of an NE. (Cisco ONS 15310 MA SDH)
SLOT-6	Slot 6 of an NE. (Cisco ONS 15310 MA SDH)
SLOT-13	Slot 13 slot of an NE.
SLOT-15	Slot 15 slot of an NE.
SLOT-17	Slot 17 slot of an NE.

## 25.24 RFILE

[Table 25-23](#) lists the file transfer type (RFILE) AIDs and their descriptions.

**Table 25-23** RFILE

Pattern	Description
RFILE-DB	Transferring the provisioning database.
RFILE-LOG	Transferring a log file

**Table 25-23** *RFILE*

RFILE-PKG	Transferring a software package
RFILE-COMPDB	Transferring the provisioning and system database.

## 25.25 SHELF

The SHELF AID identifies a shelf within a node. SHELF is applicable only to nodes that are set to MULTISHELF or MULTISHELFETH mode. [Table 25-24](#) lists the SHELF AIDs and their descriptions.

**Table 25-24** *SHELF*

Pattern	Description
SHELF-ALL	All of the shelves in the node.
SHELF-{1-30}	A specific shelf in the node (shelf 1 through 30).

## 25.26 SYN

[Table 25-25](#) lists the synchronization (SYN) AIDs and their descriptions.

**Table 25-25** *SYN*

Pattern	Description
SYNC[-{1-30}]-ALL	ALL synchronization AID (values 1 to 8 is the shelf ID).
SYNC[-{1-30}]-NE	NE synchronization AID (values 1 to 8 is the shelf ID).
SYNC[-{1-30}]-{BITS1,BITS2}	BITS synchronization AID (values 1 to 8 is the shelf ID).

## 25.27 SYN\_SRC

[Table 25-26](#) lists the synchronization source (SYN\_SRC) AIDs and their descriptions.

**Table 25-26** *SYN\_SRC*

Pattern	Description
BITS-1	Synchronization source is BITS-1. Format is BITS-[PORT].
BITS-2	Synchronization source is BITS-2. Format is BITS-[PORT].
FAC-{1-4,11-14}-{1-4}	Synchronization source is the 4-port STM64. Format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}-{1-4}	Synchronization source is the optical card (STM1 and STM4) facility. Format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}-{1}	Synchronization source is the optical card (STM1 and STM4) facility. Format is FAC-[SLOT]-[PORT].

Table 25-26 SYN\_SRC (continued)

Pattern	Description
FAC-{5,6,12,13}-{1}	Synchronization source is the optical card (STM16 and STM 64) facility. Format is FAC-[SLOT]-[PORT].
INTERNAL	Set the SYN_SRC to be the system default value. This value is only applied for the SYNC-NE AID on the ED-SYNCN command.
NONE	Set the SYNC_SRC value to the default value for BITS-OUT. This value only applies to the BITS-1 and BITS-2 AID of the ED-SYNCN command.
SYNC-NE	SYNC-NE source. It is only used in the alarm report or alarm retrieve commands.

## 25.28 SYNC\_REF

Table 25-27 lists the synchronization reference (SYNC\_REF) AIDs and their descriptions. The Facility AID formats for line timing are as follows:

- Format for optical facilities without PPM: FAC-[SLOT]-[PORT]
- Format for optical facilities with PPM: FAC-[SLOT]-[PPM]-[PORT]

Table 25-27 SYNC\_REF

Pattern	Description
ALL	Equivalent to a combination of SYNC-ALL, BITS-1, and BITS-2. This AID is valid only for the commands RTRV-SYNCN, RTRV-ALM-SYNCN, and RTRV-COND-SYNCN.
SYNC-ALL	All synchronization references.
SYNC-{1-30}-NE	NE synchronization AID.
SYNC-{1-30}-{BITS1, BITS2}	BITS1 and BITS2 synchronization AIDs.

## 25.29 SYNC\_SW

Table 25-28 lists the synchronization switch (SYNC\_SW) AIDs and their descriptions.

Table 25-28 SYNC\_SW

Pattern	Description
INT	Internal clock. The "INT" value of the SYNC_SW is only applied for the SYNC-NE AID on the OPR-SYNC_SW command.
PRI	Primary timing reference.
SEC	Secondary timing reference.

## 25.30 UDC

Table 25-29 lists the user data channel (UDC) AIDs and their descriptions F-UDC and DCC-UDC channels on the AIC-I card.

Table 25-29 UDC

Pattern	Description
ALL	ALL AID is applicable to retrieve-only commands, for example: RTRV-ALM-UDCF, RTRV-COND-UDCF, RTRV-ALM-UDCDCC, and RTRV-COND-UDCDCC. It corresponds to a superset of F-UDC and DCC-UDC AIDs.
UDC-{F,DCC}-{A,B}	F-UDC and DCC-UDC AIDs for A and B channels.

## 25.31 VC

Table 25-30 lists the virtual channel (VC) AIDs and their descriptions for the SDH frame-level AID set. The VC4 AID formats are as follows:

- For Optical Facilities: VC4-[SLOT]-[PORT]-[VC4]
- For Electrical Facilities: VC4-[SLOT]-[VC4]

The VC3 AID formats are as follows:

- For Optical Facilities: VC3-[SLOT]-[PORT]-[VC4]-[VC3]
- For Electrical Facilities: VC3-[SLOT]-[VC4]-[VC3]

The VC12 AID formats are as follows:

- For Optical Facilities: VC12-[SLOT]-[PORT]-[VC4]-[VC3]-[TUG2]-[VC12]
- For Electrical Facilities: VC12-[SLOT]-[VC4]-[VC3]-[TUG2]-[VC12]

The VC11 AID formats are as follows:

- For Optical Facilities: VC11-[SLOT]-[PORT]-[VC4]-[VC3]-[TUG2]-[VC11]
- For Electrical Facilities: VC11-[SLOT]-[VC4]-[VC3]-[TUG2]-[VC11]

Table 25-30 VC

Pattern	Description
VC11-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-4}	VC11 for the STM1E-12 card
VC11-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}	VC11 for STM4-4
VC11-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-4}	VC11 for STM1-8 card
VC11-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-4}	VC11 for STM-16 card
VC11-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-4}	VC11 for STM-4 card
VC11-{1-6,12-17}-{1-14}-1-11{1-7}-{1-2}	VC11 for the E1 card
VC11-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}	VC11 for the STM-1 card
VC12-{1-4,14-17}-{1-12}-1-{1-3}-{1-7}-{1-3}	VC12 for the STM1E-12 card
VC12-{1-4,14-17}-{1-4}-{1-4}-{1-3}-{1-7}-{1-3}	VC12 for STM4-4 card

Table 25-30 VC (continued)

Pattern	Description
VC12-{1-4,14-17}-{1-8}-1-{1-3}-{1-7}-{1-3}	VC12 for STM1-8 card
VC12-{1-6,12-17}-1-1-{1-7}-{1-2}	VC12 AIDs for the E1 card
VC12-{1-6,12-17}-1-{1-16}-{1-3}-{1-7}-{1-3}	VC12 for STM-16 card
VC12-{1-6,12-17}-1-{1-2}-{1-7}-{1-3}	VC12 for E1-42 card
VC12-{1-6,12-17}-1-{1-4}-{1-3}-{1-7}-{1-3}	VC12 for STM-4 card
VC12-{1-6,12-17}-{1-4}-1-{1-3}-{1-7}-{1-3}	VC12 for STM-1 card
VC3-{1-14,14-17}-{1-12}-1-{1-3}	VC3 for the STM1E-12 card
VC3-{1-4,14-17}-{1-4}-{1-4}-{1-3}	VC3 for the STM4-4 card
VC3-{1-4,14-17}-{1-8}-1-{1-3}	VC3 for the STM1-8 card
VC3-{1-6,12-17}-1-{1-16}-{1-3}	VC3 for the STM-16 card
VC3-{1-6,12-17}-1-{1-4}-{1-3}	VC3 for the STM-4 card
VC3-{1-6,12-17}-{1-4}-1-{1-3}	VC3 for the STM-1 card
VC3-{1-6,12-17}-{1-4}-{1-3}	VC3 AID for DS3i-N-12 and E3 card
VC4-{1-4,14-17}-1	VC4 for E1-42 card
VC4-{1-4,14-17}-1-1	VC4 for STM1-8 card
VC4-{1-4,14-17}-{1-12}-1	VC4 for the STM1E-12 card
VC4-{1-4,14-17}-{1-4}-1	VC4 for the STM4 card and VC4-4c for the STM4-4 card
VC4-{1-4,14-17}-{1-4}-{1-2}	VC4-3c for the STM4-4 card
VC4-{1-4,14-17}-{1-4}-{1-3}	VC4-2c for the STM4-4 card
VC4-{1-4,14-17}-{1-4}-{1-4}	VC4 for the STM4-4 card
VC4-{1-4,14-17}-{1-8}-1	VC4 for the STM1-8 card
VC4-{1-6,12-17}-1	VC4 for E1 card
VC4-{1-6,12-17}-1-1	VC4-4c for the STM-4 card. VC4-16c for the STM-16 card
VC4-{1-6,12-17}-1-{1,2,3,5,6,7,9,10,11,13,14,15}	VC4-2c for the STM-16 card
VC4-{1-6,12-17}-1-{1,2,4,5,6,7,9,10,13,14}	VC4-3c for the STM-16
VC4-{1-6,12-17}-1-{1,5,9,13}	VC4-4c for the STM-16 card
VC4-{1-6,12-17}-1-{1-16}	VC4 for the STM-16 card
VC4-{1-6,12-17}-1-{1-2}	VC4-3c for the STM-4 card
VC4-{1-6,12-17}-1-{1-3}	VC4-2c for the STM-4 card
VC4-{1-6,12-17}-1-{1-4}	VC4 for the STM-4 card
VC4-{1-6,12-17}-1-{1-9}	VC4-8c for the STM-16 card (454)

Table 25-30 VC (continued)

Pattern	Description
VC4-{1-6,12-17}-{1-4}	VC4 for DS3i-N-12, E3, G1000 cards
VC4-{1-6,12-17}-{1-4}-1	VC4 for STM-1 card

## 25.32 WDMANS


**Note**

The WDMANS AID does not apply to the Cisco ONS 15310 MA SDH platform.

The WDMANS AID is used to access the automatic optical node setup (AONS) application of the NE. [Table 25-31](#) lists the synchronization switch (SYNCSW) AIDs and their descriptions.

Table 25-31 WDMANS

Pattern	Description
WDMSIDE-{UNKNOWN,A,B,C,D,E,F,G,H}	Automatic MSTP node setup identifier
WDMNODE	Automatic MSTP node setup identifier

## 25.33 WLEN


**Note**

The WLEN AID does not apply to the Cisco ONS 15310 MA SDH platform.

This AID represents the single wavelength inside an external facility. If the facility is of type OTS (line), the wavelengths contained are all the available wavelengths in the node (currently 32). If the facility is of type OCH (CHAN), the wavelength is the one correspondent wavelength customized for that channel. [Table 25-32](#) lists the WLEN AIDs and their descriptions.

Table 25-32 WLEN

Pattern	Description
WLEN-{E,W}-{ADD,DROP,EXP}-{1530.33,1531.12,1531.90,1532.68,1534.25,1535.04,1535.82,1536.61,1538.19,1538.98,1539.77,1540.56,1542.14,1542.94,1543.73,1544.53,1546.12,1546.92,1547.72,1548.51,1550.12,1550.92,1551.72,1552.52,1554.13,1554.94,1555.75,1556.55,1558.17,1558.98,1559.79,1560.61,1577.44,1577.86,1578.27,1578.69,1579.10,1579.52,1579.93,1580.35,1580.77,1581.18,1581.60,1582.02,1582.44,1582.85,1583.27,1583.69,1584.11,1584.53,1584.95,1585.36,1585.78,1586.20,1586.62,1587.04,1587.46,1587.88,1588.30,1588.73,1589.15,1589.57,1589.99,1590.41,1590.83,1591.26,1591.68,1592.10,1592.52,1592.95,1593.37,1593.79}	Wavelength identifier

## 25.34 CTC Port Numbers and TL1 Aids

Table 25-33 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
OPT-RAMP-C	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
	DC-RX	7	LINE-shelf-slot-4-RX
	DC-TX	8	LINE-shelf-slot-4-TX
	RAMAN-RX	9	LINE-shelf-slot-5-RX
	RAMAN-TX	10	LINE-shelf-slot-5-TX
OPT-AMP-17C	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-AMP-C	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	7	LINE-shelf-slot-4-RX
	DC-TX	8	LINE-shelf-slot-4-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OSC-CSM	COM-RX	2	LINE-shelf-slot-1-RX
	COM-TX	3	LINE-shelf-slot-1-TX
	LINE-RX	4	LINE-shelf-slot-2-RX
	LINE-TX	5	LINE-shelf-slot-2-TX
	OSC-RX	6	LINE-shelf-slot-3-RX
	OSC-TX	7	LINE-shelf-slot-3-TX
	OC3	1	FAC-shelf-slot-1



Table 25-33 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
40 WXC	EXP-i-RX	1..8	LINE-shelf-slot-i-RX (i=1..8)
	EXP-TX	11	LINE-shelf-slot-10-TX
	ADD-RX	9	LINE-shelf-slot-9-RX
	DROP-TX	10	LINE-shelf-slot-9-TX
	COM-RX	12	LINE-shelf-slot-11-RX
	COM-TX	13	LINE-shelf-slot-11-TX
80-WXC-C	EAD-i-R/T	1..8	LINE-shelf-slot-i (i=1..8)
	ADD R/T	9	LINE-shelf-slot-9
	COM R/T	10	LINE-shelf-slot-10
	DROP-TX	11	LINE-shelf-slot-11-TX
	COM-RX	13	LINE-shelf-slot-13-RX
	EXP-TX	12	LINE-shelf-slot-12-TX
32 DMX	CHAN TX	1-32	CHAN-shelf-slot-i-TX (i=1..32)
	COM-RX	33	LINE-shelf-slot-1-RX
OPT-PRE	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	3	LINE-shelf-slot-2-RX
	DC-TX	4	LINE-shelf-slot-2-TX
40 DMX	CHAN TX	1 - 40	CHAN-shelf-slot-i-TX (i=1..40)
	COM RX	41	LINE-shelf-slot-1-RX
4 MD	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	CHAN-RX	5	CHAN-shelf-slot-3-RX
	CHAN-TX	6	CHAN-shelf-slot-3-TX
	CHAN-RX	7	CHAN-shelf-slot-4-RX
	CHAN-TX	8	CHAN-shelf-slot-4-TX
	COM-RX	9	LINE-shelf-slot-1-RX
	COM-TX	10	LINE-shelf-slot-1-TX
40 MUX	CHAN-RX	1-40	CHAN-shelf-slot-i-RX (i=1..40)
	COM-TX	41	LINE-shelf-slot-1-TX
32 DMX L	CHAN TX	1 - 32	CHAN-shelf-slot-i-TX (i=1..32)
	COM RX	33	LINE-shelf-slot-1-RX

Table 25-33 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
32 WSS L	ADD-RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	PT	33-64	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	69	LINE-shelf-slot-3-TX
	EXP-RX	66	LINE-shelf-slot-2-RX
	EXP-TX	65	LINE-shelf-slot-2-TX
	COM-RX	68	LINE-shelf-slot-1-RX
	COM-TX	67	LINE-shelf-slot-1-TX
OPT-BST	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-BST E	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-AMP L	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	7	LINE-shelf-slot-4-RX
	DC-TX	8	LINE-shelf-slot-4-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-BST L	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-2-TX
	OSC-RX	3	LINE-shelf-slot-1-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-1-RX
	LINE-TX	6	LINE-shelf-slot-2-TX

Table 25-33 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
MMU	EXPA-RX	5	LINE-shelf-slot-3-RX
	EXPA-TX	6	LINE-shelf-slot-3-TX
	EXP-RX	1	LINE-shelf-slot-1-RX
	EXP-TX	2	LINE-shelf-slot-1-TX
	COM-RX	3	LINE-shelf-slot-2-RX
	COM-TX	4	LINE-shelf-slot-2-TX
PSM	W-RX	1	LINE-shelf-slot-1-RX
	W-TX	2	LINE-shelf-slot-1-TX
	P-RX	3	LINE-shelf-slot-2-RX
	P-TX	4	LINE-shelf-slot-2-TX
	COM-RX	5	LINE-shelf-slot-3-RX
	COM-TX	6	LINE-shelf-slot-3-TX
AD 1B	BAND-RX	1	BAND-shelf-slot-1-RX
	BAND-TX	2	BAND-shelf-slot-1-TX
	EXP-RX	3	LINE-shelf-slot-1-RX
	EXP-TX	4	LINE-shelf-slot-1-TX
	COM-RX	5	LINE-shelf-slot-2-RX
	COM-TX	6	LINE-shelf-slot-2-TX
AD 1C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	EXP-RX	3	LINE-shelf-slot-1-RX
	EXP-TX	4	LINE-shelf-slot-1-TX
	COM-RX	5	LINE-shelf-slot-2-RX
	COM-TX	6	LINE-shelf-slot-2-TX
AD 2C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	EXP-RX	5	LINE-shelf-slot-1-RX
	EXP-TX	6	LINE-shelf-slot-1-TX
	COM-RX	7	LINE-shelf-slot-2-RX
	COM-TX	8	LINE-shelf-slot-2-TX

Table 25-33 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
AD 4C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	CHAN-RX	5	CHAN-shelf-slot-3-RX
	CHAN-TX	6	CHAN-shelf-slot-3-TX
	CHAN-RX	7	CHAN-shelf-slot-4-RX
	CHAN-TX	8	CHAN-shelf-slot-4-TX
	EXP-RX	9	LINE-shelf-slot-1-RX
	EXP-TX	10	LINE-shelf-slot-1-TX
	COM-RX	11	LINE-shelf-slot-2-RX
	COM-TX	12	LINE-shelf-slot-2-TX
32 WSS	ADD-RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	PT	33-64	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	69	LINE-shelf-slot-3-TX
	EXP-RX	66	LINE-shelf-slot-2-RX
	RXP-TX	65	LINE-shelf-slot-2-TX
	COM-RX	68	LINE-shelf-slot-1-RX
	COM-TX	67	LINE-shelf-slot-1-TX
40 WSS CO	ADD-RX	1-40	CHAN-shelf-slot-i-RX (i=1..32)
	PT	41-80	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	85	LINE-shelf-slot-3-TX
	EXP-RX	82	LINE-shelf-slot-2-RX
	RXP-TX	81	LINE-shelf-slot-2-TX
	COM-RX	84	LINE-shelf-slot-1-RX
	COM-TX	83	LINE-shelf-slot-1-TX
32 DMXO	CHAN-TX	1-32	CHAN-shelf-slot-i-TX (i=1..32)
	COM-RX	33	LINE-shelf-slot-1-RX
32 MUXO	CHAN RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	COM TX	33	LINE-shelf-slot-1-TX

Table 25-33 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
SMR-1	EXP-RX	1	LINE-shelf-slot-1-RX
	EXP-TX	2	LINE-shelf-slot-1-TX
	DC-RX	3	LINE-shelf-slot-2-RX
	DC-TX	4	LINE-shelf-slot-2-TX
	OSC-RX	5	LINE-shelf-slot-3-RX
	OSC-TX	6	LINE-shelf-slot-3-TX
	ADD-RX	7	LINE-shelf-slot-4-RX
	DROP-TX	8	LINE-shelf-slot-4-TX
	LINE-RX	9	LINE-shelf-slot-5-RX
	LINE_TX	10	LINE-shelf-slot-5-TX
SMR-2	DC-RX	1	LINE-shelf-slot-1-RX
	DC-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	ADD-RX	5	LINE-shelf-slot-3-RX
	DROP-TX	6	LINE-shelf-slot-3-TX
	LINE-RX	7	LINE-shelf-slot-4-RX
	LINE_TX	8	LINE-shelf-slot-4-TX
	EXP-TX-1	9	LINE-shelf-slot-5-TX
	EXP-RX-2	10	LINE-shelf-slot-6-RX
	EXP-RX-3	11	LINE-shelf-slot-7-RX
	EXP-RX-4	12	LINE-shelf-slot-8-RX

