6.1 FC_MR-4 Card Overview

For hardware specifications, see the “A.8.1 FC_MR-4 Card Specifications” section on page A-47.

The FC_MR-4 card uses pluggable Gigabit Interface Converters (GBICs) to transport non-SONET/SDH-framed, block-coded protocols over SONET/SDH. The FC_MR-4 enables four client Fibre Channel (FC) ports to be transported over SONET/SDH, encapsulating the frames using the ITU-T generic framing procedure (GFP) format and mapping them into either T1X1 G.707-based virtual concatenated (VCAT) payloads or standard contiguously concatenated SONET payloads. The FC_MR-4 card has the following features:

- Four FICON ports operating at 1 Gbps or 2 Gbps
  - All four ports can be operational at any time due to subrate support
  - Advanced distance extension capability (buffer-to-buffer credit spoofing)
- Pluggable GBIC optics
- Dual rate (1G/2G): MM (550 m) and SM (10 km)
- Single rate (1G): SX (550 m) and LX (10 km)

- SONET/SDH support
  - Four 1.0625-Gbps FC channels can be mapped into one of the following:
    SONET containers as small as STS1-1v (subrate)
    SDH containers as small as VC4-1v (subrate)
    SONET/SDH containers as small as STS-18c/VC4-6v (full rate)
  - Four 2.125-Gbps FC channels can be mapped into one of the following:
    SONET containers as small as STS1-1v (subrate)
    SDH containers as small as VC4-1v (subrate)
    SONET/SDH containers as small as STS-36c/VC4-12v (full rate)

- Frame encapsulation: ITU-T G.7041 transparent generic framing procedure (GFP-T)
- High-order SONET/SDH VCAT support (STS1-Xv and STS-3c-Xv/VC4-Xv)
- Differential delay support for VCAT circuits

Figure 6-1 shows the FC_MR-4 faceplate and block diagram.
Figure 6-1  **FC_MR-4 Faceplate and Block Diagram**

![Block Diagram](image)

### 6.1.1 FC_MR-4 Card-Level Indicators

Table 6-1 describes the two card-level LEDs on the FC_MR-4 card.

**Table 6-1  **FC_MR-4 Card-Level Indicators**

<table>
<thead>
<tr>
<th>Card-Level Indicators</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>FAIL LED (Red)</td>
<td>The red FAIL LED indicates that the card processor is not ready. Replace the card if the red FAIL LED persists.</td>
</tr>
<tr>
<td>ACT LED (Green)</td>
<td>If the ACT/STBY LED is green, the card is operational and ready to carry traffic.</td>
</tr>
<tr>
<td>ACT LED (Amber)</td>
<td>If the ACT/STBY LED is amber, the card is rebooting.</td>
</tr>
</tbody>
</table>
6.1.2 FC_MR-4 Port-Level Indicators

Each FC_MR-4 port has a corresponding ACT/LNK LED. The ACT/LNK LED is solid green if the port is available to carry traffic, is provisioned as in-service, and is in the active mode. The ACT/LNK LED is flashing green if the port is carrying traffic. The ACT/LNK LED is steady amber if the port is not enabled and the link is connected, or if the port is enabled and the link is connected but there is a SONET/SDH transport error. The ACT/LNK LED is not lit if there is no link.

You can find the status of the card ports using the LCD screen on the ONS 15454 SDH fan-tray assembly. Use the LCD to view the status of any port or card slot; the screen displays the number and severity of alarms for a given port or slot. Refer to the Cisco ONS 15454 Troubleshooting Guide for a complete description of the alarm messages.

6.1.3 FC_MR-4 Compatibility

The FC_MR-4 cards can be installed inSlots 1 to 6 and 12 to 17 when used with the XC10G and XC-VXC-10G cards. When the shelf uses the XCVT card, the FC_MR-4 can be used in only the high-speed (slots 5/6 and 12/13).

The FC_MR-4 card can be provisioned as part of any valid ONS 15454 SONET/SDH network topology, such as a path protection, bidirectional line switched ring (BLSR), or linear network topologies. The FC_MR-4 card is compatible with Software Release 4.6 and greater.

6.2 FC_MR-4 Card Modes

The FC_MR-4 card can operate in two different modes:

- Line rate mode—This mode is backward compatible with the Software R4.6 Line Rate mode.
- Enhanced mode—This mode supports subrate, distance extension, differential delay, and other enhancements.

The FC_MR-4 card reboots when a card mode changes (a traffic hit results). The Field Programmable Gate Array (FPGA) running on the card upgrades to the required image. However, the FPGA image in the card’s flash memory is not modified.

6.2.1 Line-Rate Card Mode

The mapping for the line rate card mode is summarized here.

- 1 Gbps Fibre Channel/FICON is mapped into:
  - STS-24c, STS-48c
  - VC4-8c, VC4-16c
  - STS1-Xv where X is 19 to 24
  - STS3c-Xv where X is 6 to 8
  - VC4-Xv where X is 6 to 8
- 2 Gbps Fibre Channel/FICON is mapped into:
  - STS-48c
  - VC4-16c
– STS-1-Xv where X is 37 to 48
– STS-3c-Xv where X is 12 to 16
– VC4-Xv where X is 12 to 16

6.2.2 Enhanced Card Mode

The features available in enhanced card mode are given in this section.

6.2.2.1 Mapping

1 Gbps Fibre Channel/FICON is mapped into:
– STS-1, STS-3c, STS-6c, STS-9c, STS-12c, STS-18c, STS-24c, STS-48c
– VC4-1c, VC4-2c, VC4-3c, VC4-4c, VC4-6c, VC4-8c, VC4-16c
– STS-1-Xv where X is 1 to 24
– STS-3c-Xv where X is 1 to 8
– VC4-Xv where X is 1 to 8

2 Gbps Fibre Channel/FICON is mapped into:
– STS-1, STS-3c, STS-6c, STS-9c, STS-12c, STS-18c, STS-24c, STS-36c, STS-48c
– VC4-1c, VC4-2c, VC4-3c, VC4-4c, VC4-6c, VC4-8c, VC4-12c, VC4-16c
– STS-1-Xv where X is 1 to 48
– STS-3c-Xv where X is 1 to 16
– VC4-Xv where X is 1 to 16

6.2.2.2 SW-LCAS

VCAT group (VCG) is reconfigurable when the software link capacity adjustment scheme (SW-LCAS) is enabled, as follows:

- Out-of-service (OOS) and out-of-group (OOG) members can be removed from VCG
- Members with deleted cross-connects can be removed from VCGs
- Errored members can be autonomously removed from VCGs
- Degraded bandwidth VCGs are supported
- VCG is flexible with SW-LCAS enabled (VCG can run traffic as soon as the first cross-connect is provisioned on both sides of the transport)

6.2.2.3 Distance Extension

This following list describes the FC_MR-4 card distance extension capabilities:

- Enabling of a storage access networking (SAN) extension over long distances through buffer-to-buffer (B2B) credit spoofing.
  – 2300 km for 1G ports (longer distances supported with lesser throughput)
  – 1150 km for 2G ports (longer distances supported with lesser throughput)
FC_MR-4 Card Modes

- Negotiation mechanism to identify whether a far-end FC-over-SONET card supports the Cisco proprietary B2B mechanism
- Auto detection of FC switch B2B credits from FC-SW standards-based exchange link parameters (ELP) frames
- Support for manual provisioning of credits based on FC switch credits
- Automatic GFP buffers adjustment based on roundtrip latency between two SL ports
- Automatic credits recovery during SONET switchovers/failures
- Insulation for FC switches from any SONET switchovers; no FC fabric reconvergences for SONET failures of less than or equal to 60 ms

6.2.2.4 Differential Delay Features

The combination of VCAT, SW-LCAS, and GFP specifies how to process information for data and storage clients. The resulting operations introduce delays. Their impact depends on the type of service being delivered. For example, storage requirements call for very low latency, as opposed to traffic such as e-mail where latency variations are not critical.

With VCAT, SONET paths are grouped to aggregate bandwidth to form VCGs. Because each VCG member can follow a unique physical route through a network, there are differences in propagation delay, and possibly processing delays between members. The overall VCG propagation delay corresponds to that of the slowest member. The VCAT differential delay is the relative arrival time measurement between members of a VCG. The FC_MR-4 card is able to handle VCAT differential delay and provides these associated features:

- Supports a maximum of 122 ms of delay difference between the shortest and longest paths.
- Supports diverse fiber routing for VCAT circuit.
- All protection schemes are supported (path protection, automatic protection switching [APS], 2-fiber BLSR, 4-fiber BLSR).
- Supports routing of VCAT group members through different nodes in the SONET network.
- Differential delay compensation is automatically enabled on VCAT circuits that are diverse (split fiber) routed, and disabled on VCAT circuits that are common fiber routed.

**Note**

Differential delay support for VCAT circuits is supported by means of a TL1 provisioning parameter (EXTBUFFERS) in the ENT-VCG command.

6.2.2.5 Interoperability Features

The interoperability features are as follows:

- Maximum frame size setting to prevent accumulation of oversized performance monitoring parameters for virtual SAN (VSAN) frames
- Ingress filtering disable for attachment to third-party GFP-over-SONET/SDH equipment

6.2.3 Link Integrity

The link integrity features are as follows:

- Data port disabled if upstream data port is not able to send over SONET/SDH transport
• Data port disabled if SONET/SDH transport is errored

6.2.4 Link Recovery

Link recovery has the following features:
• Reduces the impact of SONET/SDH disruptions on attached Fibre Channel equipment
• Speeds up the recovery of Inter-Switch Links (ISLs)
• Allows monitoring of B2B credit depletion due to SONET outage and full recovery of the credits, thus preventing the slow decay of bandwidth/throughput

Note Distance extension and link recovery cannot be enabled at the same time.

6.3 FC_MR-4 Card Application

The FC_MR-4 card reliably transports carrier-class, private-line Fibre Channel/FICON transport service. Each FC_MR-4 card can support up to four 1-Gbps circuits or four 2-Gbps circuits. Four 1.0625-Gbps FC channels can be mapped into containers as small as STS-1 (subrate), with a minimum of STS-18c/VC4-6v for full rate. Four 2.125-Gbps FC channels can be mapped into containers as small as STS-1 (sub-rate), with a minimum of STS-36c/VC4-12v for full rate.

The FC_MR-4 card incorporates features optimized for carrier-class applications such as:
• Carrier-class Fibre Channel/FICON
• 50 ms of switch time through SONET/SDH protection as specified in Telcordia GR-253-CORE

Note Protection switch traffic hit times of less than 60 ms are not guaranteed with differential delay in effect.

• Hitless software upgrades

Note Hitless software upgrades are not possible with an activation from Software R5.0 to Software R6.0 in enhanced card mode. This is because the FPGA must be upgraded to support differential delay in enhanced mode. Upgrades are still hitless with the line rate mode.

• Remote Fibre Channel/FICON circuit bandwidth upgrades through integrated Cisco Transport Controller (CTC)
• Multiple management options through CTC, Cisco Transport Manager (CTM), TL1, and Simple Network Management Protocol (SNMP)
• Differential delay compensation of up to 122 ms for diversely routed VCAT circuits

The FC_MR-4 payloads can be transported over the following protection types:
• Path Protection
• BLSR
• Unprotected
• Protection channel access (PCA)
The FC_MR-4 payloads can be transported over the following circuit types:

- STS
- STS\(n\)
- STS-V

**Note**

Virtual Tributary (VT) and VT-V circuits are not supported.

The FC_MR-4 card supports VCAT. See the “11.16 Virtual Concatenated Circuits” section on page 11-30 for more information about VCAT circuits.

### 6.4 FC_MR-4 Card GBICs

The FC_MR-4 uses pluggable GBICs for client interfaces. Table 6-2 lists GBICs that are compatible with the FC_MR-4 card. See the “5.12.2 GBIC Description” section on page 5-28 for more information.

**Table 6-2 GBIC Compatibility**

<table>
<thead>
<tr>
<th>Card</th>
<th>Compatible GBIC or SFP (Cisco Product ID)</th>
<th>Cisco Top Assembly Number (TAN)</th>
</tr>
</thead>
<tbody>
<tr>
<td>FC_MR-4 (ONS 15454 SONET/SDH)</td>
<td>15454-GBIC-SX</td>
<td>30-0759-01</td>
</tr>
<tr>
<td></td>
<td>15454E-GBIC-SX</td>
<td>800-06780-01</td>
</tr>
<tr>
<td></td>
<td>15454-GBIC-LX/LH</td>
<td>10-1743-01</td>
</tr>
<tr>
<td></td>
<td>15454E-GBIC-LX/LH</td>
<td>30-0703-01</td>
</tr>
<tr>
<td></td>
<td>ONS-GX-2FC-MMI</td>
<td>10-2015-01</td>
</tr>
<tr>
<td></td>
<td>ONS-GX-2FC-SML</td>
<td>10-2016-01</td>
</tr>
</tbody>
</table>