Appendix

Cisco VIM Wiring Diagrams

Figure 1: M4-Micropod with Cisco VIC

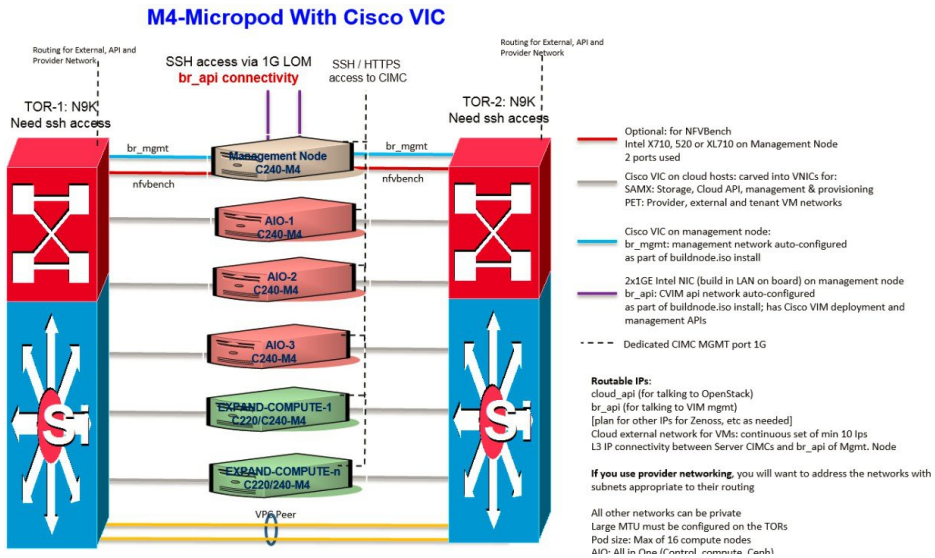

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Figure 2: M4-Full-On with Cisco VIC

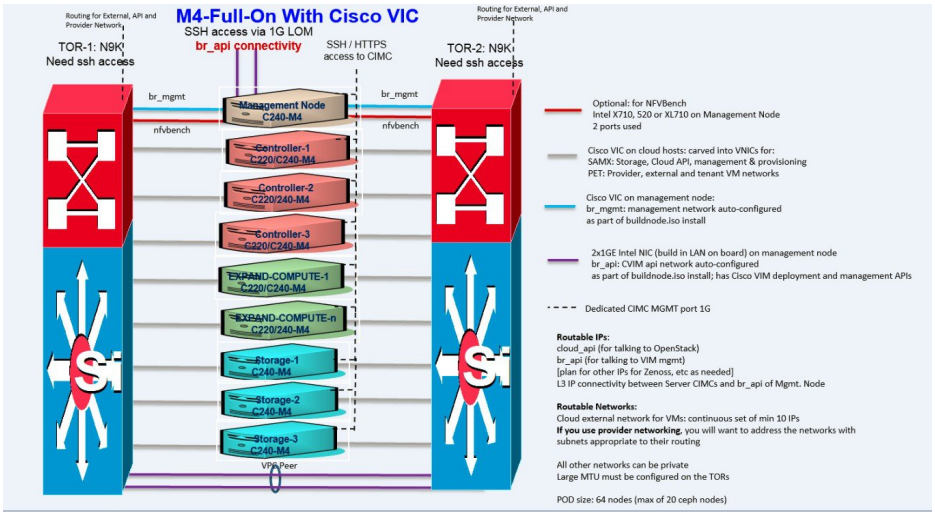
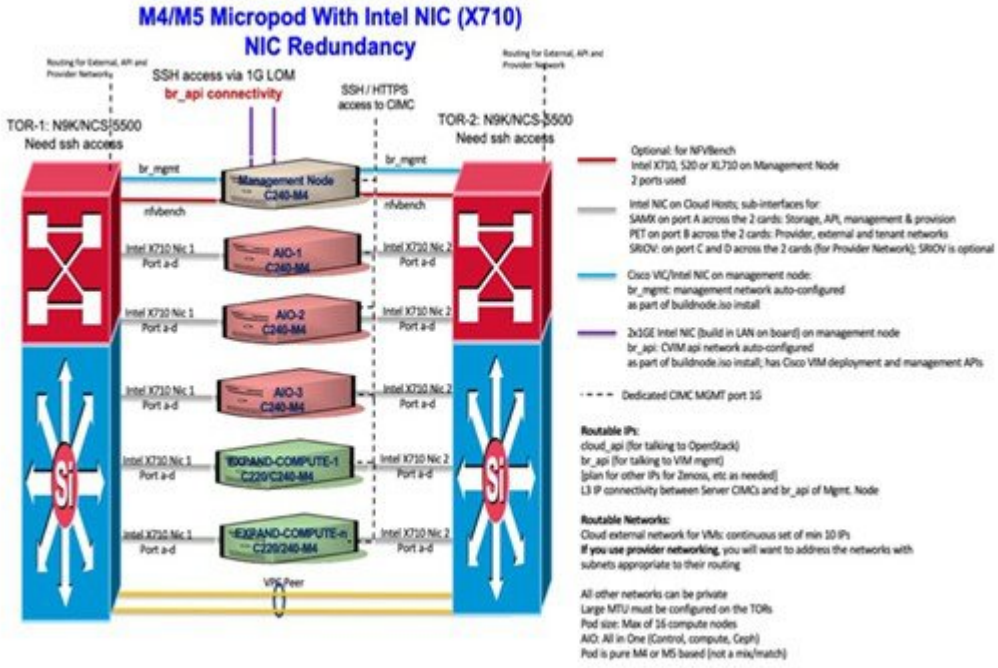


Figure 3: M4/M5 Micropod with Intel NIC (X710) - NIC Redundancy



Appendix

Figure 4: M4 Hyperconverged with Cisco VIC/NIC (1xX710) VPP based; no SRIOV

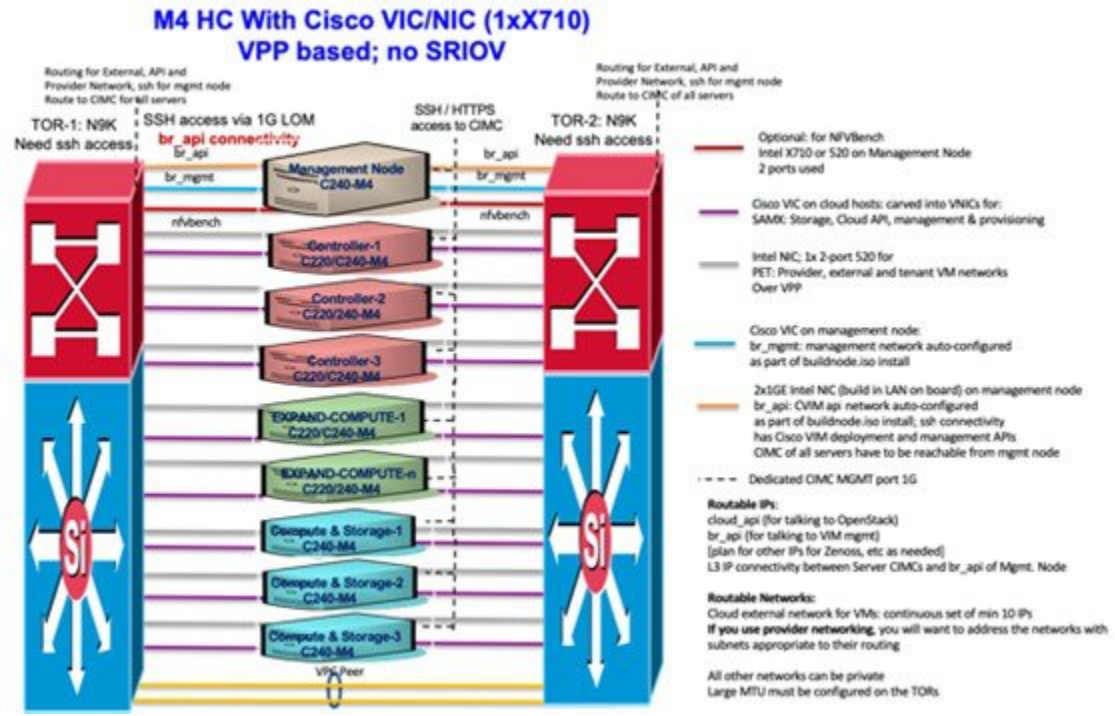
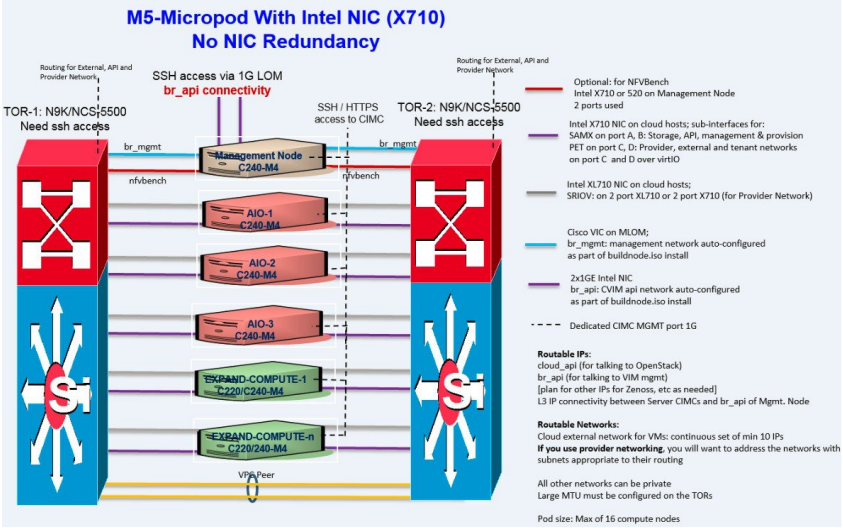


Figure 5: M5-Micropod with Intel NIC (X710) - No NIC Redundancy



Appendix

Figure 6: M4/M5 Full-On with Intel NIC (X710) and with NIC Redundancy

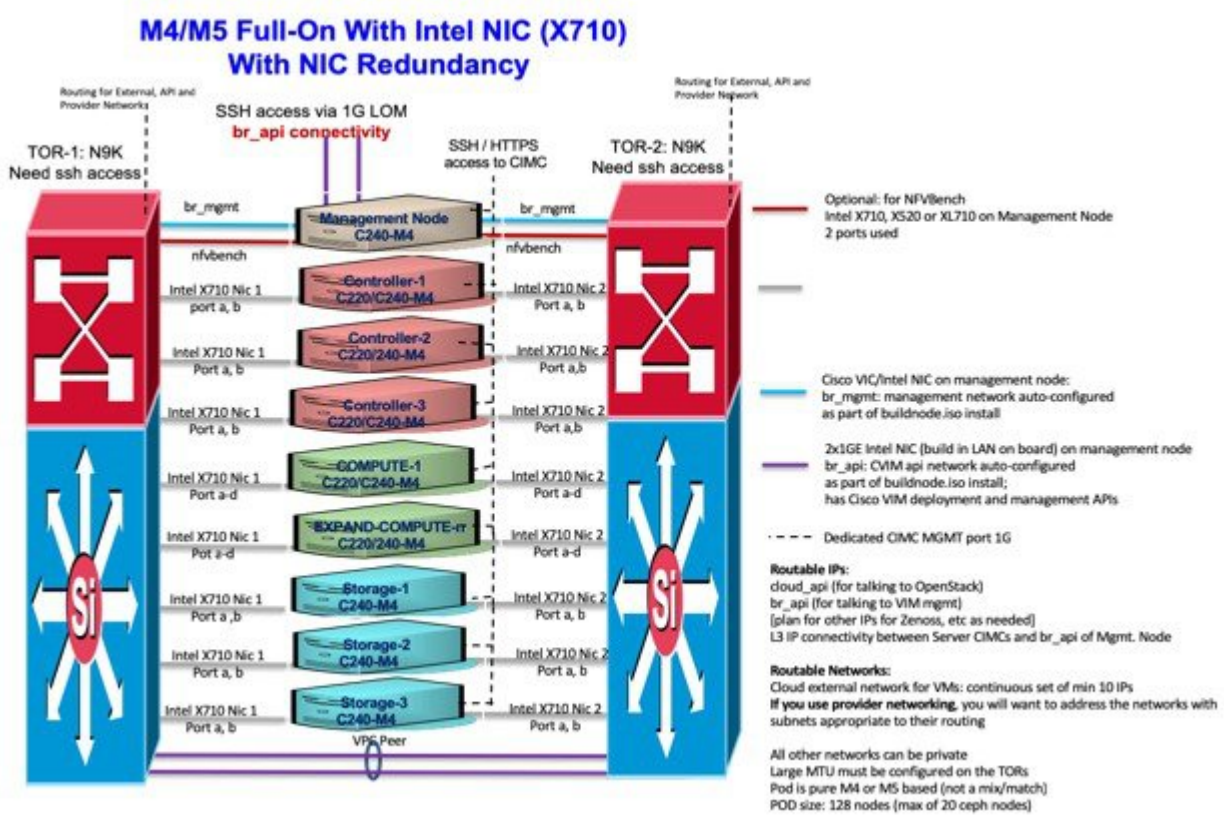


Figure 7: M4/M5 Full-On with Cisco VIC/NIC (2xXL710/2x520)

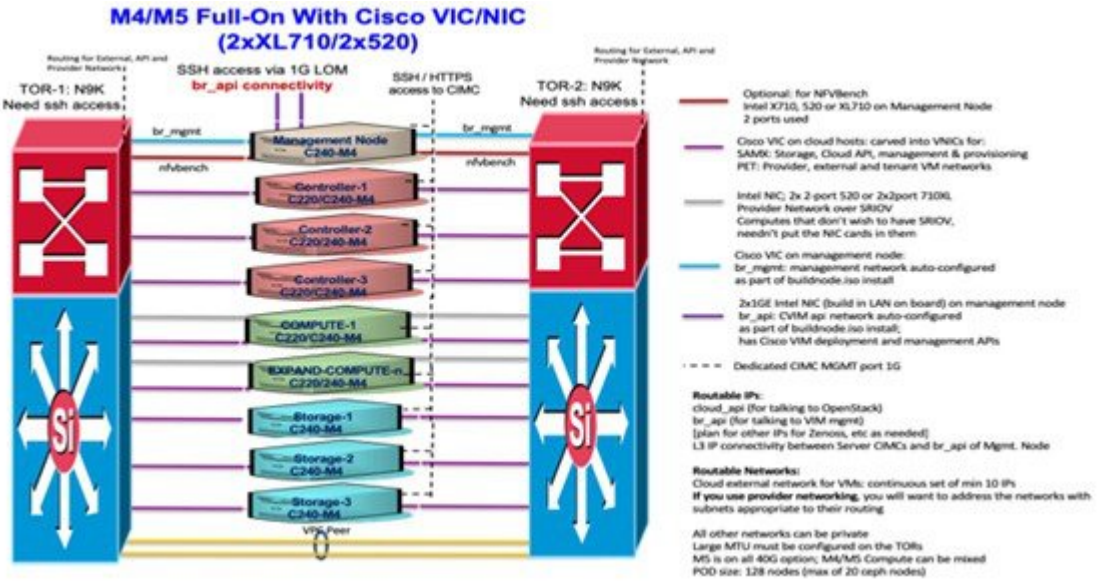


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Figure 8: M4/M5 Micropod with Cisco VIC/NIC (2xXL710/2x520)

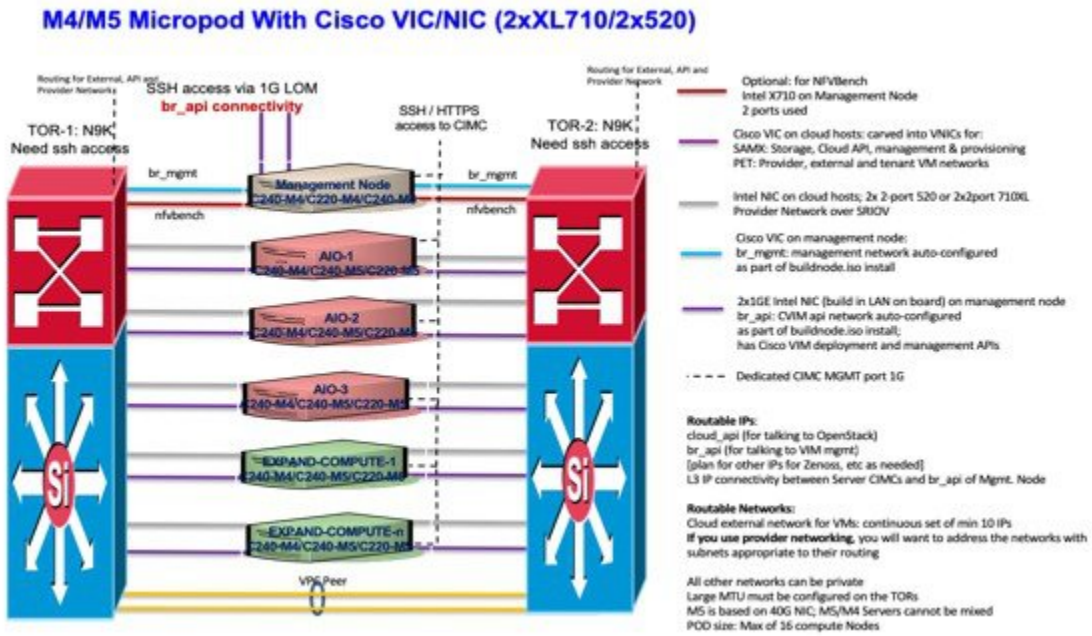
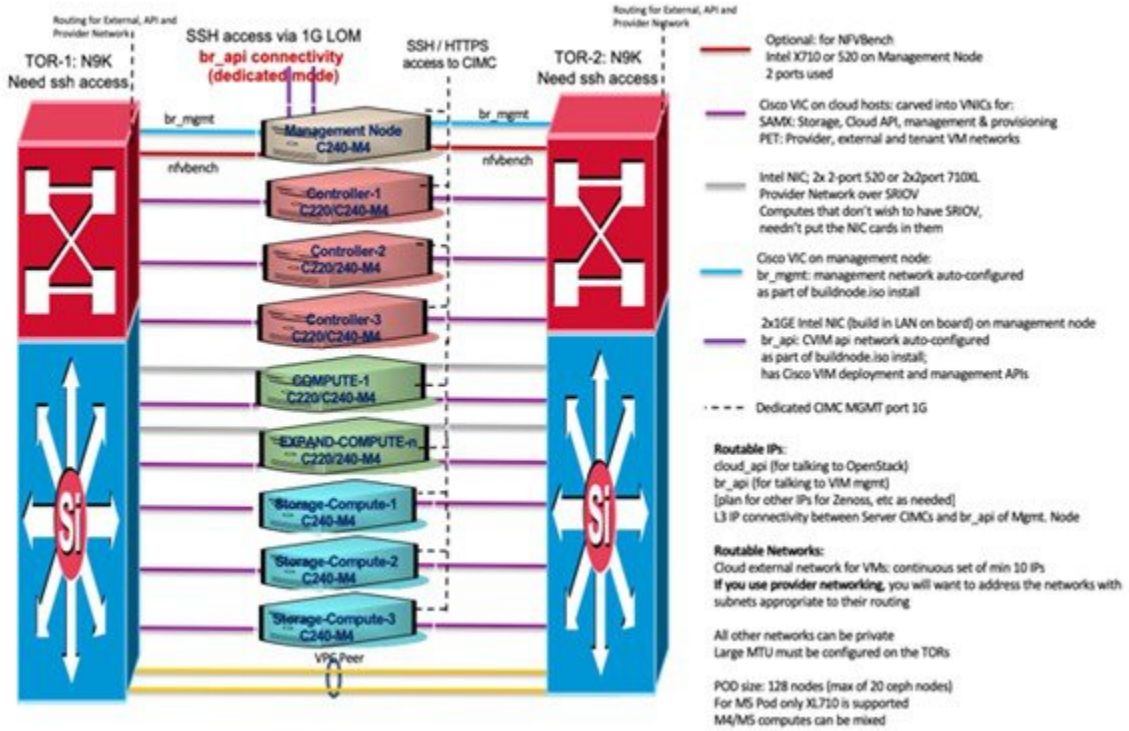


Figure 9: M4/M5-HC with Cisco VIC/NIC (2xXL710/2x520)



Appendix

Figure 10: Quanta (D52BQ-2U) Fullon Pod with 25GE Intel NIC (xxv710)

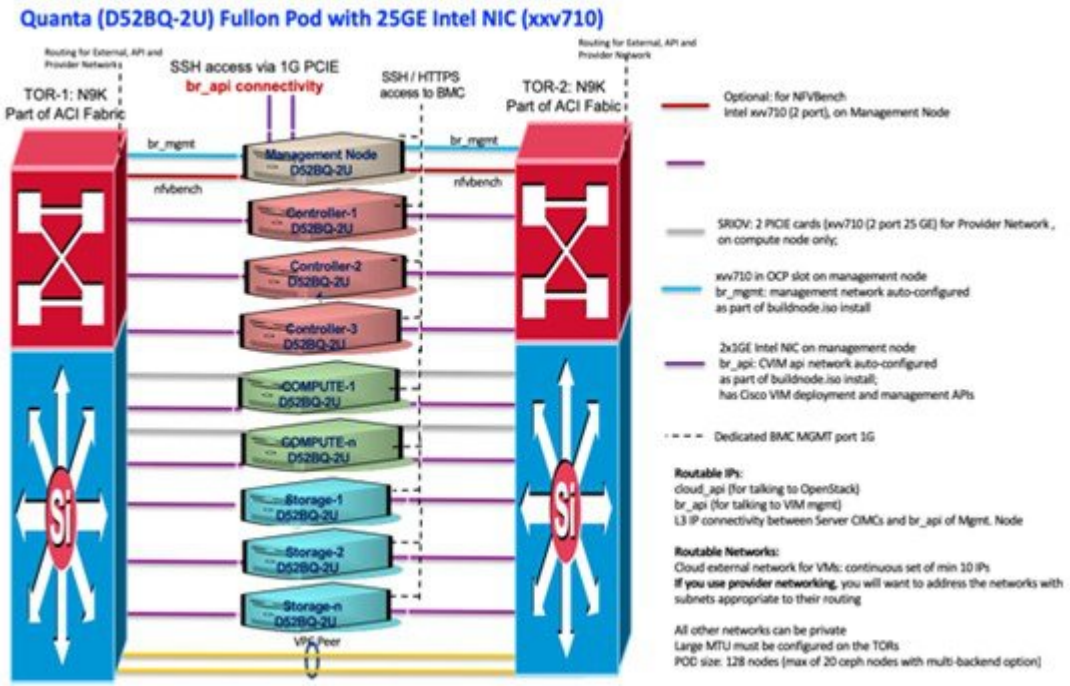


Figure 11: Quanta (D52BE-2U) Edge Pod with 25GE Intel NIC (xxv710)

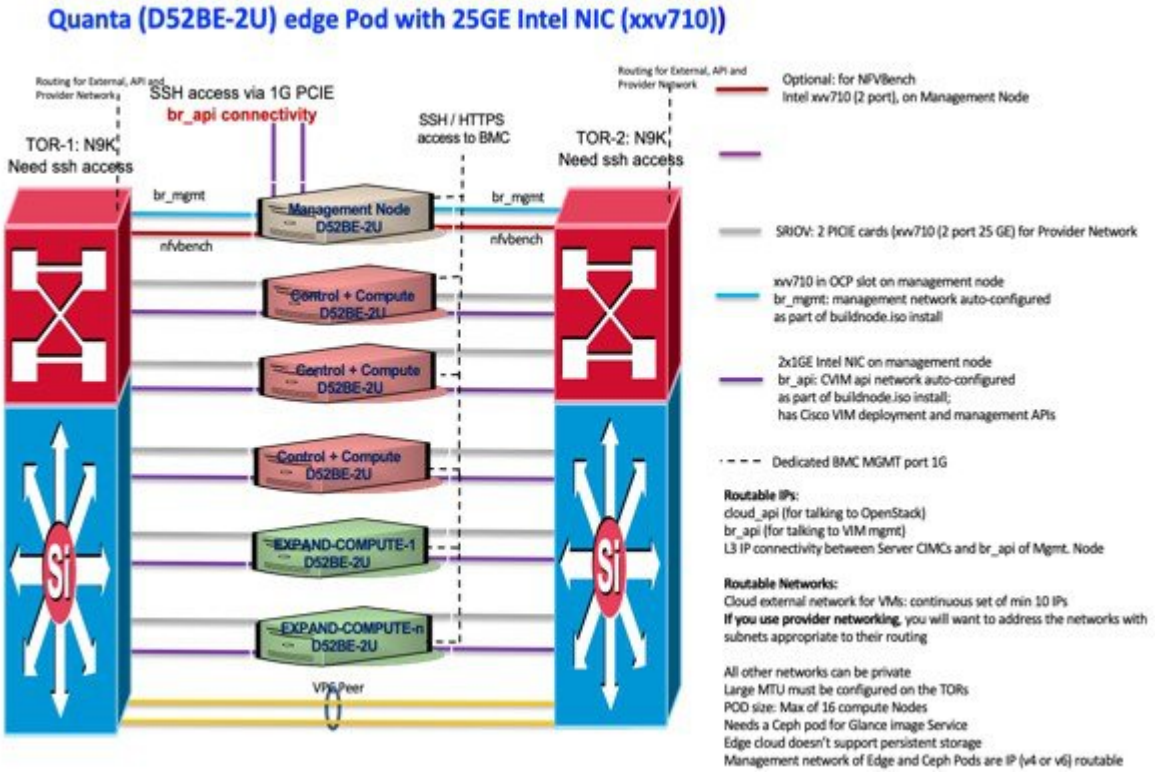
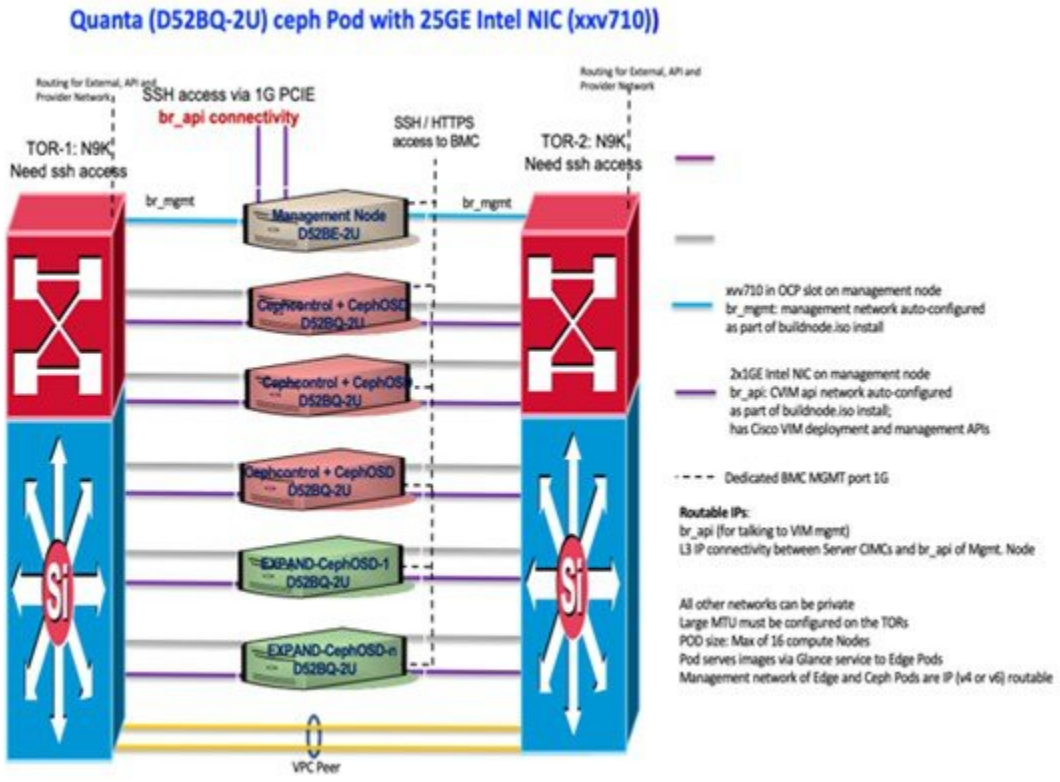


Figure 12: Quanta (D52BQ-2U) Ceph Pod with 25GE Intel NIC (xxv710)



Appendix