



## New and Changed L2VPN Features

This table summarizes the new and changed feature information for the L2VPN Configuration Guide for Cisco 8000 Series Routers, and tells you where they are documented.

- [New and Changed L2VPN Features, on page 1](#)

## New and Changed L2VPN Features

**Table 1: L2VPN Features Added or Modified in IOS XR Release 7.3.x**

Feature	Description	Changed in Release	Where Documented
CFM on VPLS	This feature was introduced.	Release 7.3.2	<a href="#">CFM on VPLS</a>
Split-Horizon Groups	This feature was introduced.	Release 7.3.2	<a href="#">Split-Horizon Groups</a>
Traffic Storm Control	This feature was introduced.	Release 7.3.2	<a href="#">Traffic Storm Control</a>
Transparent Layer 2 Protocol Tunneling	This feature was introduced.	Release 7.3.2	<a href="#">Transparent Layer 2 Protocol Tunneling</a>
Virtual Private LAN Services	This feature was introduced.	Release 7.3.2	<a href="#">Virtual Private LAN Bridging Services</a>
GTP Load Balancing	This feature was introduced.	Release 7.3.2	<a href="#">GTP Load Balancing</a>
Inter-AS Mode for L2VPN Pseudowire	This feature was introduced.	Release 7.3.15	<a href="#">Inter-AS Mode</a>
Load Balance MPLS PW Traffic using Control-Word	This feature was introduced.	Release 7.3.15	<a href="#">Load Balance MPLS PW Traffic using Control-Word and Flow-Label</a>

<b>Feature</b>	<b>Description</b>	<b>Changed in Release</b>	<b>Where Documented</b>
Load Balance MPLS PW Traffic using Flow-Label	This feature was introduced.	Release 7.3.15	<a href="#">Load Balance MPLS PW Traffic using Control-Word and Flow-Label</a>
Pseudowire over MPLS	This feature was introduced.	Release 7.3.15	<a href="#">Pseudowire over MPLS</a>
Pseudowire VC Type 4	This feature was introduced.	Release 7.3.15	<a href="#">VLAN Mode</a>
Pseudowire VC Type 5	This feature was introduced.	Release 7.3.15	<a href="#">Ethernet Port Mode</a>
Support of Tagged or Untagged VLAN on Physical Rewrite	This feature was introduced.	Release 7.3.15	<a href="#">Configure Local Switching Between Attachment Circuits</a>