Next-Gen Smart Switching

New products and high-availability features

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Cisco powers how people and technology work together across the physical and digital worlds

Al-ready data centers

Transform data centers to power AI workloads anywhere

Future-proofed workplaces

Modernize everywhere people and technology work and serve customers

Secure global connectivity

Digital resilience

Keep the organization securely up and running in the face of any disruption

Accelerated by Cisco Al

Agenda

- 1. Smart Switching Announcements
- 2. Security Infused into the Network
- 3. Highly Resilient Architectures

Smart Switching



The explosion of new devices, OT endpoints and Al applications in campus





Apple Intelligence



Smart Lighting



4K/8K Cameras



Manufacturing Arm



Wearables



Healthcare Devices

Enterprise AI Assistants



Wi-Fi7 pushes the limits

Multigigabit is the new 1G for today's Wi-Fi

And PoE requirements are increasing



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New threats with Al and Quantum

Al brings an explosion of new traffic patters across the network that can rapidly change.

Quantum means our standard encryption mechanisms will no longer be sufficient.

Security Needs for Al







Policy Orchestration



Optimized Traffic Flow

Security Needs for Quantum Computing



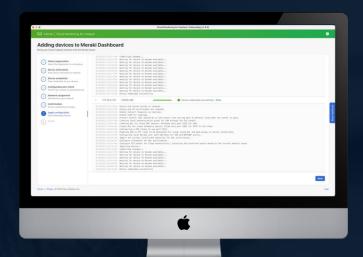
PQC Signatures



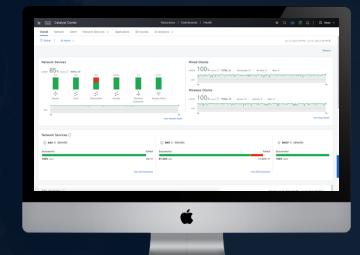
PQC Encryption
(Harvest Now, Decrypt Later)



Trustworthy Systems







Cloud

CLI/API Access

On-prem



C9000 Switch

Choice of Management Platform

Unified Hardware – Unified Licensing



The Cisco C9000 Series Smart Switches

Powerful, secure, easy to manage

Cisco Silicon One - industry's High-performance, most powerful, programmable programmable ASIC x86 CPU

Cisco IOS XE w/ secure boot & Trustworthy Solution PQC-Ready

Universal PoE+ & PoE Analytics

Switch power optimization & FMP-ready

On-device ThousandEyes App hosting





1.6 Tbps

Stacking bandwidth

4 x 100G

Uplink Modules <5 µs

Latency

4x

Higher MAC & ARP density

Cisco C9610 Smart Switch

25.6 Tbps

System bandwidth

256 x 100G

Modular Line-Cards <5 µs

Latency

10 slot

To power the core





No plans to announce EoL on any Catalyst 9000 switches

Statement of Direction



Introducing the Cisco C9350

Access Smart Switches for the AI era

17.18.1



Unmatched Scale & Performance

48 x mGig* (10G)

12 Member, 1.6 Tbps Stacking

48 x 90W Full UPoE+

Industry Leading Resiliency

Enhanced xFSU

Enhanced Stackpower+

Enhanced Stacking

Integrated Security

Quantum Resistant

Cisco Live Protect

Hypershield Ready

Cisco C9350 Models

mGig Downlinks (18.6" D)



Cisco C9350-48HX 48x 10G-mGig & 90W UP0E+



Cisco C9350-48TX 48x 10G-mGig Data-Only

1G Downlinks (15.1" D)



Cisco C9350-48U



Cisco C9350-24U 24x 1G & 60W UPOE



Cisco C9350-48P



Cisco C9350-24P



Cisco C9350-48T 48x 1G Data-Only



Cisco C9350-24T

Network Modules



C9350-NM-4C 4x 100G/40G QSFP28



C9350-NM-2C 2x 100G/40G QSFP28



C9350-NM-8Y 8x 25G/10G/1G SFP28 (50G on top 4 ports)

Power Supplies



1600W ACTitanium Rated, Port Intake



850W AC

Titanium Rated, Port Intake



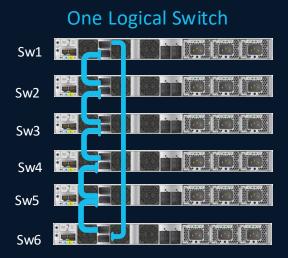
500W ACTitanium Rated, Port Intake

ahaha

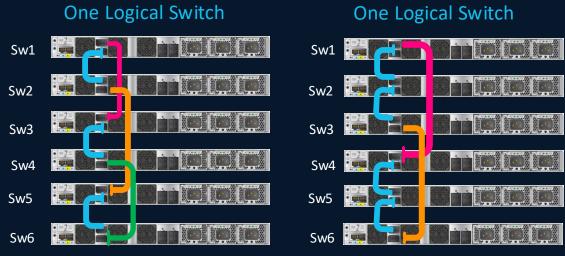
CISCO

New Stacking Architecture

Built for the new generation of campus switching



Typical Stack Cables Configurations



New Stack Cables Configurations Options

8/12* Member StackingWith **1.6T** stacking capacity

User friendly stacking design

Eliminate mandatory long stack cable requirement

Standard Ethernet Stacking

based on SPF (Shortest Path First) / VxLAN

Point-to-point architecture

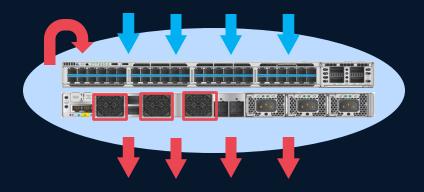
for low latency and optimized path selection

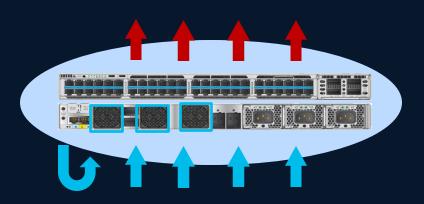
Sub-Second Stateful Switchover with SSO & NSF

Isolated bandwidth loss in event of failure



C9350 New Modular Fan Modules





Front-to-Back Airflow: Port Side Intake (PSI)

- Ideal for enclosed racks in wiring closets, ensuring front intake cooling.
- Red Handle(C9350-FAN-I)
- Supported on All SKU's



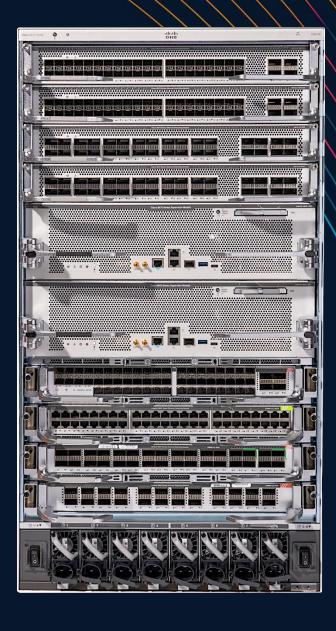


Back-to-Front Airflow: Port Side Exit (PSE)

- Suited for open-rack MDF/IDF rooms, directing airflow toward rear ventilation.
- Blue Handle(C9350-FAN-E)
- Supported on Data/Fiber SKU's



Cisco C9610 Series Core Smart Switches





Introducing C9610

Generation 3 modular core – New Hardware launch

Gen 3 Supervisors



C9610-SUP3XL/3



Enterprise Focus K100/E100



2M IPv4 / 1M IPv6

3.2 Tbps per slot

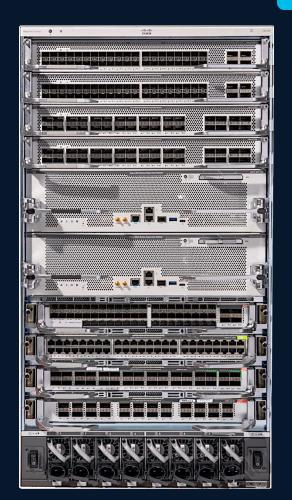
512K hosts or clients*

32G DDR4 memory

256K ACL entries*

8 core X86 CPU @2.0 Ghz

*Hardware Capable



Gen 3 Chassis

51.2 Tbps System Bandwidth

8-line card slots (1.25 RU)

2 supervisor slots (2.5 RU)

4 serviceable fan trays in rear side

Blue Beacons (system/fan tray, sup, line cards)

8 Modular power supplies

Gen 3 Line cards



C9610-LC-40YL4CD (40 x 25/50GE, 2 x 400GE, 2 x 200GE)



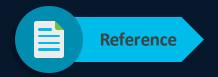
C9610-LC-32CD (30 x 40/100GE, 2 x 100/400GE)



4x FAN Trays with Front to Back Airflow



Cisco C9610R Chassis Port Density (Without breakout)



Port speed	Density with C9606 Sup 2		Density with C9610 Sup3/3XL
400G	8		16
200G	8**		16**
100G	128		256
50G	224	27	448
40G	128	2X	256
10/25G fiber	224		448
10G Copper	192		384
1G	8 (192 with Sup1)		384*



** 200G Hardware capable



^{*} Native 1G

Security fused into the Network



Security Fused into the Network



Securing application access over the WAN Protecting user access and application interactions

SASE/UZTNA

Continuous identity, posture and risk assessment across every session



Securing network connectivity
Safeguarding and optimizing network connections

Quantum-resistant:

- MACsec
- IPsec
- WAN MACsec



Securing network access
Securing connectivity to the network

Authorization and scalable segmentation

- Identity services engine (ISE)
- Software defined access (SDA) and Security Group Tags (SGT)
- NGFW

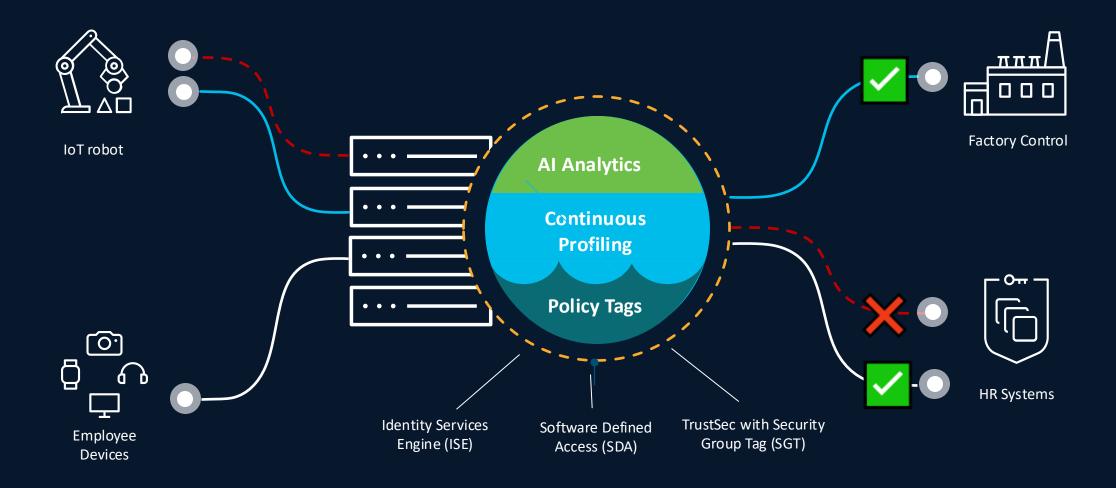


Securing the device
Protecting and ensuring compliance of devices

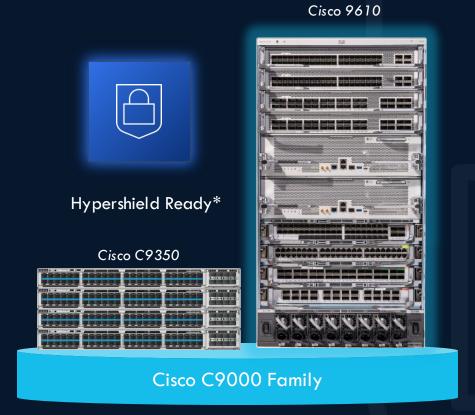
Quantum-resistant secure boot Compensating controls

Cisco as a Leader for Fabric Architectures

Scalable microsegmentation to protect every connection



Hypershield in the Campus



Stateful Inspection and Control

- Security close to the source and at Network Edge

Firewall Embedded in Traffic Flows

- Reduced Infrastructure Complexity for Effective Security

Simplified Operations

- Centralized Policy and Firewall Management

Hypershield enables distributed stateful FW enforcement

What about code vulnerabilities?

Patching takes time

Software Maintenance Upgrade (SMU)

5-6 weeks turnaround

May require reload

Limited coverage

Needs an available fix for CVE

Release-specific



Compensating Controls

Turnaround in days for high priority

Never requires reload

Can cover ~25% non-SMUable

Blocks CVE even when fix unavailable

Works across releases



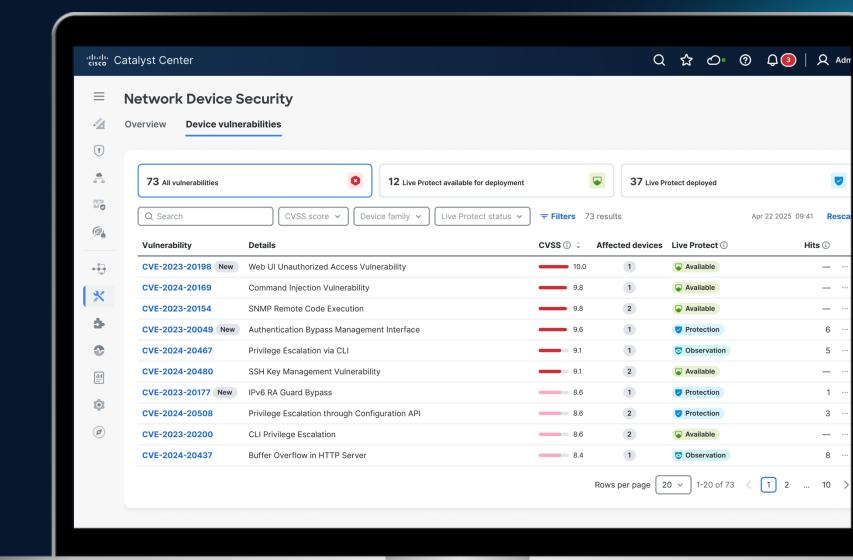
Live Protect

Mitigate new threats in nearreal time

Leveraging kernel-level compensating controls

Without upgrading the image or rebooting the device

Shown in CatC 3.x



V

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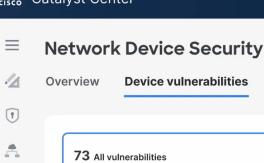
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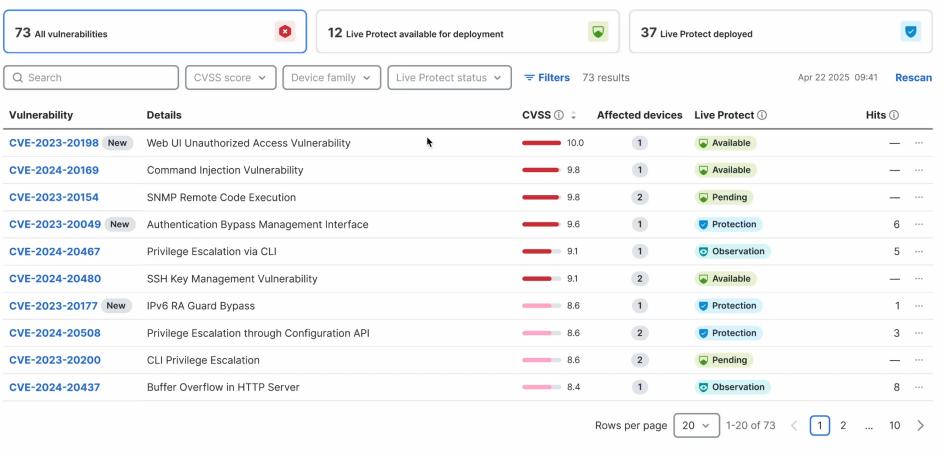
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Highly Resilient Architectures

Catalyst 9000: Redundancy at Every Layer

Platform Redundancy Hardware + PS + Fan Design Redundancy SVL + GIR + EVPN

Software Redundancy
Upgrades + Patching

9300: StackWise, StackPower

9400/9600: Dual Supervisor, Power

Supply, & Fan

9500: Power Supply & Fan

9300/9400/9500/9600 : Graceful

Insertion and Removal (GIR)

9400/9500/9600: StackWise Virtual (SVL)

9300/9400/9500/9600: EVPN

Multihoming

9300: Extended Fast Software Upgrade

(xFSU)

9400/9600: ISSU (Standalone + StackWise

Virtual)

9500: ISSU with StackWise Virtual

9300/9400/9500/9600: SMU Hot/Cold

Patching

Minimize traffic impact across all layers of redundancy



Extended Fast Software Upgrade (xFSU) Catalyst 9300/9350



Extended Fast Software Upgrade (xFSU) with Traffic Downtime under 5 Seconds

Wide Deployment Flexibility

- Supported on Catalyst 9300 series switches
 Works on both standalone and stack setups
- Supported in Multiple Network Topologies
- No Topology Modifications Needed

Supported Protocols

STP with RSTP and MSTP

Per VLAN Spanning Tree (PVST)

Static L2 Port Channels (Mode on)

UDLD

L2/L3 LACP

OSPFv2 or OSPFv3

IS-IS

Flexible NetFlow

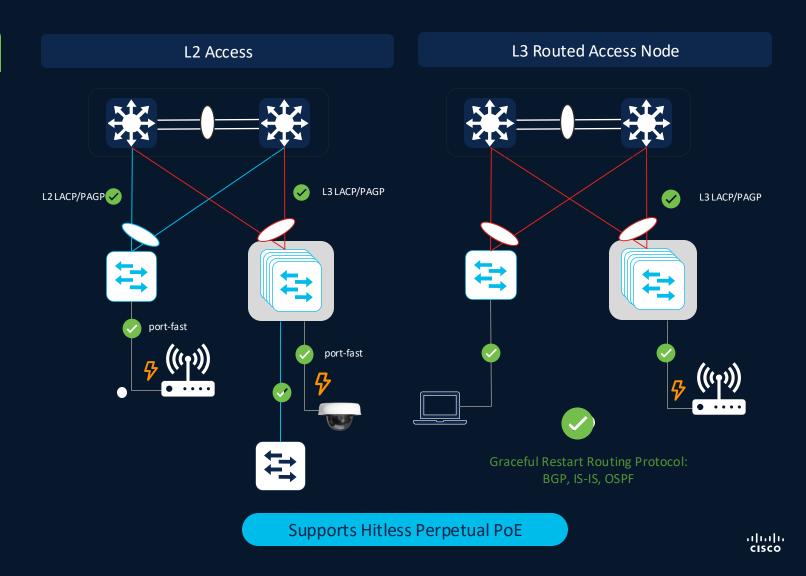
QoS

IEEE 802.1X Authentication

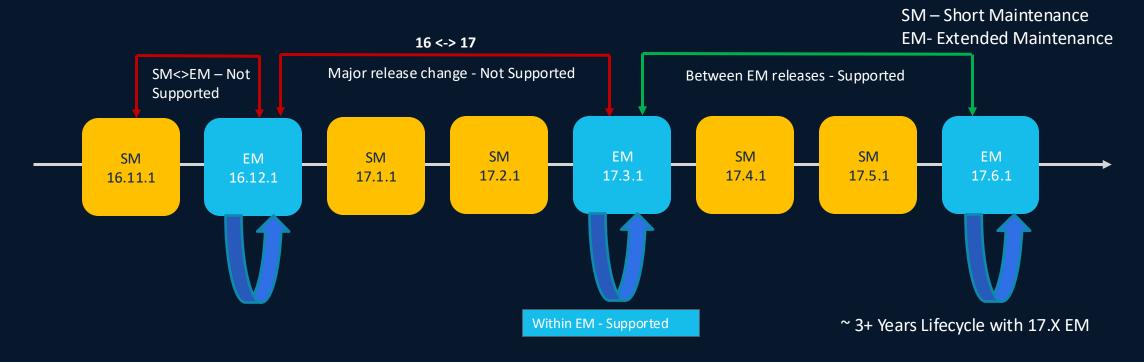
MAC Authentication Bypass

Web Authentication

Non-Fabric VRF



ISSU/xFSU Release Guidelines



Supported

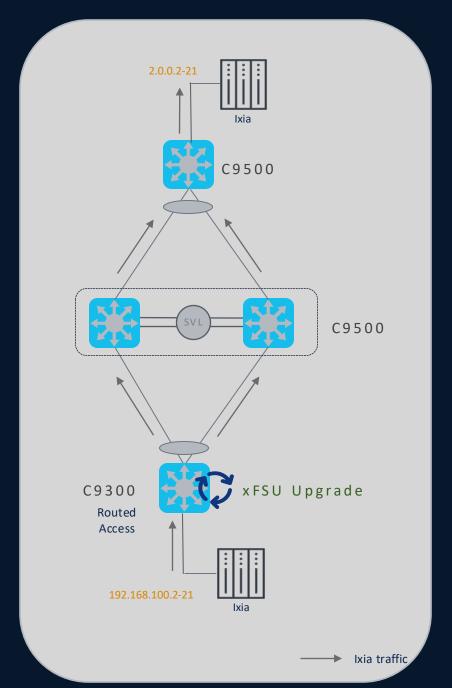
- Within EM Release Any to Any within the EM Release (16.12.X)
- Between EM Releases but within major releases [within + 2 EM releases]

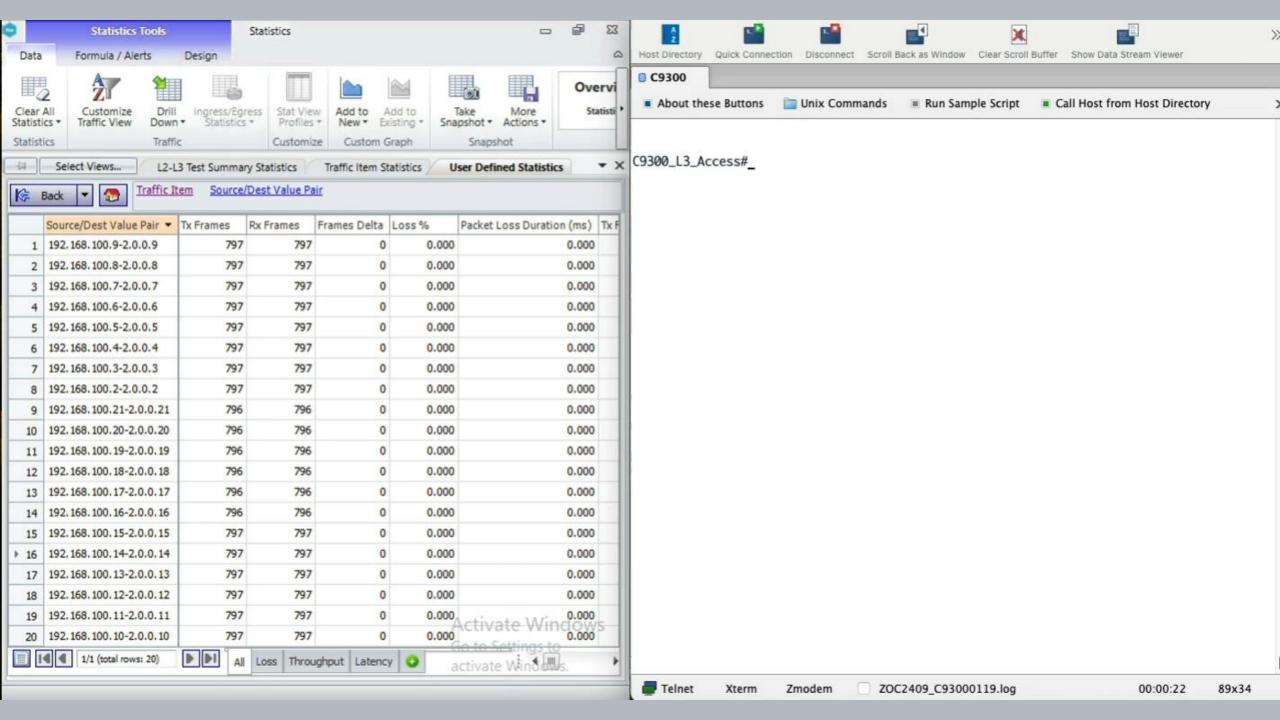
Not Supported

- SM to EM or EM to SM.(16.11.X <> 16.12.x)
- Within SM Release (16.11.1<>16.11.2)
- Beyond the + 2 EM Releases.
- When there is a major release change.
 - For example 16.x.x to 17.x.x

CISCO

xFSU Demo





Graceful Insertion & Removal (GIR)

Graceful Insertion and Removal



Upgrades with no or minimal traffic loss



Comprehensive node isolation framework



Easy execution with a single command





Highly customizable workflow

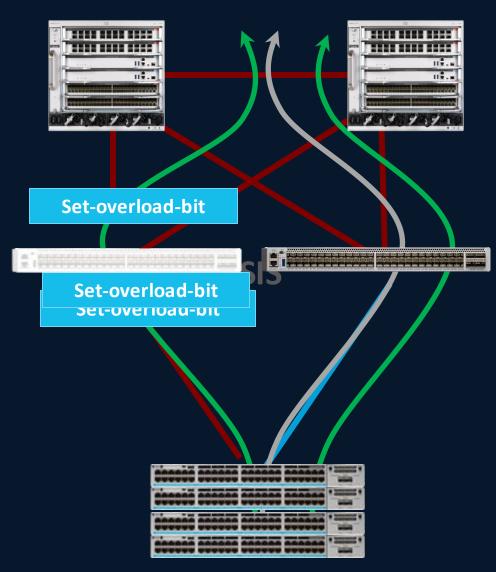


L2 and L3 Topology with GIR Isolation

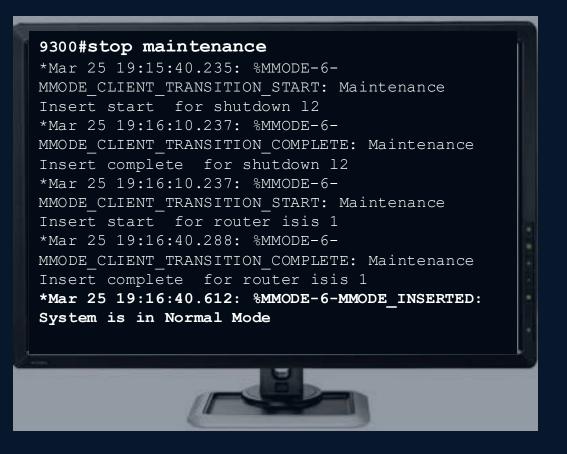
```
9300#start maintenance
Template default will be applied.
Do you want to continue?[confirm]
*Mar 25 17:43:20.162: %MMODE-6-
MMODE CLIENT TRANSITION START: Maintenance Isolate
start for router isis 1
*Mar 25 17:43:50.213: %MMODE-6-
MMODE CLIENT TRANSITION COMPLETE: Maintenance Isolate
complete for router isis 1
*Mar 25 17:43:50.213: MMODE-6-
MMODE CLIENT TRANSITION% START: Maintenance Isolate
start for shutdown 12
*Mar 25 17:44:20.214: %MMODE-6-
MMODE CLIENT TRANSITION COMPLETE: Maintenance Isolate
complete for shutdown 12
*Mar 25 17:44:20.214: %MMODE-6-MMODE ISOLATED: System
is in Maintenance
```

Order for Maintenance:

BGP -> IGPs in parallel (ISIS) -> L2

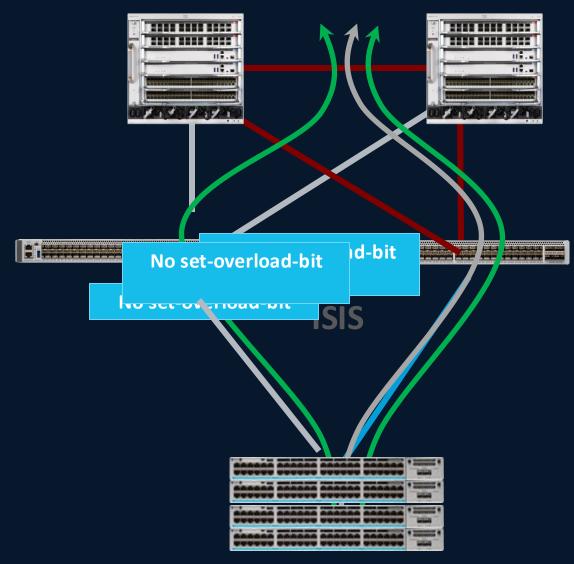


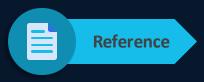
L2 and L3 Topology with GIR Isolation



Order for Maintenance:

L2 → IGPs in parallel (ISIS) -> BGP





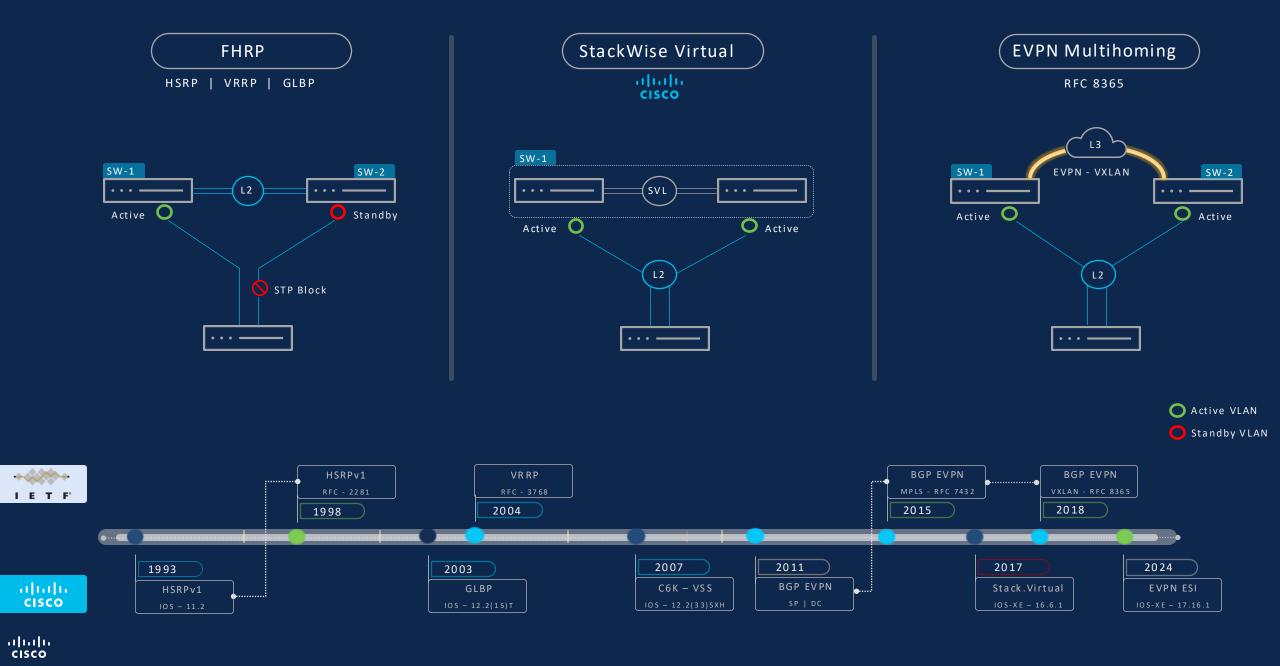
GIR Insertion and Removal Method

Protocol	Graceful Removal Mechanism	Graceful Insertion Mechanism
ISIS	Set Overload Bit	Unset Overload Bit
OSPF	Send LSAs with max metric	Refresh LSAs with original metric
BGP	Withdraw BGP Advertised routes	Re-insert the BGP Advertised Routes
HSRP	Advertise Resign Message	Advertise Hello Message
VRRP	Advertise Priority 0	Revert back with original priority

Catalyst 9K – EVPN Multihoming

17.18.1: Beta for Non-Fabric | Fabric available now

17.18.2 + 26.1.1: GA code expected in Q1 2026



EVPN Multihoming- Product Support Matrix

Catalyst 9K Modular Switch Catalyst 9600 Series – Sup-1

Catalyst 9600 Modules – Any

Catalyst 9400 Series – Sup-1

Catalyst 9400X Series — Sup-2

Catalyst 9400 Modules – Any

Catalyst 9K
Fixed Switch

Catalyst 9500 Series

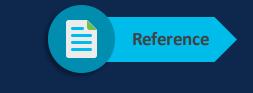
Catalyst 9500-H Series

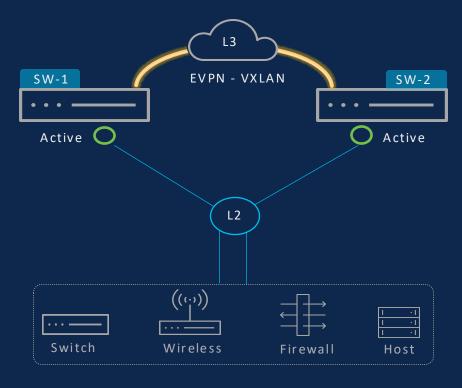
Catalyst 9300X Series

Catalyst 9300 Series

Software License

Network Advantage



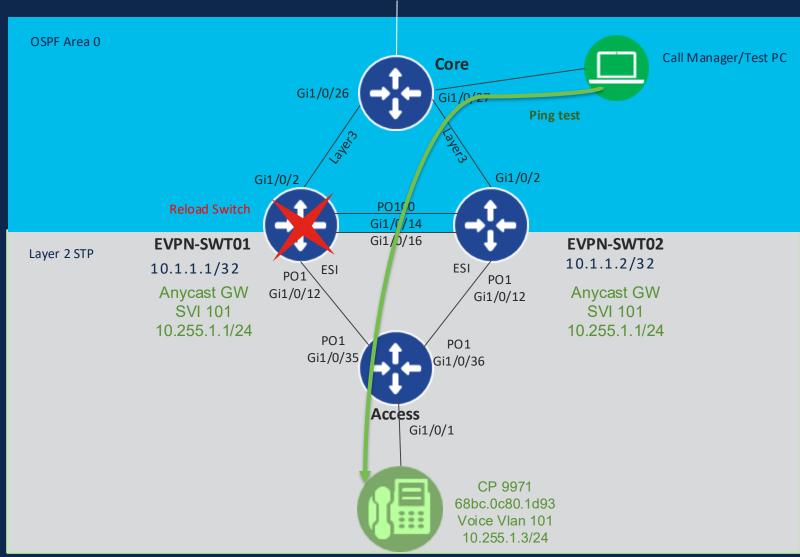






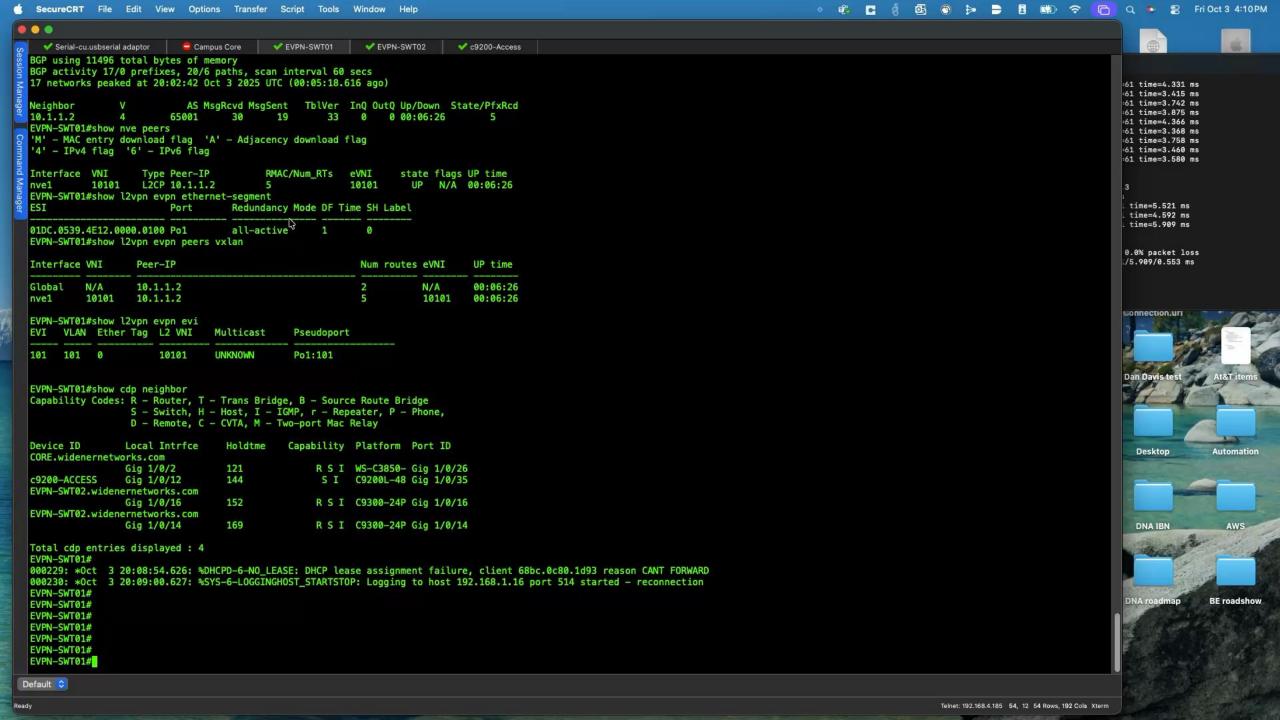
Topology



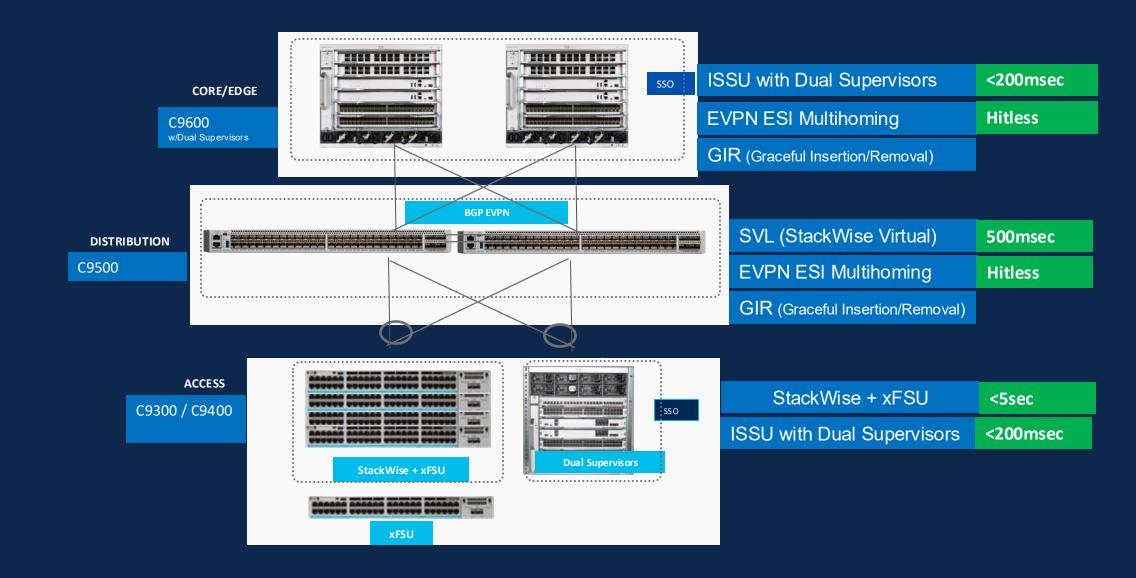


Demo





Campus Resiliency at Every Layer



Putting it all Together

New platforms for an AI future

- High density mGig and UPoE+ for WiFi7 and beyond
- Higher performance for AI workloads
- Intelligent onboarding and management
- Dual-persona: Networking + Security

Join us for Platform and AIOps updates next!

Security Infused in the Network

- Authentication, segmentation, encryption ... even for a PQC world
- Hypershield-ready for IPS/IDS enforcement at switch port level
- Live Protect for immediate response against day-0 threats before patches are developed

Highly Resilient Campus

- Hitless core / distribution layers
- Sub 5 second upgrades for standalone or stackable access



Thank you

