

From Fiction to Science

Compute for AI Deep Dive

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cisco Connect

Transform data centers to power Al workloads anywhere

Public and private clouds, edge, on-premises



Comprehensive infrastructure

Power AI with networking, compute, and storage in fullyintegrated, scalable, and modular systems for all workloads

Seamless operations and observability

Remove silos with unified management, observability, and assurance for traditional and Al workloads, across all environments

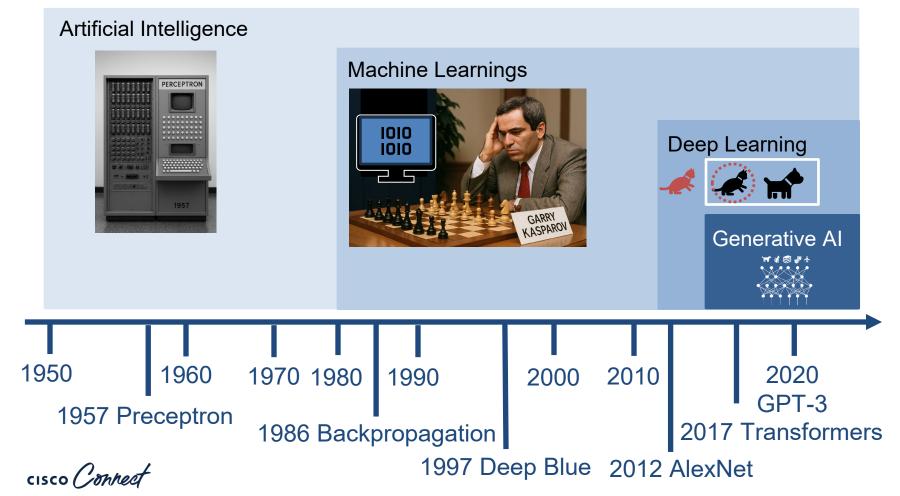
Security from ground to cloud

Protect hyper-distributed workloads by infusing security everywhere





Monumental Moments



What Al looks like...today

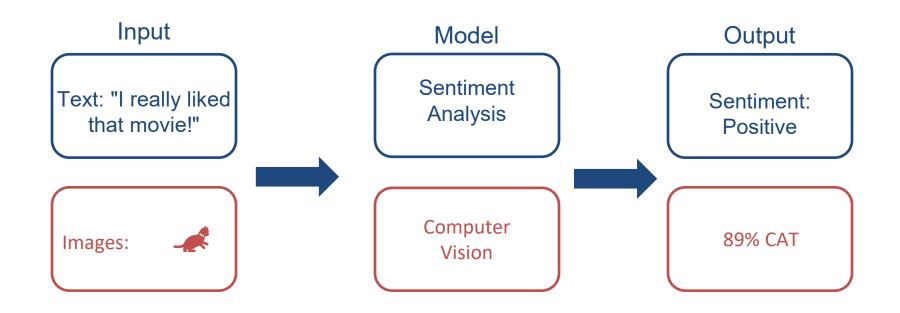
From healthcare to transportation, Al is rapidly moving from the lab to daily life. In 2023, the FDA approved 223 Al-enabled medical devices, up from just six in 2015. On the roads, self-driving cars are no longer experimental: Waymo, one of the largest U.S. operators, provides over 150,000 autonomous rides each week, while Baidu's affordable Apollo Go robotaxi fleet now serves numerous cities across China

New research suggests that machine learning hardware performance, measured in 16-bit floating-point operations, has grown 43% annually, doubling every 1.9 years. Price performance has improved, with costs dropping 30% per year, while energy efficiency has increased by 40% annually.



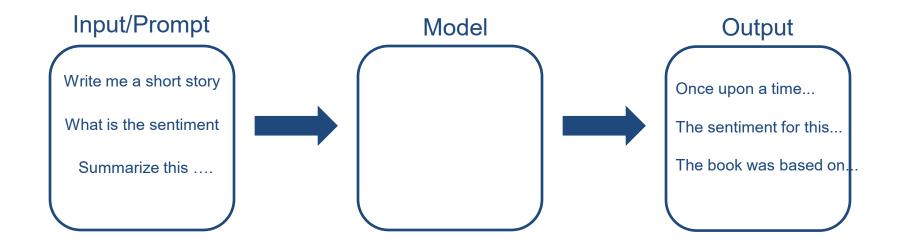
Nestor Maslej, Loredana Fattorini, Raymond Perrault, Yolanda Gil, Vanessa Parli, Njenga Kariuki, Emily Capstick, Anka Reuel, Erik Brynjolfsson, John Etchemendy, Katrina Ligett, Terah Lyons, James Manyika, Juan Carlos Niebles, Yoav Shoham, Russell Wald, Tobi Walsh, Armin Hamrah, Lapo Santarlasci, Julia Betts Lotufo, Alexandra Rome, Andrew Shi, Sukrut Oak. "The Al Index 2025 Annual Report," Al Index Steering Committee, Institute for Human-Centered Al, Stanford University, Stanford (CA) April 2025

What does inference really mean?



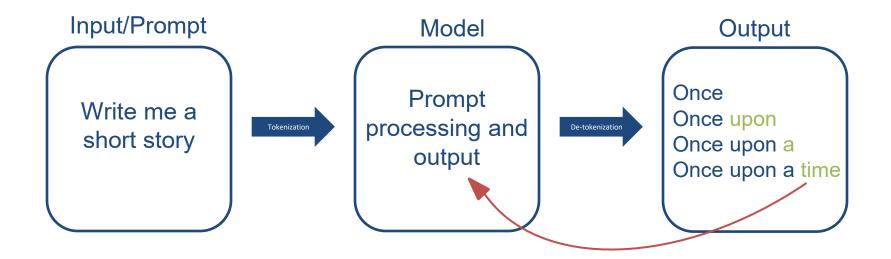


How does LLM inferencing work





How does LLM inferencing work



How does tokenization work

- Converts input text into a vector of integers
- Each token in a vocabulary has a corresponding integer
- A token is roughly four characters

T1	T2	T3				TN
1	2	3				Ν

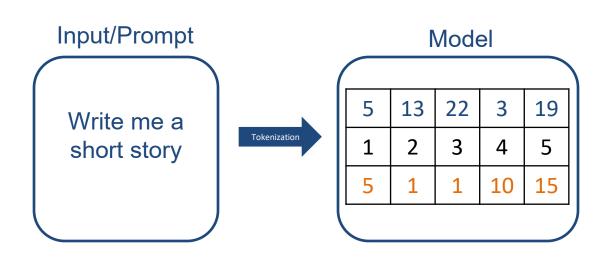
Write me a short story

5	13	22	3	19
1	2	3	4	5

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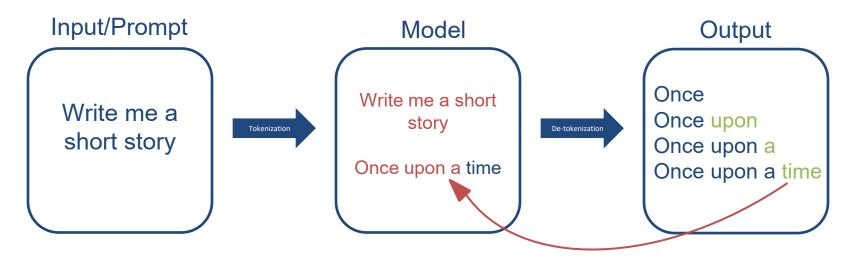
Prompt processing, Prefill, and Attention

- Numerical embeddings are supplied to the model
- The model uses these to establish context
- Attention focus
 on relevant parts
 and assigns
 weights





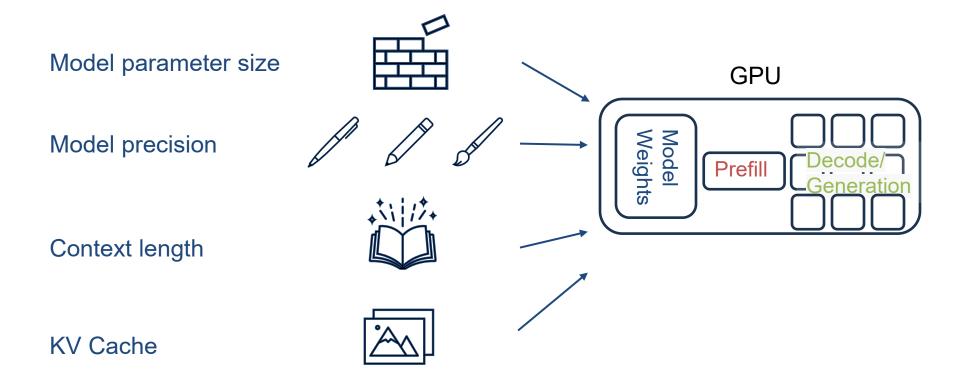
Output - Decode and Cache



- Each token gets generated using the processed prompt
- The last token generated is included in the input
- Attention vectors called KV Cache are stored in memory to speed up further token generation
- The output is the next best token based on probability

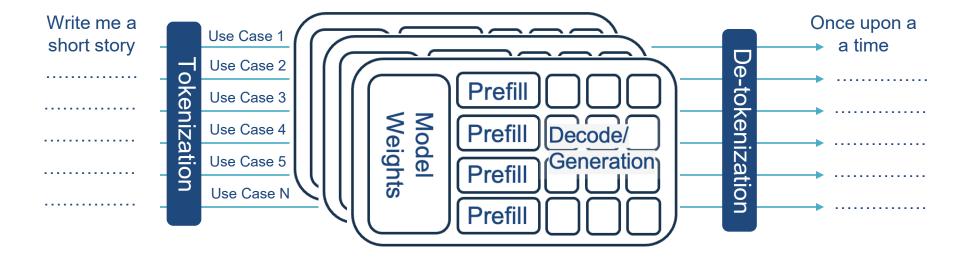


Applying this to a GPU

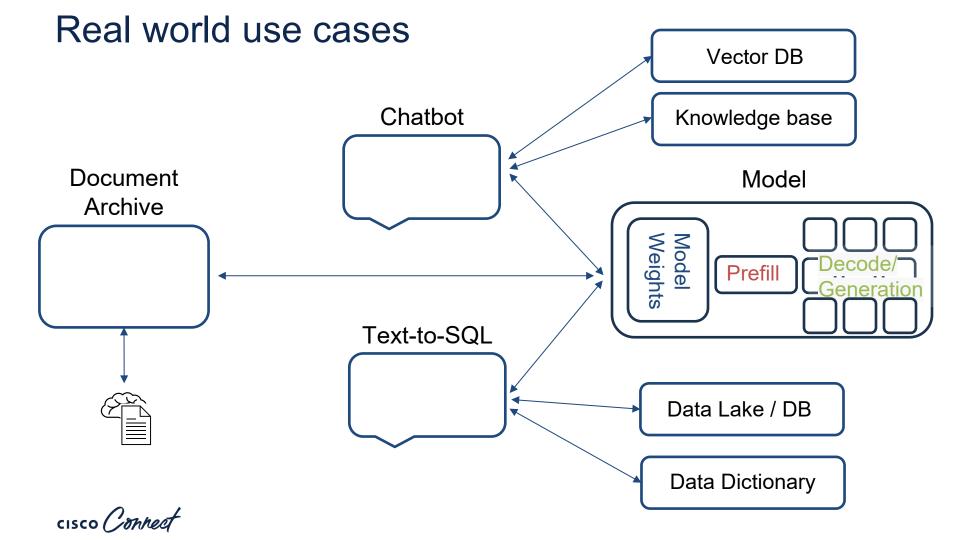


cisco Conhec

Serving multiple use cases







Using Al Model Cards

- Details model purpose, performance and usage considerations
- Use as a guide to understand model capabilities and limitations
- Dictates server and GPU requirements





Converting Model Card Details into GPU Requirements

Number of parameters determines GPU memory requirements (fp32)

- 4GB of GPU RAM per 1B parameters for inferencing. Parameter = 4 bytes
- 24GB of GPU RAM per 1B parameters for training. Parameter = 4 bytes + 20 bytes context

If an enterprise class GPU has 80GB of high bandwidth memory:

- Inferencing overrun at 20B parameters
- Training overrun at 3.3B parameters



Understanding Quantization

- Technique to reduce computational and memory requirements of models by lowering precision of the weights
- Changing from 32-bit to 16-bit to 8-bit etc., reduces the memory needed
- Ongoing discussions on if it is better to reduce number of parameters or change quantization

1B Params	Inferencing	Training	float 32
Float 32 (32b)	4GB	24GB	Sign Exponent Fraction (1 bit) (8 bits) (23 bits)
Float 16 (16b) 2GB		12GB	float 16 ("half" precision)
Int 8 (8b)	1GB	6GB	Sign Exponent Fraction (1 bit) (5 bits) (10 bits)



GPU Sizing Based on Model Card

* Amount of GPU high bandwidth memory (HBM) is the most important factor

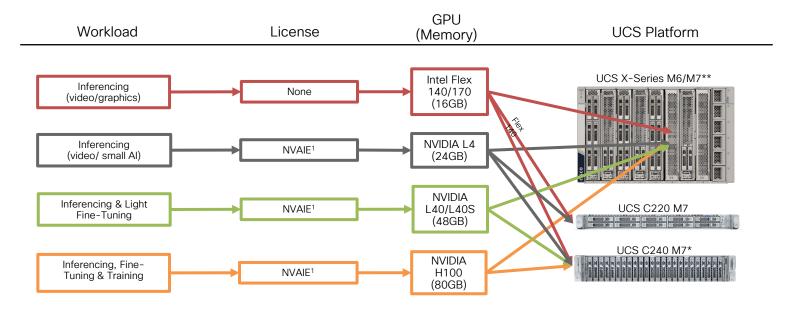
Model Size (Billions of parameters)	Float32	Float16	GPTQ 8bit	GPTQ 4bit
7B	28 GB	14 GB	7 GB	3.5 GB
13B	52 GB	26 GB	13 GB	6.5 GB
32B	128 GB	64 GB	32 GB	16 GB
65B	260 GB	130 GB	65 GB	32.5 GB

2 GB per billion parameters



AI GPUs for UCS

Platform Support



¹NVIDIA AI Enterprise (NVAIE) license required if workload virtualized



^{*} T4 is also supported in C220 M6 and A10/A20/A40/A100/A16 are supported in C240 M6 which are not listed on this slide

^{**} T4, A16, A40, A100 in X440p is supported with X210c M6; L4, L40, H100 in X440p is supported with X210c M7

UCS X-Fabric Technology and PCle Nodes with GPU

PCle node supports up to:

4x

Intel Data Center GPU Flex 140 2x

Intel Data Center GPU Flex 170

2x

Nvidia A16

2x

Nvidia H100

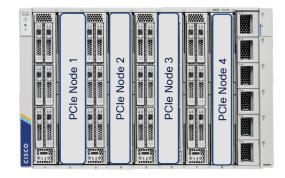
Nvidia L40

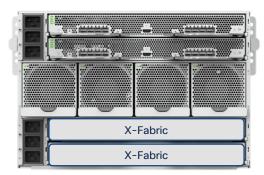
4x

Nvidia T4

Nvidia L4

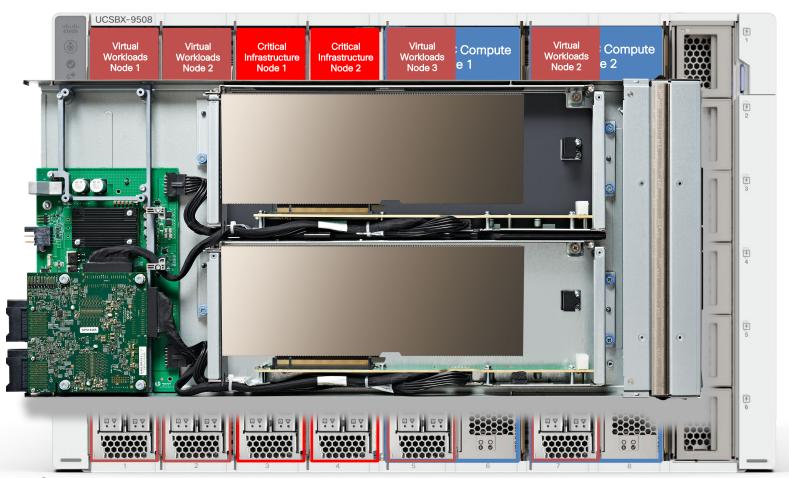
Nvidia L40S Nvidia A40





UCS X-Fabric Technology

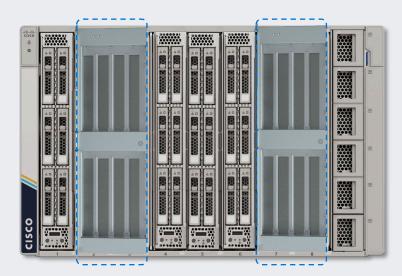
- Based on native PCle Gen. 4
- Provides GPU acceleration to enterprise application
- No backplane or cables = Easily upgrades

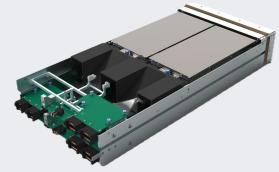


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X580p PCle Node

- Double wide PCle node for 4x FHFL GPU and PCle G5 GPU support
 - Nvidia H200-NVL, B100-NVL, B40, A16
- Support multiple vendors: Nvidia, AMD*/Intel*
- NVLink bridge support
- Support up to 600W FHFL GPU
- Managed PCle node with BMC support
- Policy based GPU management
- · Ability to support 2x GPUs per Compute node

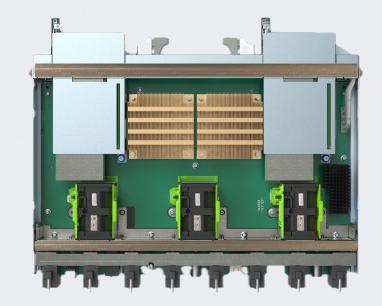






X-Fabric 9516

- PCle Gen5 Switching
- 2x CEM Slots to support HHHL NIC cards
- Managed XFM Modules with BMC support
- GPU Direct Support over RDMA
- GPU Backend(East-West Traffic) network support





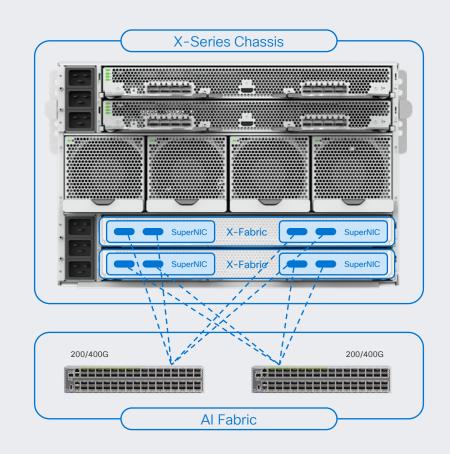


Al cluster expansion

GPU-to-GPU connectivity

with XFM external ports







Al PODs

Deploying AI with confidence



Confidently deploy Al-ready infrastructure with pre-designed full stack architecture bundles for targeted Al use cases.



Leverage automation frameworks for rapid deployment and adoption of infrastructure.



Operate with best-in-class single-support model for your AI deployment architecture, include enterprise support for select Operations Support System (OSS) tools and libraries Al Model

Al Tooling

Containers

Accelerated Compute

Networking

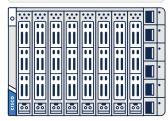
Converged Infrastructure

Management & Automation

Adoption & Support Services

INVIDIA. AI ENTERPRISE

SOPENSHIFT



PURESTORAGE"

■ NetApp[•]











CISCO Customer Experience



Cisco Al PODs for Inferencing

Large-Scale **Typical Edge Inferencing** Scale-Out Inferencing Cluster **RAG Augmented Inferencing** (Inferencing Multiple Models) use case (7B-13B Parameter) Small Medium Scale-Out Large 2x X210C compute nodes 2x X210C compute nodes 4x X210C compute nodes 1x X210C compute node 4x Intel 5th Gen 6548Y+ 4x Intel 5th Gen 6548Y+ 8x Intel 5th Gen 6548Y+ 2x Intel 5th Gen 6548Y+ Hardware 1 TB System Memory 1 TB System Memory 4 TB System Memory 512 GB System Memory Specification 10x 1 6 TB NVMe drives 10x 1 6 TB NVMe drives 20x 1 6 TB NVMe drives 5x 1.6 TB NVMe drives (Required) 2x X440p PCle 2x X440p PCle 4x X440p PCle 1x X440p PCle 4x NVIDIA I 40S 4x NVIDIA H100 NVI 8x NVIDIA I 40S 1x NVIDIA L40S 2x Fabric Interconnect 2x Fabric Interconnect 2x Fabric Interconnect X-Series FI9108 100G 6536 100G 6536 100G • 6536 100G Cisco Intersight Cisco Intersight Cisco Intersight Cisco Intersight Software Essentials Essentials Essentials Essentials specification Nvidia Al Enterprise Nvidia Al Enterprise Nvidia Al Enterprise Nvidia Al Enterprise (Required) Essentials Essentials Essentials Essentials OpenShift OpenShift OpenShift OpenShift Default OpenShift Container Platform OpenShift Container Platform OpenShift Container Platform OpenShift Container Platform Components (Optional) X210c Control Plane Cluster Single-Node Controller X210c Control Plane Cluster X210c Control Plane Cluster CI Storage CI Storage CI Storage CI Storage Add-On FlashStack ◆● FlexPod



Bringing flexibility and scalability to your Al workloads

Versatile, accelerated, 4RU rack server for broad set of Al use cases



Cisco UCS® C845A M8 Rack Server

NVIDIA MGX platform with 2/4/6/8 NVIDIA H100 NVL/H200 NVL/L40S GPUs 2 AMD 5th Gen EPYC processors



H100*8+B3220+B3140H*4



L40S*8+B3220+CX7*4

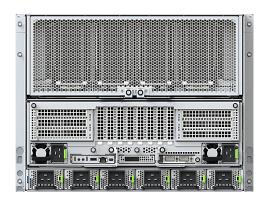


UCS C885A M8 Modular Sled Design



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UCS C885A Dense GPU Server Specifications

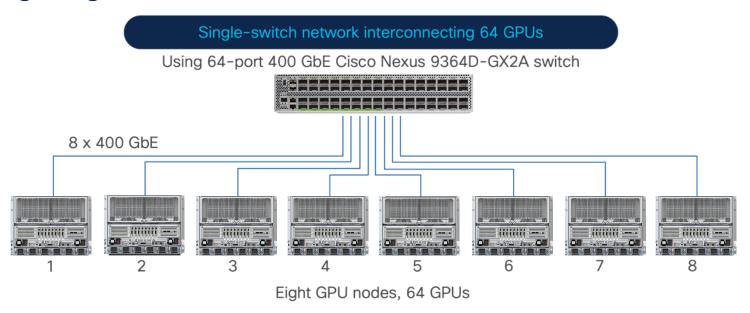




Product Specifications					
Form Factor	HGX 8RU 19" Rack Server				
Compute + Memory	 2x 4th Gen AMD EPYC 9554 (360W, 64 core, up to 3.75GHz) or 2x 5th Gen AMD EPYC 9575F (400W, 64 core, up to 5GHz) 24 DDR5 RDIMMs Up to 6,000 MT/S 				
Storage	1 PCle3 x4 M.2 NVMe (Boot Device)Up to 16 PCle5 x4 2.5" U.2 NVMe SSD (Data Cache)				
GPUs	 Nvidia: 8 x H100 (700W) or 8 x H200 (700W) or 8 x B200A (700W) AMD: 8 x MI300X (750W) 				
Network Cards	 8 PCle x16 HHHL for East-West NIC ConnectX-7 or BF3 B3140H 1 PCle x16 FHHL for North-South NIC BF3 B3220 1 OCP 3.0 X710-T2L for North-South or host management 				
Cooling	12 Hot swappable (N+1) fans for system cooling4 fans for SSD cooling				
Front IO	• 2 USB 2.0, 1 ID Button, 1 Power Button				
Rear IO	 1 USB 3.0 A, 1 USB 3.0 C, mDP, 1 ID Button, 1 Power Button, 1 USB 2.0 C (for debugging), 1 RJ45 (mgmt.) 				
Power Supply	• Up to 6 54V 3kW and 2 12V 2.7kW MCRPS/CRPS, N+1 redundancy				



Designing a Smaller Inter-GPU Backend Network



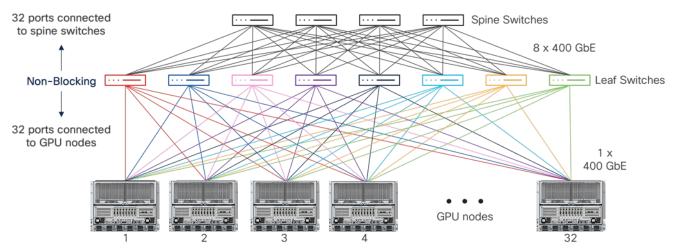
• Smaller GPU clusters can use a single-switch network. For example, up to 64 GPUs can be interconnected using the 2 RU, 64-port 400 GbE, Cisco Nexus 9364D-GX2A switch.



Designing a Larger Inter-GPU Backend Network

Rails-optimized network interconnecting 256 GPUs

Using 64-port 400 GbE Cisco Nexus 9364D-GX2A switches



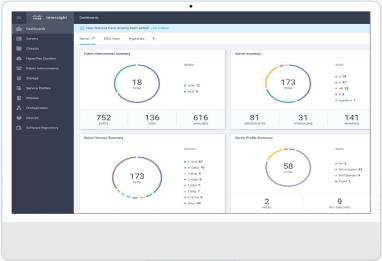
Port 1 on all nodes connects to Leaf-1, Port 2 on all nodes connects to Leaf-2, and so on.

- For a larger GPU cluster, a spine-leaf network design is the best option because of its consistent and predictable performance and ability to scale.
- The edge switch ports that connect to the GPU ports should operate at the fastest supported speeds, such as 400 GbE. The core switch ports between the leaf switches and spine switches should match or exceed the speed at which the GPUs connect to the network.

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Cisco Intersight

Data center operations, simplified



Feature Parity

More of what you love about UCS Managed Mode, now available in Intersight Managed Mode

AlOps

- Enriched topology view for full visibility
- More data and telemetry than ever before
- Bringing monitoring to the virtual appliance

Sustainability

- Energy and power dashboards
- SFP metrics



Our Unified Approach to the Data Center







