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Text Part Number: 78EE117C99-2493851052

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1.649 securerandom 0.1.0
1.649.1 Available under license
1.650 taglibs-standard-spec 1.2.5
1.650.1 Available under license
1.651 mail 1.4
1.651.1 Available under license
1.652 classmate 0.8.0
1.652.1 Available under license
1.653 jnr-ffi 2.2.16
1.653.1 Available under license
1.654 numcpus 0.6.1
1.654.1 Available under license
1.655 jcodings 1.0.58
1.655.1 Available under license
1.656 thread-safe 0.3.5
1.656.1 Available under license
1.657 go-colorable 0.1.12
1.657.1 Available under license
1.658 httpcomponents-client 4.3
1.658.1 Available under license
1.659 bzip2 1.0.8-10.el9_5
1.659.1 Available under license
1.660 protobuf 1.5.3

1.660.1 Available under license

1.661 libtool 2.4.6-46.el9

1.661.1 Available under license

1.662 mgo 2.0.0-20190816093944-a6b53ec6cb22

1.662.1 Available under license

1.663 loopy 0.5

1.663.1 Available under license

1.664 html5lib 1.1

1.664.1 Available under license

1.665 mail 1.4.2

1.665.1 Available under license

1.666 ikvm.net 8.1.5717

1.666.1 Available under license

1.667 ipaddr 1.2.2

1.667.1 Available under license

1.668 colord 1.4.5-6.el9_6

1.668.1 Available under license

1.669 vt-dictionary 3.0

1.669.1 Available under license

1.670 x-text 0.13.0

1.670.1 Available under license

1.671 javassist 3.21.0-GA

1.671.1 Available under license

1.672 dom4j 2.1.3

1.672.1 Available under license

1.673 jdo-api 2.3-eb

1.673.1 Available under license

1.674 rss 0.2.9

1.674.1 Available under license

1.675 gorilla-mux 1.8.0

1.675.1 Available under license

1.676 net-smtp 0.3.1

1.676.1 Available under license

1.677 thread-safe 0.3.5-java

1.677.1 Available under license

1.678 jmespath-community-go-jmespath 1.1.1-0.20230531184041-4a4bfbff5d01

1.678.1 Available under license

1.679 numactl 2.0.19-1.el9

1.679.1 Available under license

1.680 xmlsec 1.2.29-13.el9
1.680.1 Available under license
1.681 brotli 1.0.9-7.el9_5
1.681.1 Available under license
1.682 httpcomponents-mime 4.2.2
1.682.1 Available under license
1.683 readline-ext 0.1.1
1.683.1 Available under license
1.684 ipaddr 1.2.4
1.684.1 Available under license
1.685 free-type 2.10.4-10.el9_5
1.685.1 Available under license
1.686 jar-dependencies 0.4.1
1.686.1 Available under license
1.687 bean-validation-api 1.1.0.Final
1.687.1 Available under license
1.688 tomcat-jni 9.0.108
1.688.1 Available under license
1.689 json 1.8.3
1.689.1 Available under license
1.690 activemq-protobuf 1.1
1.690.1 Available under license
1.691 activemq-kahadb-store 5.16.7
1.691.1 Available under license
1.692 stringio 3.0.8
1.692.1 Available under license
1.693 commons-logging 1.2
1.693.1 Available under license
1.694 asm 5.0.3
1.694.1 Available under license
1.695 xml-commons-resolver 1.1
1.695.1 Notifications
1.695.2 Available under license
1.696 isc 2.0
1.696.1 Available under license
1.697 commons-lang 2.5
1.697.1 Available under license
1.698 lz4 2.6.1+incompatible
1.698.1 Available under license

1.699 commons-lang 2.2
1.699.1 Available under license
1.700 slf4j-log4j 1.7.25
1.700.1 Available under license
1.701 go-cleanhttp 0.5.2
1.701.1 Available under license

1.1 libjpeg 7

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1.2 libjpeg 6b

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The Independent JPEG Group's JPEG software
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README for release 6b of 27-Mar-1998
====================================

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our electronic mailing list. Mailing list members are notified of updates
and have a chance to participate in technical discussions, etc.

This software is the work of Tom Lane, Philip Gladstone, Jim Boucher,
Lee Crocker, Julian Minguillon, Luis Ortiz, George Phillips, Davide Rossi,
Guido Vollbeding, Ge' Weijers, and other members of the Independent JPEG
Group.

IJG is not affiliated with the official ISO JPEG standards committee.

DOCUMENTATION

ROADMAP
==========

This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:
install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran,
rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.
Programmer and internal
documentation:
libjpeg.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.
filelist.doc Road map of IJG files.
coderules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information
can also be found in the JPEG FAQ (Frequently Asked Questions) article. See
ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or
more of the REFERENCES, then looking at the documentation files (in roughly
the order listed) before diving into the code.

OVERVIEW
========

This package contains C software to implement JPEG image compression and
decompression. JPEG (pronounced "jay-peg") is a standardized compression
method for full-color and gray-scale images.
JPEG is intended for compressing
"real-world" scenes; line drawings, cartoons and other non-realistic images
are not its strong suit. JPEG is lossy, meaning that the output image is not

exactly identical to the input image. Hence you must not use JPEG if you
have to have identical output bits. However, on typical photographic images,
very good compression levels can be obtained with no visible change, and
remarkably high compression levels are possible if you can tolerate a
low-quality image. For more details, see the references, or just experiment
with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive
compression processes. Provision is made for supporting all variants of these
processes, although some uncommon parameter settings aren't implemented yet.
For legal reasons, we are not distributing code for the arithmetic-coding
variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting
the
 hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files,
plus two sample applications "cjpeg" and "djpeg", which use the library to
perform conversion between JPEG and some other popular image file formats.
The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included
considerable functionality beyond the bare JPEG coding/decoding capability;
for example, the color quantization modules are not strictly part of JPEG
decoding, but they are essential for output to colormapped file formats or
colormapped displays. These extra functions can be compiled out of the
library if not required for a particular application. We have also included
"jpegtran", a utility for lossless transcoding between different JPEG
processes, and "rdjpgcom" and "wrjpgcom", two simple applications for
inserting and extracting textual comments
 in JFIF files.

The emphasis in designing this software has been on achieving portability and
flexibility, while also making it fast enough to be useful. In particular,
the software is not intended to be read as a tutorial on JPEG. (See the
REFERENCES section for introductory material.) Rather, it is intended to
be reliable, portable, industrial-strength code. We do not claim to have
achieved that goal in every aspect of the software, but we strive for it.

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ansi2knr.c is NOT covered by the above copyright and conditions, but instead
by the usual distribution terms of the Free Software Foundation; principally,
that you must include source code if you redistribute it. (See the file
ansi2knr.c for full details.) However, since ansi2knr.c is not needed as part
of any program generated from the IJG code, this does not limit you more than
the foregoing paragraphs do.

The Unix configuration script "configure" was produced with GNU Autoconf.
It is copyright by the Free Software Foundation but is freely distributable.
The same holds for its supporting scripts (config.guess, config.sub,
ltconfig, ltmain.sh). Another support script, install-sh, is copyright
by M.I.T. but is also freely distributable.

It appears that the arithmetic coding option of the JPEG spec is covered by
patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic
coding cannot
legally be used without obtaining one or more licenses. For this reason,
support for arithmetic coding has been removed from the free JPEG software.
(Since arithmetic coding provides only a marginal gain over the unpatented
Huffman mode, it is unlikely that very many implementations will support it.)
So far as we are aware, there are no patent restrictions on the remaining
code.

The IJG distribution formerly included code to read and write GIF files.
To avoid entanglement with the Unisys LZW patent, GIF reading support has
been removed altogether, and the GIF writer has been simplified to produce
"uncompressed GIFs". This technique does not use the LZW algorithm; the
resulting GIF files are larger than usual, but are readable by all standard
GIF decoders.

We are required to state that
"The Graphics Interchange Format(c) is the Copyright property of
CompuServe Incorporated. GIF(sm) is a Service Mark property of
CompuServe Incorporated."

REFERENCES
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We
highly recommend reading one or more of these references before trying to
understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is
Wallace, Gregory K. "The JPEG Still Picture Compression Standard",
Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44.

(Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is available at ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published by Van Nostrand Reinhold, 1993, ISBN 0-442-01272-1. Price US$59.95, 638 pp. The book includes the complete text of the ISO JPEG standards (DIS 10918-1 and draft DIS 10918-2). This is by far the most complete exposition of JPEG in existence, and we highly recommend it.

The JPEG standard itself is not available electronically; you must order a paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.) In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of 1992, ANSI was charging $95 for Part 1 and $47 for Part 2, plus 7% shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department
C-Cube Microsystems, Inc.
1778 McCarthy Blvd.
Milpitas, CA 95035
phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz. There is also a plain text version at ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from ftp.sgi.com or from ftp://ftp.uu.net/graphics/jpeg/. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from ftp://ftp.sgi.com/graphics/tiff/.

ARCHIVE LOCATIONS

The "official" archive site for this software is ftp.uu.net (Internet address 192.48.96.9). The most recent released version can always be found there in directory graphics/jpeg. This particular version will be archived as ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only ftp.uu.net is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the ftp.uu.net release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of

general information about JPEG. It is updated constantly and therefore is
not included in this distribution. The FAQ is posted every two weeks to
Usenet newsgroups comp.graphics.misc, news.answers, and other groups.
It is available on the World Wide Web at http://www.faqs.org/faqs/jpeg-faq/
and other news.answers
 archive sites, including the official news.answers
archive at rtfm.mit.edu: ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/.
If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu
with body
 send usenet/news.answers/jpeg-faq/part1
 send usenet/news.answers/jpeg-faq/part2

RELATED SOFTWARE
================

Numerous viewing and image manipulation programs now support JPEG. (Quite a
few of them use this library to do so.) The JPEG FAQ described above lists
some of the more popular free and shareware viewers, and tells where to
obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free
PBMPLUS software, which provides many useful operations on PPM-format image
files. In particular, it can convert PPM images to and from a wide range of
other formats, thus making cjpeg/djpeg considerably more useful. The latest
version is distributed by the NetPBM group, and is available from numerous
sites,
 notably ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/.
Unfortunately PBMPLUS/NETPBM is not nearly as portable as the IJG software is;
you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford,
is available from ftp://havefun.stanford.edu/pub/jpeg/. This program
is designed for research and experimentation rather than production use;
it is slower, harder to use, and less portable than the IJG code, but it
is easier to read and modify. Also, the PVRG code supports lossless JPEG,
which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS
================

Some JPEG programs produce files that are not compatible with our library.
The root of the problem is that the ISO JPEG committee failed to specify a
concrete file format. Some vendors "filled in the blanks" on their own,
creating proprietary formats that no one else could read.
  (For example, none
of the early commercial JPEG implementations for the Macintosh were able to

exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether
SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO
=====

The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without
sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.3 minizip 1.01

1.3.1 Available under license :

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1.4 xz 5.2.2

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1.10 tdb 1.46.5

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file system consistency checker (e2fsck.static). The EXT2 utilities
were written by Theodore Ts'o <tytso@mit.edu> and Remy Card
<card@masi.ibp.fr>.

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#
This is a Makefile stub which handles the creation of BSD shared
libraries.
#
In order to use this stub, the following makefile variables must be defined.
#
BSDLIB_VERSION = 1.0
BSDLIB_IMAGE = libce
BSDLIB_MYDIR = et

```
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#

all:: image

real-subdirs:: Makefile
 @echo " MKDIR pic"
 @mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:  $(BSD_LIB)

$(BSD_LIB): $(OBJS)
 (cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBJS))
 $(MV) pic/$(BSD_LIB) .
 $(RM) -f ../$(BSD_LIB)
 (cd ..; $(LN) $(LINK_BUILD_FLAGS) \
  `echo $(my_dir) | sed -e 's;lib/;;'`/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
 @echo " INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
 @$(INSTALL_PROGRAM) $(BSD_LIB) \
  $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
 @-$(LDCONFIG)

install-strip: install

install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall::
 $(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
 $(RM)
 -rf pic
 $(RM) -f $(BSD_LIB)
 $(RM) -f ../$(BSD_LIB)
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This is the Debian GNU/Linux prepackaged version of the translation
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written by Theodore Ts'o <tytso@mit.edu> and Remy Card
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Index: tdbsa/tdb.c
===
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
 /*
-   Unix SMB/CIFS implementation.
+   trivial database library - standalone version

-   trivial database library - private includes
-
-   Copyright (C) Andrew Tridgell   2005

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Gadi Oxman, August 1995

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<one line to give the program's name and a brief idea of what it does.>
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```
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  under certain conditions; type `show c' for details.
```

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be called something other than `show w' and `show c'; they could even be
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```
 Yoyodyne, Inc., hereby disclaims all copyright interest in the program
 `Gnomovision' (which makes passes at compilers) written by James Hacker.

 <signature of Ty Coon>, 1 April 1989
 Ty Coon, President of Vice
```

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repository from the development branch, please contact me
(tytso@mit.edu)
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package are flexible, if you give me enough lead time.

Theodore Ts'o
23-June-2007

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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this
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```
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  Gnomovision
comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
  This is free software, and you are welcome to redistribute it
  under certain conditions; type `show c' for details.
```

The hypothetical commands `show w' and `show c' should show the appropriate
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necessary. Here is a sample; alter the names:

```
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 `Gnomovision' (which makes passes at compilers) written by James Hacker.

 <signature of Ty Coon>, 1 April 1989
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```

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That's all there is to it!
This is the Debian GNU/Linux prepackaged version of the EXT2 file
system utilities (e2fsck, mke2fs, etc.). The EXT2 utilities were
written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

Sources were obtained from http://sourceforge.net/projects/e2fsprogs

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This package was added to the e2fsprogs debian source package by
Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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consider it more useful to permit linking proprietary applications with the
library. If this
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Public License instead of this License.
enclave {

  trusted {

  };

  untrusted {
     /* define OCALLs here. */

  };
};

1.14 fdisk 2.37.4

1.14.1 Available under license :

NR START END SECTORS SIZE NAME UUID
1 32 7679 7648 3.7M 8f8378c0-01
2 7680 16383 8704 4.3M 8f8378c0-02
5 7936 12799 4864 2.4M
6 12544 16127 3584 1.8M

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pnm2png / png2pnm --- conversion from PBM/PGM/PPM-file to PNG-file

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1.20 zlib 1.2.7

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1.21 zlib 1.3.1

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu
/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.3.1, January 22nd, 2024

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jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for
Comments) 1950 to 1952 in the files http://tools.ietf.org/html/rfc1950
(zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).
*/
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/* gzlib.c -- zlib functions common to reading and writing gzip files
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*/

1.22 arptables 1.8.10

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1.23 libestr 0.1.11-4.el9.x86_64

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No license file was found, but licenses were detected in source scan.

Name: libestr
Version: 0.1.10
Release: 3%{?dist}
Summary: String handling essentials library

License: LGPLv2+
URL: http://libestr.adiscon.com/
Source0: http://libestr.adiscon.com/files/download/libestr-%{version}.tar.gz

BuildRequires: autoconf
BuildRequires: automake
BuildRequires: libtool

%description
This package compiles the string handling essentials library
used by the Rsyslog daemon.

%package devel
Summary: Development files for %{name}
Requires: %{name}%{?_isa} = %{version}-%{release}

%description devel
The package contains libraries and header files for
developing applications that use libestr.

%prep
%setup -q

%build
autoreconf -if
%configure --disable-static --with-pic
V=1 make %{?_smp_mflags}

%install
make install INSTALL="install -p" DESTDIR=%{buildroot}
rm -f %{buildroot}/%{_libdir}/*.{a,la}

%post -p /sbin/ldconfig

%postun
-p /sbin/ldconfig

%files
%{!?_licensedir:%global license %%doc}
%license COPYING

```
%doc README AUTHORS ChangeLog
%{_libdir}/lib*.so.*

%files devel
%{_includedir}/libestr.h
%{_libdir}/*.so
%{_libdir}/pkgconfig/libestr.pc

%changelog
* Mon Apr 04 2022 Attila Lakatos <alakatos@redhat.com> - 0.1.10-3
- Rebuild due to the following message: libestr-0.1.10-2.el8 has already been built
 resolves: rhbz#2056293

* Mon Apr 04 2022 Attila Lakatos <alakatos@redhat.com> - 0.1.10-2
- Rebuild libestr to add the devel package to CRB
 resolves: rhbz#2056293

* Fri Aug 10 2018 Jiri Vymazal <jvymazal@redhat.com> - 0.1.10-1
- rebase to v0.1.10
 resolves: rhbz#1614724

* Wed Feb 07 2018 Fedora Release Engineering <releng@fedoraproject.org> - 0.1.9-10
- Rebuilt for https://fedoraproject.org/wiki/Fedora_28_Mass_Rebuild

* Thu Aug 03 2017 Fedora Release Engineering <releng@fedoraproject.org> - 0.1.9-9
- Rebuilt for https://fedoraproject.org/wiki/Fedora_27_Binutils_Mass_Rebuild

* Wed Jul 26 2017 Fedora
 Release Engineering <releng@fedoraproject.org> - 0.1.9-8
- Rebuilt for https://fedoraproject.org/wiki/Fedora_27_Mass_Rebuild

* Fri Feb 10 2017 Fedora Release Engineering <releng@fedoraproject.org> - 0.1.9-7
- Rebuilt for https://fedoraproject.org/wiki/Fedora_26_Mass_Rebuild

* Thu Feb 04 2016 Fedora Release Engineering <releng@fedoraproject.org> - 0.1.9-6
- Rebuilt for https://fedoraproject.org/wiki/Fedora_24_Mass_Rebuild

* Wed Jun 17 2015 Fedora Release Engineering <rel-eng@lists.fedoraproject.org> - 0.1.9-5
- Rebuilt for https://fedoraproject.org/wiki/Fedora_23_Mass_Rebuild

* Sun Aug 17 2014 Fedora Release Engineering <rel-eng@lists.fedoraproject.org> - 0.1.9-4
- Rebuilt for https://fedoraproject.org/wiki/Fedora_21_22_Mass_Rebuild

* Thu Jul 17 2014 Tom Callaway <spot@fedoraproject.org> - 0.1.9-3
- fix license handling

* Sat Jun 07 2014 Fedora Release Engineering <rel-eng@lists.fedoraproject.org> - 0.1.9-2
- Rebuilt for https://fedoraproject.org/wiki/Fedora_21_Mass_Rebuild
```

*
Tue Jan 07 2014 Tomas Heinrich <theinric@redhat.com> - 0.1.9-1
- rebase to 0.1.9
- remove patch 0; doesn't seem to be necessary anymore

* Sat Aug 03 2013 Fedora Release Engineering <rel-eng@lists.fedoraproject.org> - 0.1.5-2
- Rebuilt for https://fedoraproject.org/wiki/Fedora_20_Mass_Rebuild

* Tue Apr 02 2013 Tomas Heinrich <theinric@redhat.com> - 0.1.5-1
- rebase to 0.1.5

* Wed Dec 12 2012 Mahaveer Darade <mdarade@redhat.com> - 0.1.4-1
- upgrade to upstream version 0.1.4
- correct an impossible timestamp in an older changelog entry

* Thu Sep 20 2012 mdarade <mdarade@redhat.com> - 0.1.3-3
- Fixed broken configure script

* Mon Aug 27 2012 mdarade <mdarade@redhat.com> - 0.1.3-2
- Removed unnecessary macros in spec file.

* Tue Aug 7 2012 Mahaveer Darade <mdarade@redhat.com> - 0.1.3-1
- Initial port libestr-0.1.3

Found in path(s):
* /libestr-c8-zip/libestr/SPECS/libestr.spec

1.24 nanopb 0.3.9.5

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1.25 opencore-amr 0.1.6.3

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1.29 nfs-utils 2.5.4

1.29.1 Available under license :

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1.30 jruby 1.0

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1.33 libcxx 17.0.6-5.el9.x86_64

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  * Fixes for Solaris 10 32/64-bit confusion.
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  * Optimize Java serialization of strings so that UTF-8 encoding happens only
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  * Added atomicops for AIX (POWER)
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1.35 zlib-ng 2.2.1

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c) Accompany it with the information you received as to the offer

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1.45 grub2 2.06-104.el9_6.alma.1.x86_64

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@item
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cause the whole of the work to be licensed at no
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@item

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is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
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source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

@item
A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
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However, linking a ``work that uses the Library'' with the Library
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it
contains portions of the Library), rather than a ``work that uses the
library''. The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a ``work that uses the Library'' uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative

work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

 Otherwise,
 if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

@item
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directing the user to the copy of
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@enumerate a
@item
Accompany the work with the complete corresponding
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with the Library, with the complete machine-readable ``work that
uses the Library'', as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

@item
Use a suitable shared library mechanism for linking with the Library. A
suitable mechanism is one that (1) uses at run time a copy of the
library already present on the user's computer system, rather than
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 and (2) will operate
properly with a modified version of the library, if the user installs
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@item
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@enumerate a
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facilities. This must be distributed under the terms of the

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@item
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/* Extract the last component (base name) of a file name.

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along with this program. If not, see <https://www.gnu.org/licenses/>. */

#ifndef _BASENAME_LGPL_H

```
#define _BASENAME_LGPL_H

#include <stddef.h>

#ifndef DOUBLE_SLASH_IS_DISTINCT_ROOT
# define DOUBLE_SLASH_IS_DISTINCT_ROOT 0
#endif

#ifdef __cplusplus
extern "C" {
#endif


/*
 Return the address of the last file name component of FILENAME.
  If FILENAME has some trailing slash(es), they are considered to be
  part of the last component.
  If FILENAME has no relative file name components because it is a file
  system root, return the empty string.
  Examples:
          FILENAME      RESULT
          "foo.c"       "foo.c"
          "foo/bar.c"   "bar.c"
          "/foo/bar.c"  "bar.c"
          "foo/bar/"    "bar/"
          "foo/bar//"   "bar//"
          "/"           ""
          "//"          ""
          ""            ""
  The return value is a tail of the given FILENAME; do NOT free() it!  */

/* This function was traditionally called 'basename', but we avoid this
  function name because
    * Various platforms have different functions in their libc.
      In particular, the glibc basename(), defined in <string.h>, does
      not consider trailing slashes to be part of the component:

 FILENAME      RESULT
          "foo/bar/"    ""
          "foo/bar//"   ""
    * The 'basename' command eliminates trailing slashes and for a root
      produces a non-empty result:
          FILENAME      RESULT
          "foo/bar/"    "bar"
          "foo/bar//"   "bar"
          "/"           "/"
          "//"          "/"
*/
```

```
extern char *last_component (char const *filename) _GL_ATTRIBUTE_PURE;

/* Return the length of the basename FILENAME.
  Typically FILENAME is the value returned by base_name or last_component.
  Act like strlen (FILENAME), except omit all trailing slashes.  */
extern size_t base_len (char const *filename) _GL_ATTRIBUTE_PURE;


#ifdef __cplusplus
} /* extern "C" */
#endif

#endif /* _BASENAME_LGPL_H */
```

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This means that if you want to contribute software to GNU, you have to do something to give us legal permission to use it. There are three ways this can be done:

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This allows the FSF to act to stop violations of the GPL.

* Keep the copyright and release the program yourself under the GNU GPL. (This alternative is too impractical for contributions to a preexisting FSF-copyrighted GNU program.)

* Put the code in the public domain. Then there is nothing to stop hoarding of modified versions, but we
can still use the program in GNU.

Most of these alternatives require a signed piece of paper, or in some cases a digital signature, to make it happen.

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distributing the program yourself--as long as you do so under the GNU terms. You don't have to wait for us to start distributing. You can start distributing as soon as you attach our standard copyleft to the files. (Ask for our advice on how to do this.)

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from us, and uses it as a basis for a proprietary product in violation
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piece of paper--a disclaimer of rights--from you confirming this. If the
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happen is that we might some day be forced to stop using it.

The law says that anyone can copyright a modified version of the public
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public domain; only the changes are copyrighted.) If we make extensive
changes,
 we will probably do this and add our usual copyleft. If we make
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If you are employed to do programming, or have made an agreement with your
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 Digital Simulation Corporation hereby disclaims all copyright interest
 in the program "sample.el" (a program to direct assemblers to make passes
 at compilers under GNU Emacs) written by Hugh Hacker.

 <signature of Mo Ghoul>, 1 April 1987

Mo Ghoul, President of Vice, Digital Simulation Corp.

The description of what the program does is just to make it clearer
what the disclaimer covers.

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what you did was change an existing program, it should say this:

...in the changes and enhancements made by Hugh Hacker to the
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If someone else contributed more than a few lines here or there to the
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his contribution is just a fraction of the whole work, it is satisfactory
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[Which files have you changed so far, and which new files have you written
so far?]

```
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*  GRUB  --  GRand Unified Bootloader
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*
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*  along with GRUB.  If not, see <http://www.gnu.org/licenses/>.
*/

#ifndef GRUB_BSD_CPU_HEADER
#define GRUB_BSD_CPU_HEADER 1

#include <grub/types.h>
```

```
#include <grub/relocator.h>

#include <grub/i386/freebsd_reboot.h>
#include <grub/i386/netbsd_reboot.h>
#include <grub/i386/openbsd_reboot.h>
#include <grub/i386/freebsd_linker.h>
#include
 <grub/i386/netbsd_bootinfo.h>
#include <grub/i386/openbsd_bootarg.h>

enum bsd_kernel_types
 {
   KERNEL_TYPE_NONE,
   KERNEL_TYPE_FREEBSD,
   KERNEL_TYPE_OPENBSD,
   KERNEL_TYPE_NETBSD,
 };

#define GRUB_BSD_TEMP_BUFFER   0x80000

#define FREEBSD_B_DEVMAGIC OPENBSD_B_DEVMAGIC
#define FREEBSD_B_SLICESHIFT OPENBSD_B_CTRLSHIFT
#define FREEBSD_B_UNITSHIFT OPENBSD_B_UNITSHIFT
#define FREEBSD_B_PARTSHIFT OPENBSD_B_PARTSHIFT
#define FREEBSD_B_TYPESHIFT OPENBSD_B_TYPESHIFT

#define FREEBSD_MODTYPE_KERNEL  "elf kernel"
#define FREEBSD_MODTYPE_KERNEL64 "elf64 kernel"
#define FREEBSD_MODTYPE_ELF_MODULE "elf module"
#define FREEBSD_MODTYPE_ELF_MODULE_OBJ "elf obj module"
#define FREEBSD_MODTYPE_RAW  "raw"

#define FREEBSD_BOOTINFO_VERSION 1

struct grub_freebsd_bootinfo
{
 grub_uint32_t version;
 grub_uint8_t unused1[44];
 grub_uint32_t length;
 grub_uint8_t unused2;
 grub_uint8_t boot_device;
 grub_uint8_t unused3[18];
 grub_uint32_t kern_end;
 grub_uint32_t environment;
  grub_uint32_t tags;
} GRUB_PACKED;

struct freebsd_tag_header
```

```
{
 grub_uint32_t type;
 grub_uint32_t len;
};

grub_err_t grub_freebsd_load_elfmodule32 (struct grub_relocator *relocator,
       grub_file_t file, int argc,
       char *argv[], grub_addr_t *kern_end);
grub_err_t grub_freebsd_load_elfmodule_obj64 (struct grub_relocator *relocator,
          grub_file_t file, int argc,
          char *argv[],
          grub_addr_t *kern_end);
grub_err_t grub_freebsd_load_elf_meta32 (struct grub_relocator *relocator,
      grub_file_t file,
      const char *filename,
      grub_addr_t *kern_end);
grub_err_t grub_freebsd_load_elf_meta64 (struct grub_relocator *relocator,
      grub_file_t file,
      const char *filename,
      grub_addr_t *kern_end);

grub_err_t grub_netbsd_load_elf_meta32 (struct grub_relocator *relocator,
     grub_file_t file,
     const char *filename,
     grub_addr_t *kern_end);
grub_err_t grub_netbsd_load_elf_meta64 (struct grub_relocator *relocator,
     grub_file_t
 file,
     const char *filename,
     grub_addr_t *kern_end);

grub_err_t grub_bsd_add_meta (grub_uint32_t type,
        const void *data, grub_uint32_t len);
grub_err_t grub_freebsd_add_meta_module (const char *filename, const char *type,
      int argc, char **argv,
      grub_addr_t addr, grub_uint32_t size);

struct grub_openbsd_ramdisk_descriptor
{
 grub_size_t max_size;
 grub_uint8_t *target;
 grub_uint32_t *size;
};

grub_err_t grub_openbsd_find_ramdisk32 (grub_file_t file,
     const char *filename,
     grub_addr_t kern_start,
     void *kern_chunk_src,
```

```
    struct grub_openbsd_ramdisk_descriptor *desc);
grub_err_t grub_openbsd_find_ramdisk64 (grub_file_t file,
    const char *filename,
    grub_addr_t kern_start,
    void *kern_chunk_src,
    struct grub_openbsd_ramdisk_descriptor *desc);

extern grub_uint8_t grub_bsd64_trampoline_start, grub_bsd64_trampoline_end;
extern grub_uint32_t grub_bsd64_trampoline_selfjump;
extern grub_uint32_t grub_bsd64_trampoline_gdt;

#endif
 /* ! GRUB_BSD_CPU_HEADER */
```

The files in this directory describe the gnulib modules.
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Description:
realpath, canonicalize_file_name: Provide canonical absolute file name

Files:
lib/canonicalize-lgpl.c
m4/canonicalize.m4
m4/double-slash-root.m4
m4/lstat.m4

Depends-on:
extensions
stdlib
nocrash
double-slash-root [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
eloop-threshold   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
errno   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
fcntl-h   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
filename   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
idx   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
intprops   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
libc-config   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME
 = 1]
memmove   [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]

mempcpy [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
pathmax [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
rawmemchr [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
readlink [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
scratch_buffer [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
stat [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
stdbool [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
sys_stat [test $HAVE_CANONICALIZE_FILE_NAME = 0 || test
$REPLACE_CANONICALIZE_FILE_NAME = 1]
unistd [test $HAVE_CANONICALIZE_FILE_NAME = 0
|| test $REPLACE_CANONICALIZE_FILE_NAME = 1]

configure.ac:
gl_CANONICALIZE_LGPL
gl_CONDITIONAL([GL_COND_OBJ_CANONICALIZE_LGPL],
[test $HAVE_CANONICALIZE_FILE_NAME = 0 || test $REPLACE_CANONICALIZE_FILE_NAME =
1])
gl_MODULE_INDICATOR([canonicalize-lgpl])
gl_STDLIB_MODULE_INDICATOR([canonicalize_file_name])
gl_STDLIB_MODULE_INDICATOR([realpath])

Makefile.am:
if GL_COND_OBJ_CANONICALIZE_LGPL
lib_SOURCES += canonicalize-lgpl.c
endif

Include:
<stdlib.h>

License:
LGPLv2+

Maintainer:
all
Description:
Concatenate two arbitrary file names.

Files:
lib/filenamecat.h
lib/filenamecat-lgpl.c
m4/filenamecat.m4

Depends-on:
c99
basename-lgpl
filename
malloc-posix

configure.ac:
gl_FILE_NAME_CONCAT_LGPL

Makefile.am:
lib_SOURCES += filenamecat-lgpl.c

Include:
"filenamecat.h"

License:
LGPLv2+

Maintainer:
Jim Meyering
Files:
tests/test-update-copyright.sh

Depends-on:

configure.ac:
abs_aux_dir=`cd "$ac_aux_dir"; pwd`
AC_SUBST([abs_aux_dir])

Makefile.am:
TESTS += test-update-copyright.sh
TESTS_ENVIRONMENT += abs_aux_dir='$(abs_aux_dir)'

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@page
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and a brief idea of what it does.}
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@smallexample

Gnomovision version
69, Copyright (C) @var{year} @var{name of author}
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
@end smallexample

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@example
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.

@var{signature of Ty Coon}, 1 April 1989
Ty Coon, President of Vice
@end example

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Description:
Provide the GNU Free Documentation License in Texinfo format.

Notice:
Don't use this module! Instead, copy the referenced license file into your version control repository.

Files:
doc/fdl.texi

Depends-on:

configure.ac:

Makefile.am:

Include:

License:
unmodifiable license text

Maintainer:
all
Description:
Help make libraries relocatable, that is, to allow them to function
properly when copied to an arbitrary directory.

Files:
doc/relocatable.texi
lib/relocatable.h
lib/relocatable.c
lib/relocatable.valgrind
m4/relocatable-lib.m4

Depends-on:
strdup

configure.ac:
gl_RELOCATABLE_LIBRARY
if test $RELOCATABLE = yes; then
 AC_LIBOBJ([relocatable])
fi

Makefile.am:
DEFS += -DNO_XMALLOC

Include:
"relocatable.h"

License:
LGPLv2+

Maintainer:
Ben Pfaff
Description:
Extract specific portions of filenames.

Files:
lib/dirname.h
lib/dirname-lgpl.c
lib/stripslash.c

Depends-on:
basename-lgpl
double-slash-root
filename

malloc-posix
stdbool

configure.ac:

Makefile.am:
lib_SOURCES += dirname-lgpl.c stripslash.c

Include:
"dirname.h"

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Maintainer:
Jim Meyering, Eric Blake

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Version 2.1, February 1999

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Files:
tests/test-canonicalize-lgpl.c
tests/signature.h
tests/null-ptr.h
tests/macros.h

Depends-on:
ignore-value
same-inode
symlink

configure.ac:

Makefile.am:
TESTS += test-canonicalize-lgpl
check_PROGRAMS += test-canonicalize-lgpl
test_canonicalize_lgpl_LDADD = $(LDADD) $(LIBINTL)

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Description:
Extract the last component (base name) of a file name.

Files:
lib/basename-lgpl.h
lib/basename-lgpl.c

Depends-on:
double-slash-root
filename
stdbool

configure.ac:

Makefile.am:
lib_SOURCES += basename-lgpl.c

Include:
"basename-lgpl.h"

License:
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Maintainer:
all

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Files:
tests/test-getcwd-lgpl.c
tests/signature.h
tests/macros.h

Depends-on:

configure.ac:

Makefile.am:
TESTS += test-getcwd-lgpl
check_PROGRAMS += test-getcwd-lgpl
test_getcwd_lgpl_LDADD = $(LDADD) $(LIBINTL)
Description:
Ensure getcwd(NULL, 0) returns a buffer allocated by the malloc() function.

Files:
lib/getcwd-lgpl.c
m4/getcwd.m4

Depends-on:
unistd
free-posix [test $REPLACE_GETCWD = 1]
strdup [test $REPLACE_GETCWD = 1]

configure.ac:
gl_FUNC_GETCWD_LGPL
gl_CONDITIONAL([GL_COND_OBJ_GETCWD_LGPL], [test $REPLACE_GETCWD = 1])
gl_UNISTD_MODULE_INDICATOR([getcwd])

Makefile.am:
if GL_COND_OBJ_GETCWD_LGPL
lib_SOURCES += getcwd-lgpl.c
endif

Include:
<unistd.h>

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Maintainer:
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Description:
Locating a program in PATH (LGPLed version).

Files:
lib/findprog.h
lib/findprog.c
lib/findprog-lgpl.c
m4/findprog.m4
m4/eaccess.m4

Depends-on:
stdbool
sys_stat
strdup
concat-filename
access
stat
unistd

configure.ac:
gl_FINDPROG
gl_MODULE_INDICATOR([findprog-lgpl])

Makefile.am:
lib_SOURCES += findprog.h findprog-lgpl.c

Include:
"findprog.h"

License:
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Maintainer:
all

1.46 zstd 1.3.1

1.46.1 Available under license :

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1.47 openssl 1.0.2k

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1.48 mariadb 10.11.14

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Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest possible use to the public, the best way to achieve this is to make it free software which everyone can redistribute and change under these terms.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a) Accompany the work with the complete corresponding machine-readable
source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the
user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these

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Christian Hammers <ch@debian.org>.

The MariaDB packages were initially made by http://ourdelta.org/, and
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/* Structure of the name list */

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 const char *name;
 const char *location;
 const char *comment;
};

/*
 Output from "SHOW CONTRIBUTORS"

  Get permission before editing.

 Names should be encoded using UTF-8.

 See also https://mariadb.com/kb/en/log-of-mariadb-contributions/
*/

struct show_table_contributors_st show_table_contributors[]= {
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A. HISTORY OF THE SOFTWARE
==========

Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands
as a successor of a language called ABC. Guido remains Python's
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us)
in Reston, Virginia where he released several versions of the
software.

In May 2000, Guido and the Python core development team moved to
BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope

Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes

2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1.1	2010	PSF	yes
3.1.3	3.1.2	2010	PSF	yes
3.1.4	3.1.3	2011	PSF	yes
3.2	3.1	2011	PSF	yes
3.2.1	3.2	2011	PSF	yes
3.2.2	3.2.1	2011	PSF	yes
3.2.3	3.2.2	2012	PSF	yes
3.3.0	3.2	2012	PSF	yes

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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```
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  trusted {

  };

  untrusted {
    /* define OCALLs here. */

  };
};
```

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1.49 binutils 2.35.2-63.el9.x86_64

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Foundation, Inc., 51 Franklin Street - Fifth Floor, Boston, MA
02110-1301, USA. */

#ifndef __VTV_H__
#define __VTV_H__

/* We could have used an enumeration here but it just makes it more
difficult for the compiler to generate a call to this. These are
used as arguments to the function __VLTChangePermission, declared
below. */

```
#define __VLTP_READ_ONLY  0
#define __VLTP_READ_WRITE 1

#ifdef __cplusplus
extern "C" void __VLTChangePermission (int);
#else
extern void __VLTChangePermission (int);
#endif

#ifdef BIG_PAGE_SIZE
/* TODO - Replace '4096' below with correct big page size.  */
#define VTV_PAGE_SIZE 4096
#else
#if defined(__sun__) && defined(__svr4__) && defined(__sparc__)
#define VTV_PAGE_SIZE 8192
#else
#define VTV_PAGE_SIZE 4096
#endif
#endif



#endif /* __VTV_H__ */
```

(This file is under construction.) -*- text -*-

If you've contributed to gas and your name isn't listed here, it is not meant as a slight. I just don't know about it. Email me, nickc@redhat.com and I'll correct the situation.

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++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration

including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of
Buffalo University and Torbjorn Granlund of the Swedish Institute of
Computer Science.

Keith
Knowles at the Open Software Foundation wrote the original MIPS
back end (tc-mips.c, tc-mips.h), and contributed Rose format support
that hasn't been merged in yet. Ralph Campbell worked with the MIPS
code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors
(tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format
(obj-ieee), was written by Steve Chamberlain of Cygnus Solutions.
Steve also modified the COFF back end (obj-coffbfd) to use BFD for
some low-level operations, for use with the Hitachi, 29k and Zilog
targets.

John Gilmore built the AMD 29000 support, added .include support, and
simplified the configuration of which versions accept which
pseudo-ops. He updated the 68k machine description so that Motorola's
opcodes always produced fixed-size instructions (e.g. jsr), while
synthetic instructions remained shrinkable (jbsr). John fixed many
bugs, including true tested cross-compilation support, and one bug in
relaxation
that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT
syntaxes for the 68k, completed support for some COFF targets (68k,
i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael

Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support,
and made a few other minor patches. He handled the binutils releases
for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of
Cygnus Solutions (original, incomplete implementation), Pete
Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner
of the Open Software Foundation (i386 mainly), and Ken Raeburn of
Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many
 small
bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon
University. Additional work was done by Ken Raeburn of Cygnus
Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000
series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30
(tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error
checking, updating the code, and improving the 16 bit support, using
patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If
you've contributed significant work and are not mentioned on this list, and
want to be, let us know. Some of the history has been lost;
 we aren't
intentionally leaving anyone out.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered
by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any
executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit

modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception,

the materials to be distributed need not include anything that is
normally distributed (in either source or binary form) with
the major
components (compiler, kernel, and so on) of the operating system on
which the executable runs, unless that component itself accompanies
the executable.

It may happen that this requirement contradicts the license
restrictions of other proprietary libraries that do not normally
accompany the operating system. Such a contradiction means you cannot
use both them and the Library together in an executable that you
distribute.

7. You may place library facilities that are a work based on the
Library side-by-side in a single library together with other library
facilities not covered by this License, and distribute such a combined
library, provided that the separate distribution of the work based on
the Library and of the other library facilities is otherwise
permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work
based on the Library, uncombined with any other library
facilities. This must be distributed
under the terms of the
Sections above.

b) Give prominent notice with the combined library of the fact
that part of it is a work based on the Library, and explaining
where to find the accompanying uncombined form of the same work.

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If any portion of this section is held invalid or unenforceable under any particular circumstance, the balance of the section is intended to apply, and the section as a whole is intended to apply in other circumstances.

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people have made
generous contributions to the wide range of software distributed through that system in reliance on consistent application of that system; it is up to the author/donor to decide if he or she is willing to distribute software through any other system and a licensee cannot impose that choice.

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and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

You
should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

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library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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[This is the first released version of the library GPL. It is
numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

The licenses for most software are designed to take away your
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Licenses are intended to guarantee your freedom to share and change
free software--to make sure the software is free for all its users.

This license, the Library General Public License, applies to some
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your libraries, too.

When we speak of free software, we are referring to freedom,
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this service if you wish), that you receive source code or can get it
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To protect your rights, we need to make restrictions that forbid
anyone to deny you these rights or to ask you to surrender the rights.
These restrictions translate to certain responsibilities for you if
you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis
or for a fee, you must give the recipients all the rights that we gave
you. You must make sure that they, too, receive or can get the source
code. If you link a program with the library, you must provide
complete object files to the recipients so that they can relink them
with the library, after making
changes to the library and recompiling
it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright
the library, and (2) offer you this license which gives you legal
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the original authors' reputations.

Finally, any free program is threatened constantly by software
patents. We wish to avoid the danger that companies distributing free
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we have made it clear that any patent must be licensed
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Most GNU software, including some libraries, is covered by the ordinary
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license, the GNU Library General Public License, applies to certain
designated libraries. This license is quite different from the ordinary
one; be sure to read it in full, and don't assume that anything in it is
the same as in the ordinary license.

The reason we have a separate public license for some libraries is that
they blur the distinction we usually make between modifying or adding to a
program and simply using it. Linking a program with a library, without
changing the library, is in some sense simply using the library, and is
analogous to running a utility program or application program. However, in
a textual and legal sense, the linked executable is a combined work, a
derivative of the original library, and the ordinary General Public License
treats it as such.

Because
of this blurred distinction, using the ordinary General
Public License for libraries did not effectively promote software
sharing, because most developers did not use the libraries. We
concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the
users of those programs of all benefit from the free status of the
libraries themselves. This Library General Public License is intended to
permit developers of non-free programs to use free libraries, while
preserving your freedom as a user of such programs to change the free
libraries that are incorporated in them. (We have not seen how to achieve
this as regards changes in header files, but we have achieved it as regards
changes in the actual functions of the Library.) The hope is that this
will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's

complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of

this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition,
mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the
Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a

work, in isolation, is not a derivative
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therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of
the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or
link a "work that uses the Library" with the Library to produce a
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under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
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a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever

changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the

Sections above.

b) Give prominent notice with the combined library of the fact
that part of it is a work based on the
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where to find the accompanying uncombined form of the same work.

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If any portion of this section is held invalid or unenforceable under any

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Appendix: How to Apply These Terms to Your New Libraries

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ordinary General Public License).

To apply these terms, attach the following notices to the library. It is
safest to attach them to the start of each source file to most effectively
convey the exclusion of warranty; and each file should have at least the
"copyright" line and a pointer to where the full notice is found.

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## Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
```

```
STATIC_O = ao

## Compilation
 rules
%.$(STATIC_O): $(srcdir)/%.c
 $(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
 $(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
 $(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
 $(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<


## Dependency rules
%.d: $(srcdir)/%.c
 @echo "generating dependency information for $<"
 @$(SHELL) -ec '$(GEN_DEPS.c) $< \
  | sed '\"s/\($*\)\.o[ :]*/\1.o $@ : /g'\" > $@; \
  [ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
 @echo "generating dependency information for $<"
 @$(SHELL) -ec '$(GEN_DEPS.cc) $< \
  | sed '\"s/\($*\)\.o[ :]*/\1.o $@ : /g'\" > $@; \
  [ -s $@ ] || rm -f $@'

## Versioned libraries rules

%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
 $(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
 $(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

##  Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup
```

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It was originally written by Tatu Saloranta (tatu.saloranta@iki.fi), and has
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* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/binders/BindNodeConfigurationToXmlPayload.java
*
/jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/endpoints/KeysList.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/TerremarkVCloudClient.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/compute/suppliers/VAppTemplatesInOrgs.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/xml/InternetServicesHandler.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/config/TerremarkVCloudExpressRestClientModule.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/functions/ParseTaskFromLocationHeader.java
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* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/functions/ReturnVoidOnDeleteDefaultIp.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/compute/TerremarkVCloudComputeService.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/domain/VAppConfiguration.java
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* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/config/TerremarkECloudRestClientModule.java
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* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/compute/functions/TemplateToInstantiateOptions.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/TerremarkECloudAsyncClient.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/handlers/ParseTerremarkVCloudErrorFromHttpResponse.java

* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/xml/InternetServiceHandler.java
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/jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/xml/TerremarkCatalogItemHandler.java
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* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/binders/BindAddNodeServiceToXmlPayload.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/xml/PublicIpAddressHandler.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/domain/TerremarkVDC.java
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* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/domain/IpAddress.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/domain/TerremarkOrg.java
* /jclouds-terremark-1-0-beta-7-sources-jar/org/jclouds/vcloud/terremark/binders/TerremarkBindCloneVAppParamsToXmlPayload.java

1.61 go-sysconf 0.3.12

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1.62 libuser 0.63-16.el9

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that any
application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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1.63 jruby-complete 9.4.6.0

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1.67 gstreamer 1.22.12-3.el9

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1.69 boost 1.75.0-10.el9

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Julian Seward, jseward@acm.org
bzip2/libbzip2 version 1.0.8 of 13 July 2019

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http://www.boost.org/LICENSE_1_0.txt)
*/

#ifndef BOOST_PREDEF_OS_BSD_H
#define BOOST_PREDEF_OS_BSD_H

/* Special case: OSX will define BSD predefs if the sys/param.h
* header is included. We can guard against that, but only if we
* detect OSX first. Hence we will force include OSX detection
* before doing any BSD detection.
*/
#include <boost/predef/os/macos.h>

#include <boost/predef/version_number.h>
#include <boost/predef/make.h>

/* tag::reference[]
= `BOOST_OS_BSD`

http://en.wikipedia.org/wiki/Berkeley_Software_Distribution[BSD] operating system.

BSD has various branch operating systems possible and each detected
individually. This detects the following variations and sets a specific
version number macro to match:

* `BOOST_OS_BSD_DRAGONFLY` http://en.wikipedia.org/wiki/DragonFly_BSD[DragonFly BSD]
* `BOOST_OS_BSD_FREE` http://en.wikipedia.org/wiki/Freebsd[FreeBSD]
*
`BOOST_OS_BSD_BSDI` http://en.wikipedia.org/wiki/BSD/OS[BSDi BSD/OS]
* `BOOST_OS_BSD_NET` http://en.wikipedia.org/wiki/Netbsd[NetBSD]
* `BOOST_OS_BSD_OPEN` http://en.wikipedia.org/wiki/Openbsd[OpenBSD]

NOTE: The general `BOOST_OS_BSD` is set in all cases to indicate some form
of BSD. If the above variants is detected the corresponding macro is also set.

[options="header"]
|===

```
| {predef_symbol} | {predef_version}

| `BSD` | {predef_detection}
| `+_SYSTYPE_BSD+` | {predef_detection}

| `BSD4_2` | 4.2.0
| `BSD4_3` | 4.3.0
| `BSD4_4` | 4.4.0
| `BSD` | V.R.0
|===
*/ // end::reference[]

#include <boost/predef/os/bsd/bsdi.h>
#include <boost/predef/os/bsd/dragonfly.h>
#include <boost/predef/os/bsd/free.h>
#include <boost/predef/os/bsd/open.h>
#include <boost/predef/os/bsd/net.h>

#ifndef BOOST_OS_BSD
#define BOOST_OS_BSD BOOST_VERSION_NUMBER_NOT_AVAILABLE
#endif

#if !defined(BOOST_PREDEF_DETAIL_OS_DETECTED) && ( \
  defined(BSD) || \
  defined(_SYSTYPE_BSD)
 \
  )
#   undef BOOST_OS_BSD
#   include <sys/param.h>
#   if !defined(BOOST_OS_BSD) && defined(BSD4_4)
#       define BOOST_OS_BSD BOOST_VERSION_NUMBER(4,4,0)
#   endif
#   if !defined(BOOST_OS_BSD) && defined(BSD4_3)
#       define BOOST_OS_BSD BOOST_VERSION_NUMBER(4,3,0)
#   endif
#   if !defined(BOOST_OS_BSD) && defined(BSD4_2)
#       define BOOST_OS_BSD BOOST_VERSION_NUMBER(4,2,0)
#   endif
#   if !defined(BOOST_OS_BSD) && defined(BSD)
#       define BOOST_OS_BSD BOOST_PREDEF_MAKE_10_VVRR(BSD)
#   endif
#   if !defined(BOOST_OS_BSD)
#       define BOOST_OS_BSD BOOST_VERSION_NUMBER_AVAILABLE
#   endif
#endif

#if BOOST_OS_BSD
#   define BOOST_OS_BSD_AVAILABLE
```

```
#   include <boost/predef/detail/os_detected.h>
#endif

#define BOOST_OS_BSD_NAME "BSD"

#endif

#include <boost/predef/os/bsd/bsdi.h>
#include <boost/predef/os/bsd/dragonfly.h>
#include <boost/predef/os/bsd/free.h>
#include <boost/predef/os/bsd/open.h>
#include <boost/predef/os/bsd/net.h>

#include <boost/predef/detail/test.h>
BOOST_PREDEF_DECLARE_TEST(BOOST_OS_BSD,BOOST_OS_BSD_NAME)
```

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[#mpl]
MPL Support, <boost/mp11/mpl.hpp>
:toc:
:toc-title:
:idprefix:

The header `<boost/mp11/mpl.hpp>`, when included, defines the
necessary support infrastructure for `mp_list` and `std::tuple`
to be valid link:../../../../libs/mpl[MPL] sequences.

NOTE: `mpl.hpp` is not included by `<boost/mp11.hpp>`.

It's also possible to only enable support for `mp_list` by
including `<boost/mp11/mpl_list.hpp>`, and for `std::tuple`
by including `<boost/mp11/mpl_tuple.hpp>`. This may be required
because some libraries, such as Boost.Fusion, contain their own MPL
support for `std::tuple`, which conflicts with Mp11's one.
<!DOCTYPE html>
<html>
 <head></head>
 <body>
  <h3>
   Copyright Test
  </h3>

   1963, 1964, 1965 Jane Doe

   2018 Joe Blow, John Coe

   1977, 1985 Someone else

 </body>
</html>
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"http://www.boost.org/tools/boostbook/dtd/boostbook.dtd">
<article id="copyright_test" last-revision="DEBUG MODE Date: 2000/12/20 12:00:00 $"
xmlns:xi="http://www.w3.org/2001/XInclude">
 <title>Copyright Test</title>
 <articleinfo>

```
  <copyright>
    <year>1963</year> <year>1964</year> <year>1965</year> <holder>Jane Doe</holder>
  </copyright>
  <copyright>
    <year>2018</year> <holder>Joe Blow, John Coe</holder>
  </copyright>
  <copyright>
    <year>1977</year> <year>1985</year> <holder>Someone else</holder>
  </copyright>
 </articleinfo>
</article>
```

```
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/regex/OpCode.java
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* $Id: RegexFilenameFilter.java,v 1.9 2003/11/07 20:16:23 dfs Exp $
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* $Id: splitExample.java,v 1.8 2003/11/07 20:16:23 dfs Exp $
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* /oro-2-0-8-sources-4-jar/examples/splitExample.java
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* /oro-2-0-8-sources-4-jar/examples/filter.java

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* /oro-2-0-8-sources-4-jar/tools/oroToApache.java
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* $Id: matchResultExample.java,v 1.7 2003/11/07 20:16:23 dfs Exp $
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* /oro-2-0-8-sources-4-jar/examples/matchResultExample.java
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* /oro-2-0-8-sources-4-jar/examples/awk/prefixExample.java
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/regex/Perl5Pattern.java
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/regex/PatternMatcherInput.java
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/PatternCacheFIFO2.java
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* $Id: MalformedPatternException.java,v 1.8 2003/11/07 20:16:25 dfs Exp $
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* /oro-2-0-8-sources-4-jar/examples/awk/strings.java
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*/
/**
  * Determines if the contents of a PatternMatcherInput, starting from the
  * current offset of the input contains a pattern.
  * If a pattern match is found, a MatchResult
   * instance representing the <b>first</b> such match is made acessible via
  * {@link #getMatch()}.  The current offset of the
  * PatternMatcherInput is set to the offset corresponding to the end
  * of the match, so that a subsequent call to this method will continue
  * searching where the last call left off.  You should remember that the
  * region between the begin and end offsets of the PatternMatcherInput are
  * considered the input to be searched, and that the current offset
  * of the PatternMatcherInput reflects where a search will start from.
  * Matches extending beyond the end offset of the PatternMatcherInput
  * will not be matched.  In other words, a match must occur entirely
  * between the begin and end offsets of the input.  See
  * {@link org.apache.oro.text.regex.PatternMatcherInput PatternMatcherInput}
  * for more details.
  * <p>
  * As a side effect, if a match is found, the PatternMatcherInput match
  * offset information is updated.  See
```

```
 the PatternMatcherInput
  * {@link org.apache.oro.text.regex.PatternMatcherInput#setMatchOffsets
  * setMatchOffsets(int, int)} method for more details.
  * <p>
  * The pattern must be an AwkPattern instance, otherwise a
  * ClassCastException will be thrown.  You are not required to, and
  * indeed should NOT try to (for performance reasons), catch a
  * ClassCastException because it will never be thrown as long as you use
  * an AwkPattern as the pattern parameter.
  * <p>
  * This method is usually used in a loop as follows:
  * <blockquote><pre>
  * PatternMatcher matcher;
  * PatternCompiler compiler;
  * Pattern pattern;
  * PatternMatcherInput input;
  * MatchResult result;
  *
  * compiler = new AwkCompiler();
  * matcher  = new AwkMatcher();
  *
  * try {
  *   pattern = compiler.compile(somePatternString);
  * } catch(MalformedPatternException e) {
  *   System.err.println("Bad pattern.");
  *   System.err.println(e.getMessage());
   *   return;
  * }
  *
  * input   = new PatternMatcherInput(someStringInput);
  *
  * while(matcher.contains(input, pattern)) {
  *   result = matcher.getMatch();
  *   // Perform whatever processing on the result you want.
  * }
  *
  * </pre></blockquote>
  * <p>
  * @param input  The PatternMatcherInput to test for a match.
  * @param pattern  The Pattern to be matched.
  * @return True if the input contains a pattern match, false otherwise.
  * @exception ClassCastException If a Pattern instance other than an
  *         AwkPattern is passed as the pattern parameter.
  */

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 * /oro-2-0-8-sources-4-jar/org/apache/oro/text/awk/AwkMatcher.java
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* $Id: LeafNode.java,v 1.7 2003/11/07 20:16:24 dfs Exp $
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/regex/Perl5Matcher.java
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/*
* $Id: AwkFilenameFilter.java,v 1.7 2003/11/07 20:16:23 dfs Exp $
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/awk/StarNode.java
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* $Id: Perl5Debug.java,v 1.11 2003/11/07 20:16:25 dfs Exp $
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* /oro-2-0-8-sources-4-jar/examples/awk/substituteExample.java
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* /oro-2-0-8-sources-4-jar/org/apache/oro/text/perl/ParsedSubstitutionEntry.java
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1.77 pciutils 3.7.0-7.el9

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If such an object file uses only numerical parameters, data
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1.80 mysql-jdbc 8.0

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No license file was found, but licenses were detected in source scan.

/mysql-connector-java-8-0-11-jar/com/mysql/cj/protocol/AuthenticationPlugin.class: binary file matches
/mysql-connector-java-8-0-11-jar/com/mysql/cj/protocol/a/NativeAuthenticationProvider.class: binary file matches
/mysql-connector-java-8-0-11-jar/com/mysql/cj/protocol/a/authentication/MysqlClearPasswordPlugin.class: binary file matches
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/mysql-connector-java-8-0-11-jar/com/mysql/cj/protocol/a/authentication/MysqlOldPasswordPlugin.class: binary file matches
/mysql-connector-java-8-0-11-jar/com/mysql/cj/protocol/a/authentication/Sha256PasswordPlugin.class: binary file matches

Found in path(s):
* /bin/grep

1.81 glibc 2.34-168.el9_6.14.alma.1

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of the original library. The ordinary
General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the @dfn{Lesser} General Public License because it does @emph{Less} to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the
same job as widely used non-free libraries. In this
case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a ``work based on the library'' and a ``work that uses the library''. The former

contains code derived from the library, whereas the latter must
be combined with the library in order to run.

@subheading TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION

@enumerate 0
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This License Agreement applies to any software library or other program
which contains a notice placed by the copyright holder or other
authorized party saying it may be distributed under the terms of this
Lesser General Public License (also called ``this License''). Each
licensee is addressed as ``you''.

A ``library'' means a collection of software functions and/or data
prepared so as to be conveniently linked with application programs
(which use some of those functions and data) to form executables.

The ``Library'', below, refers to any such software library or work
which has been distributed under these terms. A ``work based on the
Library'' means either the Library or any derivative work under
copyright law: that is to say, a work containing the Library or a
portion of it,
either verbatim or with modifications and/or translated
straightforwardly into another language. (Hereinafter, translation is
included without limitation in the term ``modification''.)

``Source code'' for a work means the preferred form of the work for
making modifications to it. For a library, complete source code means
all the source code for all modules it contains, plus any associated
interface definition files, plus the scripts used to control compilation
and installation of the library.

Activities other than copying, distribution and modification are not
covered by this License; they are outside its scope. The act of
running a program using the Library is not restricted, and output from
such a program is covered only if its contents constitute a work based
on the Library (independent of the use of the Library in a tool for
writing it). Whether that is true depends on what the Library does
and what the program that uses the Library does.

@item
You may copy and distribute
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you conspicuously and appropriately publish on each copy an
appropriate copyright notice and disclaimer of warranty; keep intact
all the notices that refer to this License and to the absence of any
warranty; and distribute a copy of this License along with the

Library.

You may charge a fee for the physical act of transferring a copy,
and you may at your option offer warranty protection in exchange for a
fee.

@item
You may modify your copy or copies of the Library or any portion
of it, thus forming a work based on the Library, and copy and
distribute such modifications or work under the terms of Section 1
above, provided that you also meet all of these conditions:

@enumerate a
@item
The modified work must itself be a software library.

@item
You must cause the files modified to carry prominent notices
stating that you changed the files and the date of any change.

@item
You must
cause the whole of the work to be licensed at no
charge to all third parties under the terms of this License.

@item
If a facility in the modified Library refers to a function or a
table of data to be supplied by an application program that uses
the facility, other than as an argument passed when the facility
is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
root function must still compute square roots.)
@end enumerate

These requirements apply to the modified work as a whole. If
identifiable sections
of that work are not derived from the Library,
and can be reasonably considered independent and separate works in
themselves, then this License, and its terms, do not apply to those
sections when you distribute them as separate works. But when you

distribute the same sections as part of a whole which is a work based
on the Library, the distribution of the whole must be on the terms of
this License, whose permissions for other licensees extend to the
entire whole, and thus to each and every part regardless of who wrote
it.

Thus, it is not the intent of this section to claim rights or contest
your rights to work written entirely by you; rather, the intent is to
exercise the right to control the distribution of derivative or
collective works based on the Library.

In addition, mere aggregation of another work not based on the Library
with the Library (or with a work based on the Library) on a volume of
a storage or distribution medium does not bring the other work under
the scope of this
License.

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You may opt to apply the terms of the ordinary GNU General Public
License instead of this License to a given copy of the Library. To do
this, you must alter all the notices that refer to this License, so
that they refer to the ordinary GNU General Public License, version 2,
instead of to this License. (If a newer version than version 2 of the
ordinary GNU General Public License has appeared, then you can specify
that version instead if you wish.) Do not make any other change in
these notices.

Once this change is made in a given copy, it is irreversible for
that copy, so the ordinary GNU General Public License applies to all
subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

@item
You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and
2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

@item
A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a ``work that uses the Library''. Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a ``work that uses the Library'' with the Library
creates an executable that is a derivative of the Library (because
it
contains portions of the Library), rather than a ``work that uses the
library''. The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a ``work that uses the Library'' uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise,
if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

@item
As an exception to the Sections above, you may also combine or
link a ``work that uses the Library'' with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the
Library is used in it and that the Library and its use are covered by
this License. You must supply a copy of this License. If the work
during execution displays copyright notices, you must include the
copyright notice for the Library among them, as well as a reference

directing the user to the copy of
this License. Also, you must do one
of these things:

@enumerate a
@item
Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable ``work that
uses the Library'', as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

@item
Use a suitable shared library mechanism for linking with the Library. A
suitable mechanism is one that (1) uses at run time a copy of the
library already present on the user's computer system, rather than
copying library functions into the executable,
and (2) will operate
properly with a modified version of the library, if the user installs
one, as long as the modified version is interface-compatible with the
version that the work was made with.

@item
Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

@item
If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above
specified materials from the same place.

@item
Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.
@end enumerate

For an executable, the required form of the ``work that uses the
Library'' must include any data and utility programs needed for
reproducing the executable from it. However, as a special exception,
the materials to be distributed need not include anything that

is
normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

@item
You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

@enumerate a
@item
Accompany the combined library with a copy of the same work based on the Library,
uncombined with any other library
facilities. This must be distributed under the terms of the Sections above.

@item
Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.
@end enumerate

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@end enumerate

@subheading END OF TERMS AND CONDITIONS

@page
@subheading How to Apply These Terms to Your New Libraries

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redistribution under these terms (or, alternatively, under the terms of the
ordinary General Public License).

To apply these terms, attach the following notices to the library. It is
safest to attach them to the start of each source
file to most effectively
convey the exclusion of warranty; and each file should have at least the
``copyright'' line and a pointer to where the full notice is found.

@smallexample
@var{one line to give the library's name and an idea of what it does.}
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You should have received a copy of the GNU Lesser General Public
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USA.
@end
smallexample

Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your
school, if any, to sign a ``copyright disclaimer'' for the library, if
necessary. Here is a sample; alter the names:

@smallexample
Yoyodyne, Inc., hereby disclaims all copyright interest in the library
`Frob' (a library for tweaking knobs) written by James Random Hacker.

@var{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice
@end smallexample

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Preamble

The licenses for most software are designed to take away your freedom to share and change it. By contrast, the GNU General Public Licenses are intended to guarantee your freedom to share and change free software--to make sure the software is free for all its users.

This license, the Lesser General Public License, applies to some specially designated software packages--typically libraries--of the Free Software Foundation and other authors who decide to use it. You can use it too, but we suggest you first think carefully about whether this license or the ordinary General Public License is the better strategy to use in any particular case, based on the explanations below.

When we speak of free software, we are referring to freedom of use, not price. Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish); that you receive source code or can get it if you want it; that you can change the software and use pieces of it in new free programs; and that you are informed that you can do these things.

To protect your rights, we need to make restrictions that forbid distributors to deny you these rights or to ask you to surrender these rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know

that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in

non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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0. This License Agreement applies to any software library or other program which contains a notice placed by the copyright holder or other authorized party saying it may be distributed under the terms of this Lesser General Public License (also called "this License"). Each licensee is addressed as "you".

A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based

on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library,

and can be
reasonably considered independent and separate works in
themselves, then this License, and its terms, do not apply to those
sections when you distribute them as separate works. But when you
distribute the same sections as part of a whole which is a work based
on the Library, the distribution of the whole must be on the terms of
this License, whose permissions for other licensees extend to the
entire whole, and thus to each and every part regardless of who wrote
it.

Thus, it is not the intent of this section to claim rights or contest
your rights to work written entirely by you; rather, the intent is to
exercise the right to control the distribution of derivative or
collective works based on the Library.

In addition, mere aggregation of another work not based on the Library
with the Library (or with a work based on the Library) on a volume of
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subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or
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under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding
machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to

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1.85 slf4j 1.7.10

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* /slf4j-api-1-7-10-sources-jar/org/slf4j/impl/StaticMDCBinder.java
* /slf4j-api-1-7-10-sources-jar/org/slf4j/Logger.java
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1.86 golang-snappy 0.0.4

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1.92 afero 1.6.0

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1.93 color 1.13.0

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1.94 polkit 0.117-13.el9

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```
; 7zAsm.asm -- ASM macros
; 2018-02-03 : Igor Pavlov : Public domain

MY_ASM_START macro
 ifdef x64
   .code
 else
   .386
   .model flat
   _TEXT$00 SEGMENT PARA PUBLIC 'CODE'
 endif
endm

MY_PROC macro name:req, numParams:req
 align 16
 proc_numParams = numParams
 ifdef x64
   proc_name equ name
 else
   proc_name equ @CatStr(@,name,@, %numParams * 4)
 endif
 proc_name PROC
endm

MY_ENDP macro
 ifdef x64
   ret
 else
   if proc_numParams LT 3
     ret
   else
     ret (proc_numParams - 2) * 4
   endif
 endif
 proc_name ENDP
endm

ifdef x64
 REG_SIZE equ 8
 REG_LOGAR_SIZE equ 3
else
 REG_SIZE equ 4
 REG_LOGAR_SIZE equ 2
endif

 x0 equ EAX
 x1 equ ECX
```

```
x2 equ EDX
x3 equ EBX
x4 equ ESP
x5 equ EBP
x6 equ ESI
x7 equ EDI

x0_W equ AX
x1_W equ CX
x2_W equ DX
x3_W equ BX

x5_W equ BP
x6_W equ SI
x7_W equ DI

x0_L equ AL
x1_L equ CL
x2_L equ DL
x3_L equ BL

x0_H equ AH
x1_H equ CH
x2_H equ DH
x3_H equ BH

ifdef x64
x5_L
equ BPL
x6_L equ SIL
x7_L equ DIL

r0 equ RAX
r1 equ RCX
r2 equ RDX
r3 equ RBX
r4 equ RSP
r5 equ RBP
r6 equ RSI
r7 equ RDI
x8 equ r8d
x9 equ r9d
x10 equ r10d
x11 equ r11d
x12 equ r12d
x13 equ r13d
x14 equ r14d
x15 equ r15d
```

```
else
 r0 equ x0
 r1 equ x1
 r2 equ x2
 r3 equ x3
 r4 equ x4
 r5 equ x5
 r6 equ x6
 r7 equ x7
endif

MY_PUSH_4_REGS macro
  push   r3
  push   r5
  push   r6
  push   r7
endm

MY_POP_4_REGS macro
  pop    r7
  pop    r6
  pop    r5
  pop    r3
endm


ifdef x64

; for WIN64-x64 ABI:

REG_PARAM_0 equ r1
REG_PARAM_1 equ r2
REG_PARAM_2 equ r8
REG_PARAM_3 equ r9

MY_PUSH_PRESERVED_REGS macro
  MY_PUSH_4_REGS
  push   r12
  push   r13
  push   r14
  push   r15
endm


MY_POP_PRESERVED_REGS macro
  pop    r15
  pop    r14
  pop    r13
```

```
  pop     r12
  MY_POP_4_REGS
endm

endif
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-017-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
  {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-017.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
```

</pre>
</body>
</html>
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wOF2OTTO; @g
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-

.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$v<gebd`P "6
ee,v``JW4ESke;`vQ_6FTY6.-?yqe(Sq
Snappy framing format description
Last revised: 2013-10-25

This format decribes a framing format for Snappy, allowing compressing to files or streams that can then more easily be decompressed without having to hold the entire stream in memory. It also provides data checksums to help verify integrity. It does not provide metadata checksums, so it does not protect against e.g. all forms of truncations.

Implementation of the framing format is optional for Snappy compressors and decompressor; it is not part of the Snappy core specification.

1. General structure

The file consists solely of chunks, lying back-to-back with no padding in between. Each chunk consists first a single byte of chunk identifier, then a three-byte little-endian length of the chunk in bytes (from 0 to 16777215, inclusive), and then the data if any. The four bytes of chunk header is not counted in the data length.

The different chunk types are listed below. The first chunk must always be the stream identifier chunk
(see section 4.1, below). The stream
ends when the file ends -- there is no explicit end-of-file marker.

2. File type identification

The following identifiers for this format are recommended where appropriate. However, note that none have been registered officially, so this is only to be taken as a guideline. We use "Snappy framed" to distinguish between this format and raw Snappy data.

File extension:	.sz
MIME type:	application/x-snappy-framed
HTTP Content-Encoding:	x-snappy-framed

3. Checksum format

Some chunks have data protected by a checksum (the ones that do will say so explicitly). The checksums are always masked CRC-32Cs.

A description of CRC-32C can be found in RFC 3720, section 12.1, with examples in section B.4.

Checksums are not stored directly, but masked, as checksumming data and then its own checksum can be problematic. The masking is the same as used in Apache Hadoop: Rotate the checksum by 15 bits, then add the constant 0xa282ead8
(using wraparound as normal for unsigned integers). This is equivalent to the following C code:

```
uint32_t mask_checksum(uint32_t x) {
  return ((x >> 15) | (x << 17)) + 0xa282ead8;
}
```

Note that the masking is reversible.

The checksum is always stored as a four bytes long integer, in little-endian.

4. Chunk types

The currently supported chunk types are described below. The list may be extended in the future.

4.1. Stream identifier (chunk type 0xff)

The stream identifier is always the first element in the stream.
It is exactly six bytes long and contains "sNaPpY" in ASCII. This means that a valid Snappy framed stream always starts with the bytes

```
0xff 0x06 0x00 0x00 0x73 0x4e 0x61 0x50 0x70 0x59
```

The stream identifier chunk can come multiple times in the stream besides the first; if such a chunk shows up, it should simply be ignored, assuming it has the right length and contents. This allows for easy concatenation of compressed files without the need for re-framing.

4.2. Compressed data (chunk type 0x00)

Compressed data chunks contain a normal Snappy compressed bitstream; see the compressed format specification. The compressed data is preceded by the CRC-32C (see section 3) of the _uncompressed_ data.

Note that the data portion of the chunk, i.e., the compressed contents,
can be at most 16777211 bytes (2^24 - 1, minus the checksum).
However, we place an additional restriction that the uncompressed data
in a chunk must be no longer than 65536 bytes. This allows consumers to
easily use small fixed-size buffers.

4.3. Uncompressed data (chunk type 0x01)

Uncompressed data chunks allow a compressor to send uncompressed,
raw data; this is useful if, for instance, uncompressible or
near-incompressible data is detected, and faster decompression is desired.

As in the compressed chunks, the data is preceded by its own masked
CRC-32C (see section 3).

An uncompressed data chunk, like compressed data chunks, should contain
no more than 65536 data bytes, so
the maximum legal chunk length with the
checksum is 65540.

4.4. Padding (chunk type 0xfe)

Padding chunks allow a compressor to increase the size of the data stream
so that it complies with external demands, e.g. that the total number of
bytes is a multiple of some value.

All bytes of the padding chunk, except the chunk byte itself and the length,
should be zero, but decompressors must not try to interpret or verify the
padding data in any way.

4.5. Reserved unskippable chunks (chunk types 0x02-0x7f)

These are reserved for future expansion. A decoder that sees such a chunk
should immediately return an error, as it must assume it cannot decode the
stream correctly.

Future versions of this specification may define meanings for these chunks.

4.6. Reserved skippable chunks (chunk types 0x80-0xfd)

These are also reserved for future expansion, but unlike the chunks
described in 4.5, a decoder seeing these must skip them and continue
decoding.

Future versions of this specification
 may define meanings for these chunks.
C_OBJS = $(C_OBJS) \
 $O\XzCrc64.obj
!IF "$(CPU)" == "IA64" || "$(CPU)" == "MIPS" || "$(CPU)" == "ARM" || "$(CPU)" == "ARM64"
C_OBJS = $(C_OBJS) \
!ELSE
ASM_OBJS = $(ASM_OBJS) \
!ENDIF
 $O\XzCrc64Opt.obj
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");

```
  body {
   font-size: 20px;
  }
  pre {
   font-size:
 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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LZMA SDK 18.05

LZMA SDK provides the documentation, samples, header files,
libraries, and tools you need to develop applications that
use 7z / LZMA / LZMA2 / XZ compression.

LZMA is an improved version of famous LZ77 compression algorithm.
It was improved in way of maximum increasing of compression ratio,
keeping high decompression speed and low memory requirements for
decompressing.

LZMA2 is a LZMA based compression method. LZMA2 provides better
multithreading support for compression than LZMA and some other improvements.

7z is a file format for data compression and file archiving.
7z is a main file format for 7-Zip compression program (www.7-zip.org).
7z format supports different compression methods: LZMA, LZMA2 and others.
7z also supports AES-256 based encryption.

XZ is a file format for data compression that uses LZMA2 compression.
XZ format provides additional features: SHA/CRC check, filters for
improved compression ratio, splitting to blocks and streams,

LICENSE

LZMA
SDK is written and placed in the public domain by Igor Pavlov.

Some code in LZMA SDK is based on public domain code from another developers:
1) PPMd var.H (2001): Dmitry Shkarin
2) SHA-256: Wei Dai (Crypto++ library)

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include it to GNU GPL or GNU LGPL code.

LZMA SDK Contents

Source code:

- C / C++ / C# / Java - LZMA compression and decompression
- C / C++ - LZMA2 compression and decompression
- C / C++ - XZ compression and decompression
- C - 7z decompression
- C++ - 7z compression and decompression
- C - small SFXs for installers
(7z decompression)
- C++ - SFXs and SFXs for installers (7z decompression)

Precomiled binaries:

- console programs for lzma / 7z / xz compression and decompression
- SFX modules for installers.

UNIX/Linux version

To compile C++ version of file->file LZMA encoding, go to directory
CPP/7zip/Bundles/LzmaCon
and call make to recompile it:
make -f makefile.gcc clean all

In some UNIX/Linux versions you must compile LZMA with static libraries.
To compile with static libraries, you can use
LIB = -lm -static

Also you can use p7zip (port of 7-Zip for POSIX systems like Unix or Linux):

http://p7zip.sourceforge.net/

Files

DOC/7zC.txt - 7z ANSI-C Decoder description
DOC/7zFormat.txt - 7z Format description
DOC/installer.txt - information about 7-Zip for installers
DOC/lzma.txt - LZMA compression description
DOC/lzma-sdk.txt - LZMA SDK description (this file)
DOC/lzma-history.txt - history of LZMA
SDK
DOC/lzma-specification.txt - Specification of LZMA
DOC/Methods.txt - Compression method IDs for .7z

bin/installer/ - example script to create installer that uses SFX module,

bin/7zdec.exe   - simplified 7z archive decoder
bin/7zr.exe   - 7-Zip console program (reduced version)
bin/x64/7zr.exe - 7-Zip console program (reduced version) (x64 version)
bin/lzma.exe   - file->file LZMA encoder/decoder for Windows
bin/7zS2.sfx   - small SFX module for installers (GUI version)
bin/7zS2con.sfx - small SFX module for installers (Console version)
bin/7zSD.sfx   - SFX module for installers.

7zDec.exe

7zDec.exe is simplified 7z archive decoder.
It supports only LZMA, LZMA2, and PPMd methods.
7zDec decodes whole solid block from 7z archive to RAM.
The RAM consumption can be high.

Source code structure

Asm/ - asm files (optimized code for CRC calculation and Intel-AES encryption)

C/ - C files (compression / decompression and other)
  Util/
   7z   - 7z decoder program (decoding 7z files)
   Lzma   - LZMA program (file->file LZMA encoder/decoder).
   LzmaLib - LZMA library (.DLL for Windows)
   SfxSetup - small SFX module for installers

CPP/ -- CPP files

 Common - common files for C++ projects
 Windows - common files for Windows related code

 7zip   - files related to 7-Zip

   Archive - files related to archiving

     Common   - common files for archive handling
     7z   - 7z C++ Encoder/Decoder

   Bundles - Modules that are bundles of other modules (files)

     Alone7z   - 7zr.exe: Standalone 7-Zip console program (reduced version)
     Format7zExtractR - 7zxr.dll: Reduced version of 7z DLL: extracting from 7z/LZMA/BCJ/BCJ2.

Format7zR - 7zr.dll: Reduced version of 7z DLL: extracting/compressing to 7z/LZMA/BCJ/BCJ2
LzmaCon - lzma.exe: LZMA compression/decompression
LzmaSpec - example code for LZMA Specification
SFXCon -
7zCon.sfx: Console 7z SFX module
SFXSetup - 7zS.sfx: 7z SFX module for installers
SFXWin - 7z.sfx: GUI 7z SFX module

Common - common files for 7-Zip

Compress - files for compression/decompression

Crypto - files for encryption / decompression

UI - User Interface files

Client7z - Test application for 7za.dll, 7zr.dll, 7zxr.dll
Common - Common UI files
Console - Code for console program (7z.exe)
Explorer - Some code from 7-Zip Shell extension
FileManager - Some GUI code from 7-Zip File Manager
GUI - Some GUI code from 7-Zip

CS/ - C# files
7zip
Common - some common files for 7-Zip
Compress - files related to compression/decompression
LZ - files related to LZ (Lempel-Ziv) compression algorithm
LZMA - LZMA compression/decompression
LzmaAlone - file->file LZMA compression/decompression
RangeCoder - Range Coder (special code of compression/decompression)

Java/ - Java files
SevenZip
Compression - files related to compression/decompression
LZ - files related to LZ (Lempel-Ziv) compression algorithm
LZMA - LZMA compression/decompression
RangeCoder - Range Coder (special code of compression/decompression)

Note:
Asm / C / C++ source code of LZMA SDK is part of 7-Zip's source code.
7-Zip's source code can be downloaded from 7-Zip's SourceForge page:

http://sourceforge.net/projects/sevenzip/

LZMA features

- Variable dictionary size (up to 1 GB)
- Estimated compressing speed: about 2 MB/s on 2 GHz CPU
- Estimated decompressing speed:
  - 20-30 MB/s on modern 2 GHz cpu
  - 1-2 MB/s on 200 MHz simple RISC cpu: (ARM, MIPS, PowerPC)
- Small memory requirements for decompressing (16 KB + DictionarySize)
- Small code size for decompressing: 5-8 KB

LZMA decoder uses only integer operations and can be
implemented in any modern
32-bit CPU (or on 16-bit CPU with some conditions).

Some critical operations that affect the speed of LZMA decompression:
1) 32*16 bit integer multiply
2) Mispredicted branches (penalty mostly depends from pipeline length)
3) 32-bit shift and arithmetic operations

The speed of LZMA decompressing mostly depends from CPU speed.
Memory speed has no big meaning. But if your CPU has small data cache,
overall weight of memory speed will slightly increase.

How To Use

Using LZMA encoder/decoder executable

Usage: LZMA <e|d> inputFile outputFile [<switches>...]

e: encode file

d: decode file

b: Benchmark. There are two tests: compressing and decompressing
with LZMA method. Benchmark shows rating in MIPS (million
instructions per second). Rating value is calculated from
measured speed and it is normalized with Intel's Core 2 results.
Also Benchmark checks possible hardware errors (RAM
errors in most
cases). Benchmark uses these settings:
(-a1, -d21, -fb32, -mfbt4). You can change only -d parameter.
Also you can change the number of iterations. Example for 30 iterations:
LZMA b 30

Default number of iterations is 10.

<Switches>

-a{N}: set compression mode 0 = fast, 1 = normal
default: 1 (normal)

d{N}: Sets Dictionary size - [0, 30], default: 23 (8MB)
The maximum value for dictionary size is 1 GB = 2^30 bytes.
Dictionary size is calculated as DictionarySize = 2^N bytes.
For decompressing file compressed by LZMA method with dictionary
size D = 2^N you need about D bytes of memory (RAM).

-fb{N}: set number of fast bytes - [5, 273], default: 128
Usually big number gives a little bit better compression ratio
and slower compression process.

-lc{N}: set number of literal context bits - [0, 8], default: 3
Sometimes lc=4 gives gain for big files.

-lp{N}: set number
of literal pos bits - [0, 4], default: 0
lp switch is intended for periodical data when period is
equal 2^N. For example, for 32-bit (4 bytes)
periodical data you can use lp=2. Often it's better to set lc0,
if you change lp switch.

-pb{N}: set number of pos bits - [0, 4], default: 2
pb switch is intended for periodical data
when period is equal 2^N.

-mf{MF_ID}: set Match Finder. Default: bt4.
Algorithms from hc* group doesn't provide good compression
ratio, but they often works pretty fast in combination with
fast mode (-a0).

Memory requirements depend from dictionary size
(parameter "d" in table below).

MF_ID Memory Description

bt2 d * 9.5 + 4MB Binary Tree with 2 bytes hashing.
bt3 d * 11.5 + 4MB Binary Tree with 3 bytes hashing.
bt4 d * 11.5
+ 4MB Binary Tree with 4 bytes hashing.
hc4 d * 7.5 + 4MB Hash Chain with 4 bytes hashing.

-eos: write End Of Stream marker. By default LZMA doesn't write
       eos marker, since LZMA decoder knows uncompressed size
       stored in .lzma file header.

-si: Read data from stdin (it will write End Of Stream marker).
-so: Write data to stdout

Examples:

1) LZMA e file.bin file.lzma -d16 -lc0

compresses file.bin to file.lzma with 64 KB dictionary (2^16=64K)
and 0 literal context bits. -lc0 allows to reduce memory requirements
for decompression.

2) LZMA e file.bin file.lzma -lc0 -lp2

compresses file.bin to file.lzma with settings suitable
for 32-bit periodical data (for example, ARM or MIPS code).

3) LZMA d file.lzma file.bin

decompresses file.lzma to file.bin.

Compression ratio hints

Recommendations

To increase the compression ratio for LZMA compressing it's desirable
to have aligned
 data (if it's possible) and also it's desirable to locate
data in such order, where code is grouped in one place and data is
grouped in other place (it's better than such mixing: code, data, code,
data, ...).

Filters

You can increase the compression ratio for some data types, using
special filters before compressing. For example, it's possible to
increase the compression ratio on 5-10% for code for those CPU ISAs:
x86, IA-64, ARM, ARM-Thumb, PowerPC, SPARC.

You can find C source code of such filters in C/Bra*.* files

You can check the compression ratio gain of these filters with such
7-Zip commands (example for ARM code):
No filter:
 7z a a1.7z a.bin -m0=lzma

With filter for little-endian ARM code:
 7z a a2.7z a.bin -m0=arm -m1=lzma

It works in such manner:
Compressing   = Filter_encoding + LZMA_encoding
Decompressing = LZMA_decoding + Filter_decoding

Compressing and decompressing speed of such filters is very high,
so it will not increase decompressing
 time too much.
Moreover, it reduces decompression time for LZMA_decoding,
since compression ratio with filtering is higher.

These filters convert CALL (calling procedure) instructions
from relative offsets to absolute addresses, so such data becomes more
compressible.

For some ISAs (for example, for MIPS) it's impossible to get gain from such filter.

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http://www.7-zip.org
http://www.7-zip.org/sdk.html
http://www.7-zip.org/support.html
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shutilwhich contains code from Python 3.3 and uses the same license:

A. HISTORY OF THE SOFTWARE
==========

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual
Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes

2.1.2
2.1.1 2002 PSF yes
2.1.3 2.1.2 2002 PSF yes
2.2.1 2.2 2002 PSF yes
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2.3.4 2.3.3 2004 PSF yes
2.3.5 2.3.4 2005 PSF yes
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2.5.3 2.5.2 2008 PSF yes
2.6 2.5 2008 PSF yes
2.6.1 2.6 2008 PSF yes
2.6.2 2.6.1 2009 PSF yes
2.6.3 2.6.2 2009 PSF yes
2.6.4 2.6.3 2009 PSF yes
2.6.5 2.6.4 2010 PSF yes
3.0 2.6 2008 PSF yes
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3.1.4 3.1.3 2011 PSF yes
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3.2.1 3.2 2011 PSF yes
3.2.2 3.2.1 2011 PSF yes
3.2.3 3.2.2 2012 PSF yes
3.3.0 3.2 2012 PSF yes

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(1) GPL-compatible doesn't mean that we're distributing Python under the GPL. All Python licenses, unlike the GPL, let you distribute a modified version without making your changes open source. The GPL-compatible licenses make it possible to combine Python with

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible,
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Valid copyright Element With dir Attribute in div Element in text Element 2</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
<meta name="assert" content="The copyright element contains a text element that contains a div element with rtl as the value for the dir attribute." />
<style type="text/css"><![CDATA[
@import url("support/test-fonts.css");
body
{
font-size: 20px;
}
pre {
font-size: 12px;
}
.test {

```
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div dir="rtl"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Nested div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-022-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains nested div elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
  {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-022.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
```

```
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div&gt;
    div 1 start
    &lt;div&gt;
     div 2 start
     &lt;div&gt;
      div 3
     &lt;/div&gt;
     div 2 end
    &lt;/div&gt;
    div 1 end
   &lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Two span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-028-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains two span elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
  {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-028.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
```

```
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span&gt;span 1&lt;/span&gt; and &lt;span&gt;span 2&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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diff --git a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/SFXSetup.dsp b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/SFXSetup.dsp
index b1d740e..61cb260 100644
--- a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/SFXSetup.dsp
+++ b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/SFXSetup.dsp
@@ -54,7 +54,7 @@ BSC32=bscmake.exe
ADD BSC32 /nologo
LINK32=link.exe
ADD BASE LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib oleaut32.lib uuid.lib odbc32.lib odbccp32.lib /nologo /subsystem:windows /machine:I386
-# ADD LINK32 comctl32.lib kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib oleaut32.lib uuid.lib odbc32.lib odbccp32.lib /nologo /subsystem:windows /machine:I386 /out:"C:\Util\7zS.sfx" /opt:NOWIN98
+# ADD LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib oleaut32.lib uuid.lib odbc32.lib odbccp32.lib
 delayimp.lib /nologo /subsystem:windows /machine:I386 /out:"Release\7zS.sfx" /opt:NOWIN98 /delayload:user32.dll /delayload:shell32.dll /delayload:oleaut32.dll
SUBTRACT LINK32 /pdb:none

!ELSEIF "$(CFG)" == "SFXSetup - Win32 Debug"
@@ -81,7 +81,7 @@ BSC32=bscmake.exe
ADD BSC32 /nologo

```
LINK32=link.exe
# ADD BASE LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib
oleaut32.lib uuid.lib odbc32.lib odbccp32.lib /nologo /subsystem:windows /debug /machine:I386 /pdbtype:sept
-# ADD LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib
oleaut32.lib uuid.lib odbc32.lib odbccp32.lib comctl32.lib /nologo /subsystem:windows /debug /machine:I386
/out:"C:\UTIL\7zSfxS.exe" /pdbtype:sept
+# ADD LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib
oleaut32.lib uuid.lib odbc32.lib odbccp32.lib delayimp.lib /nologo /subsystem:windows /debug
 /machine:I386 /out:"Debug\7zSfxS.exe" /pdbtype:sept /delayload:user32.dll /delayload:shell32.dll
/delayload:oleaut32.dll

!ELSEIF  "$(CFG)" == "SFXSetup - Win32 ReleaseD"

@@ -107,9 +107,9 @@ BSC32=bscmake.exe
# ADD BASE BSC32 /nologo
# ADD BSC32 /nologo
LINK32=link.exe
-# ADD BASE LINK32 comctl32.lib kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib
shell32.lib ole32.lib oleaut32.lib uuid.lib odbc32.lib odbccp32.lib /nologo /subsystem:windows /machine:I386
/out:"C:\UTIL\7zWinSR.exe"
+# ADD BASE LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib
ole32.lib oleaut32.lib uuid.lib odbc32.lib odbccp32.lib /nologo /subsystem:windows /machine:I386
/out:"C:\UTIL\7zWinSR.exe"
# SUBTRACT BASE LINK32 /debug /nodefaultlib
-# ADD LINK32 comctl32.lib kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib
ole32.lib oleaut32.lib uuid.lib odbc32.lib odbccp32.lib /nologo /subsystem:windows /machine:I386
 /out:"C:\Util\7zSD.sfx" /opt:NOWIN98
+# ADD LINK32 kernel32.lib user32.lib gdi32.lib winspool.lib comdlg32.lib advapi32.lib shell32.lib ole32.lib
oleaut32.lib uuid.lib odbc32.lib odbccp32.lib delayimp.lib /nologo /subsystem:windows /machine:I386
/out:"ReleaseD\7zSD.sfx" /opt:NOWIN98 /delayload:user32.dll /delayload:shell32.dll /delayload:oleaut32.dll
# SUBTRACT LINK32 /pdb:none

!ENDIF
diff --git a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/SfxSetup.cpp b/other-
licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/SfxSetup.cpp
index 1705a8d..e3a3bb2 100644
--- a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/SfxSetup.cpp
+++ b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/SfxSetup.cpp
@@ -125,6 +125,179 @@ static void ShowErrorMessageSpec(const UString &name)
  ShowErrorMessage(NULL, message);
}

+/* BEGIN Mozilla customizations */
+
+static char const *
+FindStrInBuf(char const * buf, size_t bufLen, char const * str)
+{
+  size_t index = 0;
```

```
+
 while (index < bufLen) {
+    char const * result = strstr(buf + index, str);
+    if (result) {
+      return result;
+    }
+    while ((buf[index] != '\0') && (index < bufLen)) {
+      index++;
+    }
+    index++;
+  }
+  return NULL;
+}
+
+static bool
+ReadPostSigningDataFromView(char const * view, DWORD size, AString& data)
+{
+  // Find the offset and length of the certificate table,
+  // so we know the valid range to look for the token.
+  if (size < (0x3c + sizeof(UInt32))) {
+    return false;
+  }
+  UInt32 PEHeaderOffset = *(UInt32*)(view + 0x3c);
+  UInt32 optionalHeaderOffset = PEHeaderOffset + 24;
+  UInt32 certDirEntryOffset = 0;
+  if (size < (optionalHeaderOffset + sizeof(UInt16))) {
+    return false;
+  }
+  UInt16 magic = *(UInt16*)(view + optionalHeaderOffset);
+  if (magic == 0x010b) {
+    // 32-bit executable
+    certDirEntryOffset = optionalHeaderOffset + 128;
+  } else if (magic == 0x020b) {
+    // 64-bit executable; certain header fields are wider
+    certDirEntryOffset
 = optionalHeaderOffset + 144;
+  } else {
+    // Unknown executable
+    return false;
+  }
+  if (size < certDirEntryOffset + 8) {
+    return false;
+  }
+  UInt32 certTableOffset = *(UInt32*)(view + certDirEntryOffset);
+  UInt32 certTableLen = *(UInt32*)(view + certDirEntryOffset + sizeof(UInt32));
+  if (certTableOffset == 0 || certTableLen == 0 ||
+      size < (certTableOffset + certTableLen)) {
+    return false;
```

```
+  }
+
+  char const token[] = "__MOZCUSTOM__:";
+  // We're searching for a string inside a binary blob,
+  // so a normal strstr that bails on the first NUL won't work.
+  char const * tokenPos = FindStrInBuf(view + certTableOffset,
+                                       certTableLen, token);
+  if (tokenPos) {
+    size_t tokenLen = (sizeof(token) / sizeof(token[0])) - 1;
+    data = AString(tokenPos + tokenLen);
+    return true;
+  }
+  return false;
+}
+
+static bool
+ReadPostSigningData(UString exePath, AString& data)
+{
+  bool retval = false;
+
  HANDLE exeFile = CreateFileW(exePath, GENERIC_READ, FILE_SHARE_READ, NULL,
+                               OPEN_EXISTING, FILE_ATTRIBUTE_NORMAL, NULL);
+  if (exeFile != INVALID_HANDLE_VALUE) {
+    HANDLE mapping = CreateFileMapping(exeFile, NULL, PAGE_READONLY, 0, 0, NULL);
+    if (mapping != INVALID_HANDLE_VALUE) {
+      // MSDN claims the return value on failure is NULL,
+      // but I've also seen it returned on success, so double-check.
+      if (mapping || GetLastError() == ERROR_SUCCESS) {
+        char * view = (char*)MapViewOfFile(mapping, FILE_MAP_READ, 0, 0, 0);
+        if (view) {
+          DWORD fileSize = GetFileSize(exeFile, NULL);
+          retval = ReadPostSigningDataFromView(view, fileSize, data);
+        }
+        CloseHandle(mapping);
+      }
+    }
+    CloseHandle(exeFile);
+  }
+  return retval;
+}
+
+// Delayed load libraries are loaded when the first symbol is used.
+// The following ensures that we load the delayed loaded libraries from the
+//
 system directory.
+struct AutoLoadSystemDependencies
+{
+  AutoLoadSystemDependencies()
```

```
+ {
+   HMODULE module = ::GetModuleHandleW(L"kernel32.dll");
+   if (module) {
+     // SetDefaultDllDirectories is always available on Windows 8 and above. It
+     // is also available on Windows Vista, Windows Server 2008, and
+     // Windows 7 when MS KB2533623 has been applied.
+     typedef BOOL (WINAPI *SetDefaultDllDirectoriesType)(DWORD);
+     SetDefaultDllDirectoriesType setDefaultDllDirectories =
+       (SetDefaultDllDirectoriesType) GetProcAddress(module, "SetDefaultDllDirectories");
+     if (setDefaultDllDirectories) {
+       setDefaultDllDirectories(0x0800 /* LOAD_LIBRARY_SEARCH_SYSTEM32 */ );
+       return;
+     }
+   }
+
+   static LPCWSTR delayDLLs[] = { L"uxtheme.dll", L"userenv.dll",
+                                  L"setupapi.dll", L"apphelp.dll",
+                                  L"propsys.dll", L"dwmapi.dll",
+                                  L"cryptbase.dll",
 L"oleacc.dll",
+                                  L"clbcatq.dll" };
+   WCHAR systemDirectory[MAX_PATH + 1] = { L'\0' };
+   // If GetSystemDirectory fails we accept that we'll load the DLLs from the
+   // normal search path.
+   GetSystemDirectoryW(systemDirectory, MAX_PATH + 1);
+   size_t systemDirLen = wcslen(systemDirectory);
+
+   // Make the system directory path terminate with a slash
+   if (systemDirectory[systemDirLen - 1] != L'\\' && systemDirLen) {
+     systemDirectory[systemDirLen] = L'\\';
+     ++systemDirLen;
+     // No need to re-NULL terminate
+   }
+
+   // For each known DLL ensure it is loaded from the system32 directory
+   for (size_t i = 0; i < sizeof(delayDLLs) / sizeof(delayDLLs[0]); ++i) {
+     size_t fileLen = wcslen(delayDLLs[i]);
+     wcsncpy(systemDirectory + systemDirLen, delayDLLs[i],
+     MAX_PATH - systemDirLen);
+     if (systemDirLen + fileLen <= MAX_PATH) {
+       systemDirectory[systemDirLen + fileLen] = L'\0';
+
    } else {
+       systemDirectory[MAX_PATH] = L'\0';
+     }
+     LPCWSTR fullModulePath = systemDirectory; // just for code readability
+     LoadLibraryW(fullModulePath);
+   }
```

```
+  }
+} loadDLLs;
+
+BOOL
+RemoveCurrentDirFromSearchPath()
+{
+  // kernel32.dll is in the knownDLL list so it is safe to load without a full path
+  HMODULE kernel32 = LoadLibraryW(L"kernel32.dll");
+  if (!kernel32) {
+    return FALSE;
+  }
+
+  typedef BOOL (WINAPI *SetDllDirectoryType)(LPCWSTR);
+  SetDllDirectoryType SetDllDirectoryFn =
+    (SetDllDirectoryType)GetProcAddress(kernel32, "SetDllDirectoryW");
+  if (!SetDllDirectoryFn) {
+    FreeLibrary(kernel32);
+    return FALSE;
+  }
+
+  // If this call fails we can't do much about it, so ignore it.
+  // It is unlikely to fail and this is just a precaution anyway.
+  SetDllDirectoryFn(L"");
+  FreeLibrary(kernel32);
+  return TRUE;
+}
+
+/* END Mozilla customizations */
+
int APIENTRY WinMain(HINSTANCE hInstance, HINSTANCE /* hPrevInstance
 */,
   #ifdef UNDER_CE
   LPWSTR
@@ -133,13 +306,35 @@ int APIENTRY WinMain(HINSTANCE hInstance, HINSTANCE /* hPrevInstance */,
   #endif
   /* lpCmdLine */,int /* nCmdShow */)
{
+  /* BEGIN Mozilla customizations */
+  // Disable current directory from being in the search path.
+  // This call does not help with implicitly loaded DLLs.
+  if (!RemoveCurrentDirFromSearchPath()) {
+    WCHAR minOSTitle[512] = { '\0' };
+    WCHAR minOSText[512] = { '\0' };
+    LoadStringW(NULL, IDS_MIN_OS_TITLE, minOSTitle,
+                sizeof(minOSTitle) / sizeof(minOSTitle[0]));
+    LoadStringW(NULL, IDS_MIN_OS_TEXT, minOSText,
+                sizeof(minOSText) / sizeof(minOSText[0]));
+    MessageBoxW(NULL, minOSText, minOSTitle, MB_OK | MB_ICONERROR);
```

```
+    return 1;
+  }
+  /* END Mozilla customizations */
+
  g_hInstance = (HINSTANCE)hInstance;

  NT_CHECK

-  #ifdef _WIN32
-  LoadSecurityDlls();
-  #endif
+  // BEGIN Mozilla customizations
+  // Our AutoLoadSystemDependencies
 (see above) does the same job as the
+  // LoadSecurityDlls function, but slightly better because it runs as a static
+  // initializer, and it doesn't include LOAD_LIBRARY_SEARCH_USER_DIRS in
+  // the search path, which partially defeats the purpose of calling
+  // SetDefaultDllDirectories at all.
+  //#ifdef _WIN32
+  //LoadSecurityDlls();
+  //#endif
+  // END Mozilla customizations

  // InitCommonControls();

@@ -172,6 +367,18 @@ int APIENTRY WinMain(HINSTANCE hInstance, HINSTANCE /* hPrevInstance */,
  UString dirPrefix ("." STRING_PATH_SEPARATOR);
  UString appLaunched;
  bool showProgress = true;
+
+  /* BEGIN Mozilla customizations */
+  bool extractOnly = false;
+  if (switches.IsPrefixedBy_NoCase(L"/extractdir=")) {
+    assumeYes = true;
+    showProgress = false;
+    extractOnly = true;
+  } else if (!switches.IsEmpty()) {
+    showProgress = false;
+  }
+  /* END Mozilla customizations */
+
  if (!config.IsEmpty())
  {
    CObjectVector<CTextConfigPair>
 pairs;
@@ -204,7 +411,8 @@ int APIENTRY WinMain(HINSTANCE hInstance, HINSTANCE /* hPrevInstance */,
  }
```

```
  CTempDir tempDir;
-  if (!tempDir.Create(kTempDirPrefix))
+  /* Mozilla customizations - Added !extractOnly */
+  if (!extractOnly && !tempDir.Create(kTempDirPrefix))
   {
    if (!assumeYes)
      ShowErrorMessage(L"Can not create temp folder archive");
@@ -222,7 +430,9 @@ int APIENTRY WinMain(HINSTANCE hInstance, HINSTANCE /* hPrevInstance */,
    }
   }

-  const FString tempDirPath = tempDir.GetPath();
+  /* BEGIN Mozilla customizations - added extractOnly  parameter support */
+  const FString tempDirPath = extractOnly ? switches.Ptr(12) : GetUnicodeString(tempDir.GetPath());
+  /* END Mozilla customizations */
   // tempDirPath = L"M:\\1\\"; // to test low disk space
   {
    bool isCorrupt = false;
@@ -250,6 +460,28 @@ int APIENTRY WinMain(HINSTANCE hInstance, HINSTANCE /* hPrevInstance */,
    }
   }

+  /* BEGIN Mozilla customizations */
+  // Retrieve and store
 any data added to this file after signing.
+  {
+    AString postSigningData;
+    if (ReadPostSigningData(fullPath, postSigningData)) {
+      FString postSigningDataFilePath(tempDirPath);
+      NFile::NName::NormalizeDirPathPrefix(postSigningDataFilePath);
+      postSigningDataFilePath += L"postSigningData";
+
+      NFile::NIO::COutFile postSigningDataFile;
+      postSigningDataFile.Create(postSigningDataFilePath, true);
+
+      UInt32 written = 0;
+      postSigningDataFile.Write(postSigningData, postSigningData.Len(), written);
+    }
+  }
+
+  if (extractOnly) {
+    return 0;
+  }
+  /* END Mozilla customizations */
+
   #ifndef UNDER_CE
   CCurrentDirRestorer currentDirRestorer;
   if (!SetCurrentDir(tempDirPath))
```

```
diff --git a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/resource.h b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/resource.h
index 533197e..975d779 100644
--- a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/resource.h
+++ b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/resource.h
@@ -4,3 +4,5 @@
#define IDS_EXTRACTION_ERROR_MESSAGE     8
#define IDS_CANNOT_CREATE_FOLDER      3003
#define IDS_PROGRESS_EXTRACTING       3300
+#define IDS_MIN_OS_TITLE              70
+#define IDS_MIN_OS_TEXT               71
diff --git a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/resource.rc b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/resource.rc
index 9e88fd4..9fda0d0 100644
--- a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/Bundles/SFXSetup/resource.rc
+++ b/other-licenses/7zstub/src/CPP/7zip/Bundles/SFXSetup/resource.rc
@@ -11,6 +11,8 @@ BEGIN
  IDS_EXTRACTION_ERROR_MESSAGE  "File is corrupt"
  IDS_CANNOT_CREATE_FOLDER  "Cannot create folder '{0}'"
  IDS_PROGRESS_EXTRACTING  "Extracting"
+  IDS_MIN_OS_TITLE  "Setup Error"
+  IDS_MIN_OS_TEXT  "Microsoft Windows 7 or newer is required."
END

#include "../../UI/FileManager/ProgressDialog.rc"
diff --git a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/UI/FileManager/ProgressDialog.cpp b/other-licenses/7zstub/src/CPP/7zip/UI/FileManager/ProgressDialog.cpp
index 27d42b2..9b61369 100644
--- a/c:/Users/mhowell/Downloads/lzma1805/CPP/7zip/UI/FileManager/ProgressDialog.cpp
+++ b/other-licenses/7zstub/src/CPP/7zip/UI/FileManager/ProgressDialog.cpp
@@ -165,7 +165,8 @@ bool CProgressDialog::OnButtonClicked(int buttonID, HWND buttonHWND)
     bool paused = Sync.GetPaused();
     Sync.SetPaused(true);
     _inCancelMessageBox = true;
-    int res = ::MessageBoxW(*this, L"Are you sure you want to cancel?", _title, MB_YESNOCANCEL);
+    // Mozilla Customization - Removed redundant cancel button from dialog.
+    int res = ::MessageBoxW(*this, L"Are you sure you want to cancel?", _title, MB_YESNO);
     _inCancelMessageBox = false;
     Sync.SetPaused(paused);
     if (res == IDCANCEL || res == IDNO)
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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Content in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains content." />
  <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
```

```
   font-size:
 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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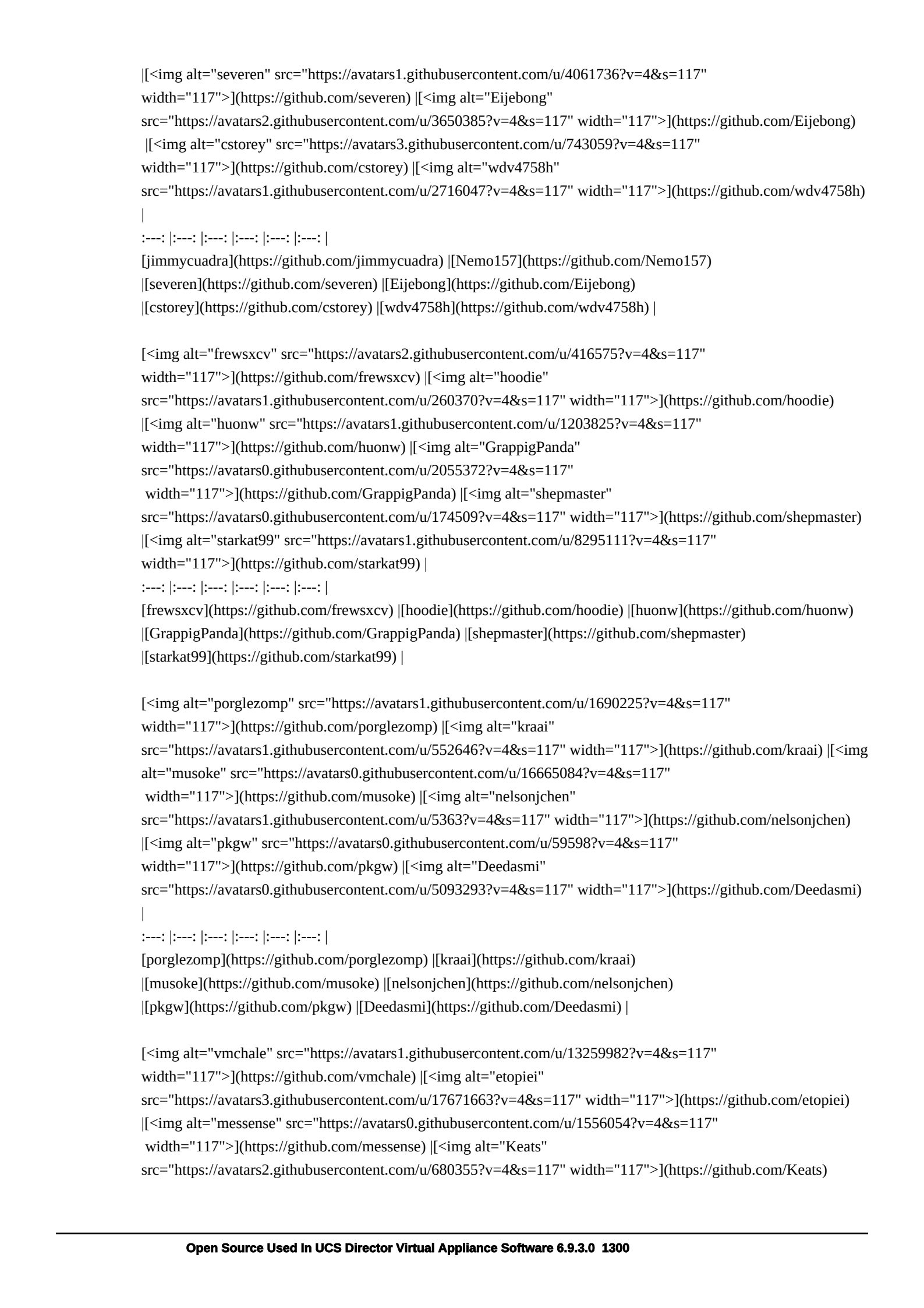
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>

```
 <title>WOFF Test: Content in copyright Element</title>
 <link rel="author" title="Tal Leming" href="http://typesupply.com" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <link rel="match" href="metadatadisplay-schema-copyright-010-ref.xht" />
 <meta name="assert" content="The copyright element contains content." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face {
   font-family: "WOFF
Test";
   src: url("support/metadatadisplay-schema-copyright-010.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
wOF2OTTO5 @a
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$v6V <:9/gC*m iF-BDENX!o64 qQu7Vlx;)1#
The OpenLDAP Public License
 Version 2.8, 17 August 2003
```

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with the distribution, and

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BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES;
LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER
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distribute verbatim copies of this document is granted.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">

```
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Nested div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-018-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains nested div elements." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-018.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div&gt;Paragraph 1&lt;/div&gt;
   &lt;div&gt;Paragraph 2&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
```

```
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With class Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with &quot;class1 class2 class3&quot; as the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span class="class1 class2 class3"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
```

</html>
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One No Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element matches the schema. It contains one text element that does not have a language tag." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size:
 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {

```
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in div Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a div element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
```

```
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div dir="ltr"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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wOF2OTTO6 @b
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,x.J^G< T>?aB7lvj-"A>%u*b/j?l>tF
H+DnG9
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in span Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-027-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with ltr as the value for the dir attribute."

```
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-027.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span dir="ltr"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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7z Format description (4.59)

This file contains description of 7z archive format.
7z archive can contain files compressed with any method.
See "Methods.txt" for description for defined compressing methods.

Format structure Overview

Some fields can be optional.

Archive structure
~~~~~~~~~~~~~~~~~~
SignatureHeader
[PackedStreams]
[PackedStreamsForHeaders]
[
~~~~~~~~~~~~~~~~~~

```
 Header
 or
 {
   Packed Header
   HeaderInfo
 }
]
```

```
Header structure
~~~~~~~~~~~~~~~~~
{
 ArchiveProperties
 AdditionalStreams
 {
   PackInfo
   {
     PackPos
     NumPackStreams
     Sizes[NumPackStreams]
     CRCs[NumPackStreams]
   }
   CodersInfo
   {
     NumFolders
     Folders[NumFolders]
     {
       NumCoders
       CodersInfo[NumCoders]
       {
         ID
         NumInStreams;
         NumOutStreams;
         PropertiesSize
         Properties[PropertiesSize]
       }
       NumBindPairs
       BindPairsInfo[NumBindPairs]
        {
         InIndex;
         OutIndex;
       }
       PackedIndices
     }
     UnPackSize[Folders][Folders.NumOutstreams]
     CRCs[NumFolders]
   }
```

```
  SubStreamsInfo
  {
    NumUnPackStreamsInFolders[NumFolders];
    UnPackSizes[]
    CRCs[]
  }
}
MainStreamsInfo
{
  (Same as in AdditionalStreams)
}
FilesInfo
{
  NumFiles
  Properties[]
  {
    ID
    Size
    Data
  }
}
}
```

HeaderInfo structure
~~~~~~~~~~~~~~~~~~~~

```
{
 (Same as in AdditionalStreams)
}
```

Notes about Notation and encoding

7z uses little endian encoding.

7z archive format has optional headers that are marked as
[]
Header
[]

REAL_UINT64 means real UINT64.

UINT64 means real UINT64 encoded with the following scheme:

```
 Size of encoding sequence depends from first byte:
 First_Byte  Extra_Bytes        Value
 (binary)
```
~~~~~~~~~~~~~~~~~~~~

```
0xxxxxxx              : ( xxxxxxx          )
10xxxxxx    BYTE y[1]  : (  xxxxxx << (8 * 1)) + y
110xxxxx
   BYTE y[2]  : (   xxxxx << (8 * 2)) + y
...
1111110x    BYTE y[6]  : (      x << (8 * 6)) + y
11111110    BYTE y[7]  :                    y
11111111    BYTE y[8]  :                    y
```

Property IDs

0x00 = kEnd

0x01 = kHeader

0x02 = kArchiveProperties

0x03 = kAdditionalStreamsInfo
0x04 = kMainStreamsInfo
0x05 = kFilesInfo

0x06 = kPackInfo
0x07 = kUnPackInfo
0x08 = kSubStreamsInfo

0x09 = kSize
0x0A = kCRC

0x0B = kFolder

0x0C = kCodersUnPackSize
0x0D = kNumUnPackStream

0x0E = kEmptyStream
0x0F = kEmptyFile
0x10 = kAnti

0x11 = kName
0x12 = kCTime
0x13 = kATime
0x14 = kMTime
0x15 = kWinAttributes
0x16 = kComment

0x17 = kEncodedHeader

0x18 = kStartPos
0x19 = kDummy

7z format headers

SignatureHeader
~~~~~~~~~~~~~~~~
BYTE kSignature[6] = {'7', 'z', 0xBC, 0xAF, 0x27, 0x1C};

ArchiveVersion
{
  BYTE Major;   // now = 0
  BYTE Minor;   // now = 2
};

UINT32 StartHeaderCRC;

StartHeader
{
  REAL_UINT64
NextHeaderOffset
  REAL_UINT64 NextHeaderSize
  UINT32 NextHeaderCRC
}

.....

ArchiveProperties
~~~~~~~~~~~~~~~~~~
BYTE NID::kArchiveProperties (0x02)
for (;;)
{
 BYTE PropertyType;
 if (aType == 0)
   break;
 UINT64 PropertySize;
 BYTE PropertyData[PropertySize];
}

Digests (NumStreams)
~~~~~~~~~~~~~~~~~~~~~
 BYTE AllAreDefined
~~~~~~~~~~~~~~~~~~~~~

```
 if (AllAreDefined == 0)
 {
  for(NumStreams)
    BIT Defined
 }
 UINT32 CRCs[NumDefined]


PackInfo
~~~~~~~~~~~~~
 BYTE NID::kPackInfo  (0x06)
 UINT64 PackPos
 UINT64 NumPackStreams

 []
 BYTE NID::kSize    (0x09)
 UINT64 PackSizes[NumPackStreams]
 []

 []
 BYTE NID::kCRC      (0x0A)
 PackStreamDigests[NumPackStreams]
 []

 BYTE NID::kEnd


Folder
~~~~~~~
 UINT64 NumCoders;
 for (NumCoders)
 {
  BYTE
  {
    0:3 CodecIdSize
    4:  Is Complex Coder
    5:  There Are Attributes
    6:  Reserved
    7:  There are more alternative methods. (Not used anymore, must be 0).
  }
  BYTE
 CodecId[CodecIdSize]
  if (Is Complex Coder)
  {
    UINT64 NumInStreams;
    UINT64 NumOutStreams;
  }
  if (There Are Attributes)
```

```
  {
    UINT64 PropertiesSize
    BYTE Properties[PropertiesSize]
  }
}

NumBindPairs = NumOutStreamsTotal - 1;

for (NumBindPairs)
{
  UINT64 InIndex;
  UINT64 OutIndex;
}

NumPackedStreams = NumInStreamsTotal - NumBindPairs;
if (NumPackedStreams > 1)
  for(NumPackedStreams)
  {
    UINT64 Index;
  };
```

Coders Info

~~~~~~~~~~~~

```
BYTE NID::kUnPackInfo  (0x07)


BYTE NID::kFolder  (0x0B)
UINT64 NumFolders
BYTE External
switch(External)
{
  case 0:
    Folders[NumFolders]
  case 1:
    UINT64 DataStreamIndex
}


BYTE ID::kCodersUnPackSize  (0x0C)
for(Folders)
  for(Folder.NumOutStreams)
    UINT64 UnPackSize;
```
~~~~~~~~~~~~

[]
BYTE NID::kCRC (0x0A)
UnPackDigests[NumFolders]
[]

BYTE NID::kEnd

SubStreams Info
~~~~~~~~~~~~~~~
BYTE NID::kSubStreamsInfo; (0x08)

[]
BYTE NID::kNumUnPackStream;
(0x0D)
UINT64 NumUnPackStreamsInFolders[NumFolders];
[]

[]
BYTE NID::kSize (0x09)
UINT64 UnPackSizes[]
[]

[]
BYTE NID::kCRC (0x0A)
Digests[Number of streams with unknown CRC]
[]

BYTE NID::kEnd

Streams Info
~~~~~~~~~~~~~~

[]
PackInfo
[]

[]
CodersInfo
[]

```
[]
SubStreamsInfo
[]

BYTE NID::kEnd


FilesInfo
~~~~~~~~~~
BYTE NID::kFilesInfo;  (0x05)
UINT64 NumFiles

for (;;)
{
  BYTE PropertyType;
  if (aType == 0)
    break;

  UINT64 Size;

  switch(PropertyType)
  {
    kEmptyStream:   (0x0E)
      for(NumFiles)
        BIT IsEmptyStream

    kEmptyFile:     (0x0F)
      for(EmptyStreams)
        BIT IsEmptyFile

    kAnti:          (0x10)
      for(EmptyStreams)
        BIT IsAntiFile

    case kCTime: (0x12)
    case kATime: (0x13)
    case kMTime: (0x14)
      BYTE AllAreDefined
      if (AllAreDefined == 0)
      {
        for(NumFiles)
          BIT TimeDefined
       }
      BYTE External;
      if(External != 0)
        UINT64 DataIndex
```

```
      []
      for(Definded Items)
        UINT64 Time
      []

    kNames:     (0x11)
      BYTE External;
      if(External != 0)
        UINT64 DataIndex
      []
      for(Files)
      {
        wchar_t Names[NameSize];
        wchar_t 0;
      }
      []

    kAttributes:  (0x15)
      BYTE AllAreDefined
      if (AllAreDefined == 0)
      {
        for(NumFiles)
          BIT AttributesAreDefined
      }
      BYTE External;
      if(External != 0)
        UINT64 DataIndex
      []
      for(Definded Attributes)
        UINT32 Attributes
      []
  }
}


Header
~~~~~~~
 BYTE NID::kHeader (0x01)

 []
 ArchiveProperties
 []

 []
 BYTE NID::kAdditionalStreamsInfo; (0x03)
 StreamsInfo
 []
```

[]
BYTE NID::kMainStreamsInfo; (0x04)
StreamsInfo
[]

[]
FilesInfo
[]

BYTE NID::kEnd

HeaderInfo
~~~~~~~~~~
 []
BYTE NID::kEncodedHeader; (0x17)
StreamsInfo for Encoded Header
[]

End of document
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% - in particular, please no \catcode or \lccode changes,
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%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
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%
% For more unformation see
%
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%
% Special thanks to: Jim Hefferon and Robin Fairbairns
%
% There are few basic rules in mongolian
%
% 1. If there is a consonant then it can use hyphen before it.
% And if there're two consonants then it can use hyphen before second consonant.
% Are there three consonants then it can put hyphen before third consonant,
% also are there four consonants then it can use hyphen before fourth consonant.
% 2. In the case of hardsign or softsign, it's possible to hyphen after these signs.
% For instance, -, -
% 3. However one vowel can be belong to a syllable, it's not possible to use hyphen.
%
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OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">

```
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With class Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-030-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with
&quot;class1 class2 class3&quot; as
 the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-030.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span class="class1 class2 class3"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
```

</body>
</html>
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!IF "$(CPU)" == "AMD64"
CFLAGS_C_SPEC = -D_LZMA_DEC_OPT
ASM_OBJS = $(ASM_OBJS) \
$O\LzmaDecOpt.obj
!ENDIF
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Valid copyright Element With Mixed text Element Language Tags 1</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
<meta name="assert" content="The copyright element matches the schema. One text element does not have a language tag. One text element has a language tag." />
<style type="text/css"><![CDATA[
@import url("support/test-fonts.css");
body

```
 {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Copyright without language.
  &lt;/text&gt;
  &lt;text xml:lang="en"&gt;
   Copyright with "en" language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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7-Zip method IDs for 7z and xz archives

Version: 17.01
Date: 2017-05-27

Each compression or crypto method in 7z is associated with unique binary value (ID).
The length of ID in bytes is arbitrary but it can not exceed 63 bits (8 bytes).

xz and 7z formats use same ID map.

If you want to add some new ID, you have two ways:
1) Write request for allocating IDs to 7-Zip developers.
2) Generate 8-bytes ID:

3F ZZ ZZ ZZ ZZ ZZ MM MM

3F - Prefix for random IDs (1 byte)
ZZ ZZ ZZ ZZ ZZ - Developer ID (5 bytes). Use real random bytes.

MM MM - Method ID (2 bytes)

You can notify 7-Zip developers about your Developer ID / Method ID.

Note: Use new ID, if old codec can not decode data encoded with new version.

List of defined IDs
---

00 - Copy

03 - Delta
04 - BCJ (x86)
05 - PPC (big-endian)
06 - IA64
07 - ARM (little-endian)
08 - ARMT (little-endian)
09 -
SPARC

21 - LZMA2

02.. - Common
  03 [Swap]
    - 2 Swap2
    - 4 Swap4

03.. - 7z
  01 -
    01 - LZMA

  03 - [Branch Codecs]
    01 - [x86 Codecs]
      03 - BCJ
      1B - BCJ2 (4 packed streams)
    02 -
      05 - PPC (big-endian)
    03 -
      01 - Alpha
    04 -
      01 - IA64
    05 -
      01 - ARM (little-endian)
    06 -
      05 - M68 (big-endian)
    07 -
      01 - ARMT (little-endian)
    08 -
      05 - SPARC

  04 -
    01 - PPMD

  7F -
    01 - experimental method.

04.. - Misc codecs

  00 - Reserved

  01 - [Zip]
    00 - Copy (not used. Use {00} instead)
    01 - Shrink
    06 - Implode
    08 - Deflate
    09 - Deflate64
    0A - Imploding
    0C - BZip2 (not used. Use {040202} instead)

0E - LZMA (LZMA-zip)
5F - xz
60 - Jpeg
61 - WavPack
62 - PPMd (PPMd-zip)
63 - wzAES

02 -
02 - BZip2

03 - [Rar]
01 - Rar1
02 - Rar2
03 - Rar3
05 - Rar5

04 - [Arj]
01 - Arj(1,2,3)
02 - Arj4

05 - [Z]

06 - [Lzh]

07 - Reserved for 7z

08 - [Cab]

09 - [NSIS]
01 - DeflateNSIS
02 - BZip2NSIS

F7 - External codecs (that are not included to 7-Zip)

0x xx - reserved

10 xx - reserved (LZHAM)
01 - LZHAM

11 xx - reserved (Tino Reichardt)
01 - ZSTD
02 - BROTLI
04 - LZ4
05 - LZ5
06 - LIZARD

06.. - Crypto

F0 - Ciphers without hashing algo

01 - [AES]
0x - AES-128
4x - AES-192
8x - AES-256
Cx - AES

x0 - ECB
x1 - CBC
x2 - CFB
x3 - OFB
x4 - CTR

F1 - Combine Ciphers

01 - [Zip]
01 - ZipCrypto (Main Zip crypto algo)

03 - [RAR]
02 -
03 - Rar29AES (AES-128 + modified SHA-1)

07 - [7z]
01 - 7zAES (AES-256 + SHA-256)

End of document
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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With class Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element with &amp;quot;class1 class2 class3&amp;quot; as the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
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 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&amp;lt;?xml version="1.0" encoding="UTF-8"?&amp;gt;
&amp;lt;metadata version="1.0"&amp;gt;
```

<copyright>
<text class="class 1 class2 class3">
Copyright without language.
</text>
</copyright>
</metadata>
</pre>
</body>
</html>

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LZMA compression

Version: 9.35

This file describes LZMA encoding and decoding functions written in C language.

LZMA is an improved version of famous LZ77 compression algorithm.
It was improved in way of maximum increasing of compression ratio,
keeping high decompression speed and low memory requirements for
decompressing.

Note: you can read also LZMA Specification (lzma-specification.txt from LZMA SDK)

Also you can look source code for LZMA encoding and decoding:
C/Util/Lzma/LzmaUtil.c

LZMA compressed file format

Offset	Size	Description
0	1	Special LZMA properties (lc,lp, pb in encoded form)
1	4	Dictionary size (little endian)
5	8	Uncompressed size (little endian). -1 means unknown size
13		Compressed data

ANSI-C LZMA Decoder
~~~~~~~~~~~~~~~~~~~

Please note that interfaces for ANSI-C code were changed in LZMA SDK 4.58.
If you want to use old interfaces you can download previous version of LZMA SDK
from
sourceforge.net site.

To use ANSI-C LZMA Decoder you need the following files:
~~~~~~~~~~~~~~~~~~~

1) LzmaDec.h + LzmaDec.c + 7zTypes.h + Precomp.h + Compiler.h

Look example code:
C/Util/Lzma/LzmaUtil.c

Memory requirements for LZMA decoding

Stack usage of LZMA decoding function for local variables is not
larger than 200-400 bytes.

LZMA Decoder uses dictionary buffer and internal state structure.
Internal state structure consumes
state_size = (4 + (1.5 << (lc + lp))) KB
by default (lc=3, lp=0), state_size = 16 KB.

How To decompress data

LZMA Decoder (ANSI-C version) now supports 2 interfaces:
1) Single-call Decompressing
2) Multi-call State Decompressing (zlib-like interface)

You must use external allocator:
Example:
void *SzAlloc(void *p, size_t size) { p = p; return malloc(size); }
void SzFree(void *p, void *address) { p = p; free(address); }
ISzAlloc alloc = { SzAlloc, SzFree };

You can use p = p; operator to disable compiler
warnings.

Single-call Decompressing

When to use: RAM->RAM decompressing
Compile files: LzmaDec.h + LzmaDec.c + 7zTypes.h
Compile defines: no defines
Memory Requirements:
- Input buffer: compressed size
- Output buffer: uncompressed size
- LZMA Internal Structures: state_size (16 KB for default settings)

Interface:
int LzmaDecode(Byte *dest, SizeT *destLen, const Byte *src, SizeT *srcLen,
    const Byte *propData, unsigned propSize, ELzmaFinishMode finishMode,

```
    ELzmaStatus *status, ISzAlloc *alloc);
In:
  dest     - output data
  destLen  - output data size
  src      - input data
  srcLen   - input data size
  propData - LZMA properties  (5 bytes)
  propSize - size of propData buffer (5 bytes)
  finishMode - It has meaning only if the decoding reaches output limit (*destLen).
       LZMA_FINISH_ANY - Decode just destLen bytes.
       LZMA_FINISH_END - Stream must be finished after (*destLen).

     You can use LZMA_FINISH_END, when you know that
                    current output buffer covers last bytes of stream.
  alloc    - Memory allocator.

Out:
  destLen  - processed output size
  srcLen   - processed input size

Output:
  SZ_OK
    status:
      LZMA_STATUS_FINISHED_WITH_MARK
      LZMA_STATUS_NOT_FINISHED
      LZMA_STATUS_MAYBE_FINISHED_WITHOUT_MARK
  SZ_ERROR_DATA - Data error
  SZ_ERROR_MEM  - Memory allocation error
  SZ_ERROR_UNSUPPORTED - Unsupported properties
  SZ_ERROR_INPUT_EOF - It needs more bytes in input buffer (src).

If LZMA decoder sees end_marker before reaching output limit, it returns OK result,
and output value of destLen will be less than output buffer size limit.

You can use multiple checks to test data integrity after full decompression:
  1) Check Result and "status" variable.
  2) Check that output(destLen) = uncompressedSize, if you know real uncompressedSize.
  3) Check that output(srcLen)
= compressedSize, if you know real compressedSize.
     You must use correct finish mode in that case. */


Multi-call State Decompressing (zlib-like interface)
----------------------------------------------------

When to use: file->file decompressing
Compile files: LzmaDec.h + LzmaDec.c + 7zTypes.h
```

Memory Requirements:
- Buffer for input stream: any size (for example, 16 KB)
- Buffer for output stream: any size (for example, 16 KB)
- LZMA Internal Structures: state_size (16 KB for default settings)
- LZMA dictionary (dictionary size is encoded in LZMA properties header)

1) read LZMA properties (5 bytes) and uncompressed size (8 bytes, little-endian) to header:

```
 unsigned char header[LZMA_PROPS_SIZE + 8];
 ReadFile(inFile, header, sizeof(header)
```

2) Allocate CLzmaDec structures (state + dictionary) using LZMA properties

```
CLzmaDec state;
LzmaDec_Constr(&state);
res = LzmaDec_Allocate(&state, header, LZMA_PROPS_SIZE, &g_Alloc);
if (res != SZ_OK)
  return res;
```

3)
Init LzmaDec structure before any new LZMA stream. And call LzmaDec_DecodeToBuf in loop

```
LzmaDec_Init(&state);
for (;;)
{
  ...
  int res = LzmaDec_DecodeToBuf(CLzmaDec *p, Byte *dest, SizeT *destLen,
      const Byte *src, SizeT *srcLen, ELzmaFinishMode finishMode);
  ...
}
```

4) Free all allocated structures

```
LzmaDec_Free(&state, &g_Alloc);
```

Look example code:
C/Util/Lzma/LzmaUtil.c

How To compress data

Compile files:
7zTypes.h
Threads.h
LzmaEnc.h
LzmaEnc.c
LzFind.h
LzFind.c

LzFindMt.h
LzFindMt.c
LzHash.h

Memory Requirements:
- (dictSize * 11.5 + 6 MB) + state_size

Lzma Encoder can use two memory allocators:
1) alloc - for small arrays.
2) allocBig - for big arrays.

For example, you can use Large RAM Pages (2 MB) in allocBig allocator for
better compression speed. Note that Windows has bad implementation for
Large RAM Pages.
It's OK to use same allocator for alloc and allocBig.

Single-call Compression with
callbacks

Look example code:
C/Util/Lzma/LzmaUtil.c

When to use: file->file compressing

1) you must implement callback structures for interfaces:
ISeqInStream
ISeqOutStream
ICompressProgress
ISzAlloc

```
static void *SzAlloc(void *p, size_t size) { p = p; return MyAlloc(size); }
static void SzFree(void *p, void *address) {  p = p; MyFree(address); }
static ISzAlloc g_Alloc = { SzAlloc, SzFree };

CFileSeqInStream inStream;
CFileSeqOutStream outStream;

inStream.funcTable.Read = MyRead;
inStream.file = inFile;
outStream.funcTable.Write = MyWrite;
outStream.file = outFile;
```

2) Create CLzmaEncHandle object;

```
CLzmaEncHandle enc;
```

```
enc = LzmaEnc_Create(&g_Alloc);
if (enc == 0)
  return SZ_ERROR_MEM;
```

3) initialize CLzmaEncProps properties;

```
LzmaEncProps_Init(&props);
```

Then you can change some properties in that structure.

4) Send LZMA properties to LZMA Encoder

```
res = LzmaEnc_SetProps(enc, &props);
```

5) Write encoded properties
to header

```
  Byte header[LZMA_PROPS_SIZE + 8];
  size_t headerSize = LZMA_PROPS_SIZE;
  UInt64 fileSize;
  int i;

  res = LzmaEnc_WriteProperties(enc, header, &headerSize);
  fileSize = MyGetFileLength(inFile);
  for (i = 0; i < 8; i++)
    header[headerSize++] = (Byte)(fileSize >> (8 * i));
  MyWriteFileAndCheck(outFile, header, headerSize)
```

6) Call encoding function:

```
    res = LzmaEnc_Encode(enc, &outStream.funcTable, &inStream.funcTable,
      NULL, &g_Alloc, &g_Alloc);
```

7) Destroy LZMA Encoder Object

```
LzmaEnc_Destroy(enc, &g_Alloc, &g_Alloc);
```

If callback function return some error code, LzmaEnc_Encode also returns that code
or it can return the code like SZ_ERROR_READ, SZ_ERROR_WRITE or SZ_ERROR_PROGRESS.

Single-call RAM->RAM Compression

Single-call RAM->RAM Compression is similar to Compression with callbacks,
but you provide pointers to buffers instead of pointers to stream callbacks:

```
SRes LzmaEncode(Byte *dest,
 SizeT *destLen, const Byte *src, SizeT srcLen,
   const CLzmaEncProps *props, Byte *propsEncoded, SizeT *propsSize, int writeEndMark,
   ICompressProgress *progress, ISzAlloc *alloc, ISzAlloc *allocBig);

Return code:
 SZ_OK               - OK
 SZ_ERROR_MEM        - Memory allocation error
 SZ_ERROR_PARAM      - Incorrect paramater
 SZ_ERROR_OUTPUT_EOF - output buffer overflow
 SZ_ERROR_THREAD     - errors in multithreading functions (only for Mt version)



Defines
-------

_LZMA_SIZE_OPT - Enable some optimizations in LZMA Decoder to get smaller executable code.

_LZMA_PROB32   - It can increase the speed on some 32-bit CPUs, but memory usage for
                 some structures will be doubled in that case.

_LZMA_UINT32_IS_ULONG  - Define it if int is 16-bit on your compiler and long is 32-bit.

_LZMA_NO_SYSTEM_SIZE_T  - Define it if you don't want to use size_t type.


_7ZIP_PPMD_SUPPPORT - Define it if you don't want to support PPMD method in AMSI-C .7z decoder.


C++
 LZMA Encoder/Decoder
~~~~~~~~~~~~~~~~~~~~~~~~~
C++ LZMA code use COM-like interfaces. So if you want to use it,
you can study basics of COM/OLE.
C++ LZMA code is just wrapper over ANSI-C code.


C++ Notes
~~~~~~~~~~~~~~~~~~~~~~~~~
If you use some C++ code folders in 7-Zip (for example, C++ code for .7z handling),
you must check that you correctly work with "new" operator.
7-Zip can be compiled with MSVC 6.0 that doesn't throw "exception" from "new" operator.
So 7-Zip uses "CPP\Common\NewHandler.cpp" that redefines "new" operator:
operator new(size_t size)
{
 void *p = ::malloc(size);
 if (p == 0)
```

```
  throw CNewException();
 return p;
}
```

If you use MSCV that throws exception for "new" operator, you can compile without
"NewHandler.cpp". So standard exception will be used. Actually some code of
7-Zip catches any exception in internal code and converts it to HRESULT code.
So you don't need to catch CNewException, if you call COM interfaces of 7-Zip.

---

http://www.7-zip.org
http://www.7-zip.org/sdk.html
http://www.7-zip.org/support.html
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
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 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Content in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />

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 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
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 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
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 <meta name="assert" content="The license element contains content." />
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  @import url("support/test-fonts.css");
  @font-face {
   font-family: "WOFF Test";
   src:
 url("support/metadatadisplay-schema-license-013.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
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fails.</p>
 <div class="test">P</div>
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==========

contextlib2 is a derivative of the contextlib module distributed by the PSF
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Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands
as a successor of a language called ABC. Guido remains Python's
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Release	Derived from	Year	Owner	GPL-compatible? (1)
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1.1	2010	PSF	yes
3.1.3	3.1.2	2010	PSF	yes
3.1.4	3.1.3	2011	PSF	yes
3.2	3.1	2011	PSF	yes
3.2.1	3.2	2011	PSF	yes
3.2.2	3.2.1	2011	PSF	yes
3.3	3.2	2012	PSF	yes

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- -

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the LPPL Work was available from:
http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

Portions of this file were originally made available under the following license
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Slovenian hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted
from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: slhyph.tex (2007-01-29)
% Author: Matja Vreko <matjaz at mg-soft.si>
%
% The above mentioned file should become obsolete,

```
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%               and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated
 patterns into TeX Live
%
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%
% The copyright statement of this file is thus:
%
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%    "a better world" as long as you respect the copyright of original file.
%    If you're the original author of patterns or taking over a new revolution,
%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only
 "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
```

```
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-----------------------------------------------------------------------------
%
% This is `slhyph.tex' as of 15. 4. 97.
%
% Copyright (C) 1990 Matja Vreko, TeXCeX (SLO)
%               [slovenian hyphenation patterns]
%
% This program can be redistributed and/or modified under the terms
% of the LaTeX Project Public License Distributed from CTAN
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% version 1 of the License, or any later version.
%
% This file contains slovene hyphen patterns with
%
% Generation of hyphen patterns for TeX
%
%         Matja Vreko, TeXCeH (SLO), 1990
%   Email: matjaz@mg-soft.si
%
% Changes:
%  1990       First version of `hyphen.si' (Matja Vreko, TeXCeX)
%
% Some cosmetic changes done later on, but none of these apply any more;
% the patterns are still the same as they were originally:
%
%  1994-05-17 Use of code page 852 in patterns (Leon lajpah)
%  1995-04-06 Release of `sihyph21.tex'
%  1995-06-20 Added \slovenehyphenmins
%             Release of `sihyph22.tex'
%  1997-15-04 Some changes concerning "c, "s, "z and ...
%             Release of `sihyph23.tex'
%  2007-01-20 `sihyph23.tex' renamed to `slhyph.tex'
%             (sl is the proper language code for Slovenian)
%
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Nested span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
```

```
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-026-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains nested span elements."
/>
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-026.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
    &lt;span&gt;
     span 1 start
     &lt;span&gt;
      span 2 start
      &lt;span&gt;
       span 3
      &lt;/span&gt;
      span 2 end
     &lt;/span&gt;
     span 1 end
    &lt;/span&gt;
```

```
 &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Two div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains two div elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div&gt;Paragraph 1&lt;/div&gt;
   &lt;div&gt;Paragraph 2&lt;/div&gt;
  &lt;/text&gt;
```

</license>
</metadata>
</pre>
</body>
</html>

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in span Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
```

```
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span dir="ltr"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">

```
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in div Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-023-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a div element with ltr as
the value for the dir attribute." />
  <style
 type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-023.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div dir="ltr"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

MZ@ !L!This program cannot be run in DOS mode.

$+rErErE8rE>rErDSrE+]rE(rErE9rE=rERichrEPEd@Z"@`mx .text
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uD:tCf"u D:A&E9EuHk!UHMf4QfQUIA;}rOHHI$;H(A_A^A]A_^][3HAAAHHL$SH0HD$ H3HHAAAAH
HK0"HH0[HUH HHM@HH]HUH HHM@H z_H]HSUVWH(HcYHHt-
HHGHH,HtHMHuHOH(_^][HSUVWHhHD$
HHHL$8.#HL$8(HL$8'Hc}Ht,HHHEH4HtH6H.uEHL$H!HL$(!LD$(HTHHL8t3H$HtHT$HH"3HHf
|$0t*HT$(HL$8T#HL$(HL$HnHL$(HL$H|HL$8qHh_^][HUH HHM8]H]HUH HHMH]H]HUH HHM(]H]HUH
HHH]DL$ SUVWATAUAVAWHHHD$ MLH39ZHf8-
$DkD;vqMI8tHH8u$A;~9GA;D$w(I$H&:t$D$$I;$rD;}HN H42vAUH<@H.A8\ u8\tHN H2GDED$D+AD
D;}HN Hg2\DALtPAI$f<QwQILD;|HN Hh2I$HPHL$(K
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TAu-I$f<P-D:tA5HN HM2!A;T$tHN H22HHA_A^A]A_^][HUH HHM(ZH]HUH HHG
H]HT$SUVWATAUAVAWHHHD$0MEHAA(HA
A8HA0fHcqHt,HHHEHHtHHuEHMH5HtHYLDHHHECvHCH$Ht#D0HXHt$ L
EHH3H]3A9t$3I$H}}FHHtEE]{t%Hf8-uEL$HHtG2H$Ht
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eESCH [AHSUVWH(HIHIH LHuH?H[LH}tH6\
LHguHHSHHH#HC3@H(_^][HgH(AAu3H(H(bHLt1@HACIHICHKICE3IH(HHE3
LDr1L$AAD*A0EI
DsA0IcEIEtHHAIAuAHHSH@LLH;wqmE3H
r9L\$ HAIHD*A0EIH
LsA0IcEIEtHL HAIDuAH@[DAAuAMcLIEAAA
A@7AP0BAAuLAHAHuAMcLIEIAA
A@7AP0BAAuDBAEAAA
AA7EA0ABAIyHE3
LDr1L$AAD*A0EI
DsfA0IcfEIEtHHfAIAufAHHy -HHHSVWH0HD$ HHHMHDG3Ar'Hf:"ufB|B"uA@GfB3H9_t-
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HL$8fA<rt$@HHLHLD8L$@;vIf8u
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t7K9KuH DCHA<AADCHuH _^[2HSWHHHD$ HHBHfHL$(HT$(HSD\ADDAuHHL$(HT$(H`HL$(HH_[HUH
HHM(6LH]HSH yHtH t2
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eHH [HSVWHPHD$
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HHM8JH]HUH HHM8JH]H(DZHADDAu
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HH0[HSH@HHHT$ HHHL$ HH@[HDHEtHDD:uH+DHfEtHfDfD;uH+HBf=wf fHfuHSUVWATH
HLA$f}IHf;tQfarfzwsfwfHHfarfzwf fv$Hf;ufuH A_^][2L *EIA@f=wfA A<w fD;u
Hu2EtHHDfD;At2LED
IHE:t"A@<wAH AAA<wA A:uEu2LD
EIHfE;At0fAw2A@f=wAH AAA<wA Af;u fEu2
HfA;uDHfEu2D EHfE;uDHEu2L7E
AIfD;t%AAf=wfA A@<wA AfD;uDHEu2HSUVWH(fHHraf}Hf;tMfarfzwf fv%Hfarfzwf
fvHf;ufHfu2H(_^][HSUVWATH HLA<$f]IHf;tSfarfzwwfwfHfarfzwf fvlHf;rwfu3H A_^][HSVWH
;QHr;@s3JrDFHAHHHH~H _^[H<HLHDH)3XHSVWH @HrH<HLHDH)3(JHHH~H
^[HSWH(HHJ{H{H([QTH(DAAA+;vATH(HSH HHNCCHHH [HSWH(H3H8t
HH8u3LHAIuHH(_[HSWH(HRHLHHAIuHH(_[HSUVWH(3HH@82t
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Hffu;~vOHHH~H3~tLDEHAIIuH(_^][HSVWH 3HH@8:t
HH8uKHHHu{HH _^[HSWH(HRHKLHAIHuGCHH(_[HSH0HHT$ HT$ HPH0[HSWH(HH< t<
t< uH+tDAHH D+A)_H(_[HcQL HtND
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A;PIAGXHHHHLLHfBH _^[HSUVWATH HRALFI<$HHHLf,;fD;IH
A_^][HSUVWATAUH($AH+MLhI<$HLHHCDEH;MI0IH(A]A_^][HSH0A@MDBHHD$@D$
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3HHf9tHHf8uHDCMHHH _^[HSVWH 3HH8t
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tf; uHH+HtDGD+HAAM)_H(_[HcQLt IDPf8 tf8
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[HSWH(ADADAHA;vAA+t H AD+HAJIAM)_H(_[t-SWH(HH DGHYD+AMp)_H(_[HSH HI9KuTH*SH

SH [H(ytH(HSH HI9KuTHSH

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[HSUVWH(3IfA9Ht5IHf8ut"DFHDHhMH^H(_^][HSUVWH(AXIHt"DHHLHhM^H(_^][fA;tRSWH(H9E3ED9IL
v0IJHDDHExfFOAE;JrH(_[HSUVWATAUH(BIHHt[A;@uIHtEDgDk39nv6HDH(x"EHLHA;nrH(A]A_^][HcEH
H HSVWH HIHIHqk HAgtH6 AHNu
HC3@H _^[HSH HHt@HHKH3HH [HSUVWH8HAAfDJHEHL3D$(HD$ u$trH*HL$`D$`tN;Cv
HHDNL3|$(HD$ BuHW*HL$`D$`tNCHfHH8_^][DL$
SUVWATAUAVLH@HL$E3DqD0E4$DJALE;HLMsMsEs3MsA;u/A;H)H$$tND$tAu@;Cv
HHEMMEA:HHIEA:HL$8IEHD$0t$(HT$ 3 A:uD9AA;u H)H$$tN_CHD4H@A^A]A_^][H8HDXA_HD
H8HL$SVWH@HD$8AHHD$0iD$0HDxHD A_DHH6HH@_^[HUH HE0t
e0HM`,/H
]HtH
E32f=9w5Aw8DGA0EA+D;wEHf=0sHtH
A3HtH
E3:f=9w=IM;w:DOHAMI+L;wMHf=0sHtH
I3HtH
E3DA@ vA@wA@
A@wA@AuADH3HtH
AHSVWIE3IL;HLAH<sMfCS<<s
A@:<s
A ,<s
A<s
A<AH;t&<?wHAtEuNsMtfCSIIs)Mtf
f-(f$fCTSfCSIMtfCSIE2H;LA_^[HSVWH HBHfDILL HHT$@3HD$@H3[HD$@H;Gv
HHHT$@LL`HL$@OHfQH _^[HSUVWATH HBHH9AH,GLAHH+HH;tEAI=r1=sH%(A;wL;tA$A;wIHL;u;Zv
HH;HA?AHs

HsA"A,@JHD(E;w\H;tW$A;wJA
HDAA,AA"ABAE"EA"DBAHB$, A"AA"JAHBH;/H^H
A_^][H(=AtGH(H(=AtH(HT$SUVWH(ff:HH3f*u!HMH@:f;tHD$Xzf?uf;uk2H(_^][f;t[@85HAufarfzwf
fvHfarfzwf
fvHf;uHD$XHf]Hf8HD$Xf;Ff;ohHSWH(ALHHBIH;tHCf8\tf8/tH;HuLIL+IHHH(_[HSWH(ALHHBIH;t/f{\tf{/uH
H;tHCf8\tf8/tH;HuLIL+IlHHH(_[HSWH(HHHD$@GHD@HJ@<HH(_[Q3;vH f9*tf9?t
H;r23f;t(fy:u!f9Quf=arf=zv
f-Af=wHSH yt0yt*yt$9YuHH\HH t3H [HSUVWATAUAVH ELHu
D8AIB+E33Et$8Su8StD8Su8StEt {uPIcLcg3tWL3I{IIHHH(H tt+=a>tMuHI;srK;tHI;~2H A^A]A_^][H(39Au9A
u9A@uy0uHA(HH(HSUVWH(3HH9Y v73==HFHUHHIt
tH;^ rH(_^][DA03EtHQ(H
yuywHA;r2HSVWH y0Hw/39y v3HCHuH;{ r2H _^[HSUVWATH HA(Hq8HE3A9~Mv#3HDIHuH;~r2H
A_^][HSUVWH(3HH9Yv53=<HHULIt|tH;^rH(_^][HSH
yHtWH E3HD9@u,{rBHAD9@u8HAHOHru D@HAHH htA@3H [HSVH(DAHEu3LM
I ,t
AyuAtIBxuD;txIBHHu[HHHHH u5~tHL@It Hdt9^F^H(^[HL$SUVWH8HD$
HHHAAztHHH{tD3HD$hHtHMHHLE3KHLCHHuHH8_^][HUH HHM`>#H]HUH HHMhH
]HSUVWATH0HD$

LHH;uHHcyHt,HHHH,HtHMBH:uCA|$;{v6HHH{tDCIHHMvHH3{tD3HD$`HtI$HH}LE3KHLCHHuHH0A_^][
HUH HHM`H]HL$SUVWATAUH8HD$
LHHAAZtHHH_tc3LHHD$xHt/IMH)HHD[D^KNCFCF3OHH4GHIuHH8A]A_^][HUH HHMp!H]HUH
HHMxzH]HSUVWATAUHHHD$
HLHczH3;t,HHHL$(L;tI$+I#;u^A};HL$(;vlLIMfA<\tfA</uKDD+HAHL$(H$H;tHT$(HfHHHBEI;r+IEHPDHL$(|
BH$H;tHT$(HHHHHL$(;HHA]A_^][HUH HHM(H]HUH HHH]HUH HHH]DD$SUVWHHHD$(IAHHEL3)t
7DLH@{HHHD$ HHL$0HL$0HHtH.H&HL$0DD$8AIHQ_uD$8L$ HGLDHT0HtGHc\$8Ht-
HHHD$0H4HtHHuHL$0FHc\$8Ht-HHHD$0H4HtHmHeuHL$0W@HH_^][HUH HHM0H]HSUVWH8HD$
AI@HuOH;tMHD$(HtHSH3L3HHDL@HNt2H8_^][HUH HHM(H]HSUVWATH0HD$
AI@L2HHD$`HtHCCC3IL$HHH@k@{$CH0A\_^][HUH HHM`H]HSUVWATAUH8HD$
LHZq;qv5HHtDGIHHoHH/wtc3LCHHD$pHt/IMH)HHD[D^KNCFCF3OHH4GHIuHH8A]A_^][HUH HHMpH
]HSVWH0HD$ HHHHD$`Ht'HHCD_D[GCGCGC3HHH0_^[#HUH
HHM`fH]HSUVWATAUAVH HcYHteLIHII4HtGHcnt/LIHIM,MtIMIuHHuHH A^A]A_^][HL$SH0HD$
HH8IHK(@HKHKH0[HUH HHM@HH]HL$SWH8HD$ HHYH\$XHK8HK(HKaHK'HH8_[HUH HHMP~H
]HUH HHMXHZH]HL$SUVWH8HD$ HHcYHtMHHHH4(Ht$hHt-HN8'HN(HNHNkHcuHH8_^][OHUH
HHM`H]HUH HHMhHH]HL$SVWH0HD$ HHH^H\$XHGHHWHK~HW(HKHW8HK(.HWHHK8
HH0_^[HUH HHMPH]HUH HHMXHH]HUH HHMXH^H]HUH HHMXH(H]HTHLSUVWH8HD$
H3H1qqZ;tHHH_;HH\$pHHHD$xHtQHD$hHH)HHHSHN8HSHNjHS(HN(HS8HN8H\$p3OHH4GHHH\$pwHH
8_^][HUH HHM`H]HUH HHMxH]HUH HHMxHFH]HUH HHMxHH]HUH HHMxH("H]HSH HHHHCC
C$HC(C0C4HHC8C@CDH [HSVWH0HD$
HHHHHD$`Ht@HHHVHKHVHKHV(HK(?HV8HK813HHH0_^[:HUH HHM`vH]HUH HHM`HH]HUH
HHM`HbH ]HUH HHM`H(H]HSUVWATAUAVHpHD$
EI@HE3AxHH0D8stE;tHA:HHiDA;}yH|$(HHL$0KLt$@Dt$HDtLLtPDtXDt\Lt$`Dt$hDt$lHOHT$(@DHL$
`HLPHL@gHL$0,HH0I;tHHHDCAIHQMjCAAHGH<{D9stD8stHH A:uDsA:tHO(H-
HO8HA:tHO(HHO8HHpA^A]A_^][HUH HHM(H]HUH HHM(HH]HSUVWHxHD$ HHH8H8+39k
3HCHHHHHHH|$(H$HL$0p3HD$@DHDLHDPDXD$\HD$`D$hD$lHT$(HOi$HL$`HLPHL@HL$0Q
HOHHH;k &Hx_^][HUH HHM(6H]HUH HHM(H^H
]DDTHL$SUVWATAUAVAWLHHD$hLE3E9auHIC`H
IK`|gLd$ Dd$(Dd$,HT$ I$8\$(Hl$
H|D9gu$D$8I;tH0H(\$(HL$0DDor/HED9`u%HED9`uHEHHA;EDA;nD|XHL xLcEt
t A;vDsAD;sbNlM}HItHTIouI;rxC;u
DD|$XDsD|$XD|$X3ED$HEtA;rHMHUHL$0D\$8D9\$<uHL$0xD\$8HL$0AfA\AfBYD\$8H}HtHHDIHUHe\$(
A;b$(L$!$(D$HL$
DHHT0I6DHT$0HL$pHXHDXHtHT$pH3HIDH$HDXHtHgHHHLptHExHct(H|HH7HtHZHRuD$(H
_HLXHH8Ht
HH3HHL$ HLX8A@2\$(Hl$ @$@HD$@DHDLHT$
HL$@D$R$8D$Q@|PDlSAIHHELD$@$0cLcd$HIEt/HAHHD$@H4HtH?H7EuHL$@(HL$0Hct(H|HH7HtHH
uHHA_A^A]A_^][HUH HHM >|H]HUH HHM0H]HUH HHMpH]HUH HHMpH]HUH HHMX2H]HUH
HHMpH]HUH HHMXHrH]HUH HHMXVH]HUH HH8H]HUH HHM@{H]HSUVWATH Q3H;vLI9Xt
I;r;t2;Lcv+H;tHEJHHHH;]rH A_^][HSUVWH83H9qv43HGHLADAD$(AHD$
H;wrHc_Ht-HHGHH,HtHMHuGH8_^][HAHSH HHIEUHCH [HAHHH(Ht@H
H@H3H(HSH HH Htqt HH [HSVWH HH3:u2H>H;HH _^[HSH0HH
%HT$ HCAffD$ =wtHT$ H2H0[HL$SH0HD$ HDHDHHit!Hx;CsCHfH{uHHHH0[HUH HEHt
eHHM@,H]HL$SWHHhHD$@H@($$HD$0D$(HHD E3D3tZHHhHSt5LBfA<@
utBfA<@
uiB;sbCfA@VLL$OADAAP0A@7A
BA IIuDT$PHHHT$HHHHh_[HUH HtHH

]HSUVWATAUAVH`HD$@MMLHH:3t3H\$0D$(D$ E3@DGHHHu`;t\HL$HrAHT$HH@:t/H\$0D$(D$
E3@EAHL$HHHL$HtHt MMIH;HH`A^A]A_^][HUH HHMHH]HSVWH@HD$ H9tHtStMHL$(AHT$(H@t
HL$(_HL$(HL$(2H@_^[HUH HHM(H]t?EHSWHHHD$
H9tHtQtKHL$(AHT$(HM?tHL$(HL$(HL$(2HH_[HUH HHM(>H
]HSUVWATH`HD$0HL]8HS8;t;tHI0tuHL$HHL$8;t;u2\$
LL$8LD$HHI>t.HT$8HL$HHL$8HL$HHL$8HL$H2H`A_^][HUH HHMH6H]HUH HHM8H
]HSUVWATAUHhHD$0HLH
3HHLHI7H6;t;tE3HIAtuHLHHL8;t;u2\$
LL$8LD$HHIN=t.E3HT$8HL$HAHL$8HL$HHL$8HL$H2HhA]A_^][HUH HHMHH]HUH HHM8H
]HSWHHHD$ H5t 3Ht`\=tQtMHL$(AHT$(H<t 3HL$(HL$(HL$(2HH_[HUH HHM(H]HI(sHSWHHHD$
HtutH&tqH4tHtQtKHL$(AHT$(H;;tHL$(HL$(HL$(2HH_[HUH HHM(2H
]G+>H(v3;H(HSUVWH(IHH=@uHHH_HxHHx};{sH{fx@H(_^][HSWHHHD$
HHHL$(LD$(HHoHL$(HH_[HUH HHM(BH]HSVWH0H3HqHT$ f0ft$ HT$
HD[AH0_^[HSUVWATAUAVAWH8IIDLUB3-DD33DIHLD:ELAEAAA
A@7AP0BBL MM+uH;B| t&F9FuH@VHfQ.Af<QVHT$ HADDH;EtHHzHB@:t
N4H;tHE3H"H@:u:~Pt=uAdH~f82H8A_A^A]A_^][AHSWH(3H8uHI[::H(_[HSUVWATAUHHD$PH;
ttPH[-t2=H.DHHL$ tHL$ ax#T$(J;u;uL$(HD$ fHHT$ HL$0|$(AH\$
H0;t"3H}?}A;HL$@p;AHT$@H63HL$@s}i|A;tHL$@~HLXHHT$@HL$X^
uHAHL@6\$x@"HHL@mHL$@]|A;u\H$kHHHuH$$@"H$u|{A;HL$
+#H\$;uf;\f;/GD;;|$(8|$(f{H\$ HL@ ;|$8}cOHD$0HH(y|$8|DHT$0HL$ HL$ t;|$8|HL$0HL$ 2HL$0HL$
@fHL$0@2HL$ @IHL$0vHL$ k20HL$0]HL$ R2HL$0DHL$ 92HA]A_^][HUH HHM H]HUH HHM0jH]HUH
HHM@JH]HUH HHMX*H]HUH HHH]HSH HHIjHH [HSUVWHXHD$
IHHT3@:u2jHL$(HL$(d@:uALHT$(HL$8gLFLH@:HL$8@:t@HL$(@HX_^][HUH HHM(.H]HUH HHM8H
]HSUVWHHD$ HHLPHLxHHL$PQ 3@:uHL$x42L$p@uyHL$x 2
@@EHL$x@:}HHL$(\$09\$4uHL$(P\$0HL$(fA\f,Y\$0HD$8HL$@>HT$(HL$8HLPHLxHcHTPHL8@:;\$0
s
\$0HD$(f,pHT$xHL$(D\$pAAtHL$(@:t'HL$(@:tDHTPHL8~@:u\HL$xHL$@HL$8pHL$(2sHLxHL@HL$8
BHL$(2EHL$xHL$@zHL$8HL$(e3HC@:u2HH_^][HUH HHMPH]HUH HHM(H]HUH HHM8:H]HUH
HHM8FH]HUH HHMP&H]HSWH(3H8uH::H(_[HSVWHPHD$
HH3@:u2jHL$(HL$(x@:uALHT$(HL$8{LFE3H@:HL$8@:t@HL$(@HP_^[HUH HHM(>H]HUH HHM8H]A
t*39A0t#HQ(f:.uy0t
y0ufz.u2HSH H9HtH dut HH [HSUVWHHD$0IHHH'tHT$@HuHH;uNt@HL$ }AHT$ H.tHT$@HL$ tHHL$
H;u2MD$@E HD$DHEHL$LHMHL$THMT$\H L$`HHUE$E%HM(HT$lH_^][HUH HHM H]HSHpH HHT$
.ttLD$ L$<HT$LC HD$$H HCHD$,C$HCHD$4C%HCD$@HHHK(Hp[H H~HSH
HHD$8H0{v1KHHm~HLHtCH;sHKfHHH [HSUVWHHD$0IHHH=uxr23rH%tE3LD$@3HHH;ubr&u2ktEHL$
MAHT$ H+tE3LD$@3HL$ +HHL$ fH;u2HL$@HNHT$HHH_^][HUH HHM H]HSHHHHuHxqH HT$ u2HD$
HT$(HHCH[HSWH(H9ItLHQm3:tp&H(_[3HAAAAAAA A$A%HSWHHHD$
H$tHpuQtHHL$(AHT$(HP*tHL$(UpHL$(HL$(HH_[HUH HHM(>H]HSH HYHJC9CuH"SHfQ*fQSH
[HSWH(HHH;Ht
H>HSLtHuH(_[2HSWH(I3:tdoH(_[HSUVWHHD$0HHH3:H_____
_$_%HO(HUs@w%HM:tdf9]u^Ef$f$:f$\f$LL$ LHT8H$C,:tH$H@HD$`HHL$`::u8\$itHD$pH@HL$`
OH;wNf9\MiHTuHL$ HHL$8;t$@s
t$@HD$8fp|$(vL$(HD$ HLHHy:uHqyHL$ sHL$8HT$8:tMtuCfz\u<H_____ _$_%HO(YHfuHT$8H:}g
HHD$`HT$8HL$hHL$HnL$HT$HHL$`:tP8$twHT$
HL$Hz:HL$H|HLHLHTHHL`:uHL$HLHL$hAHL$`HL$8,mHL$HHLhHL`HL$8jD$PvH;s
LPHDHfHHO(HT$HL\$XL@wHLHHLhHL`6HL$8@HL$ yHL$8hHL$
]H$H:EHDuH$H:tzFf9\EupH;G:H_____$_%G H$HO(0vG0HG(fXf}\tf}/f9]uYH.D;H_____ _$_%_0HG(fD_
qH ;kHDEHD$

f9VH;FHHL$8T$@9T$DuHL$8rT$@HL$8fA\fQT$@9T$DuHL$8BT$@HL$8fA*fQT$@LHT8H$K:tKHG(H
$H@pH[k;u#HT$ HHL8:@$H8$u;tuHL$8?H_____$_%;EO
HO(HT$ HL$8LHH${@H$>@H_^][HUH HHM`fH ]HUH HHM H ]HUH HHM8H ]HUH HHM`jH]HUH
HHM`vH ]HUH HHMHVH ]HUH HHH ]HUH HHM8H ]HSHH`HD$ HH@@@@@@@@@@HHHHL$(+t
D$Ht2HL$PH`[HUH HHM(^H ]HSHH`HD$ HH@@@@@@@@@@HH!HHL$(st
D$Hu2HL$PJH`[HUH HHM(H ]HSH`HD$
H3HD$(D$4D$0D$<D$8D$DD$@D$HDLDMHL$PHHL$(HL$PH`[HUH HHM(H]HSUVWATAUHHHD$
LHcYHt,HHI$H4HtHPHHuAD$33eH3H$tHHH$HHe;HL$(E33tYHf}uEJkHL$(DoH$HtHT$(HJ3HI'H;rD;@HL$(
hH_@
HR2HHA]A_^][HUH HHH]HUH HHM(H]HUH HHH]HSH H9HtH `dt HH [HSWH(yHty t HAH4H
HT$@cuct2L$@HH HHH(_[HSWH(yIty t
AuHQE3H HEH LD$@D$@Icudct2L$@HH HHH(_[ME3H(LL$8E33qH(HSH0H ILLXDXHD$ bL$XH0[
D;DGHSUVWH(IAHHAj
DLL$`;HHDGD$`mL$`tt
H+HuH(_^][2H(H b3;H(H(H LE33wb3;H(HSH0
H ID;LLPDPDGHD$ aL$PH0[HSUVWH(IAHHALL$hDHHDhThtt
H+HuH(_^][2HSVWH LL$PE3HH3:tH9|$Pt2Ha;H
_^[HSUVWATAUAVAWHhHD$@EEHH3:u2_HYD$DtH\$0Dd$(Dl$
E3EAH_HH?uW;tSHL$HAHT$HHq:t&H\$0Dd$(Dl$ E3EAHL$H._HHL$HH?HhA_A^A]A_^][HUH HHMHFH
]HSUVWAUH
HYLL$PE3HHHCA@ILL$PEHH@HDPLPt6H@2A;HGLL$PEHHID$P^u@tf<HuIBHHtOLL$PEHHD$P#t0D
$Pu!Ht#I+LL$PE3HHhuHHGHH
A]_^][HSUVWLH3H9H@i HiH@8iH IkIC(HD$0ICE3E3@D$(HD$ @x^;HD$h@s f\HCf9@u~f9Ouxf.uqf9Oukf
:udf9ou^GfLLHT@Hff$:f$@:tH$HC@@8k HHl$8HHD0H$E3E3D$((HD$];@:C
tH$HCHHl$8HHD0HD$HE3E3|$(HD$ =];@:C u<HHl$8H$HD$0HD$HE3E3L@|$(HD$ \;C @8k
tDXL\HD$THHL$HHK@:t)@8k t#H9ktHL$E33H
H_^][HSVWH0D$pHHD$(DL$ EAOH HHFsH0_^[H8AAD$ EAAH8H8AD$ EAmH8H8D$`D$(DL$
EA@H8H8D$(DD$ AA@H8H8AAA@D$(AD$ iH8HSVWH A;PIHvI"HE3tHH+ftAfHD;rfBBDCH
^[HSUVWATH IIMHHA(r/DDRBI;wAAtE;tA*2H A^][f{uIJHH;urKDK
sDcHE;GurHGu;w+A;r@u;w+A;rALGHAALGH3AEQHSWH(HHIHf:3:t0HOH3:u9_
*HOHWH[;uHWHOz[;H(_[HSH HHQD$0HHH]ft&Hf@\HHt
HHH [/HSUVWATAUAVAWH(HHAHDu
@@H@$#tHH7@"@$AE3Af9tHHf8uG<$@DED#@C/u@lH;ot0HHt;HHHGtHHoHHL3k.L@fACfE3
E3fAk@AtEEWfECfE{
fESfEkI@t3EAIEt.
dAHdftH+NfAfHfuEDHKfEC$HtH+INfEfDHfEu$t#EftL+OfA
fHfuH(A_A^A]A_^][HSUVWHhHD$@IHH3H9:tH
H>H~HDHD(D$ AE3HHL$H@:uH;tHHL$HV@HFHH$H|$8HHD0l$(H\$ E3E3
HL$H~V;uH*U$H;nt(H9>tH
H>H~H;t#HHHnH;tLHH/)H@HL$H@Hh_^][HUH HHMHH]HUH HHH
]HSUVWATHHH$AM@HHHH$HH$<3:t&$$@:
V;@:tH?:!HHL$@HL$@;}HL$@=HL$@ufy:u%;DHsDHfAHL$@HL$@HD$pAHHL$p:uHLpHLpvHD$PD
$
AEAHHL$Pr:u
HL$P@LH\$8HDhHD0\$(H\$ DM HL$P/T;u
HL$P
HLPHHA_^][HUH HHH]HUH HHM@H]HUH HHMpBH]HUH HHMP"H]HSH HHEN@AHD$@HH
[Hf\tf/tHffuH+HHSH HIt7Hf|H\t,f|H/t$9KuH(SHfQ\fQSH

[f9arf9zvf9Ar!f9Zwfy:ufy\tfy/u3HSWH(f9\Htf9/uBfy\tfy/u4fy.u-fy\tfy/u3f99tHf9uuf{
:tJ2H(_[rwHKHZ_et;vH"f;0rf;9wH;rf\f/f9tf9u[f9Qtf9AuOfy?uHf9Qtf9Au<fyUtfyuu.fy
Ntfy
nu fyCtfycuf9Qtf9Au3H(f9\tf9/uJfy\tfy/u<AgtAfy.tfy?tAHAxBD3H(f9arf9zvf9Arf9Zw
fy:u3f9\tf9/u)fy\tfy/ufy?ufy\tfy/u3f9\tf9/u*fy\tfy/ufy?tfy.ufy\t
fy/t3H(3:tH:tf9QuH(H(f9\tf9/t3:tH(H(LE3EXEEESBAAIcft0IAf:u
yAf\tf/uAIAffuH(H(gDxJLAVy3BDH(H(fyUtfyuu*fy
Ntfy
nufyCtfycufy\tfy/tHy3HyH#H(H(fA\fA/fD9 tfD9u*fD9ItfD9Aufy.ufD9ItfD9Au]3:t@K:t9fD9 tfD9u-
f9Qt"fD9ItfD9AuHH#H(HSVWH0H3HqHT$ f0ft$ NHT$ H+D[AH0_^[HSVWH
H3Hsf=.tCf<B\tf<B/CBf=.DCAFBfA\tfA/t
fEHcHBHf9\tf9/u+H+HHB+HH+xf9\tf9/uxD+:A@fEAD3D'f=\tf=/tfu"AtfDDH/HfZf@2H _^[3ftXHf=.u?tfy\tfy/u-
Aft6f=\t0f=/t*f=.uf9tf9\tf9/tfAHfu2H(tfy.tH{33ftkLAf=.tf=
uKAHftf\tf/u5f=.uFt+fA8\t$fA8/tfA9u.BtfAx\tfAx/ufA@IfuH(HSUVWATAUHxHD$ HHBHf)fudf.ufyt
f9iufyuCf{.u(HKeuHHL$8LL$8IuI|IYHLXHLXuHL$X|HL$8|;\$@s\$@HD$8fXHT$8H?HT$XH2HL$XW|HL
$8L|_fA\fA;f/cHHL$8*AHEeEEHD$8JhHL$XHL$XuHL$X{HL$8{H8WHD;l$@sDl$@HD$8fBhHT$8HWHT$
XHJHL$Xo{HL$8d{wfD9cf{/HL$(HL$(
uHL$(${27HL$(KE33LL$(IetDuwIz2etH5VVAPfE9!tfA9/fE9atfAy/AII/DuIz2H5UHL$Hf\t,f/t&C,HD$(HHHL$H
HHL$HHSHL$HoHL$HUuHL$HzHL$(z2HtHH
EEHTXHL(HHHL$XyHT$HHHL$HyHL$(yIy2HSHL$8LL$8IIYHL$X|HL$XuHL$XKyHL$8@y2VHTHA;\$
@s\$@HD$8fXHT$8HHT$XHHL$XxHL$8xHxA]A_^][HUH HHM8FH]HUH HHMX&H]HUH HHM8H
]HUH HHMXH]HUH HHM8H]HUH HHMXH]HUH HHM(H]HUH HHMHfH]HUH HHMXFH]HSVWH
AHH"t{u@uHH2H _^[HSUVWH(HIIIHtEHHt6Gu9Cu8DpuuHHk{uHHZ2H(_^][HSUVWHhHD$ IHHI%H
tuLD]I*uIHLHHLHuHL$Hv;_s_HfXHT$HHDHL$HivHL$(HtHHL$(HL$(uHL$(+v2SHL$(RL\$(IqtufIu2rtGfA
;\tfA;/fA{\tfA{/IKHuIu2HL$8:f}\t+f}/t$HD$(HHHL$8HHL$8HUHL$8HL$8suHL$85uHL$(*u2U;\$0s\$0HD$(fH
HT$(HHT$8HHL$8tHL$(t
It2Hh_^][HUH HHMH:H]HUH HHM(H]HUH HHM8H]LH3[HSWH(3Hf9tHHf8ut7Cf<A:u-
u$HtHuHEt2H(_[HSUVWATH`LH
LIIH@2BHOHBHt*LLHLD@HT$PIHD$@HHD$H@HHD$8LL$<LD$4HT$0IHD$
At,L$0D$4H@HuD$8HHD$<HHH`A_^][HSVWH@@H3&ALD$pS
H?;taLD$4H3?;tBHL$p@LD$0E33H\$(D$0D$<H\$?;tA;HL$pHAH@_^[HSH@H
NVA3H;tHpNH7AH;u3,HL$ |$0uL$(D$$
u
|$,?wYH@[HSWH(HHu30~3H{AHtHDGHf
HIuH(_[HSH H3CAHHCuf
Cf3H [HSH HHkHHCt f3
f
CH [H(3(}2;t.~~$ ~~@H(|~@uffQfQfQHQ3H(f9u3H(HSWH(HH=@DA|EA~*A ~9A~A~-
A~A~!A~A@uAHH3HH?H(_[HSWH(f:HHtHxAHHf3H(_[HSH f9Hu3yf
CH [HSH Hy-=uH$LH%
HL$@HD$@f
CH [HSH HfHH [HSH HHH [HSWH(HHFffGSH>HHGuHKH
HL$@HD$@OHH(_[HSWH(HHHffGHHGuH7KH8
HL$@HD$@HH(_[HSWH(f9@Ht
f@ffCHH(_[HSWH(f9Ht
Yf{HH(_[HSWH(f9HHt
,fH{HH(_[HSWH(f9@HHt
f@HHCHH(_[HSVWH0HHT$XA<tHT$ HL$X;u2DL$ @0A'rYA

iHDADAABAD*DDKDAABAD*DDCAgfff@AH
D*L$"DDKC-@S+A@KL$&C-@S+@KL$(AH@S+A@KL$*C:@S+@KL$,A:H@S+@K~uD$XDD$\.I
HALHBzEL[IHHiL+IAD*DAEDuA;DBAHH0_^[H(E3D
DAVtNE;At3At(AtAuOAf-
?DJpHcIHIe^ISAtIAt?At4At)A+t?B: Hxc(HE3HIHIIIbH(3LD@@HIHHIuHSWH(H8LGHHH(_[H%8HhHL$
E3A;t)L9D$ t"IA@@HT$ HHDHuAHL0^8DPHhHSH`HHD$ @HHL$
#8tHD$(H9D$HHBD$HHH`[HSWH(HLD$XHT$PH7u%@}2D$XL$PH(_[DALI
LHBzIHHH;s AH+H;vA2AH%E7
@sL
I

sL
I
HSUVWATAUAVH Dv
3IEMDLt?H5\
EHtHCHHtHSIou&D/
HA;rH A^A]A_^][HKI$K MH(HE3LDBDD
E;vH
HH;JtAHE;r2H(HRIysHSUVWATH 3HL9
v)H=
H/I$HUouH;j
r2H A_^][HMHHSUVWH(AAAAAA;
+
IIsDH5
H4t>HF;~ thHt HHPHOHtHPH_F G3H(_^][HHt~$HtHt HHRHMHtHPH]Ht HHPHHtHPHHSUVWH8HD$
I@HD$xMLD$xH\$xHtkFfHD$(Ht@H$H3Ht HHPHHtHPH>HHPHHtHPHHt HHPH8_^][HUH HHMx>H]HUH
HHM(:fH]HSWH(Dv
L3EIt-H`
LM9YutIQIHuHA;ry3
LH(_[HSUVWH8HD$ I@HD$xMLD$xjH\$xHtkFXeHD$(Ht@H#H3Ht HHPHHtHPH>HHPHHtHPHHt
HHPH8_^][HUH HHMxH]HUH HHM(dH]HL$SH0HD$ HHIHtHPHHtHPH0[HUH HHM@fH
]HSUVWH(HBID
E3EHtPH
H8H9OtAHE;r2HHt
LHAPHHtLAPHHWHo3H(_^][HSVWH 5:
3H;qYv H cHHH_cwHw9
v'HHHq
HH@H
H;
rH _^[@t<@t.t Wtt;E3
tFt<t2t4t$tt(t@H@W@3LD$HT$SH E3ILHHHILLD$@HT$8MDIIDAR
CH [HHQAHHSWH(IAHL9sDHIDD$@LL$@HPGD$@HOHGu3
H(_[HSWH(HHIIHt}BCt3CH{HH(_[HaHQHAHAHHQAHHAHSWH(IAHL9sDHIDD$@LL$@HPGD$@HGH(
[HSWH(E3HHE;tAtAtAB8AAHIHLL$@HEP CHD$@HCH([HIHQAHHHAHSUVWHhHD$
Hf.vHvHHL$(IrHL$H2qK;~';|$0s|$0HD$(fxHEHxHL$HrT$09T$4uHL$(nT$0HL$(fA_fQT$0HL$8p@<HT$(HL$
8rHL$8iuHT$HHL$8tHL$8t_;uHT$(HdrH*uHT$HHutHMHL8_HLH_HL$(w_Hh_^][HUH
HHM(H]HUH HHMHH]HUH HHM8H]HL$SH0HD$
HH;HH_HAH_HAHk_HAHIX&tHKhHtHHSpPHK(H0[HUH HHM@H(~H

]HSUVWH(HIHIHLHuHwH,LHtHLHuHS6HLH{uHSHLH_uHSHHHH#HC 3@H(_^][A A H(A A u
HP(3H(HSUVWH8ID;HAB.+LL$p3DHHHt$ +H;DtD$pED;u4+mu3'+;Du@
D;ANH8_^][HSVWH0AIHs!38Y0tG8Y1uAt<D;tAtAttHQ8HQ@H;}^L;HQ@tI3NLL$ H(@H;HL$
HO@HOHtH:u%*;Du@
D;ANH0_^[HSH H 3:u%*;Du@
D;ANH [HSVWH`HIHHT$ II)t]HtL$@D$DH HHHtHD$$HHtHD$,HH$HtHD$4HH$HtD$
3)H`_^[HSWHhHHIHHT$ p)tmDDDD@L$LI H HCLD$<HCD$PLHHKLED$HC D$
C$HD$$HC(HD$,HC0HD$4HC83A)Hh_[HSH H3:u%);Du@
D;ANH [HSWH(IHLL$@HL$@HK(3H;t:u%(;Du@
D;ANH(_[HSWH8AIrHLL$ H3H;tHL$ H:u%V(;Du@
D;ANH8_[HSVWH HyHLL$@3HDVu@2HHxtHT$@LD$PHu2%@H _^[HSH H3:u%';Du@
D;ANH [HSUVWATH03ILL;HtA9D;vhD;AB&LL$pDIHH|$ &L$pHMH;t;u%';Du@
D;ANH0A_^][HHHHHHHSWH(HtHXHH(_[HSUVWATAUAWH
MIALHtAEy0y1t(HA@HI8H;rH+H%@H+H;GAHO@HH`v
H9_PIu"MuHIH%H;H;_Ht%HO(LL$xE3HHD$xHGH1H_PtHG8H+GHH;BHXuHlHHGXHWXHO(LO`D+t8`G`
HGH'+H;OHHtyHO(LL$xE3_uW%u;@BO`A#;ss+;BHWXIDH_@HtPuKNHD$xHG@HGHHO(LL$pDIHL$ptM
HO@HOHt3B;j$HOhHDtL HWpDAu@
EANH
A_A]A_^][3H2Q HHVHAHVHAH0VHAHA(HQ@HQHHQXQ`QdHQhHQpHHSH Hy0Hu&UHC0H
[HL$SH0HD$ HHI0)UHKHH/HKH0[UHUH HHM@HvH
]3AHHA8ALHA@HSUVWH(3AHD;Hu^@8yLuLAH1E@:tCLHKLL$`DH@:u2"DD$`KHHL$`HK@9t$`CHH(_
^][HSUVWH(EAHHuMHI8H+t1H;HGHO0LdOHLHfH_8H_@+GHHDHHH(_^][HSH HHsdHCHC0HH
[HSUVWATAUHHHD$ LHH3@:u
@HALG8L;t+HW0I3;LG8HW0ADHo8@8wLHD$(HWHL$(D@:u@~H;o@L$AHW0HL$(f@:tL9tXDHW0ID
3;u0D$HW0A
D$HH;o@r@HL$(HL$(D9gHuH;o@t@HHA]A_^][HUH HHM(.H]H gDAL3DAP3H(H
HtHPH(HSWH(H;B;BHt9t-HgfHHu3H(_[HSH HHHPuHjH [HL$SH0HD$ HH0HHq0HAHF0HAH0HAH/HA
H/HA(H/HA0H_/HA8H,/HA@H/HAHH.HAPH.HAXHx.HA`HM.HAhH".HApHHtHPHHtHPHHtHPHHtHPHHt
HPHHtHPHHtHPHHtHPHHtHPHH0[ReHUH HHE@HU@HHHH#H]HUH HHM@HH]HUH HHM@HcH
]HUH HHM@HCH]HUH HHM@H#H]HUH HHM@HH]HUH HHM@HH]HUH HHM@HH]HUH
HHM@HH]AxAxHHHH
H(H0H8H@HHHPHXwH`kHh_HpSHSVWATH(HIHIAHQMHJuHH+MH,tH5MHuHSHHHH#HjHMHuHSHMH
uHSHOMHuHS HMHu HS(yHMHuu HS0ZHMHVu HS8;HFMH7u
HS@HMHu1LI8uHHHhHCHPHMHu1LI8uHHvH!HCP
HYMHu1LI8uHH/HHCXHMHCu.LI8uHHHHC`~HMHu*LI8uHHHuSHCh>H~MHu2LI8uHHTHuHCpHCx3@H
(A_^[LD$SUVWATAUAVAWH8LHTL$L$E23HD$
MtI$H9D$ WEu;LD$(H+IHD$(*'\$(;AHHDHP ;vTE;H;uHHDHP ;EMtI$H+L$ H;GH$DH9*udH|$ tYMtILD$
HT$ IPu?3;+LDHAIIu @3H8A_A^A]A_^][HSWH(HHHt HHPHHtHPH3H(_[HSH HHHtHPH3H [HSH
HDtHH+L;DGEHD$0HHLL$0PDD$0)HEu}D9wtIDAD+t&E3HAAAIE;rD3H [@EAEAHSUVWATAUAVH
MIELHtAEHOu|oX;t:+ID;ABHOPDD+LHt.;GXu*HDHWPHP t;wEy3H A^A]A_^][@HSVWH0HD$
HHxDEHHHSHP ui;v_;CP{\;s"HCH9rHHDHSHP
;unHK=M`HD$PHKxHLD$PHHL$PHtHPHL$PHtHR3@@H0_^[HUH HHMPbH
]H3HH3LHH;HHtHAIiHSWH(HHHt HHPHHtHPH3H(_[HSH HHHtHPH3H [HSUVWATH
MIALHtAE7tEDAD+t"E3HW`AAAIE;rDGhH+HW`LDhHDh#DD$hEHDHW`HP
u*;;GhwVtHG`;uHDHW`HP ;@X;GutHOxH+H;GDIHW`-)HHtu3H A\_^][AHHH`HHH` HwHkH_H
SH(GH0;H8/H@#HHHPHXH`HhHpE3H&!DAxLHDDLLH HAH HAHq HAHF HA H
HA(HHA0HHA8HHA@H_HAHH,HAPHHAXHHA`HHAhHHApLLLLLLLLHLHSH AxAxHu9H@3H [HHHH
H(H0H8H@{HHoHPcHXWH`KHh?Hp3EtAtAtHQ(HQHyMHQtI3HSWH(LQAIL+Q

E3HI;DL$PEG3Et!HILL$PHPDL$PLO EuG(HtDH(_[HSVWH MIHHtALIHI(L;r3bI+AH;HC0DGIH;S
DD$PtHKHS E3HE3P u*DD$PHKLL$PHHPHL$PtHK HKH _^[DD$SWH(IE3HHtDAH;Av,L9IuD8I!A
u@CHtD37DADD$PHIHtHLL$PPDD$PDAH)KHtDAH(_[HSUVWH(MIHHtALIHI(L;r3I+AH;HC0DBI3HDD$`
H+KHH;SHr3HC@H;w*EH+L;wEtZHC8MHHDD$`@H;S tHKHS E3HE3P u3DD$`HKLL$`HHPDD$`LC
HtDAHKH(_^][HSWH(HHI IHLL$@PHL$@tHKHKH9KsHKH(_[H(ELtAtAt:HQHQHy$MHQtIHI
IR(E3HIRE3P H(HA(HQHI LHI`(HSH AAHuHIHtHPHg<3H [HSUVWATH0HD$
IMHHI8;HHt@HHHC3H\$`Ht
HHPHt HHPHKHtHPH{Hk0Hk HCLc(HKHE3E3HP t
HHRHE3E33HP H3H0A_^][HUH HHM`>H]3ffAHAH#HSWH(f9HHtFf9+ufyt9H0Bu)f;-
uf{tHHBt2H(_[H(t3ttW'fyHIg%W3H(HSWH(HHHT$@HXHD$@H+HH(_[HSWH(f:Iuyu#BA(f:uytHT$H;tWD$
H3H(_[HSWH(yIAHu2f:t"HT$@Hu0|$@EBA3f:tWLSH(_[HSWH(HH HHT$@WLHD$@I+HH9O9Gu&@A
A;AQseHdA A@Af=wfAbtgtktmu7D@A
AE+AA;HHK3WH(_[HSVWH@HD$0Hq3yHo8HvH]8HD$`H;t*0HxH2HD$ L
DHH{HH@_^[HUH HHM`28H]HSH ALH;ArH~
HL$0D$0H HACIPH@HCH"CH [HHSH f;ItAfuf9u/QI3fuHT$8u#T$8Hf9uHH2H [HSUVWATH HH
Lf=I<3;|HDI*JHD^JX49_vHf80rf89vH;_rHDIIHHXHH A_^][IHSH HH 6HKHt HYL
kDH6H [HSUVWATH`HD$
ILH3MQHL$(39uv3HEHHL$(H;urHtKDDfDHfDJHDPD$@HHL$HuHT$@HL$(HLHIDL$8H|$0LHT$(I
PHL$(5Ht!L
sDGHHO5H`A_^][HUH
HHM(H]HUH HHM@~H]DAAMcxHJH9tAHIyAH(LyB$IHfzt
B ;GH(HSUVWH(H A4H3H;tCfCfC
HCHHH4-HKC3H(_^][HSUVWATH`HD$ IHL3H56HHMf;uHry
WHHHDDfDHfDJHDP\$@t+v
tuHTHH,IfD(fD$*HD$0f>uHHL$(f>u?H$HDuHL$(VW-
HL(f_f>u6H$H;u$HL$(_fHHL$(hWf>uDHHHQHH+H;OuHHL$(AHHL$(
HHL$(LD$HHL$(uHL$(wWQHL$(e G2HH$HtD$@D$DCHKHT$Ht3HI*3HL$HH`A_^][HUH HHM@2H
]HUH HHM(H]HUH HH1H]HSUVWATAUAVHH`HD$
HL3Hpp3xHH,BEuHL$(n1A3HL$0HE4f:uKI1H$HtHT$(HB3HHL$8)D$0HD$(fL$0+9L$4uHL$(y?L$0HT$(f4Bf
JL$0HHoI0H$HtHT$(HWB3HHL$82)HL$(0|$@Ht$83LI$HL$HAHL$(ALD$(HT$HHLD$(HT$HIDu!HL$(0HL
$H0I;rGHL$(/HL$H/Hct\HHH+HtHM/H/u2Hct(HHH+HtHM/H/uE3H/AH`A^A]A_^][HUH HHM8H]HUH
HHM(|H]HUH HH/H]HUH HH.H]HUH HHMHZ|H]HUH HHM(:|H]HSUVWATAUHxHD$ ILLzu
WfA8uXHL$8?HL$(?LD$(HT$8ILD$(HT$8IHL$(4.HL$8).t3H-{HUI$-5uHry
W9HH

DLfDPfDRHDX\$Hvt f>u6N@rWl sHL$PuHHHL$Pcf>u6HL$8>HVHL$8?HTPHL8HL$8*-
WtcLD$PHHuWG ,HH$HtDHDLCHKHT$P3HIa%3HL$PHxA]A_^][HUH HHM8zH]HUH HHM(yH]HUH
HHMH~H]HUH HHM8yH]HUH HH,H
]HSUVWHHHD$
HHAHAf:HMq1HHL$(z=xL$0;s|$0HD$(fxL$0L$0E3HT$(t'Hf8sAHD;rHp+W<HN87HL$(U+y3 OHEHHHN
y=HV HuHH_^][HUH HHM(xH]HSVWH@HD$ IHHzt#H
H2uLHHAf;tW4HL$(^;HSHL$(<HT$(HHL$(*H@_^[HUH HHM(wH]HSH HH ?HH [A3AQQHQ Q0DAQHA
I+HD;vIHHSUVWH(qH9qrq+qHI(3HtWDH'H_(HOHtWl$PHHLL$PDPt$POw9OuG9OuG0GG;GGHG
OH(_^][HSH AH9AtHGu
C9Cu3H [H(tHp
HL$8D$8H(HSVWH H;B3H91t9ytH I>H3{>H;HH _^[HE33H`HHHSUVWH(HHIAHHtHPHGHt
HHPHOHtHPH_HLGH{H@o H(_^][HSH HLI0HA8LL$0HD$8HtL
LL$0MtIHD$8y@t'HIHtHLD$8HT$0PuBLL$0HD$8{At0HC(LK(HK{ LL$0HD$8HLD$8HT$0IDP 3H [3HC

QHHQHQHHQ(HQ0HQ8A@AAHSH0HD$ HAAu.HL$@HIHtHPHKHtHPH-'3H0[HUH HHM@HH]HSH
HFuHK3{FuHK3lFH [HSH H&FHKFHKFE3CD[D[L[L[(H [HSUVWATH MIALHtAEt\ytHIEuKG;wGwt;HW
DIH_ H_(Htu)wuHOGxEH@E3H A_^][HSVWH0MIAHtAtWyuYHQ YHDLHFE3HT$ AAHL\$
HD$(Xr@+^tHt3 F H0_^[HIHSH AAHu0HHHIHA AH\DH$%3H [HIHSVWH HIHIHaHAWtHyAH>u
HC3@H _^[HSH AAHu!HHHIHCH{$3H [HSUVWH8HD$ IHHAAAHA HA($HHt@HHH{3Ht
HHPHD$`H]#HHt@HHH{3Ht HHPHH8_^][HUH HHM`rH]HSVWH IHLHtEt8HIH~
H;s+AH+H;HGHFHLIH~Ht;3H _^[EtAtAtHQ HQHyMHQtI3HSH
HHIHCHH [HSUVWATH HHI EH}IH+I;IG3H;tHMLH} H;t>H;u
D;t@H A_^][HSUVWH(HIHIHLHt,H-wLHtHvLHxu
HC3@H(_^][HSWH(LHHI;vBI@v IHIHHGHKIH;HGH}Ht HCH;H(_[HSH HQ HH;Q r
Hu3HCHC H [HSUVWATH MIALHtAEt-HHuLIHZH} Ht;3H A_^][HSWH(HHIIHLL$@PL$@HKHtH(_[HSH
AAHuHI(HtHPH 3H [HSVWH Hy HqHH;zt0H
Htb HHHCt"H H{HHtHLH|H _^[HSUVWATH IIHHHIt<AHEEI;LL$`HHDBPT$`HHH+utHu3H A_^][HSH
ILD$@LD$@|D3D;tAH;\$@H [HSUVWH(MIHHt7HDH;LL$`HHDBPT$`HH+utHu3H(_^][@HSUVWATH
HIE9Ou>tHHtDGHHI%HHwLDGED+IIILE$GH A_^][HSUVWATAUAVH
Y3ILLtAIE4+D$IEJH;zswHtPHLIt>xn;uAuIMDHHtHH@AX3HIH;{t0HHtHHHCt"HH{HHtHLIH
A^A]A_^][HSH HHIHHHK<HK'<{ t3H [HSVWH HyHu
H-=uGH{u
HK=u3HK|<HKs<HKC H9t3HjL;H _^[A H<HSH HA HH9t;HKHtn;HK;H [EAtAE3EALE3APCBD LI|y
IAHsPysJHtII;s5DA
D;t.3D;t D;tvAu3rWH0 A@ HD$(fI3rWHd4 A@ HD$(fI3HSH :HYtH+HuHA!E3D8tHHA8uAHH [H
r)9smHAHtII;sTHtHsy
u=HtAu1Qt,3;t;tvu3H(Hst<t3HHRH(HL$SH0HD$ HHytHIH(HP HKHtHPHHtHPH0[HUH HHM@H]HUH
HHM@HH]HSUVWATAUH8HD$ MMHHzv
@HD$xHIHLD$xHlHL$xHu
@HHS APtHL$xHtHPHL$xHtHP{@@t0HOH(HIPL'HOHH3PulE3H;LEMIHO@t'HOH8HPDHOH(HP
utHHtHOH9Qpt3H8A]A_^][HUH HHMxjH
]HSUVWH(HIHIHLHuH?HhlLHtH"lLHuHHSHHH#HC3@H(_^][HgHSUVWHHhHy0HH9y
HXt#HX@BLH@L+HAuH
LH+HuDF"3A;tvA@buA@mH;AuA
@kHAE3D8tHHA8uA<ADHlDF!H98AD++AtHa<HtHJD&HtH;DHT$ HJHh_^][HSH@3AIfD$
fD$"HD$(t"AtAu1HT$ k%8ADt HQPH9A0tHQHt
HL$ HL$ HHL$ 3H@[HSUVWH8HHHP HT$ AH(DC(HT$ HK*tA38C(
| u/H3LKPDBHP uaH{PrH{u8C u8C!uBC@HHPHK0HtHPH{0HHPHK8HtHPH{83CAH8_^][HSWH(HHHHP
HG8t HHPHO0HtHPH_03H(_[HSWH(3H@y@@yD@yE@yF@yB@yC@yG@yHHyP@yAHI0H;t
HPH{0HK8H;t
HPH{83H(_[H(hF3H;t%PH
AHH
HHHP0HP8P(HH(H(h3H;t&PH
HH
HHHP0HP8@(HH(HSVWH HIHIHIHA?tHfAH&u
HC3@H _^[HSUVWATH0HD$ M@HHyuM:HD$`Ht
H3HG@\H_Ht HHPHOHtHPH_@H?HD$`Ht3HH3H_H\$`Ht
HHPHHt(@H+HFFF3HoHt HHPHHtHPHHwHtH03Ht HHPHHtHPH7Ht HHPHOH(HIH0A_^][H`HUH HHM`H
]HUH HHM`H ]HUH HHM`H]HSH@IE3fDD$ fDD$"LD$(,trGt,JtNuyD8AEtsHQXHL$
cD8AFt]HQ`D8A@ADD8ACt D8ABt@D8AGtD8AHt HL$ YD8ADtHQPHT$ HL$ HdHL$
3H@[HSVWATAUAVAWHH$ELE3E;u3AtD;uD9:tWD8yDtL$ IHQPIPL$
L|$(AE;@IDLD$(3IP(A;tHL$(I;tHRAE;uL9|$(u3.IIP0 HI;tDxH8HL{IH$I;t

HHPH|$(I;t HHPHKI;tHPH{L{HL$(I;tHPL|$(HHHD$xI;t
HHIH$I;t
HHPDIHE8|AIL0I;u3I;t
HHPHHPHL$(I;tHP@HE3E33P
DA;t6I;t
HHPHHRHL$(I;tHRAAt$AL|$XL|$hMD$8AT$(HLXDD0A;t<HL$XI;t
HHPHHRHL$(I;tHRAa@t$ IL|$@IL|$8L|$HD|$!HO0HW8HD$tEHL$XsHt
HHPHt
HHPHL$(HtHR$3A8D$(
DxLLPDHHLp$E3A;tEHL$XHt
HHPHt
HHPHL$(HtHR$cD$P;D$xt A;D@t$!HL$@HT$8ED$(HH]A:uD@t$!HL$@HT$8tHt$HDT$
LLHHLXDD$0HD$pHHhHL$@HSHT$8A@uAtGDt0E39D;WHL$@HT$8E3D8T$
tET$@Dt$0IE;tD;u?E:tAtBHDpxXuAtCILPITXHDHID$`At$DAtEAtFE8T$@u7E8T$Ct)E8T$Gu"E8T
$BtD;uE;uhAI;tHHP3H$IIP8HL$XHt
HHPHt
HHPHL$(HtHRHL$XHt
HHPHt
HHPHL$(HtHPALEHL$XHt
HHPHt
HHPHL$(HtHPAHA_A^A]A_^[HUH HHM(H]HUH HHgH]HUH HHMxf
H]HUH HH'H]HUH HHMXH]HT$UH HH%H]HSH0HD$ HAAu.HL$@HI8HtHPHK0HtHPH 3H0[HUH
HHM@H0vH]HHSH AHLHD$8HH [rWHP A@ HD$(fI33rWHT A@ HD$(fI3HSVWH@HD$ HHLcAIHAHLIBy
t(fzufBaEt*fZu9fBAEtf9u%fB0Et
2E2Af1H'ffCALGHHL$(\HHHL$(H@_^[HUH HHM(rUH]HSH HHIHH [HSH@3AIfD$
fD$"HD$(tAv$AwHQHHL$ HQ8HL$ HL$ HHL$ O3H@[HSUVWH(HIH9Ku>lH{HtDCHHIeH
H;kKHH4CHKH(_^][HSUVWH(HIHIH3LH,uHH
ZLHtH7YLHuHSRH{[LHuHS6H?YLHuHSHCYLHuHS
HHHH#HC(3@H(_^][HH#<HH<HH H ;HSH A(A(HuH3H [HHHH HSUVWH(33H9y0v<3HC(3LDBIiHC(H0HiI
IHP u H;{0rHkHCC 3H(_^][HSH@3IfD$ fD$"HD$(t2&t(t#u/HQHHL$!9A0tHQ(HQ 3HL$ HL$ HHL$
]3H@[HSUVWH(HAHHA8A@fHcy HHt2HHCHH4(HtHHtHPHuC 3C0H(_^][39A
3HL$SVWATAUAVAWH`HD$HELE3E;u3AtAuD92t
WMLt$@H$HHQHHPA;Lt$8AE;@HDLD$83HP(A;tHL$8I;tHRE;uL9t$8u3mHHP0A;tHL$8I;tHRDHHHI;tHDp(
HHHHGH|HGHQHGH&HG Lw0Lw8Lw@IH|$PI;t
HHPHIHD$XI;t
HHIH\$XI;t
HHPE3HHEDt$0E;g Lk8Lk0HODt6Ht
HHPHt
HHPHL$8HtHRA-AIGHL9IE3E33IP Dt6Ht
HHPHt
HHPHL$8HtHRAHH\$(HD$ E3LD$8IHPDt.Ht
HHPHHRHL$8HtHRAzLo@Ll$@ADd$0L$HL$8E3I;tHPLt$8H3HP8I;t
HHPI;t
HHPHL$8I;tHRH`A_A^A]A_^[HUH HHM8bH]HUH HHMPBH]HUH HHMX>H]HUH HHMXH
]HT$UH0HHJH0]HL$SH0HD$ HHuHA3KHSHH)HCHKK K$HK(K0K4HK8DHH0[HUH HHM@HH]HUH
HHM@H(LH]HSUVWH(HIHIHLHuH?H,SLHmtH6SLHWuHHSHHH#HC3@H(_^][HgHL$SUVWH8HD$
HHcYHt1HHHH4(HtHHtHPHWuHH8_^][CHUH HHM`KH]HSUVWATAUAVHH$IHHHP HuE
HHELHRHH$u

HL$@OfD$PfDRHDXHHLDPPt0HL$PKHL$@XH$HtHP f|$Pt3HLPHL@
H$HtHPHT$XHL$@3HL$Pf.HL$@XDHT$hHL$@EHD$@HXHL$0
HT$0HL$
YHL$ bHL$x8
H$*
AAE2D$(A;rUHHD$ HLHHt4ADD$(E;HL$ A+f<AaA;rD$(D9d$8HD$ HLHH=aDD$(E;vHL$
A+f<A0uA;r;\$8taHHLxHL$ HL$0HL$hHL$@{H$HtHP2D+HHL0LHThHLPeHHL$x
HLPHD\$8L+HD$0JXH$.
DDDpAsHO8HAHO8HT$h
HL$E3HP t^H$HLxHL zHL$0oHL$hdHL$@YH$HtHPHE3E33HP t^HHLxHL$
HL$0HL$hHL$@H$HtHPH$HWHHO(XH$HHPlHHtH0HHR3HHOHHPG H$HEE3H$HP t^H$7HL$x-HL$
"HL$0HL$hHL$@H$HtHPHL$P HT$PHLxFHHHLHT$PP #>H$HHL$3DCP `H$HE3E33P
H$HWHHO(KHHt H$HH$Ht
HR3HHOG H$HEE3H$HP H$HtHPHL$PHL$PBHT$PHLxHLPtH$HtHPHL$PQH$HtHPHL$P.H$ HLxHL
HL$0HL$hHL$@H$HtHPHL$P ufEtaH$HLxHL HL$0HL$hHL$@wH$HtHP.HLHLxBHL$
7HL$0,HL$h!HL$@H$HtHP3H$HtHPHL$PHHLxHL$
HL$0HL$hHL$@H$HtHPSH$HtHPHL$P`HRHLxHHL$
=HL$02HL$h'HL$@H$HtHPH$HtHPHL$PHHLxHL$ HL$0HL$hHL$@H$HtHP\H$zHL$xpHL$
eHL$0ZHL$hOHL$@DH$HtHPHA^A]A_^][HUH HHH]HUH HHM@bAH]HUH HHMP~H]HUH HHMh"AH
]HUH HHM0AH]HUH HHM @H]HUH HHMx@H]HUH HHMx6H]HUH HH@H]HUH HHMPb@H]HUH
HHH]HUH HHMP"@H]HUH HH_H]HSWH8HD$ HM9t HHR H8_[HT$UH HHH]HSVWH
HIHIHexHA[tHFAHBu
HC3@H
_^[H8HD$ PmHHD$@3H;tH8HUH HHM@^H]HSH AAHu%HI8=HK(4HKH#3H
[HSVWATAUAVAWHPHD$(MLE3A;t
WM08HI;tDpH7HLs(Ds0Ds4IH$I;t
HHPADt$ A;wLt$0Lt$8Lt$@Lt$HDIGJH9I;t HHPH|$0IG N4Lt$8 LHt%H8Ht HHRMt$E3Mt$Mt$E3MHK(II;t
HHPt$ QHI]3HPA_A^A]A_^[HUH HHH]HUH HHM0jH]HT$UH HHH]HHSH AAHuH(H)3H [H(H
Ht/4H(HSWH8A0MMBHA8HHBHD$pHHD$(E3MIHD$ -u(HHW D@@D_D3H8_[3rWH0 A@ HD$(fI33rWH4
A@ HD$(fI3HSUVWATAUHHLHHI3H;t
H rHITHuHHT$ HT$ HC9CuHSH
:
SH?!ujudDG@2Au AAPA@kAv
@mH@C9CuH6SH@<
H?uuWH9SuHSHE3
[
SD9oLgA4$C9CuHDKLE7
M0BCACD9KDKuHDKLF7
N0BAICACDKD;osC9CuH:SH
]
SHHA]A_^][HSVWHH`HD$(IH3fpfpHp&,/:GtSJt+N@8HHH|@8HHL$@|xt$ @8DT$ u T$ @8t@T$ uT$ uT$;u
T$ u
T$
;HL$@|@8H9HHL$@|@8HL$0@@tH2HL$0@tHHL$09t$8tHT$0HL$@zHL$0Q@8tHHHL$@v{5@8t,HHL$@
Z{9tHHL$@zHHL$@6yHL$@x3H`_^[HUH HHM@NH]HUH HHM06H]HT$UH HHH]HSH@HD$
I3fD$(fD$*HD$0AtAAt!AuO9tGHHL$(y48t,HHL$(Lz8tHHL$(1zHHL$((xHL$(w3H@[HUH HHM(>H]HT$UH
HHH]HT$SH HHIHtHLD$83P CK
H [HkjHI8/HSH HHHukHLHH [HSH HHHH

[LE3It"IHL;DsIMNIM;uIHT$SUVWATAUAVH`HHMAw3H9uH;Hu
qHOfwLHHOnA$HLI\$@H;uH;uDHH;$Ds;Dt$HHLHPD@LH$I+H+HD$DHD$8HL$PH;D$0L$(HD$HH
T$HHOLD$PL3HD$
w;u:HlHLlPH+t$HHO+)H9\$HuH9\$Pt";uT@t<8H;u.;t*L9$u O/ODLL_ M;H`A^A]A_^][3HSUVWHHHD$
IHHL$(HL$(G39\$0u
WHD$(f8sHpf9u_;t8ttW~f9_$)H$HO:u
$$HH7LE3HHaUW#HM0LHT$(YHL$(HH_^][HUH HHM(
2H]HSUVWATAUAVAWHHD$pIHHADH$dHH_E3A;tt@
AELLPLHH_A;ukD9$uaDH0H$AD$ AEt/H$(EHHHH M+uHL3DBHP A;I;tHEL3HPA;HH$6L$AHHHI;t
H$
oHHLLHdHDXHl`D|hHL0+eL-#fLl$ LLXLDHHHL0f
u"\$h@A;DIHL$0dA;zL9|$HoDHL$0eHDHD$0HDHL$0+eHDELL$0M;vLD$@AADI M+uIAAtKHH
HI;tH!HHHPIH~HHHHHHH;vYHI;aEMLL$0IL$M;IHM+H
Ht$@Hl2HML9|2LIMLl2G|+AAO|+K<+t=AH;s~HIAH$I 2KD+HAHiLQIK+LH9sHIIL;d2zH$E3EwM;5L-
cHcHL$0b@L9tIHL$0b@AHHED8LxLxLPAHHHPH I;tHPH
HHPH(I;tHPH(DIHL$0bAH$HA_A^A]A_^][HUH HH#H]HUH HHM0H]HSVWH0HD$ IHHHP
LHHZH0_^[HT$UH HHH]HSWH(HHHHP Ht HHPH HtHPH 3H(_[HSWH(HHz33@@@@H@8H H;t
HPH H(H;t
HPH(HHHH3H(_[HL$SH0HD$ HHHHHtHPHKH\HK@HK0H0[THUH HHM@H0*H]HUH HHM@H@BH
]HSVWATAUAVAWH@HD$0MALHMtAEu3LAHIL;r3 I+H;Gu3HO L;r
HO(L;LIIHHLLt8L+tL9w8s
@HG(I
HE3E3JT+P u}LG0HHO@LD$(Lt$ NL+F+H(uKJD+HG Lw(HWH+W
HO(H+H;GHW0DI}H_MtA4$3H@A_A^A]A_^[HT$UH0HHH0]HSVWAUAVAWHHD$PEHE3E;u3AtAuD9:t
WD8tH$ HHHPH$ L|$@HHT$@HP A;L|$0EE;AHELD$03HP(A;tHL$0I;tHR_E;uL9|$0u3LHAHP0HVHD$HI;t
HHIH\$HI;t
HHPAHHD8trH I;u)I;t
HHPHL$0I;tHP@HE3E33P DA;t.I;t
HHPHL$0I;tHRAL|$`D$hD$lHD$pD|$xD|$|H\$ LL$0L(HT$`HuDD8|$|uC@E;DDHL$`I;txI;t
HHPHL$0I;tHRAD$xuxGf;u
GSD8$tB;uG4uxG'u u #|$8A;u
E;E|$8HL$0I;tHPL|$0HHP8HL$`I;tI;t
HHPHL$0I;tHRHA_A^A]_^[HUH HHM0H]HUH HHMHH]HUH HHMHH]HUH HHM`H
]HT$UH0HHGH0]HT$SVWATAUAVAWHH$ILHE3E;u HL$8uHL$8DR
t[At
WKM;u
@<IHLhHL
LLD`3HP(A;D9$ttfDt$8fDt$:Lt$@HLL$83DBHP0A;tHL$8eD$8fA;t"f=ufD9t$@tHL$8leWHL$8XeD9t$`.fDt$
8fDt$:Lt$@HLL$83DBHP0A;tHL$8eKf|$8tHL$8dW/Ld$@HIHPA;tHL$8dHL$8dH$I;t
HHIH$I;t
HHPG(DC(LGHHHuHC`W0HDA;tI;t HHRATAADt$l;WPsbHGHLE90u;fAxu3AHA=w
K|T$lI;t HHPWI;t HHPWA:uI;t HHPWCxEDt$xD;g@seAHG8L<EDt$dE;os?AIHLBHDtHt
HHRASADl$dADd$xE3Lt$XHLD$X3HP8A;t&HL$XI;tHRI;t HHRHHD$pI;t
HHIH|$pI;t
HHPAHHHH|$(Lt$ E3LHTXHPI;t
HHPHL$XI;tHRI;t HHRZD9t$ht
WILt$0HLD$0H&HHL$0I;t1HAE3AQP

A;tHL$0I;tHRL9tjD8t.HHHPA;tHL$0I;tHRHHE3E33P A;tHL$0I;tHRH)HD$pI;t
HHIH\$pI;t
HHPAHHiLIHwI;t
HHPHL$0I;tHRHA_A^A]A_^[HUH HHM8jH]HUH HHM8JH]HUH HHgH]HUH HH+H]HUH HHMXH]HUH HHMp
H]HUH HHMp~H]HUH HHM0~H]HUH HHMpH]HUH HHMpn~H]HT$UH0HHH0]HSH AAHuH=3H [HSVWATH8HD$ MHIt
WnUHHEHH;2HL$pXeHL$pHDHL$pHH9v
jHHt7@HHHC0HC8HK@hH3H\$xHt
HHPHH;{8t3HK0Ht
&HC0HC8HtHHC0H{8H~HHt HHPHHtHPHHHCHCHC HC(I$3H8A_^[HUH HHMxV|H]HT$UH HHH]HSUVWH(HHI
/HK&Hc{Ht,HHHH,HtHM\HuHH(_^][HSUVWH(HcYHHt+HHHH,HtHbHuHH(_^][HSUVWATH
Lt+3HI$H,HtHHRHHutED$I$D+HIqA)\$H A_^][HL$SWH8HD$
H8bGHD$XHGHGHL$Xkb3:tHD$XHGHHkHGHO(O0O4H_8H\$XHKKHKHK AHl?HH8_[HUH HHMPH(H]HUH HHMXZH]HUH HHMXHH]HUH HHMPH8H]HL$SUVWH8HD$ HHHHHAHHAHXHAH%HA
HH(HtHPH HtHPHsHKh%Hc{`Ht,HHHCXH,HtHH6uHKXH8_^][!HUH HHE`HU`H0HHH#
eH]HUH HHM`H[H ]HUH HHM`H xH
]HSUVWH(HIHIHPLHmuHHLHmtHOLHmuHSRHLHmuHS6HLHmuHSHLHlmuHS
HHHH#HC(3@H(_^][A(A(HHHHH HSVWATAUAVHHHD$0EMLHHHA3\$ A;s&H[MDIHO~\$ htM3\$$
s3HHHHVHO`t
\$$ u
W3T$(;WHsHG@LAxuAxuT$(HO@ HvW]HuNHG@H{uHHK}/HHK/uHHKuW 3HHA^A]A_^[HT$UH
HHH]HL$SH0HD$ HHHH=HAH=HAH=HAH=HA A(H0HHHbHCH7HCHHCHHC HHHH H(HHH0[HUH
HHM@H0aH]H8HD$ HHD$@3H;tH8HUH HHM@H]H8HD$ QHHD$@3H;tqHH HHH#H8HUH HHM@BH
]HSH A(A(Hu%H3H [HHHH HSVWH H;HHtPz;yv/H HCHCHp{H{tHHLIfHH _^[HSH HHIHVHK8MHK
DHK;HH [.HL$SH0HD$ HHI8
HK(HKHtHPHHtHPH0[HUH HHM@rH]HSH HHIpHK`HKPHK@HK(HKHKH [wHSVWH
3HHIIE3HAD$PtHLL$PPDD$PHKtuC HtAH _^[DD$SWH(HHI3HItHLL$PPDD$PAHO HtDH(_[HSH0HD$
3H9Yt<H\$@HIHLD$@H]HL$@Ht
HPHL$@HtHRH0[HUH HHM@FqH]HSUVWATH0HD$ LHH
H{@HC8H8HD$`HLD$`HHL$`HtSHHT$pPtHL$`HtHRHL$pHtHC(H;s{HtA$HL$`HL$`HHP3H{HD$`HKHLD$
`H=39s@vf3HC8H<t8HL$`HLD$pPu-HL$pHtHC(H;s@8kHtA$H;s@rHL$`HtHPHL$`HtHP3H0A_^][HUH
HHM`oH ]HUH HHM`~oH
]A@APHSUVWH(HIHIHGLHduH?H<LHdtHLHduHHSHHH#HC3@H(_^][AAH_HSUVWH(HI@9Ku6t{HtDC
HHaHH;sKH@,CHKH(_^][HL$SUVWH8HD$ HHcYHt1HHHH4(HtHHtHPH#uHH8_^][HUH HHM`v
H]HSH HHJHKAHH [4HL$SH0HD$ HHHHKHKHKH0[HUH HHM@HH]HUH HHM@HH]HUH HHM@HH
]HSVWH HHH-HWHNHW HN(D_0HW8HN@D^8HWHHNPFh3H
^[HSUVWH(3H9v23HH,HMHtHPHEH;r39v23HH,HMHtHPHEH;rH(^][H
THSUVWH(;QHAHH(H<HA39tHH8D$Xv|LCD3A9@(vIH
D9 tHA;@(ryB33A9HvI@D9tHHA;PrHHx$I@THNtD$X;tr2H(_^][HSWH(HAHxA;yv$H HHC{H{t
HL33aHCHP0t"39KvH<t;KrH(_[2HSUVWATAUAVAWHHHD$
HE3Di@DiPAIA;u2A;Ct2AA;v@IHK8DL#EF9,'vHKHDAF;4'rE3B4'H;krKC(;t2,Ll$(Dl$0Dl$4H\$8HL$(HL$(H
HA_A^A]A_^][HUH HHM(* H]H(E3LD9I0EQAvHI(9tHA;@0rAA;|_E9H AvIH9tHA;@ rAA;}H
HL$0D$0w3)_I@TI@`D8u2IH(HSUVWH(HA@D3B,HAHB<t+H4t;rH(_^][2HSUVWH(HApDHA<HA@3B,HA
B4tn3D/9C0vHK(D9tH;C0ry@339K vHCD9tHH;S rHHx%HCTHhu+;r2H(_^][H
HLXDXw3]HHHSVWH0HD$ HDAEu9Q@u*!39A8vHI09tH;G8rAx3u#39G(vHO H9t-H;G(rA39G(vHO
9tH;G(rAy

WHG Ett4Eu HGX4HHHD$PH
HuHJHLD$PH
3HL$PHtHPHY;tu utHL$PHtHRH0_^[HUH HHMPNfH]HSUVWATH DDHEtHNApEtHAHF$33t+B'H4;tu
uE;rH A_^][HSUVWATH DI8L@EAD;I8AIt"38t8CQ8CPt EA{HA@<39A0vHQ(9:tH;A0r3E39Q
vHA98tAHHD;Y rHHx938t8CP8CQu:AuHA`<EDHADL2Hu HLPDPw3Z@u
EEEIDH A_^][HHHHtH@HAH@
HSUVWHXHD$@HHty8tq8;v0HIx?HCxHHCx;v9HHHHt1E3DHIHHCxHIIut4E3DHIHHHIIuH$L[(MtTt
HC`LLSHtLKHHC`LLHSxIHl$(LT$ MHIPTtHS`HSHtLCHLC`HK0HLHl$8t$0HT$(HD$
DHSxARHHX_^][HUH HH{H]HHHQ(HHH#HSVWH 3H9v!HHH1`H;rH _^[HSH
DQ8LHMJz8HY`B<tyI@@F3A9@0vIH(D9tHA;@0ryK33A9H vI@D9tHHA;P rHHx*I@DTIJMy8tEH [H
HL$8D$8w3CWDE3E;vHH9tAHE;r3HHH@(HL$SUVWH8HD$ HHcYHt2HHHH4(HtHNHtHPHuHH8_^][HUH
HHM`H]HSVWH HIHDIHtHLIILI HAHA
yA0;y4v+HI(%HHC(HC4{4HC({0{C@;{Dv+HK8HHC8HCD{DHC8E3{@D9KvSE3Ht&I0HtHHK(IHC(JHC8IH
C(IHC8IAID;KrH _^[HSUVWH8HD$
HHH`RXHOpVLXH3H;t)H(HhHh(h0h4Hh8h@hD@hH@hP@hQHHHD^D[H>H;t HHPHH;tHPH;H~H;t
HHPHKH;tHPH{H>H;uH~Hl$`HLD$`HvHHL$`H;CPH;tHPHl$`HLD$`H4HHL$`H;CQH;tHPH8_^][HUH
HHM`]H ]HUH HHM`]H]HSUVWATAUH8HD$ MHE3Ll$pDEME:tD9A@uWA%AD9i8vHI0D9tH;G8rAA;|-
HI;tHHPHL$pI;tHPI$3u&AD9o(vHG HD9t0H;_(rA AD9o(vHG D9tH;_(rAA;}
WVHG E:tDDDLL$pHA;tHL$pI;tHR9w6fI;t
L(LhLhIHH9vHH<L9oL9/(HI;tDhH{HLkII;t HHPHOI;tHPH_HHt$pI;t HHPHKI;tHPHsLkDk HI;t
HHPHL$pI;tHPI$3HL$pI;tHP@H8A]A_^][HUH HHMpF[H]LL$ HT$SUVWHXHD$HIHE3LL$
DEYE:uD9A@uVA%AD9I8vHI0D9tH;C8rAA;|,HI;tHHPHL$ HtHPH3xD8u&AD9K(vHC HD9t0H;{(rA
AD9K(vHC D9tH;{(rAA;}
WHL$@HC E:tl,E:u HCX,HL$0HH4Ht$8HI;uHNHLD$
HHL$ Hu
@tV$$t
HCHLD$0B,@2"HD$(HL$8H HLD$(H3HL$(HHD$0LL$0DH$H3;t9HL$0H;tHRHL$(H;tHPHL$
H;tHPHL$(HHT$0P;t9HL$0H;tHRHL$(H;tHPHL$ H;tHP@HL$0H;t
HPHL$(HL$(3H;tHP@:u)HL$ $3;tH;tHP@/HL$ 9w;;H;t
H(HhHhHHH9vHL$ HH|$@H<H9oH9o(HH;thHHH\HCHkHH;t HHPHOH;tHPH_H_Ht$ H;t HHPHKH;tHPHsHk
H_H;t HHPHL$ H;tHPH$H3H;tHP@HX_^][HUH HHM WH]HUH HHM(VH]HUH HHM0VH]LL$
LD$SUVWATAUAVAWHH$LLE3H$
DHcHA;t;HHI$H<I;tHOI;tHPHeE3A;uE$ELLxIJHT`LThDTpDTtLTPDTXDT\A$A:t
ADD$H DBDD$HA:u
$
B$A:tE ID$@F,A:uE ID$@F4AE;LT$@F.LL$@IIL$uVHSHH\$@HtHHt
HHR3HHL$hE3I;tHHPE3;t$Hr!HL$@HtHPHL$PAD$E;L$LT$@F6LL$@IIL$}uUHHH\$@HtHHt
HHR3HHL$P@E3I;tHHPE3A;r!HL$@HtHPHL$P*ML$DDMLDDDHA;v;IHHD$hHHH$HHuLDE3E;
v;IAHD$PHHH$cHHuLDE3AE9$rIHl$x;I$HE8$tdL$HI;uHKHL$HH$E3I;tHPE3A;H$I;ttHPE3iLT$@HI;uHK
HLD$@HpHL$@E3I;t,E8$t HC8HHS HPE3A;u_HL$@I;t HPE3HA;nHI;tHPImIdHL$P
HL$@I;tHPI:I1HL$PHlxAA:tLL$`MA LL$`MA8A:tIQ8IQ I I;t*LL$LL$(HHD$
MMIUAR0IIHLLL8D|$0HT$(Lt$ DL$HIP ;AADA;tu#IL$;tA;u
;uA;EAE9$vXII$HH9I;tHOI;t#HPLHyHOI;t
HPLHA;$r
;ADA;t"IIHL$PKAE9$vYIH$ I$HH1A;uHA;$r!IlIcHL$P IKIBHL$PAHL$hHA_A^A]A_^][HUH HHMh~H
]HUH HHMP^H]HUH HHM@OH]HUH HHM@vOH]HUH HHH]HUH HHH]HUH HHOH]HUH HHM@NH
]HL$H8HD$ 3_H8HT$UH HHE@@HH]LD$HTHLSUVWATAUAVAWHHHD$0LHE3D$D9MIL\$
HH,Hl$(HGA:tAE$A:tE<AHHc^LA;t<IIHIlI;tHMI;tHPHE3A;uHl$(D^E;v7A3H;tH8HHHXHuH$E3HHc^LA;t6II

HI,I;tHMI;tHPHcE3A;uD^E;v7A3H;tH8HHHHuH$E3$$HL$ HHL$ I;~L$AD9_(IHW
DHGXFHGHF+A:tDTEA:uDDEA:EEEE#EHJHNAHHJHHyHHHHHJ(HuHJ0HL$HLKHH(HuHH0HL$H[H$
E3I;tGH$I;tBHDAPH$HDAP HHE3H$I;tHHRH$E3I;t HPE3H;o(wO@HHD8t=HH0I]I;tHHPE3HI;t
HPE3HL$:HH0L$II;tHHPE3HI;t HPE3HAD9_8IM+HG0D(HGXBHGHD+HHD8t8HN$K.I;tHHPE3I$I;t
HPE3I$6HN$I]I;tHHPE3I$I;t HPE3I$HI;w8S3HHA_A^A]A_^][HUH HHIH]HUH HHIH
]HSUVWATAUH(L$HHAEI39v03;tHH}pH;r39v(3;tHH15~H;rHHHb39v,3;tHHHIH;rHK@nHKXD3A
Et0HHtA;tt=@u|HA;rHKu_3Et$HHtA;u?HA;r3Et+3HIHH(uH;r3H(A]A_^][HL$SUVWH8HD$
HHcYHtXHHHH<(H|$hHt8HO8HO(~HOHtHPHHtHPHXuHH8_^][DHUH HHM`H]HUH HHMhFH
]HSUVWH(HcYHHt=HHHH,Ht"HMAHM8H0HuGH(_^][HSWH(Hd3H;HHH(HH0HHPHXH\HH`HhHlHpHHH
HHHHHHKxHHHHHHHH(_[E3HzLADADALA
DA(DA,LA0DA8DA<LAHDAPDATLAXDA`DAdLAhDApDAtLAxDDDDHHHALDDHLDDHMDL$(HMMIH
L$SH0HD$ HHHHEHAHHHKxHKhHKXHKHHK0HK HKH0[HUH HHE@HU@HHHH#H]HUH HHM@HcH
]HSUVWATAUAVAWHHHD$0MLLE3@8tEHAXF,HAHF+t
@AHH<H$HH;uHOHL$HH9$u
@A$@:to@:u ID$HD,@2HD$ HHuHOHLD$ HHL$ HHD$(LL$(EII=t<HL$(HtHRHL$ HtHPH$HtHPHL$
HH\$(HPt:Ht
HHRHL$ HtHPH$HtHPq@HtHHPHL$ HL$ HtHP@3+HD$ HHuHOHLD$ HwHL$
HuH$HtHP@3HD$(F/LL$(IIu6HL$ HH\$(LPuTHt HHP;rHL$ pHL$(HtHPHL$ HtHPH$HtHPOHt
HHPHL$ HtHPH$HtHPHtHPH$I3HHA_A^A]A_^][HUH HH@H]HUH HHM @H]HUH HHM(~@H]HUH
HHM ^@H]HUH HHM(>@H]HSUVWATH0HD$
MHHDxLLxDA@tHL$xHtHR393HH4HD$`HHuHNHLD$`HOHL$`HtHHV Pu
HL$`HtHPH;r%HL$`HtHPHL$xHtHPHDxI3H0A_^][HUH HHMx?H]HUH HHM`>H]HSUVWH(HH39~
vO0HHtHH@H@3HHPHiu ;~ r3H(_^][HSUVWH(HRHH`VHMpHDFD@8H>HHt HHRHK(HtHPH{(H~Ht
HHPHK0HtHPH{0H(_^][HSH HuH3H [HSWH(HHI3HIDD$PtHLL$PPDD$PAHOHtDH(_[HSUVWH(3IIf9Hu-
ttHBBIHr f9HT$PHHT$PH;L
f;uHf9Zu}f%u#HGzGHH+HHHIH/[Af=wf bt,gt"ktmt
tu+X
IHHHI;t2HH(_^][HSUVWHHHD$
IIHHAHoHt6HHHL$(7LDFHHL$(THL$(:H3oHgt%HHLNLFHTuW2HH_^][HUH
HHM(H]HSWH(YHt!H)XyDHXH(_[HSWH(
HWyD
HVXH(_[AAA AhHL$SUVWATH0HD$ LHHAAztHHH{tU3H yHHD$hHt!I$H
JHHHH3KHH<CHHuHH0A_^][HUH HHM`H]HUH HHMhH]HSUVWH(HcyHHt,HHHH,HtHMHuCHCCHC
C(fH(_^][HSVWH0HD$ HH0KHHD$`Ht*HH[HVHK9HV HK 3HHH0_^[HUH HHM`H]HUH HHM`H]HUH
HHM`HVH]HSUVWHHD$(IHHL$0HL$0h|$8u
WZHD$0f8xu+3HL$0LGA HHL$0P$H{HL$0tC3DBHL$0D$ LD$ HHL$0OtD$ G3H{HL$0ht/3DBHL$0DLG
AHHL$0hOLL$ LHT$0H<t \$
fHHL0NHL$0HYHL$@HzHL$0trHWhHvNuHL$@8f>tHL$@"WHL$PHO8LH]HLPHL@3$$@vHL$@@_
0;XHD$`D$hD$lHL$pH$(HT$`HO(HL$`$;~HG(LHT$@H2]HL$@2HL$0'H_^][HUH HHM0~H]HUH
HHM@^H]HUH HHMP>H]HUH HHM`H ]HUH HHM`HH]HUH HHM`H
]HSUVWH(H{HL$PCHD$PHCHCtHD$PHCHHkHCCCC
ChHc{0Ht,HHC(HH,HtHHuHK8C0H(_^][fA/f\bEtAtAtHQHQHyMHQtI3DD$SUVWATAUH(MILHtAEu3HYH;
YsW DO0Ho(3HDLPI;sDH@IH;rJA W HlH+]H;]tHMLME3HHP
uDDD$pHMALL$pH+IH;DGDD$pHMHPL$pHOHMHtL$pH(A]A_^][H H%RHL$SH0HD$ HHKHK
HK0HK@CPHKXHKhHH0[HUH HHM@VH]HUH HHM@H2H]HUH HHM@H H]HUH HHM@H0H]HUH
HHM@H@H]HUH HHM@HXH]HSH HHIhHKX
HK@HK0HK HKHH [HSH HHIxHKhHKPHK@HK0HK

HKHH [HSWH(3Hf9u2HT$@HD$@f9H(_[9|
9~9
u2HSVWH0E3AHE;HtAuHDAD$pEL@HD$]H0_^[HL$SWH8HD$ HHuHSHOgHS HO
YHS0HO0KHS@HO@=CPGPHSXHOX)HShHOhHH8_[HUH HHMPH]HUH HHMPHH]HUH HHMPH zH
]HUH HHMPH0ZH]HUH HHMPH@:H]HUH HHMPHXH]HSH HH HtPtHH [HSUVWATAUH8HD$
LHH;uHHcYHt5HHHH,HtHMHMHuGA\$;_v6HHHtDGIHH"HH7_tX3L
yHHDpHtI$H)HH=HSHN/3OHH4GHIuHH8A]A_^][HUH HHMp6H]HUH HHMpH
]HSUVWH(HcYHHtkHHHH,HtPHMxHMhHMPHM@HM0HM HMHMHuGH(_^][HSH L3HJHD$0LHHD$0u
Mu
HL$0nHL$0HH
['HL$SUVWATAUAVAWHHD$8EEHL$Hf:HL$H3;}HL$H~HqZ~HD$HHxf:Q8;L;}HL$H~HgqKHD$HHHHL
$h;\$Ps
\$PHDHf,XHDHHxHT$0@:|$0G=m@c;t$Ps
tPHDHf,pLD$H3rKHHD$0H;uhPL;t^Hl$0LD$h3NoKHD$0H;u
'L;u
HL$0HL$0HL$h}HL$H}Hs"H|$ E3E3H KHH;umH;t
HLHl$0LD$h3JJHD$0H;u
K;u
HL0^HL0HL$h}HL$H}HprHD$@HL$Xf9.HL$X|HvJH;t
HgKHl$0LD$h3JIJHD$0H;u
K;u
HL$0HL$0HL$h|HL$Ht|HqHnDgDT`H$}fu.Dl$
EEHTXHD`HD$Xf8T$`+9T$duHL$XT$`HL$Xf<AfQT$`HIuH\$03T$`;HL$X{HMIH;t
H>JHl$0LD$h3J IHD$0H;u
I;u
HL$0HL$0HL$hV{HL$HK{HpHL$X5{HHH;t
HIHl$0LD$h3JHHD$0H;u
VI;u
HL$0HL$0NHL$hzHL$Hz3YHl$0LD$h3JHHHD$0H;u
I;u
HL$0HL$0HL$h~zHL$HszHoHA_A^A]A_^][HUH
HHMHH]HUH HHMhH]HUH HHM0rH]HUH HHM@H]HUH HHMXFH]HSUVWHXHD$ IHLA5H$
Ol3@:u$LG0HW HL$(H HL$(cHIH,I;CHuIHLI;CH]IH|H;CH@8(u@8hu
@8h0uHKHKH@8hHCHHK`HHxKaH@8h`u@8ku@8ku@8ktH@8tH@8tH@8tH@8tcHH09nuHH$H@:u&LHn
HL$((H= HL$($H@8t@8t Cj CH
-l|H
knH@8$HH09nuVHpmHcG;tBH$H@:u&LH mHL$(ZHo HL$(#$|;vH
kW H@8pHxH9kHL$(:HHL$(9l$0t1H$HL$(H$@8)tl$0HL$(@)9l$0u&LH?lHL$8H
HL$8bcDHHCHL$(DvHX_^][HUH HHM(H]HSH HHH [HSVWHPHD$
AH@LA<uAATIDHTHL$(oHT$xHL$(#t|$xsHL$(uD$xtHL$(K{t"H=gHHL$(|u2H;uLD$(HkHL$8ZHo
HL$8#_HL$(uHP_^[HUH HHM(~H]HSVWH0HD$ HHtHHD$`Ht5HHsHVHKHHA3HHH0_^[!mHUH
HHM`btH ]HUH HHM`H]HSUVWH(HHH<HtOHOxtHOhtHOPtHO@sHO0sHO sHOsHsHsLDCND+III
CH(_^][HSUVWATHPHD$ AIHL(RsHHD$(HtHHKC C$3HIkHHHHK{$$C H
2HL$(HHL$(HL$('HSHL$(HL$(uHiHL$(uHiHL$(LD$(HNiHL$8H HL$8HPA_^][HUH HHM(VrH]HUH
HHM(H]HUH HHM(H]HSUVWATAUHhHD$0EILHHD$8D$@D$DIAu&LHweHL$HH
HLHDHT$8Hzlu&LHdHL$HH HL$HcHtw\$@t&LHdHL$HfH{ HL$H/33DHD8Dd$
D$LDHHQH\$@;rC3\$@t93@HD8@l$ D$EHIH\$@;rHct-HHHD$8H4HtH}pHupuHL$8gpHhA]A_^][HUH

HHM8-H]LD$SUVWATAUAVAWHhHD$@IMHcLHuA9YuO$uEHGHL$HHD$
DAHHHL$HoL$$yoLI;_D$HJz;s9LfA8@u/I$D$0D|$(D AII*RMt5IuM@D|$ ALJIdID|$
D$AII;_L$J&E3HEHL$HH HL$Ht0DHNMHeHL$HH HL$HrHhA_A^A]A_^][HUH HHMHH]DL$
DDHLSUVWATAUAVAWHxHD$@H339ZE3HEM4E3E3A~`I
Af=wf fruPAMEEfzwRH
a,sDAEtt3
AExAMAAEA9FAMIHHHL$Hu~IfB<`!u*$D$ DD$HT$HH$xfB<`@u8$D0D$(|$ DLDHH$38fB<`#uq$D$
DDHTHH$HHu5HL$HlI;uD$/Hd&HdHL$HplHcHL$H]l;ut*HELMHHL$XdHy HL$X-HxA_A^A]A_^][HUH
HHMHH]LD$SUVWATAUAVAWH(HD$`ILL3D9rLIEI,Hz^HMar@:tA8vH&AvHIN83KHL$@HAHL$
|9uHECf=wf H
^pfwXxT;}HMf|0f|4L0L@H]9H;}r3HEHHHL$ }3t$(HD$ f09t$(u-A8vH"IF8HHHT$@AHL$ f9!'HL$p{H$HT$
HHL0^{9t8HT$0HL$p|LHL@HIHAICHAICAACIN8HTpHL0iHiHiHiHiHiHiHiHLpiHL$
xiH$AIE;eHL$ PiFi{HL$0:iH$,iHiHiHiHhHhHhHL$phHL$ hE;et1EIENMH~`HL$0H
HL$0H(A_A^A]A_^][HUH HHM H]HUH HHMpH]HUH HHMpJH]HUH HHM0H
]HSUVWHHD$@IHt7tHL$ HpA 0HL$ HAHL$ HAGHHO8vHL$P(xHL$`HHL
HHHAHBHAHBABHT$PHO8EH(tH0LD$ H!Ht*HHzuH9HyHtHHHH_yH@39Pv{3HHH(H$8H
t)H$8HtHX_H;Pr1HHLMHN^HL$
XHm HL$!
HfHfHeHeHeHLpeHL$`eHL$PeH_^][HUH HHMP"H]HUH HHMPH]HSUVWATHPHD$
LHHh39x3HL$(uHL$8uHHpH)HL$(wf=HL$(jx+SHL$(HQHL$8&w;\$0s\$0HD$(fX
dHH$HtHT$(HbvHKHT$8Sv3HI/]HL$8dHL$(vdHH;x#HPA_^][HUH HHM(H]HUH HHM(.\H]HUH HHcH
]HUH HHWH]HSUVWATAUAVAWHxAAHA;HD$\s&HT]HL$`E3H
HL$`|HQHL$`H$uHL$`.iE3D9t$hu]HD$`f8v5HEcHFH\LHL$`MNHc HL$`H
hHL$`A;|G0 |$hHL$`uf9rufynuG0
bHD8tHOhH&HD8tDHD8HED8t.HAHD9ktHH_2A;u=EHD8tD8 HD8Xt?\A;t/t0A0LH[HL$`-HB
HL$`AD8AAAEAQ#EHl$TE3AAP!H/ HD$D8D$Pt+D$(HOHDED$@l$ +D$PHD8tD$(HOHDE3@l$
HEDt$XD8u0t0t0 uEO0Aw
DDAuA
$A3@:uD:t@8oEDE@8t"tt
AD:ttD9T$\w&HYHL$`E3H HL$`gHVHO8HRr9o@ADT$Xu&HYHL$`E3WHl HL$`
H@ALNHLGAH#DPD@D8DTD0F\$(D$ H3O H:OHtHHOPHHOrHO
H88tH@HxH"rHHHXHHLA88tHP:tH8QH:tH8 H :tH8!8$E3ffD8ufD8ufD8uHhHHOHOu
HGHD9it&HWHL$`E3H1 HL$`HDd$Pl$TD8t&HHAADd$(@l$ kHD8t&HHAE3Dd$(@l$ 9E:tLG8HE3@l$
H30HHHD8otHHHHoD8$D8otBtD8ot1D8ot+A;t&HOHL$`E3H
HL$`HD8t"HHHEoHDHD8 t$H
AND8o t0t0uAAu"HxA_A^A]A_^][AuDO0E3A;^~
PHD8t&HTHL$`E3H HL$`LHH
H%LE8`tHD8xtDf2f0f4f6HA:tDHHHHmD9v"Hf8.uH3rGGH8HA:9D8t&H9SHL$`E3H
HL$`aA:tmD8wt&HMHL$`E3gH| HL$`0tt u&HMHL$`E3&H; HL$`D8tHHHHvl0
8H0RHL$`E3H HL$`ueD$XH;D$\SHHFHH A:7HFHQHL$`LM]Hr HL$`& uYHOAHOHWhH`DtbLE8`tpGqrHA
H$$z3HSUVWATH@HD$
IIDHI@IAfD$(fD$*HD$0HLL$(A[P0tbL$(tWtGt8t*tD$pNH
HLpHD0HC3D$0HC)D$0HCD$0HC3HL$(HL$(fD$(fD$*HD$0HELL$(A\AHP0tHL$(iiL$(tQtDt6t)tD$pNH
HL$pGHD$0HD$0HD$0HD$0HHL$(3H@A_^][HUH HHM(H]HUH HHM(H]HSH
HD$0jhSt<Hf|Q\tf|Q/uBCfAHl;CsCHfHHH [HL$SVWH0HD$
IHHD$`gD$`t>Hf8:u5~t/HFf|A\tf|A/uuCKHfHHHjHH0_^[HUH HE`t
e`HMP8H]HSH HHIUHKH [UE3f=\tf=/uAHfuDAHQAML#AMHQL#HH#HI
DD$SUVWH(HHI3HIHtHLL$`PDD$`{ tHK(HL

AQDD$`AHCHtDH(_^][HSWHH3H|$`"HL$`H;HtE#HL$`;HEHL$`LD$`(H:!;tKLD$4HP3D$0D$<!;HL$`t#LD$0
E33H|$(H|$;uHL$`H;t"2y"HL$`;H;t"HH_[HH9svHIH;JE3H;JADH8HD$ HuHIHHtHP 3H8HT$UH HHH
]HHHD$8E3L9YHu3D8HLH0LL$PI;D8twHHD$0LLD$ HHT$(AI;vHHT$(ILD$ I;vHHD$0ILD$ AM;MDLD$
H3ILLLPHTPHIHHP HHHT$UH HHH]H8HD$ HHPH8HT$UH HHH]HSVWH@HD$
IH$AAAfD$(fD$*HD$0HA(HHLL$(P0t5D$(f=@uHD$0Hutft@3HL$(NH@_^[HUH HHM(H]HSWHHHD$
IHHL$(|bHMHL$(fHHL$(eHOHHHT$(P8HL$(PHH_[HUH HHM(>H]HSUVWHXHD$
IHHHHL$(at6HxLHL$(eHL$8HHL$(eHL8IPHBLHL$(LeHHL$(dHMHHHT$(P8HL$(PHX_^][HUH HHM(vH
]HUH HHM8VH]HSVWH@HD$
IIHHL$(aHKHL$(dHHL$(YdHKHL$(dHHL$(;dHNHHHT$(P8HL$(POH@_^[HUH HHM(H
]HALMHDARHH`0HSVWH HIHIHYHAOtHAH6u
HC3@H _^[HSH@HD$0HHE3I;t
HP(dD!A;uD8t!HJHD8AL LMM#LL$ DHAS0H@[HT$UH0HHH0]HSVWH HhHu3 3@8$t@8Ft L8HA(@8t
LE3@8#t@8Et L0E3@8"t@8Dt H(3H`HG@8YH`Hy(t7H9Pv.HHYuLHGHJH`HPX
uHhHtHPHhH _^[HSVWATHHHD$8DHH3H;t@8%@HDP0@8tXHpHH(HLDPHpHFHPXHNH;t
HPH~@H';@8@8!@8{0HK(H9yHIHHHT(HTpHT L$A>HP T$p;tU$t
@H$@:t/T$0B@8ET$0L$H@8XuLPLXHHK(5n@8Xt'@8tHPHHPH@8t H@8t
HH@8u$@8!t@8Gt@H!HKHD@8%AHAP@;u 3HHA_^[HT$UH0HHH0]HSWH8HD$
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]HSUVWH(HIH9Ku>tHmI{HtDCHHIHuIH;sKHoEHCHKH(_^][HSUVWH(HcYHHt,HHHH,HtHMIH
IuHH(_^][HHSVWLcIALEAK\HGAE;HD$ wIt$$sCLC9rwCLC9LvAC;Dr
wC;tsKLAEILE;vHAIDuO\IIIDSAIE;IHD$ wO|$$sAALA9rwALA9LvAAA;Dr
wA;|sILAEILE;vAIDy_^[HSH@ft$0DHGAE;Lo4t$ wRE;sAHIHJ~AAHL$
HI{yoAEHED;AvAHA4fot$0H@[HSH HDHHtGHH [HSUVWH(HHI0FHK
FHc{Ht-HHCHH,HtHMFHFuHKFHH(_^][FHSUVWH(HcyHHt-
HHCHH,HtHMlFHdFuHKWFHH(_^][GFHSUVWATAUAWH @2zIHLAt#HHyuAH
@@AEAu@tHCfIUpHvX39~vvE3t(C9CuH@TSHfQ\AfQSHHI,HVZAuu}u
HMuH)AI;~rH A_A]A_^][HSUVWATAUAVH@HD$ HH
)3:t2XH\$(\$0\$4HT$(HqA|$0;LHD$(M,A9]u;tQCHIM;t*HIM;u
;t\A|$0|$0|$0I;rwHc;HAHHD$(H,H;tHMLDHDD;uzHc|$0H;tnHAHHD$(H,H;tHMDHD;u>;AHc;t.HAHHD$(H,H;
tHMCHC;uAHL$(CH@A^A]A_^][HUH HHM(H]HSWH(HrAHHHtH\CHH(_[HSUVWHHHD$
AAHHy`)@(HHSHL$@SHD$PD$XD$\HL$`SHL$puS$$$LD$@HN &tHL$@zHT$@HL$(!UL^ L$AITt
HL$@9CHL$@,f#HL$(THL$(WHN`HDLL(PHL$(A3H_^][HUH HHM(RH]HUH HHM@2H]HUH
HHM@HH]HUH HHM@H H]HUH HHM@:H]HSUVWATH Lt,3HI$H,HtHMAHAHHutED$I$D+HI?A)\$H
A_^][HSVWH HcYvUH1HDHuHHHooFHDNHwH _^[HSUVWH(HcYHHt,HHHH,HtHM
1@H)@uHH(_^][@HSUVWATH0HD$ HHE3AyA;tALDMDAKD; D;
ADA;tA;}AAP;uA=u?HHD$`I;tEHUHK:QILH\)H0A\_^][HUH HHM`B?H]HSUVWATAUHXHD$
HLHCHc{Ht-HHHCL$(MtI$>I>uCIE(u3L HtF$!I$H$IP(\$33Ht
HD,(DLLD0AI8tCL$AIvP8$u(D$0fD$@HT$@H=H;$pHE3EKED9Kv\33EbH+AHHAHHtAAAuE;t
o./AHAHD;SrD[D9[s9:=H$Ht
HM3HHK5D[D9[r3HXA]A_^][HUH
HH=H]HL$SVWH0HD$ H3q
H;HH;HAH;HAHl;HAHq(@q0@q1@q3@q5@q7@q8@q9A2A4A6A:HqHHqPHqXHq`HqhHpMH
MHHHH\$XHLHsssHK LHK0L@sA@sHCLHL"#$HhHxH@HHHH (,H0$LH@HLNH0;HD$XH;t
HHHHH;t HHPHH;tHPHCHH0_^[HUH HHMPHHH]HUH HHMPHPH]HUH HHMPHX~H]HUH HHMPH`^H
]HUH HHMPHh>H]HUH HHMPHpH]HUH HHMPHH]HUH HHMPHH]HUH HHMPHH]HUH HHMXBH
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H,HtHMc6H[6uH K6H?6HcHt0HAHHH,HtHM
6H6uH5HHtHPHcHt0HAHHH,HtHM5H5uH5HxHtHPHhHtHPHe5HDHHtHPHHtHPH%5HKp5HKhHtHPHK`Ht
HPHKXHtHPHKPHtHPHKHHtHPH0A_^][HUH HHM`HHH ]HUH HHM`HPjH]HUH HHM`HXJH]HUH
HHM`H`*H]HUH HHM`Hh
H]HUH HHM`HpH ]HUH HHM`HgH]HUH HHM`HH ]HUH HHM`HH]HUH HHM`HoH ]HUH HHM`HH
]HUH HHM`Hh'H ]HUH HHM`HxH]HUH HHM`HH
]HSUVWATAUAVAWH(HcE3MA;MLHHt1HHHH4(I;tHN 2H2A;uDHhI;t
HPLhDHc(A;Ht0HH
HH4(I;tHC2H;2A;uD(HcHA;Ht1HH@HH4(I;tHN1H1A;uHDHII;HC0AD$fC8AD$
Lk@C:$$H$HHt HHPHKHI;tHPH{HHKPI;t
HPL{PHKHLCPHHHKHLC`HHHKHLChHHHKHLHHHI;tIDHHP$A;AEA;$uHI;t
HPLHAHLH$HE{AZD$H$H{pHDHDLs(BHHHBHA:uHWHHt}HDH(A_A^A]A_^][HSUVWATAUHHHD$
IHLE2D8bCALH]tD8euT~@uGD~@u27HVHL$(YD$0u2HD/H$Ht
H@3HHL$('D$0HD$(H|G9GuH=DGHfBB:DfBBDGHV0HCLDHT(I"]tF$EHc|$0HtlHHHD$(H,HtHM.H.u<Hc
\$0Ht-HHHD$(H4HtHs.Hk.uAHL$(Z.HHA]A_^][HUH HHM(H]HUH HH.H]H(LD$@"D$@H(DL$
LDTSVWATAUAVAWHpH$0DL3I0H9xtHpHKH;t
HPHsAIhH;tHPIhA%IA IPAXAYEHAIf0A$8I$0f0IL$(L9L$(AA@:t-
AAA$;tuA$AD$9A$M$A.u;[!ft$XftZHt`ILL$XAAIP0;tHL$XP!f9t$Xt2f|$XtHL$X0@ HD$`IA
HL$X@t$A@t$pD@t$@ftXftZHt$`ILL$XAZAIP0;tHL$X f|$Xu!D$AD@t$@HT$`I$0?f9t$XtHL$X}@H
HL$Xift$XftZHt`ILL$XA6AIP0;tHL$X3
f|$Xu!@t$AADl$@HT$`I$0>f9t$XtHL$X@HL$XA9$8IIL$(H9q:HIHHHT(HHT LAYAHP 9$$t
@;HL$x
;$$H$;H$:DLHH$A@:tT@t$AHTXHHI$0<HL$X)DpADl@H$)H$)HL$x)A9$8tsfA\f/I$0OCHI$00@
:tD@t$@I$0AI$0o;tD@t$@I$0@A9$8
D:A9Htx$$HTxI0dU$ A;$s2DHD$xJHI$JH zK;u"$
@:uAHLx4HTxHLX
I$0H;HL$XQ($$HL$x;t*$H<H;tH(H(HL$x
(M$%AAI9;M$XMPAHIL$(J;|A8t$6uA8$t3dM$IL$@H;t,I$L$"$@:u3&A8$tJAN;A$uA8$t
tuQI$AF;uA8$t3A;D~itFUA8$t$$=;%X$$;X$$A$;@t$B;sD$B;u,A8$tA8uDB`A8uDBO$A;$s7DIJHI$JH
H;tDB@:t$;uWA8$uM@tA$$$+$"$$$$I"$I9$QI9$uU %HH;tpH]"HHI$H;t HHPI$H;tHPI$IID(HAI$$PIHL$x
A8$thA9vuA8$uFA$t;$9uHLx3HLxfA:f4QIHL$x!9I$I$A8$@LHD$(|$ LDHTxAR HL$x
$HH$$;A8$A8$tJ#HH;tpHd!HHsHH;t HHPH$$}ftXftZHt$`ILL$XA
$IP0;tHL$X4f|XuD`A$@A$Gf9tXAGHLXMDM$(Ll$ MA
$I;tI$EHL$ M$0AI;t_I$FHD$
M$8AIy;t1@$ID$(L$AH4;tA8$tA9vuA8$u/AtAEMA$A8$6IHLX3HL$XnAD$A~;uQ!H$H;t
HL2HHI4A8uAuSD@FAu;II;u/!!H$H;t
H1HHIANIH|D:t>AD$8EA%A:G9GuH/DGHAfBBAfB4BDGHT$XH5HL$X
MDIH@8$WA8$u,A9vt&ANIH|H;tHq Hi AFA9vHL$X0LD$XIIA8$0HH$H;tHH 0HI$HIHA8$"t
A8utGI$0HGA8$#tA8$EtG@wA8$$t9A8FtI8HGGID$(@8tHHGGHO
HT$X1HHL$XCHHLH0A$uH$$@:u-
ITpLDHHL$XHHL$H1HL$XA8$!ADXHL$`e/LT$XD$E$HD$E;tEC<I$@HA;;D;t#;}D$DGD$HL$`A;I$@
HHHL$H0T$P9T$TuHL$H,TPHLHfA:f4QTPIHLX/HLXmHTXHLH2HL$XD@$A8$teHT$HI$80@8$
t
I$fA9$8HLHaHSH;t HHP3A8$
HzH-HTHH|@:A$;tEH$HLHHH;t
HHP3eHL$H2D@HT$xHLHLHHLX,HLxjILHMLAXIPML#AFI8ML#HHD8LL$0LD$(LT$
LLHT$XAS(;tGHL$XHHLHH$H;t HHRe$;o[tJHLXkH]HLHRHDH;t HHP@HL$X!H$HLHHH;t

HHP@AAHLXHHLHHH;t HHP3:HLXsHeHLHZHLH;t HHP3AHLX
AHLH=@:LD$HHI;t<H$HLHHH;t HHRcHHLHH$H;t
HHP@ADHTHHLX*HL$Xl@:LD$HHI;tGHLXHHLHHH;t HHR
HLXHHLHHH;t HHP@=
HTXHLHa@:LLHLDXHI;tGHL$X?H$1HL$H&H$H;t HHRHLXHHLHHH;t HHP@uHL$X
HLH1`@:LD$HHI;t<H$]HLHRHDH;t HHRH$!HL$HH$H;t HHP3HL$Hxc@:@LD$HHKI;t<H$HLHHH;t
HHRFH$|HL$HqH$cH;t HHP3
HL$Hw;WHL$Hf94QHTHHLX';|$`s
|$`HD$Xf4xH$8wsH$`&HT$XH$8t@:t$XtHL$X]H$`HLXHIHTH((@8$
A9$8H$&Dl$@D:t.I$HL$XrHH$'HLXI0H$)H$@:u]L$HI;H$HLHHH;t HHRW
HL$x<%D|$AD:uD:tI$0HL$x'LDxHI$G@:udL$H#I;tGHL$x#H$HL$H
H$H;t HHR9$zD:HTxHLH5^@:YLLxLDHHI;6HLxH}HLHrHdH;t
HHRA8t$2Ht$XHt$`@8t$pAHT$xHL$X@:$$H$#H$#$L$LD$`HT$XH$@:M$HpIl;HHHL$XyHL$xnH$`HL$
HUH$GH;t HHRDL$`LDXAHL$H@:LD$HH
ID;tmHHHLXHLxHHLHH$H;t
HHRBHxHkHL$X`HL$xUH$GA9$8AD$A9$A8LLDXDAH$(M;t.HL$HH$H;t
HHRv@8JDEDE;tCGAAHIHLXt;t%;}
EDEQDAD;AI$
H<9wuHT$HH"HHL$H{Z@:uyLLD$HH+I;t.HL$HH$H;t HHRtHL$HH$H;t HHP3FD@$D:DD0+LH;tpH=
IEIELM$`L$(L;tIEIPHT$HI$`A$ EAAHq(HU@:LD$HH I;t>L;tIEIPHL$HH$H;t HHRAL;tIEIPHL$HjH$\H;t
HHP3A8t$:A8$ A8XIPHI$`Hz@A$YI$`H y@:uXLD$HHI;t>L;tIEIPHL$H
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H;t HHR=D:tjA8$u`D|$XHT$HHL$`E$HHTXI@.E;HuI@HHHTHHL`
A8$ t`I$`HE3E3I$P ;t>L;tIEIPHL$HH$H;t HHRdL;t
IEIPI$hH;tHPM$hL;t
IEIPI$hH;t HHPH;t HHPHH$HL$H5H$'HLX@I9$x;tuzI$pH;t HHPHOH;tHPH_I$xH;t HHPH;t
HHPHHIpHO(HHwG A$H;tHHH$H$HH;t HHP3HpA_A^A]A_^[HUH HHMXH]HUH HHMXH]HUH
HHMXH]HUH HHMx2XH]HUH HHHXH]HUH HHH]HUH HHMXWH]HUH HHMxH]HUH HHMXWH
]HUH HHMxrWH]HUH HHH]HUH HHMXnH]HUH HHMXWH]HUH HHk H]HUH HHK H]HUH HHVH
]HUH HHMXVH]HUH HHH]HUH HHMHRVH]HUH HHMX2VH]HUH HHMXVH]HUH HHTH]HUH
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HHMXUH]HUH HHMxTH]HUH HHMXTH]HUH HHHTH]HUH HH_H]HUH HH(H]HUH
HHMX!H]HT$UH@HHdH@]HSUVWATH@HD$
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HnMHHGHHuHcHt1HHHL$(MtIL$ sIkuHU3H@A_^][HUH HHM(RH]HSWH(H5HoDHG(H(_[HSH A A
HuH3H [HHHHSH@HD$
I3fD$(fD$*HD$08AhtHQpHL$(t~tdtPt<t(turHQHHL$(bHQ@HL$(RHQ8HL$(BQPHL$(T3HQ0HL$(p#QPHL$(
HQXHL$(7HHL$(BHL$(3H@[HUH HHM(ZH]HT$UH HHH]H8HD$ LMtIIP"Hu3HHPH8HT$UH HHH
]H8HD$ LMtIIP "Hu3HHPH8HT$UH HHH]HL$SH0HD$ HH'HHmHAHBHAHHAHtHIxHHHtHPHH0[HUH
HHM@H]HSWH(HWtHHH(_[HSWH8HD$ HHHtJHD$PHHLD$PH%UHL$PHtHHPHL$PHtHR*HHu@HHP
H8_[HUH HHMPH]HT$UH HHH]HSVWATHXHD$ MHHIyht
HtHH}HHL$(fA\f/HL$(JHL$(0uHL$(b:HL$8LD$8HT$(HO
2uHL$8,HL$(!HT$8HO0_uHL$8HL$(GPtHL$8HL$(HH$Ht@HHLHHHCHgHCHHCH3H$Ht
HHPHK(HT$8hququ.Ht
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HHRHL$8HL$(HHW0[H$HtHT$(HK3HH$H=CxHHt HHPHHtHPHI$HL$82HL$('3HXA_^[HUH HHM(~KH
]HUH HHM8^KH]HUH HHH]HUH HH{H]HUH HHwH]HT$UH HH5H]H

H%HIHSUVWATAUAVAWH)ILqE3HMEiDHD$HEE\$L|$Hii|$HiPF\$LA#2A%EAE+D#AAA+#DEAAA;rE;A
AGAFADPD#iiAiPFD;wAAAAD#E;r%iiiPFA\$L|$HEAA+D;DGIAHEH+EtIIM+uC1EAH;HDHIA_A^A]A_^
][HSLQ
HL+QD;LDGAI;EGEt!3EICHICHHIuAICMtE3[HSUVWATH HHI AH_ILH+H;HGHt-(tHOL)tO,LIG,H_
HtH+H%@H A_^][HSVWH yIAHtILFHt;3H _^[H8JLL$@LD$PHHDHHTXHD$ t#L$@D$DT$LH H
HL$HHHHi'H8HSH HHAHAHCDLfHC8H
[HSWH(HHHHA8DL+L*H+C8HGH(_[HSUVWATH`HYPHIHLs(H3;H9_`u3HOHT$ H\$PeL_PHO`E3A8[,HT$
t"HG0IHD@HG8HEHD$HHP HG8I$HD$HHG0HEHD$@HP;tH_PH"Hs(H`A_^][HQLAL
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HSVWM;B9AtMVDE3AJAA;v=DAr r
HHHHfHD$@@BAIIL;wMLD3LII_^[ALQHD$(IH@BHT$0IIL;wMLDL3IIMtGSUVWH(IHHHDH;HHDBP
HH;w tH+uH(_^][H%EDAuA;wE2EuEtAJ
=vDK@HAH0HA%`HKHBHHSUVWATAUAVH`D3ILDB@HL$ I3HHLHtu
HHPusIIPMEIIAS MHT IAS(M$IAS0@wG3tD@HT$ AA3
HIuHt;uHH;y3H`A^A]A_^][AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA
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3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3AA3HwHSV
WH Hq =V HHt)HKHPCuDU S,Hu{3H _^[LAt(DAAIA2H DD3IuAAHSH Lt=LEAHAAiiAAiPFA@AIHuIH
[mHSVWHH ApHHL$ DB@H$HxHL$_;v+LHHT
AH_^[HSWH(HAHL$ ELHHT$ HD H(_[HSUVWATH HHIHKY8m4AIHHHL I4CIHDIHL
IDHIH|$pH\$xHuDC H sHHGzGAHH+HHHHD3HHHHi@BHHHHb3HHHkdAHHHH?H
A_^][HSUVWATH0HHR0HHIEHV Ht)HHtH
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JHHHHHHt)H$LLHL$(HL H?HHtL"IIHHBIHHHBHJH0A_^][HSUVWH8I@A
IHLMHH|$(LDHC3I3@t$ LHCI3LHCIHH8_^][HL$SWHXHD$
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exHMp<H]HSUVWH(HHHIAHLHHA@HtHAHHHMHH9H=iU
tHHHHHHLHT$pHAT$xAHH(_^][MQlLAp3HSVWH@HHIXAHHPuct]HF0LNLHF W`HOH_HD$
DGxHOXLHGpH\$0HD$(GlHD$ du
HOXHP3H@_^[HSVWHHHIXAHHP@HG0LO LGHHD$(HG(HKHD$ {dHKXHtLHADCh
HL$@HA8HD$`DC|HD$pHKXHD$`HDhHDpHD$hHC8HD$0HCpHT$@HD$(ClLD$ HD$p3H_^[HSVWH
HAHH38t
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D HH _^[HSWH(H3H8
t
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W|H(_^][HSUVWATLH`HBHICLMKMCIS0ICAC0$sI|$HHHHuY33LLHDDDHDLHDLLDDHT$@HD$
D$@3T@HHH|;|HAL$HT$@EC9CuHSH
(
SHT$@HD[D9[uHcSH
)
SH`A_^][HSWHhHHHuoH
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@MHuH
@GHuH

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SHT$@HHT$@HHh_[HSH0E;HD$PsAHHuHT$ A HT$ HHH0[HSVWHPHD$ HHD
AHdHL$(HHHL$(f?u $Jt)f? u $!tHBHW$HH(HO,HT$(HT$(HHHO.HT$(|HT$(HHWHO
HH;t5Hu @t*HHHT$(HO0HT$(HgHtHXW HtAHIH5Wu
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H`HH
HDHHHWHHH;tHHHfuHHHHP_^[HUH HHM(n4H]HSHPH3HKHJHL$ HT$ HHP[HSH`HD$ HAHHL$8
t4HL$(HT$(HL$8HT$(HHL$(IHH=M tHH|H`[HUH HHM(3H]HSWHHHD$
HAH3>t9HDA!HHL$(<HHLHL$(ArHH_[HUH HHM(2H
]HSWH(HIHIHkHAuHC@H(_[AAHSUVWATAUAVAWHHHHD$0HHi(E3Lu0Lu8LpPLpXHY I;tI;t
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HHPMDHI;HHVXA PDA;t4I;t
HHRHHRH$I;tHPAH$HHVxAP DA;t4I;t
HHRHHRH$I;tHPAML9vHYHI;t%HE0I+H=vHPDA;bL}0HNHHAL;A:uAt D8tHtAEH@,P(Dh)Lp L9v
tbLH(HPDHN HPLHPHN LHPH9DKH(HHOHG LwHNHF8LHD$(Lt$ E3LHARDHA,E3M;u $E:t
9$HA H HHE0H HE8IL;fHvI;t
HHPHHPH$I;tHPAHt
HHPHHPH$HtHPAHHPHHPH$HtHPAI;t
HHPHHPH$I;tHP@THNI;t
HPLvHN I;t
HPLv I;t
HHPHHPH$I;tHP3HHA_A^A]A_^][HUH HHH]HUH HH{H]HUH HH[H
]HUSH8Hl$0HAHHHHLA8H+A0HQ4HAMHD$(LD$ LA(L#Q HI@C@3He[]HL$SH0HD$
HHI8HtHPHH0[MHUH HHM@NH]HSUVWATH 33H;HHAHA;rLLV
A2IA3Ius);AULAAAiiDiPFAIIT$TuDLISvIDA;u rrHH
A_^][HHHA8HSWH(H6xHLfAxuA@)HCDLADANH(_[HSWH(H3;|HLfAxuAHH;u?
H;|HHfzuB;|CH(_[HSH AAHuHIHy3H
[HL$USWH0Hl$ HEHHAPHHH+EH]0}tHC(HXPH{(H~3He_[]HT$UH Hj E@HH
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@CH0H(E3I;H@I;t H98t8L8HzH@I;t H8I;t
E;HEH$H8L@MI;vYD$D$AiiADAiPFADD$AGIL;rDHHELAH8FH8H@rH@H(L9 tTHHPI;t
H9Ht8LHH]HPI;t HHI;t
0HHH-I;tLxLxDxH(IHI;t HHPHI;tHPHHHKI;tL9ct/L{IHCI;tLcM;t
LXL9`uRHHI;tLxLxDxH(IHXI;t HHPH`I;tHPH`HXHKI;tH9st*4L{HHCI;tHs
uHXAD`,@(Dx)Lx L|$@H_ I;tI;t HHPH\$@H_I;t
HHPH\$@LHLHv,HH$I;tDHHD$0LD$0IA;tNH$I;tHRHHPAE9~v!IHD8xtHA;VrW\L|$8HLD$8H+HHL$8I;tI
HH`PA;t5HL$8I;tHRH$I;tHPHHPL|$0HLD$0HV*HHL$0I;HHDPA;t;HL$0I;tHRHL$8I;tHPH$I;tHP8L3HL$`%|
HO I;t"HPHO HDHT$`P HL$0$(dHI;tDxHHIHt$PI;t
HHPHD$`HFHn L~HI;tDxHHEEIHl$XI;tHEHPDeHOHL|$(L|$
E3LHPA;tZHUHRHHRHL$0I;tHPHL$8I;tHPH$I;tHPHHPUHEHPHHPHL$0I;tHPHL$8I;tHPH$I;tHPHHP3HxA
_A^A]A_^][HUH HHM@H]HUH HHH]HUH HHM8H]HUH HHM0H]HUH HHMPzH]HUH HHMXZH
]HSUVWATAUAVAWHhHD$HH(HE3I;tDxHQHIHt$PI;t
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HHP@V&HI;tDxHHGIH|$XI;t
HHPNl(M}0M}8L|$0HLD$0H&HHL$0I;tjHUPDA;tTHL$0I;tHRI;t
HHPH$I;tHPHHPI;t HHPAHL$0I;tHPLHLH&HH$I;t|HHD$0HhLD$0DA;tWH$I;tHRI;t

HHPH$I;tHPHHPI;t HHPAL$HLHHH$I;HXHDB
HRPDA;tkH$I;tHRH$I;tHPI;t
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HHPH$I;tHPHHPI;t HHPAVHL$0I;tHPIL|$0H$I;HHUXA PDA;tkH$I;tHRH$I;tHPI;t
HHPH$I;tHPHHPI;t HHPAH$HHUxAP DA;tkH$I;tHRH$I;tHPI;t
HHPH$I;tHPHHPI;t HHPAH$HT$0E3L9eH^L9t0IE0H+H=v!HHPDZIE0HD$0HHHH HNHF
LfGHHD$@AtD8uAGL9tuL L;HRHHRHPpHHPL HPHnL HPHDN7JJD8LHD$(HD$@HD$
E3LHARDjE3D8gtG;HIE0H IE8IL;}HHT$0H$H$HtHPH$HtHPHt
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HHPH$I;tHPHHPI;t HHP3HhA_A^A]A_^][HUH HHMPyH]HUH HHM8yH]HUH HHyH]HUH HHMXbyH
]HUH HHM0ByH]HUH HHyH]HUH HHxH]HUH HHM0xH]HUSWH Hl$
HAHHHgqHQHH+VO3H_[]HSUVWH(3H9iv$3HHHLHH(;orHGHt HYL
(DblHJH(_^][HSUVWH(3H9iv$3HHHHHP;orHGHt HYL
PDkHH(_^][HSUVWATAUAVAWLHHD$PEMLH$PH$HHDILDICHD$@E3Dd$H;HH$PHt)8HpHHD$ L
dDS'Hk3Ht$@t$HFLhLp@H(H+uH$3t3HLHLH_uADd$H;rPEtHAHHHH(HuD$H3tiHuH(;rSH$]+ IMt1IEIPuD*
HuHTXHFHDxHH$Mt{DIHHL$XL3IH$HHEtH$@HHE3HD$0HD$(D|$
ME3HT$XIM]IASt3HL$@HA_A^A]A_^][HUH HHM@^H]HUH HHPH]LL$
SUVWATAUAVAWHHH$EHE3A;DH$HH$9^A:u
@LtpLtxDIL|$@II3HII;t3E3Lt$pHtLHD$xI;tH\$pI;t
DHUDL;AEDH3H$H3HHHDHD$hH$0HD$PE3Dt$X;HoH$Ht)8HhHqHD$ L
DSOH'h3Hl$PE3Hu8H$LHH`H>HD$`HHLD$`H2HT$`Ht+H$LH=AIHLxHHFHNHD$hHFD~LD$HAFH
$HtFEFAiP%HFHL$`HtHPH$>AHPD;pH$HL$PH$HL$P@HL$`HtHPHHLPH$3tEDfHHHLH
[uEDt$XA;rHL$P*UAEtHAHHHPI+uD$X3(HE@uAHP;rHl$@HLPLD$HAIH$HD$@H$+LD$@H$H$(^t/H$
HL$@HtHPHL$P5`HL$@Hu4H$XHL$@HtHPHL$P@"HD$HHLD$HHQHT$HHtgH$LH\t@HL$HHtHPH$H
L$@HtHPHL$PlHHD(HD$@HD$
LLDhAIt@HL$HHtHPH$SHL$@HtHPHL$P"HL$HHtHPH$HL$@HtHPHl$@HH$7Hl$hLLLHHHtuI
HHHHHT$0H$HT$($T$ LDHHLHASt
HL$P(IH$OHHHLP3HL$xeHHA_A^A]A_^][HUH HHMpH]HUH HHMPrH]HUH HHH]HUH HHH]HUH
HHM`6mH]HUH HHM@mH]HUH HHH]HUH HHMHlH]HSUVWATH0HD$
LHH;uHHcyHt,HHHH,HtHMMHuCA|$;{v6HHEH{tDCIHH_HMH3{tU3H HHD$`Ht!I$H
JHHHH4N3KHH<CHHuHH0A_^][HUH
HHM`H ]HL$SVH8HD$ HHh]H`HtHPHPH@HHtHPHHtHPHL
$kDFH?_HK8L
kDFH'_HK HtHPHKHtHPL
AHHH8^[^HUH HL
TAHMP^H]HUH HHMPHrjH]HUH HHMPH RjH]HUH HHMPH8L
0jAL^H]HUH HHMPHL
iA^H]HUH HHMPHiH]HUH HHMPHiH]HUH HHMPH8[H]HUH HHMPHH;H
]HSUVWATAUAVAWHHD$`MEDL3H$@HAHHMXH$$$HoHffHHHKHLpnLHHHLpHCH$XHL
XPLPHHHLHLd@HHL8HD$0CD$(CD$
LL$hEAIP=@tuLHMXHPH$HH$H/H$HHHH|3HA_A^A]A_^][HUH HHH]HUH HHHH]HUH HHH]HUH
HH}H]HUH HHMpH]HL$SUWH@HD$0HH}HD$ L
%DCH[3HoHo HO8HfHD$ L
DCHb[HHifHD$ L
DCH-[HHHHHH0H8H@HHHPHXH`HhH\$hH+kkHKfHK HH@_][HUH HL
\AHM`YH ]HUH HHM`HzeH]HUH HHM`H ZeH ]HUH HHM`H8L

8eATYH]HUH HHM`HL
eA!YH]HUH HHM`HdH ]HUH HHM`HdH]HUH HHM`H8cH ]HUH HHM`HHCH]HUH HHM`H`GdH]HUH HHMhZH]HUH HHMhHH]HSVWH@HD$0H3H9iHHD$`H;t*0HxHHD$ L
DHXH;HH@_^[HUH HHM`H]HSH H Ht HYL
DWHjH [HL$SUVWATAUAVAWHHH$IA@IHH$(HSH$HS HLL(LHKDD$ y
@n9\$(t
W^D$x
@tH$uq;uHH$@xH$HfzuByDH${H$;v;vAD$xHL$HE3H\$HSHk3EHDHHHHD$0HD$8LL$0LEAKHL$0HHtHPHL$0HUHtHHPHL$0HuHu
H9uHT$8HtHHPHL$0HtHPH$yAAHM`L3vWHM@3DFgWHMpL3YWE3ELHDPHDXLL$PL3H$LRHL$PHHtHPHL$PI$HtHHPHL$PI4$Hu HZHT$XHtHHPHL$PHtHPDID;$xFD$xDHD;Dl$
FHL$8HtHPHL$0HtHPHt"L
DCHSHKS HT$8HtHHPHL$0HtHPHt"L
DCHSHKh@HL$XHtHPHL$PHtHPHt"L
kDCH.SHKHT$XHtHHPHL$PHtHPHt"L
DCHRHK@[D$ UD$$;E3LMtLIIGDL$E3HHMH$kHMHHHL}L}LmDuAHiHLL$ DH7uAHD;r,Ht"L
DCHQHK]HL$hr3$DvD$E3Ls8H$HIH;HB3H$`HHHDIFID{hHHt#@H@@HHHF`3HuHt
HHPHMHtHPHuHD$hHFPHL+uH\$HEuINH$HA`HA@H;v.AiP%IFMFH!INXu:IN[uaDAMD;L$$LvHL$h{Ht"L
lDCH/PHKHL$h{Ht"L
4DCHOHKyAA;v tHH|II+u$t6HL$h&{Ht L
DCIHOHKyH$HK(HH$E3L$3H$HCHH$t(H LXL$HH$II+uH$HEEH$HPt1HL$h_zL
DCIHNHKR3$$$xDDE3LI$EuaH$HIL$@I$HAEH;HB3H$`HHIDI$0I$Hi`AHA@H1HCHI$0
HvxHHfzuBANA$E;3$xAD$ Mt$Eu uM2AnMFI0iP%IANHHi,ujI$x;D$ IrHA!3Hu`xHEID;tfHLhixL
DCHLHKZHL$h6xL
DCHLHK'3E;HD^At;E3ADI,A|-AEt-IIL+u$xAIL+uH\$Ht1HL$hwL
=DCIHLHK$AAAt3HL$h=wL
DCIHKHK0HK(HH$E3L$3H$$xHCHH$t(H LXL$HH$II+uH$HE3H$HPt.HL$hvL
,DCIHJHKvHEH$HPt.HL$h8vL
DCIHJHK.HL$h
vL
DCIH{JHKb3H$HA_A^A]A_^][HUH HH/H]HUH HHHH]HUH HHH]HUH HHMH*H]HUH HHM0BH]HUH HHM0fUH]HUH HHM0FUH]HUH HHM0&UH
]HUH HHMPNBH]HUH HHMPTH]HUH HHMPTH]HUH HHMPTH]HUH HHMh6H
]HSUVWATAUAVAWHHD$PMEDL3H$0H=AHWHKXD_D[CGCH$$$HHfthftjHt$pHWHL$h6HLXLDhHHzHLXIEu
HDGCxGC|HCHD$HH\$@H$(HD$8GD$0H$ HD$(HL$ L$E3I=@u3@8sduHKXHPuC3HKXHPHL$h3H$H
)HL$h3HL$h3H$3HA_A^A]A_^][HUH HHH]HUH HHHH]HUH HHH]HUH HHMhhH]HUH HHMXH]DL$
HT$SUVWATAUAVAWHH$MLH%3@:uG HHhAL@LHH@8;tH$@H;t
s8D$t8D$tH$h8D$d\$pHH@|bH($0$4H$8H$HHHH DA9}IEHHHHHH$p3:Y9KH$ HSH$ 3:HH
3:H$H;H;H;t
H9$tOH3H$HSHH$ 3H;tHHH;bHLDHH$ W
D$L;$X3H;t-HH<HE3H$HLHASH$ H^{H}8ffH$9KtcH$HKH$3f9u29St-
H;wH$>2#HH$X2HSH$11HbH$y3:tgHVLH$H9fH$3;HS/H]H$7HH$:HXLHHeH$X3;!$;<HHi@BH$H
;t,HHFH$E3H/LHASH$w.HcHH:HHH_H_vH$:9$u#fuH/vH:uaLLpDDtHHd3;_HH-
HHDdbHH$e-H$oHLLHH(<s3;H$-H$!H$EHE;uH$HMWH$ H$HWoH$ H$H@@H$ HiHH
pH$:H@H$ AH$HbWH$+H$H3H$+H$H _H$~+H$HW0H$gH$A+H$KHH$+H$!Hx3H;HfH
wjH;HwHjH;H$H.H$$t

H$pHH$$H$HHH$$H$ HH$$HHHHLHASHHGHL$x<HT$xH9u
9$tlHH$HD$D9$uHTxH>;t*D$D;tHHHLHT$xHAHHPHL$x5H$'HH9tHHHLHASHH:H9tHH$HLH
ASH$LHHAHLH$H;@E3I;tHHi@BH;sHI;wEHHPA;0hHHi@BHDD
hDL+MMDHHi3IH3L;uLH1HADATAAHH,A|pHi@BHH$
=H;HBHH;s*$3;tHH$HHH$A}%IsHEE3H{HHPLHASD$tD$(HCHD$
LhDDdH"HZH$@H$]LHHAH$LHASL$XL$3D$p=jD|lAH(b;|(H$(HfzuDjDl$lADd$a@@|$a9$@uHZH$8)L
;$$$DH(:HHDHL|$@Ht$8D$0Hl$(HT$ L$(DD$pAI#H3H$8H$JHH$-@:tHuH$PHH8HH-
@:H;uH$HFA@:DDDl$lD$t'H$DH=fE3HWHBHLx:H$A;|1SH$HL$x;$sHD4HHA:D9$tPHPH$A:t
H3HL$xS9A;u
D9$PtHT$xH$HTA:t)D$tHGHHTxH$HA:u+HL$xH$EHAHL$xuH$gAu!H$TH@HHPLHUHAEDD$pAs_A#
AA;FAHpEADD$pA;rHHPLHASAH$8AHHH$E;vLI3H7MDt$hD9$h_A
AA$;D$lrHT$x3H@8|$xt
I@8|xuDx:HDx
I@|xHHT$xHD;H$xHHP$E3A;G$sWA#@T$p;FL$IAHDLDXDDPLDHLD$@H$(HL$8HD$0Dd$(D$tD$
LDH$l3;H$HHDAHH$xH8$HH$xGA;HHPMEA uD$hAD$h;$hWSHH$H
$\HH$H5E3M;tYHHPLH~HAE;v1HL$83HIHAHHHM+uHHPHH$HYA
H hH$8DD$`@|cHH8@:t
Dd$cEDd$`$|$p|$h38T$bu
;V
:ADd$h;t.L$t;
L$hA;u
r|$h
H
H$$$$$HDDHHHHHHHHH;vHHPLHAS3:AEAA8T$dt=Av7AEHH;$hvwH$
3EADl$lD:t+A;t&HH.HEHLHASEAL|$(HHD$
EHHhLHASLHAS3D:tDADDLeADEDdeD$D:AE$C@:E$pD:t>
HQDH#HWLHHADHH5HHPHHE3E:HED$HAE~A
HHEHnHAHUHAH8HA:t$EHHEHHE:t
A;uHH3HAaHHPD$ H+3L$A
HH"EHHAHHAHH@:t$EHCHEH1HD:t;uHHnHAbHHPLHAS3LH$H;LEL$@8|$`H$ D9$hD;t HHPAHHt
E3I;uEE3EH$8HD$0HHD(H\$ LT$hIA;H$HPLH3H;LEL$umH;IIML$A
=M;s:IDII;r:t+$;tLLL$LILAHHPDL38D$cuW8D$au
H;uDEDL$lH;LE$HHD0Ht$(Hl$ |$hI3;|$hL|$8Dd$0HHD(HHD
LAI3;aH$HPAHHL$H$3H;AH$xHL$0H$HL$(HD$ LI3;
H$HPED;$hHEHH@:EDH=HWHL$x"H$X:HL$x3;|4SHT$xH$Xb;$sHHDx333HHLx:tC9$`tgHkH$XH-;u
9$PtFH$XH$HH:u-H$XHL$xEH A3;G$GG3DH$XkHL$x`AD:t)$$$D$D9$h_T$l@EAA]@8$pDA;v;v
A;wH$ A;H$p3L8$t
MB8uB:BAH$H2$H$(H$hJH$8H$x HHHHZH$x3:tnf$PfRXHhH$:L$PHH$h[H$3;H$P3H;t
H$$H=rHHHLHHHL@Ht$8D$0Hl$(HD$ LhDDhAH$HHR3;3@8$pt'H$hAAT$l;(3HhTl3ED;$h$$pD-
H\$eE3A:tHHWHD-
HiHHPA:tdDHHLM$HdLHHAL$MH?LHASDHMHLHIHHHHHHHHHHHHHL$MHLHAS$
ADllD|$`\$aH$ HwH$H[H$H?H$H#|H$H
@^H$P}H$hCH$yH'H$hHPHH(CH$&H}H$(3HcH$(WHA_A^A]A_^][HUH HH(H]HUH HH(HH]HUH
HH(;H]HUH HHH]HUH HHH]HUH HH H]HUH HHFH]HUH HHcH]HUH HHXCH]HUH HH#H]HUH
HHH]HUH HHH]HUH HHMxH]HUH HHH]HUH
HHH]HUH HHcH]HUH HHCH]HUH HH#H]HUH HHMxH]HUH HHH]HUH HHMxH]HUH HHXH]HUH
HHhH]HUH HHhH_H]HUH HHh{H]HUH HHP_DH]HUH HHH]HSH HD$0HH [HSUVWHHHD$
IHHD$xAHjA;vP+Hf<j.uBEHBIt/DHT$(HvuLHHHL$(~Hkf.H~/DHT$(H7uLHHHL$(~H,~ufA~HH

HLHHHHH_^][HUH HHM(H]HUH HHM(H]HSH HD$0HHH [HSWH(HHELHHCpD3H;t L
HAH(_[H(HLHtHHHAHP3H(HSH@HD$
HHHL$(%HHL$(/D\$0D9\$4uHL$(D\$0HL$(AfA\DfBYD\$0HL$(f9\tf9/u<uHL$(HL$(2|H@[HUH HHM(^H
]HSWHxHD$ HHzHHL$8HL$`<LHHL$(HL$(4tHL$(f|HL$`[|6HT$(HL$8VtHT$`HHL$(.|HL$`#|Hx_[HUH
HHM8H]HUH HHM(fH]HL$SVWH0HD$ IHHeHHHtHHHHH0_^[HUH HHMPH]HSH HHI@R{HK0I{HK H
[;{HSUVWATAUAVAWLH8ADDpL3@8MLDEHIMICHAICGo;Dt#$;H;IzD$L;wHH;trzHL;HtI+zLHDLIMH$
AHD$ F;Dt$H;tR_H$D;tHDHKxJ[A3$;DHHkpH;t
HDIH8A_A^A]A_^][HSUVWHhHD$ HH38Y@NA@
k@;I(IHL$($E3HV0HL$(D:\$8\$<HL$@HL$PL$LF8HV0HL$8$:t&HL$PxHL$@xHL$(x3$=(E$HL$PxHL$@x
D$FD;DDD$H9^0tHN0|xH^0D$H^8D;uHL$(]x3,HopHH;tHHT$(HL$(3x3Hh_^][HUH HHM(H]HUH
HHM8HnH]HUH HHM8""H]HSVWH H;HtPHzH2H;yt0H HtwHHHCt"HVwH{HHtHLHHH
^[HSUVWH(HI9Ku>lHw{HtDCHHIfHwH;kKH4CHKH(^][HL$SVWH0HD$
HHHHHBHAHBHAHBHAH H ^HC0HC8Hw8HtHXvHC0LHW0HHs8HC@HCHHwHHtH
vHC@LHW@HHsHGPCPGTCTGXCXG\C\G`C`HH0_^[HUH HHMPH zH]HUH HHMPH0ZH
]HT$SUVWATAUAVHH0HD$ AIHL@XHBLA]xI$LEHXAy;_v
HYHfJ_AEH+L4JEEMIUIux2I$HBHM4FDBMHIKL]A4yHH0A^A]A_^][HUH HtHMx.H
]HSH0HA0EHNLAD$PIA EITHD$ HH0[HSH0HA0EHNLA D$PIA EIXHD$ fHH0[HSVWH
A3H;At:;Ht"HHsDCHIHH>HsCH3CC;Ct=;Ht#HHsDCHSIHHHKsCHsCC(;C,t=;Ht#HHQsDC(HS IHHHK
]sC(Hs C,C8;C<t:;t#HHsDC8HS0IHHtHK0sC8H{0C<H _^[HSWH(AA(QHHH|HtHrHrCH(_[HSUVWH(HHI
rHKrHc{Ht,HHHH,HtHMrHwruHH(_^][crHSUVWATAUHH$Hy@39i8E3HG0MdI|$8DPDTHLXHLh$ L$
MD$8IT$0HL$PuHL$hqHL$Xq/|$TuHL$hqHL$XqHT$ HLPHL uHL
bqHL$hWqHL$XMqHLxDHHHT$xH|H$
qt/HL$xpHL$ pHL$hpHL$Xp\HT$xHL$@DHT$0HOD$H9D$8rEHL$0pHL$@pHL$xpHL$
pHL$hvpHL$XlpHD$8H+HD$@HHHL$0"tEHL$0<pHL$@1pHL$x&pHL$
pHL$hpHL$XpDD$HD+D$8HHL@ef$H$f|A\tSf|A/tKoHL$0oHL$@oHL$xoHL$
oHL$hoHL$XouSH$boHL$0WoHL$@LoHL$xAoHL$ 6oHL$h+oHL$X!oD$A;uSH$oHL$0nHL$@nHL$xnHL$
nHL$hnHL$XnAD9D$(wSH$nHL$0nHL$@nHL$xnHL$ |nHL$hqnHL$XgnHHL
dHHH.ntSHnHL0nHL$@nHL$xmHL$
mHL$hmHL$Xm\DH$H$?dHD HHHHH$|$PAHHdulHWmHImH$;mHL$00mHL$@%mHL$xmHL$
mHL$hmHL$Xl}IL$@H$HlHlHlHL0lHL$@lHL$xlHL$ lHL$hlHL$X{lI;o8CHA]A_^][HUH HHMPHH
]HUH HHMPrH]HUH HHM H]HUH HHMxbH]HUH HH?H]HUH HHM@"H]HUH HHM0H]HUH HHH
]HUH HHH]HUH HHH]HSUVWATH 3HL9zvU3HEH{u:{u4A|$u{u{tHHH uHIHH;}rH A_^][HSUVWATH
HcyDHHt,HHHH,HtHM`jHXjuD;cCv)H?jIHHCiDcHH A_^][HSUVWH(HcYHHt>HHHH,Ht#HM@iHM0iHM
iHiuHH(_^][iHSUVWATHHD$
AAHHL$P(zHD$`HDhHDpHDxLIHD0IDHD8IDHD@IDHDHAD$
$AD$$$$$$IT$(HL$P{hhH$HtHT$0H3HM0H]aE\$
AAtHEH"A|$$tHEXI$HEhHEPI$HE`HL$pxhHL$`nhHL$PdhHA_^][HUH HHM0H]HUH HHhH
]HSVWH0HD$ IAHHHO gHHD$P3H;t HuyHHH0_^[N`HUH HHMPgH]HL$SH0HD$ H3HAAHAAAHA
A(A,HA0A8A<A@AAHAHHAPHAXHA`HAhHApHAxHHHHHHH0[HUH HHM@.$H]HUH HHM@HH
]HUH HHM@H H]HUH HHM@H0.H]HUH HHM@HxVH]HUH HHM@HH
]HSUVWHxHD$@AHfA\IHL$HPLLHDHH}8LDHHHLXuL$0Hl$(HHT LDHHL$XWe;}8u
HBHL$H?eHx_^][HUH HHMHH]HUH HHMXH
]HSUVWATAUAVHH$EEHLHD$pH$@HL$xovHL$HXuLD$0HTHHLpc|$0HL$HtHL$HldH$HHL$8HT$`H
LHL@HD$8HT$`HLxALD$83Ht^HL$`dHc\$@Ht-
HHHD$8H<HtHcHcuHL$8cHLHcePuuALD$8AHku^HL$`cHc\$@Ht-
HHHD$8H<HtH_cHWcuHL$8IcHL$H>cH$IA(sIT$(H$tHT$`H$wL\$XL$$$HHD
AEAHXHbHL$`bHct$@Ht-
HHHD$8HHtHtbHlbuHL$8^bHL$HSbHL$HrLD$0HT$HHL$plLqH$@H$trHH$XHaHLHa

HL$Ha3HL$xaHL$peHA^A]A_^][HUH HHMpH]HUH HHMpH]HUH HHMHH]HUH HHH]HUH HHM8H]HUH HHM`~H]HUH HH[H]DL$
DD$SUVWATAUAVAWHHD$@LLHQ(HL$hNrD$DHHL$X<`HD$H3H;tHT$hHrHHHL$XXHT$XHL$HA F @"ALD$H3I:Hct$PH;t-HHHD$HL$(L;tI$_I_;uHL$H_Hct$`H;t-
HHHDXL(L;tI$_I_;uHL$Xw_Hct$PL;t2IIHD$HI,H;tHM?_H7_;uHLH_DAF
@"ALD$X@IH$:D$8MF(H$HLxnLHHvDHL$x^D;tLHct$`H;t-
HHHDXL(L;tI$o^Ig^;uHL$XY^ADDu8Lt$ D$$HyE^
ADEDED$D8]AMF(H$HL$xnHl$8Dd$0HL$XHL$(HD$ DDIIDHL$x]D;tLHct$`H;t-
HHHDXL(L;tI$k]Ic];uHL$XU]A;HE0H4L$MHHD;tLHct$`H;t-HHHD$XL$(L;tI$\I\;uHL$X\AH9^8tIHct$`H;t-
HHHDXL(L;tI$\I\;uHL$X\AF uIHct$`H;t-HHHD$XL$(L;tI$J\IB\;uHL$X4\iHH$9Xu-
HT$hI;|IGH4H$HDD:uNH;uIHct$`H;t-
HHHDXL(L;tI$[I[;uHL$X[HHL$XH;u9IK[H$H;tHT$hHmHHHL$XSDd$8Hl$0HDXHD(HHD
MN(D$$HDHct$`H;t-HHHL$XL$)L;tI$ZIZ;uHL$XZAHL$hZHA_A^A]A_^][HUH
HHMhH]HUH HHMXH]HUH HHMHNZH]HUH HHMHH]HUH HHMxH]HUH HHMxrH]HUH HHYH]LL$ DDTSUVWATAUAVAWHXH$@IEL@$@u@EHLILA&@3A9v0v=3IF(Hxx
HH}
HA;v0r3HL$@3D$HDLA9F0H$IF(HHL L$8MHHL$Ph@H$39wuq@8sukA9t$u@f\HL$P]jQ{uK@8stEHHHe}@:t2@T$X9T$\uHL$PfT$XHL$PfA\f4QT$XHHhIHl@:tIHjKHTPH@:u4HT$PI;.H$WHLPWDAAt
@8stQ@8suKIEpIH;tHA@HT$P;H$0WHL$P%WbH$$$VHD$pH;tH$HhHHH$uOD:AL$3I@:tjHc$Ht0HHH$H<HtHVHxVuHgVHYVHL$PNVHc$Ht0HHH$H<HtHVHVuH$UAD$At'L$HTPIDHHD
D$$IAM8IE0H|HLLHIEH8t@8tHI)EhH$I)E`H$.UHL$P#U`@A}AHD$`DhDlTHD$pHtH$Hf3HHL$``ML
l$8D$0HD$`HD$(HD$PHD$ D$D$IH$DqHc\$hHl$`t0HtHH>HtHITHATuH$H-
TL$8EuH$THL$PTEH$$$IIzx2D$H;+HL$@HuHD$@IFH6IeSHD$pHt
IH4e3HHLD8Ll$0H$HD$(Ht$
L$D$$H9Hc$Ht0HHH$H<HtHRHRuHRHRHL$PR$$HHHA;F0HDHHL$@EH$mRHL$PbR1H$ORHL$PDRH$1RHL$P&RH$RHL$PRHc\$hHl$`t(H|HH7HtHQHQuHQH$QHL$PQAwHc$Ht0HHH$H4HtHoQHgQuH$
VQH$HQHL$P=Q
E3E9f 3H$LD;s
A?fIFHLCHHL$p#aH$eH$a}uT{uf\HL$pb>HKut1T$x9T$|uHL$p*_T$xHL$pfA\fQT$x}u{tHSHt!HSHbHTpH:ueHnwu!HPHLpPD$HWHT$pIfHOHLpODHuOIEpIHtHA@HT$p3$H$|OHL$pqODHHD`D$hD$lD$8Ll$0HD$`HD$(Hl$
L$D$$H#Hc\$hHt-HHHL$`H<1HtHNHNuHL$`NH$NHLpNHD$HAIHH$E;f
HgSHsNHL$phN:H$XNHL$pMNH$=NHL$p2N3HL$@!Nt}AF0}3tf3IF(HzrNHH(}uBHMfy:u7zuzu+zt%ruHEf8*tf8?uHA;^0rH$A;^0HD$@DHDLHL$@E3\$H3HD$@H(HL$`^HT$`L$hf|J\L$hfJHH]HT`H$_@|$8Ll$0L|$(H$HD$ D$D$IH$u1H$LHL$`LAH\$HD;H$cLHL$`XLHc\$HHHHHD$@H,HtHM%LHLuHLHc\$HHt-HHHD$@H4HtHKHKu@6Hct-HHHD$@H4HtHKHKu3HL$@KH$ H$(\HH$ 3HH[LHH$
$It#%=uHI*@|$8Ll$0L|$(Hl$ EDIHHJHH$*[L$HH 8=HIUtH$:JIH$*JHJH(
JH$ 3VHIH(IH$ x*HIH(IH$ LHXA_A^A]A_^][HUH HHM@H]HUH HHMPH]HUH HHH]HUH HHH]HUH HHMpHH]HUH HHM`^H ]HUH HHMpHH ]HUH HHH ]HUH HHMprHH ]HUH HHMpH ]HUH HHH ]HUH HHM`H]HUH HHM@~H]HUH HHM`VH]HUH HH3H]HUH HH H]HUH HH H]HUH HHH]HSUVWATAUAVAWHhHD$@IMDH39qHt$HtPtT3NHEL,A}uDMDHDAuAA~tMDHIMD$0H\$(HTHHT MDAD3FEuH;uAL$fAH0H3HhA_A^A]A_^][HUH HHMHH]HSUVWH(HHFHOx7HO0HO
EHOEHcwHt,HHHH,HtHMEHEuHH(_^][ELL$
SUVWATAUAVAWHHHD$`MIHE3ML|$PAD|$XD|$\HL$pH$H$0LL$pLHH$H$EG0`A;tHL$p3DAAA;IH$
HAPuYDHT$
HL$p
HIwDH$I;t
HHCVIHHL$P=HL$ nD$H;rl$XLdPHLp*A;u&E3HRHL$ UHjHL$ L|$@D|$HD|$LA;vhIHL$ THHT$

HKICH$I;tHT$ HUIHHL$@[<HL$ CHHuL|$0D|$8D|$<HT$0HL$@T$8IT$8H$HAD9|$8IMHD$0E|O4
CH$HtIHTLE3H$JHLBHD$@N4BH$E3I;tIHTLMKHLCA;vHHDHHH}eA;tIH;|$8;0DHNMHnRHL$ HHL$
YHL$0PBHc\$HHAA;t.HAHHD$@H4I;tHBHBA;uHL$@BHcI<AHHI;tHAHAA;uIA3HHA_A^A]A_^][HUH
HHMP&H]HUH HHMpZH]HUH HHM H]HUH HH7AH]HUH HHM@H]HUH HHM ~H]HUH HH@H
]HUH HHM0>H]HUH HH@H]HUH HHw@H]HL$SUVWHXHD$HHHH(7HW8H+39w
vTHHGHHHH9quHe@:uHHHH;o rO ;0HHt(HGL(LD$;C$HEHHGH(H$HPIHlE@:H$H(HL$
H(}pH$H8HL$ H8cpHGH(H$H;tH^rH$?HODG $D+IHHHG
$H;_ LD$ 6Hl(HHl(O C;9w vaHHGH(LCHHL(NfA\HHL$8HHHL$8W>HL$(L>H;w rHX_^][HUH
HHM(H]HUH HHM8H]HSVWH 3H9YvHHHHQcH;_rH _^[HSH 3HHAAAAAAAAAAAAHA AA
AAAAAHA(HA0HA8HA@HAHHAPA`HhMHH [HSH HHI(&=HKH [=HSH HH Ht7HH [HIh<HL$SH0HD$
HH<H<H<H<HKp<HKH<HK8<HKHtHPHKHtHPHKHtHPHHtHPH0[HUH HHM@
H]HUH HHM@HH]HUH HHM@HH]HL$SUVWATH0HD$ LHHAAztHHj;H{tG3H Q;HHtI$HHA
3KHH<CHHuHH0A_^][HUH HHM`H]HSVWH HcyHHtHHHH:uHH _^[:Ht|SVWH
H0H:HHt@HJHHC3HHpt HHPHxHtHPHxHpHp(H _^[HSUVWATH Y3LHt*HE<3HHI$\tyw;uH
A_^][HL$SUVWH8HD$ HHcYHt+HHHH,HtHH9uHH8_^][9HUH HHM`
H]HL$SVWH0HD$ H3H1qqHqqqHq @q(@q)H0IH_@H\$X@3@s@s@sssCHsHKIHK(IHH0_^[HUH HHMPH
]HUH HHMPHFH]HUH HHMPH0H]HUH HHMXHH]HL$SUVWH8HD$
HHOhL8HOXC8HO098HcoHt,HHHGH4HtH
8H8uHO7Hc_Ht+HHHH,HtHH7uHH8_^][7HUH HHM`H ]HUH HHM`HH]HUH HHM`H0H ]HUH HHM`H@H
]HUH HHM`H]LL$
LD$SUVWATAUAVAWHXH$@MHHDRHJlH$3L$ILuH$$$HD$`D$hDlHHWHL$p4HHH$HAD9kv<HHHF
HHsGHIZ=tHHHHH$HH
H$RGLHHL$p
PH$5H$53@H$_FHT$pH$XG8H$,FLHHLpYDD;tPH$fB|Y\t
fB|Y/uAD$fBYD$D;tHHGAH$4I[D8_0.MH$IAP(tH$4H$4H$4HL$p4Hc|$hHt.HHHD$`H,HtHMl4Hd4uHL$`V
4H$D3H$$$HDH DE3D$1D$8$<AD9$XA:uGE:tBD$0D8G&u\D8tSL$0HMEE3A;H$(L$H
|E3A;YH$D8G&uD8$0A:t\H$H$UE3A:uA:HHfA;uD8$3uAf\t@f/AEE:uH$H$E3A:tH$jE3A;$&H$H$2H$2H$
w2HL$pl2Hc\$hHE3A;t.HHHD$`H4I;tH:2H22A;uHL$`#2H$H$2H$1H$1HL$p1Hc\$hHE3A;t.HHHD$`H4I;tH1H
1A;uHL$`1E3D9$H$HHPXL3HAS`H$7H$=1H$/1H$!1HL$p1Hc\$hHA;t.HHHD$`H4I;tH0H0A;uHL$`0ZH$E3A
:t9{0H$I;tH$HBBIHHL$`)D9d$xuH*HL$pDHL$pA:@A;DH>AHCHTpHDH0H$/H$/HL$p/Hc\$hHA;t.HH
HD$`H4I;tH/H/A;uHL$`/G0ML#$HW
HHDPDdHHD`HD$@HD$pHD$8G3D$0G1D$(H$Ht$
LH$H(D8g0D8g3D8g$H$HA;H$.H$.H$.HL$p.Hc\$hHA;t.HHHD$`H4I;tH.H.A;uHL$`q.H$a.D8g3t
D8tEHD8g0IH\$ E3AIP8fDfDLIL,IP@A;up$A;tctStBt2t $NH<tH$H$I$$IIIH$P#IH\$
EDHIP8AI;tH(IL$A;DHHP`I;t
H'H$,H$,H$,HL$p,Hc\$hHA;t.HHHD$`H4I;tH,H,A;uHL$`,H$,HXA_A^A]A_^][HUH HHyH]HUH HHM`H
]HUH HHMpyH]HUH HHkyH]HUH HHKyH]HUH HH+yH]HUH HHyH]HUH HHxH]HUH HHxH]HUH
HHxH]HUH HHHH]HUH HHH gxH]HUH HHH]HUH HH*H]HUH HHoH]HUH HH'H]LL$
LDHTHL$SUVWATAUAVAWHHH$IH$E3Lu0Lu(Lu
LuLuLuLuMLtpLDDL$E8t$0tE~DEyDAE;IHH=:IL$E8t$0u2HHHH$sA:tN$udHH!LLlpH$%)HA;
kHH}:HHpHHA:HHoHAIL$I;tH(HH$E;v
L3HP^(H$I;t
HuHIH$I;t
HHPA@@tQADAL$@DLLLLLH$HL$@t*IIIPtHHRH'3Ht$x@@tPDhD;HH$@84H$HL,HH8A8t$0tH$$
,IUH$A@:m$[IED$3IUIPHH$qHHLXHpID8HHHpHDXHHHAD0$E3L$IH$9L$HpH9A;uD9$<
|At$@It$ MMLHIPPDE3A;IA;DPHD8@0u^HVH(u6IUH$E3A:t$ u
HHDxH$(%E3H$u%Hct$`LA;tIIHD$XIN%A;uHL$X?%H$H$#%L$LlpH3H$D8F0$A;L$EA;vrE3Il$hLH
HH$ZA;|;v

M+,AHD;rHLDH$E3M;tBHD$pILI;MLLl$pIIIPDE3A;Ll$pLlpLlpHHTD8F0uD8uAHHL9HL$LH
HDHHHD@H|$8Lt$0T$(Ht$
LHH$`D3D;E3D8f0uH$H$HH$H$HT$xHHP0HHHA8H$D9`H$"Hct$`L3;t'I3IHD$XI";uHHLX"H$-
H$"L$H$3D$hDhHHA;LPHL"HHPH9"H$&"Hct$`HA;tHHHD$XH(!A;uHL$X!H$nH$!HHPH!@zH$!Hct$`
HE3A;tHHHD$XH(!A;uHL$Xp!HHT!HHPHA!AH$+!Hct$`HE3A;tHHHD$XH(!A;uHL$X HpH HHPH
A~H$ Hct$`HE3A;tHHHD$XH( A;uHL$Xt H$H$X HHPHE AH$/ Hct$`HA;tHHHD$XH(
A;uHLXHwH$HHPH@H(1H$HfH$@8t$Qu@:tdIIIPE3A;tHHRHg(IHT$xIP A;t$HHRH=H$/wHHE
HHE(HHE0HHEHHEH$@HEHHH0HMHHPHH$3HHA_A^A]A_^][HUH HHlH]HUH HHjH]HUH HHkH
]HUH HH+H]HUH HHH]HUH HHojH]HUH HHWH]HUH HHMXH]HUH HHpH]HUH HHiH
]HSWH(YHrHHLPHI`$tf_t43DLBf:tf\t
f/tf. ufBHIuuH.H(_[HSUVWH(H3H-_.HU338tH<u9FrDH'#t8rHXf0f w"HHHXHftf=.tf=
tHrf_H63H(_^][QE3A;Hf8.ufD9Ptfx.ufD9Pu DQfDA;Ev[IL B
f=:t6f=*t0f=?t*f= r$f=<tf=>tf=|tf="tf=/tf=\ufB
_AHD;ArD8~t2HcAA;Ht&HLHfB<.tfB< ufB_uHL$SH0HD$ HD$P -D$PHX{uf_H-HH0[HUH HEPt
ePHM@xhH]HL$SUVWH8HD$ HHD$ho+D$h39}vH3t'C9CuH.)SHfQ\fQSHEHHG/H;}rHH8_^][HUH
HEht
ehHM`gH ]HSUVWATAUAVAWHHHD$ EID3k:IHDc9XujA9hHA9XAE9`HAHH;{9~HHHH
:{H:tOHH,D9evfHUHHL$(q*"H$H;tHT$(H*HLHHL$(D9evDeHEfX;tDLc;~DoIHI9Yu^D:uND:u
F;t@HI,H;tHMHHDFE+IJHFf_i*DAI;~wD:uj9^uL_HL$(3)HH$H;tHH)HHHHL$(
NHHL9Yu f_)HHA_A^A]A_^][HUH HHM(FeH]HUH HHH]HUH HHM(eH]HUH HH_H
]HQHHSWHLCBLu#EAASA AB LHupAwLEtUHcAEEAAA
A@7AP0BAA
AA7A
AQ0BIAJLHu_[HSWH(HIHIHHAuHC@H(_[H(AAu
HP 3H(HSH Hl(HHtHH [HSUVWATH HIM9Ou>tHHtDGHHIHHwLDGED+IIILM$GH
A_^][HSUVWH(3H9yHyHv.HHFH(HHPDCHK3H;~rH(_^][HSUVWATH A3LHiH9YHv%HHGDIHH HP H;_rH
A_^][LL$ DDTSUVWATAUAVAWHxEDHtHyEHAHt
Hy(HA8Hy HA03HL$ DBD\$ EDEE3D9{D\$ yE3EHCI4uAHHnHHP(Eu,3LF\9Vv!DNEAHALL+uHHPHHT$
HAP HDFHHVP H$E3D9pvK3LIEH$A(fHHP
DHE;urDHHT0HP(D$Eu9E3HD9Fv)DVLL$0L+ADDDAHL+uE3HD9Fv)DVLL$0L+ADDDAHL+uDID;{Hx
A_A^A]A_^][HL$SUVWH8HD$ HHcYHt:HHHH4(HtHNHHtHPHuHH8_^][nHUH HHM`_H
]HoHSUVWH8HD$ IH0HHD$xHt*HHHUHKHU HK #3LHH8_^][HUH HHMxH]HUH HHMx^H]HUH
HHMxH^H]HL$SUVWATAUAVAWHH$HL$:D$(E3A;uVHzHL$`"HAOH$ I;t
HH"IHHL$
HL$`D$(ML|$0AD|$8D|$<IL|$@AD|$HD|$LAAA;YML|xDDHH$!HD$
IHL$xTA;D9$uHH$KHHH$uA;;HHA:AL$A;tM;t(M;sJ;uHL$0l$8Lt$0;tLDxHL@T\$HHL$xJAI;t$(H|$@.H
L$x&AHcALd$@A;t(I4AHH.I;tH@H3A;uI&IHc\$(HA;HAHHD$
H,I;tHMHA;uhL|$PD|$XD|$\HL$PHcH|$@A;t(H4AHH.I;tHR@HA;uAD|$HAD|$8AD9|$XvqMHD$PMAA;tM;t(
M;sJ;uHL$0l$8Lt$0;tLD$xHL$@\$HI;t$XrH|$@HL$P
HL$x?HL$x?HcAHl$@A;t)H|AHH7I;tHf?H
A;uH
I
Hc\$(HA;t.HAHHD$ H4I;tH]
HU
A;u@HH|$pML+L$H$ HL$`~L$ HT$`I4DfH$ HH6H$(HL$(H_H$(Ht!E3HHDH$(HtHPH$ HP0D@[
3HHD$PHtHHH3H$HHL$ Mt
I$IPHHtHPL&HNHT$`sDvHAM3H8H@IuHL$`H$ HtHPAHt$pHHt$pD;Lg,HL`H$
HtHPHct'H4AHH.HtH=HAuH5HL$0*Hc\$(Ht-HAHHD$ H4HtH

H
uAyHH$(HQH$(H$(HtHPHL$`
H$ HtHPHct'H4AHH.HtH+<Hg
uH[
HL$0P
Hc\$(Ht-HAHHD$ H4HtH"
H
uAHL$`
H$ HtHPHct'H4AHH.HtH;H uH HL$0 Hc\$(Ht-HAHHD$ H4HtH Hx u@HL$ e Hct'H4AHH.HtH:H4 uH(HL$0 Hc\$(Ht-HAHHD$ H4HtHHuHL$ 3HA_A^A]A_^][HUH HHM 6H]HUH HHM`VH ]HUH HH gH ]HUH HHM0UH ]HUH HHM@N:H ]HUH HHMxUH ]HUH HHMxHjUH ]HUH HHMx9H ]HUH HHMP.UH ]HUH HH kH ]HUH HHM`TH]HUH HH(+H]HUH HHMP*H]HUH
HHMPH
]HSUVWATAUAVAWHH$8IILHH$@GH$E3E8|$HSLLLL$IG$$$DHDDDHHHAOhH$hI;tH$HI HHpHHHHEHPA;tAD$$HL$@FL$@LAT$H9HL$@uA;t@tHHuHEH$HPA;tHHD$PL|$XD|$`D|$dIHL$PA ;tHL$X eL$L$L|$xL|$pL|$hHHE8|$t&HEHP A;tDHL$X HHHHEHP(A;tHLXHH I;A:uHHL$XWL|$8HEHT$PHP@A;tHeHL$XcED9$x-IL$LHL(rEDl$ D$hE8|$t@xHI;tDxHHII;t HHPH$1x8H$hI;t
HHII;t HHPHHpHDiPAADl$
A`hEH(H$@HHL$(H$(E:EHT$@H$@HK(ED$HT$@tA:uXHuDHT$@HVPH${HL$@jHL$(_I;2HHP$HL$@ =HEEHT$(HPHA;gHL$PEuyE3EuHEHT$8HP0lHL$xAHHP}D$xEt"HHLPzDxML\$8ADl$
LL$(D$hAHL$PL]EALD$PIHASXE3A;<HEHT$8HP0A;THL$(2I;t HHPAHHHD;$x&CHL$@HL$(I;t
HHPH,HL$X(HL$(I;t
HHPHHLXHL(zHt
HHPHHL$X{HL$(EHt
HHPHHL$XFHL$(I;t
HHPHXHL$XWHL$(I;t
HHPH&HL$X%HEHT$PHP`HHLXH@dHA_A^A]A_^][HUH HH@/H]HUH HHH]HUH HHhH]HUH HHM@vLH]HUH HHMP~H]HUH HH {H]HUH HHsH]HUH HHM(KH]HUH HHhOH]HUH HH(KH]HUH HHM@KH]fg@sLZeINgHL$SUVWATAUH8HD$ LHHAAZtHHxH_tX3L
HHDxHtIMH)HH#HSHN3OHH4GHIuHH8A]A^][HUH HHMpJH]HUH HHMxH]HUH HHMx^JH]HL$SUVWATAUAVH0HD$
LHHAAztHH^H{ts3HEHHD$xHt?I$L4HH@MnMt!IHMIH{Lo3KHH<CHHuHH0A^A]A_^][HUH
HHMpfIH]HUH HHMxH]HSUVWH(3HH9Y0v%3HF(HMHH|uH;^0rH(_^][HSVWH0HHf.H*HX;kHOHHHL$ A|$(t?HL$ H5u*39^v#3HFHT$ H6yH;^rHL$
H0_^[HSUVWH(3HH9Yv&3HFHUHHIuH;^rH(_^][HSUVWATH@HD$
IHLA@39zHHxf.,8HykDD+DHT$(Ht|$0tKHT$(I1yHHL$(u0Hw}HL$(;{u#HL$(2FHL$(2H@A_^][HUH HHM(FH]HL$SWH8HD$ HHHBHAHBHAHH
HS(HO(;HC8HG8C@G@CAGACDGDHSHHOH+HH8_[HUH HHMPH^FH]HUH HHMPH(ZH]HSUVWATAUHHHD$ HLHczH3;t-HHHEL$L;tI$jIb;u]HL$(A9]uHL$(@A9]HEeT$0IE4f uJ;toH$H;tHT$(H HHH\$0HD$(fT$0)9T$4uHL$(NT$0HL$(f4AfQT$0HIrT$0;t3XH$H;tHT$(H" HHHHL$(QHHA]A_^][HUH HHM(DH]HUH HHH]HUH HHH]HSUVWATAUAVAWLHxHD$
IHHE3MkEkEkMkEkEkISHHT$8HELt$(Hc\$@Ld$8Hct$0A;HG(MII+LHLHHLXIT-
HL$HD;s2IUHL$XHHLXuD`HD$Xf
HH$HtHT$HHHOHT$X3HI`HL$XHL$HAID;9Lt$(E3AA;t)I<AHH/I;tHMhH`A;uISA;t(IAHH;I;tH2H*A;uIHxA_ A^A]A_^][
HUH HHM(H]HUH HHM8bH]HUH HHMH:BH]HUH HHMHH]HUH HHsH]HUH HHAH
]HSUVWH(HcyPHHt-

HHCHHH,HtHMHuHKHHc{0Ht6HHC(HH,HtHMHMHuHK(HKH(_^[HSUVWATAUAVAWHH$LiIc]HE3A;t-
HHIEH<I;tHHIA;uEeED9%\L=ZI?Dd$
Ld$(Ld$0HL$8Ld$HDdPDdTLdXDd`DdaDddLdhDdpDd$tHWHL$8L_(L\$(HG8HD$0D$
HSHEHWI;t
H$>HW I;t
H$(L$HHL
jHDH6HG0HD$XL9g0D$`GDdDaHoHc\$pHA;t2HHHDhLMtI$IE3A;uDd$pA;DeHD;kHHtHH@3HHL$h
L;ct0HHt\HHCEt"IHLcEtMHHvIA+eE3yHI;t L L`IHHL$hHwH;{t(HI;tL#LcI;t'HHH{I;tLHHE3XfH$I;tHT$
HIHIHL$ AID;5Y 3HA_A^A]A_^][HUH HHM H]HUH HHs=H]HUH HHS=H]HUH HHH]HL$SH0HD$
HH5HK +HK8!HKHE3D[XD[YD[ZD[[D[\D[]D[|HH0[HUH HHM@H<H]HUH HHM@H <H]HUH
HHM@H8f<H]HSH HHIHHK8HK HKH [3rWH8p7 A@ HD$(fI33@fA3HSWHHAHILL$ fD$ fD$"HD$(P0uD$
f=uf9\$(3HL$ |HH_[ft@MAMA@vMA?fHSWHHAHILD$ fD$ fD$"HD$(P@uD$ f=uf9\$(3HL$
5|HH_[ft@HSVWH@HD$
IIAfD$(fD$*HD$0HLD$(P@tUL$(tJt:t)tt@3HD$0HHD0HD$0HHcD$0H3HL$(|{H@_^[HUH HHM(6H
]E3EHAf:u y
Af\ADAHfuHSVWH@HD$ IIfD$(fD$*HD$0AIHLL$(AP0t
L$(t[t=t/t"t@HL$(zBHD$0HD$0HD$0HD$0HHL$(rz3HL$(dz3H@_^[HUH HHM(H]HSUVWH(H
u&3H5YHHMIu
Hr2H(_^][3H(Htt1ttHh1HL$0D$0NHtA3H(HSUVWATAUAVAWLHHHD$ AMHHE3DLLHI+HDy
Dy!Dy"Dy#Dy$Dy(Ly0Dy@HA8fD8DyPHAHfD8fE{fE{M{HMCAWGHP@A;tHL$(xHS#HL$(CHL(xfD|$(f
D|$*L|$0HLD$(HHP@A;tHL$(x43HL$(C(HL$(sxfD|$(fD|$*L|$0HLD$(7HP@A;tHL$(>xD$(fA;tHf=HDT$0HK8
jHL$(xfD|$(fD|$*L|$0HLD$(IHP@A;tHL$(wsD$(fA;tH1f=HDT$0HKHHL$(wA;tD8{#2C$(LL,HGA;fD|$(fD|$*
L|$0HLD$($HP@A;tHL$()woL$(A;tXtGt5t#tHL$(w@GHD$0H'HD$0HD$0HHcD$0HHL$(vAA;tWHMHHI+HD
8t-HJH;sHC
H+HK0 H;vC!3HHA_A^A]A_^][HUH HHM(H]HUH HHM(H]HUH HHM(H]HUH HHM(H]HUH HHM(H
]HSWH(Hz8MHHtiHJ8E3E3HIP uT$H3t=| u<HH;rHK8$HL$HHHT$
APtG"3H(_[I3HSWHHHML$M@MIHPuo@thLL$`LD$0P,HD$`uM8D$`uELD$`PUHD$`u/8D$`u'H$L3DJAHH
D$ AR8u3HH_[HSWH(HHHT$@HLHD$@H;tdftefxuUAf=wf btIgt"ktmt
tu,P
@+HL;r2
ILH(_[HSUVWHXHD$
IHHf:HUHL$8UyHHL$8?~+DHT$(HHHL$8HL$(iHT$8HCHL$8yHf9*ufyt;;2f9#ufyt"2CC2;~HHHf:,8HynDD+
DHT$(H^|$0twHL$(f9tZHAf8u!9at9et9rufC;C5C/f9suNH$Ht5H$HCCHL$(}E;~s!FHL$(12HL$(#2HX_^][HUH
HHM8~0H]HUH HHM(^0H]HSVWH H;HHtPz;yv/H HCHCHd{H{tHHLIHH _^[HL$SWH8HD$
HHHHHBHAHHHS HO
HC0HG0HS8HO8HSHHOHD[XD_XCYGYCZGZC[G[C\G\C]G]HC`HG`HChHGhHCpHGpCxGxC|G|HH8_[HU
H HHMPH/H]HUH HHMPH .H]HUH HHMPH8.H]HL$SWH8HD$
HHBABABABABABAHBHAHHHS(HO(HH8_[HUH HHMPHF.H]HL$SUWH0HD$ H3H)HiHiHiHY
H\$X@+@k@k@kkkCHkHK
HK(H_XH\$X@+@k@k@kkkCHkHKHK(HHH@@ @
@@@
@@HH0_][HUH HHMP~H]HUH HHMPHZH]HUH HHMPH:H]HUH HHMPHH]HUH HHMXH,H]HUH
HHMPH H]HUH HHMXHZ,H]HUH HHMPHX~H]HUH HHMPH,H]HUH HHMPH+H]HSWH8HD$ HHHP
Ht HHPHK0HtHPH{03H8_[HT$UH HHH]HSVWH@HD$ IHA@IfLL$`AH
|$`HHFfD$(fD$*HD$0HHLL$(AP0tKD$(f=u6C9CuHSHfQ.fQSHT$0Hft@HL$(kHL$(k3H@_^[HUH HHM(H
]HSVWH@HD$ IIfD$(fD$*HD$0AIH HLL$(AP0t L$(t[t=t/t"t@HL$(MkBHD$0HD$0HD$0HD$0HHL$(k3HL$(
k3H@_^[HUH HHM(H]HSUVWHHHD$ IIHfD$(fD$*HD$0AAA@H
HLL$(AP0t>D$(f=@uHD$0HE#ft@tHHE3HL$(;jHH_^][HUH HHM(H]H8HD$ HI0HtHP@H8HT$UH HHH

]H(LI Mu3(MHA8HD$0tIHD$0IHQ0LD$0IP H(H(HI HtHP
Ii3H(H(HI HtHP H(H(LIMt*HHA HD$0tHHD$0IHQLD$0IP
3H(HSUVWH(HIHIH[`LHT}uH[HX.LH9}tH.LH#}uHSH6-
LH}uHSHHHH#HC3@H(_^][HKH+H3HHSUVWH(HcYHHtGHHHH,Ht,HMHHM8HM
HMHuHH(_^][HSUVWH(HcYHHtGHHHH,Ht,HMHHM8HM {HMrHjuGH(_^][HL$SWH8HD$ HHH
HHtHPHOHKHtHPHOHKHtHPHOHKHtHPHW HK HWXHKXHHuHHaHHMDDHHHHHHHHHHHH

HH8_[HUH HHMPvH]HUH HHMPHRH]HUH HHMPH2H]HUH HHMPHH]HUH HHMPH H]HUH
HHMPHXH]HUH HHMPHO#H
]HUH HHMPH/#H]HL$SVWH0HD$ H3q HHHHAHkHAH@HAH(HO82HO`@wpHOxHHHHHHH0_^[HUH
HHMPH(B"H]HUH HHMPH8"!H]HSUVWH(HIHIHZLHwtH(LHwuHS6Hy(LHwuHSH'LHwuHSHHHH#HC
3@H(_^][H(A`Hh3H(H3H'HHSUVWHHHD$
IHH(7LHV(HL$(HN8H}2HL$(btD$p3HHL$pCwHcHt0HHHH,HtHMH
uFpHH_^][HUH HHM(B H]HL$SUVWH8HD$
HHHtHPHHsHcHt0HHHH,HtHM@H8uH(HKxHK`HK(H8_^][HUH HHM`H(fH]HUH HHM`H8FH]HUH
HHM`Hx&H ]HUH HHM`HH]HUH HHM`HH ]HUH HHM`HH]HSH y0HD$0u
HHQ(HHHH [HSWH8HD$ IHAHQHHt HHRHHtHPH3H8_[HT$UH HHH]DI E3E;t0LYLCIL;sDw
H@H9BrDAE;uAHSH HHfHK0HtHPHC03H [A 3HSVWHpHD$
I3ft$8ft$:Ht$@DHAJANA/AtlAAvIAt&A9sP{HSHHL$8_h@8sX^HS0HL$8aKHSHL$8`8JHT$PHTPHL(9st=
T$09T$4uHL$(T$0HL$(fA.f4QT$0HSHL$(9s(t=T$09T$4uHL$(bT$0HL$(fA.f4QT$0HS
HL$(yHT$(HL8^HL({9s@tvHS8HL$8^fAtLAt1AtA*uO@8sYtIHS`HL8_9HHL8_*@8s[t$HSpHL$8_@8sZt
HShHL8_HHL8}]HL$8\3Hp_^[HUH HHM8H]HUH HHM(2H]HT$UH HHH]DL$
DDHTHL$SVWATAUAVAWLHHD$xELHAD$0tDq
EsPHy0tEu3E3LD$H3T$@A;s+tAHCHLALDHT@D$0L$IIIPHD$HAHIHD$PHt
HH3Ht$`Ht
HHPE3IH0AHE3I;tDxHHLIH|$hI;t
HHPH[0I;t HHPHOI;tHPH_(HI;tDxHUHL{IH\$pI;t
HHPILI;tPDx(HIHIDHcID$H7ID$HID$
M|$0M|$8M|$@ML$M;tI$IPED|$@E;HD$HHF0HF8HlDA;tDM;tI$IPI;t
HHPI;t
HHRI;t HHRAL|8A|$0tAAH$H$H@HHD$XH$HELD$8P(DtUHL$8HtHPMtI$IPHt
HHPHt
HHRHt HHRAHD$XH@HD$PHDHuH|$8uE3sH$HAL$IP0DtUHL$8HtHPMtI$IPHt
HHPHt
HHRHt HHRAUHL$8LHtHPHL$8HSHtHHPHL$8LsHtHPHD$8HD$PHCC C!H$HH0HE3E3HT$XHP
DtUHL$8HtHPMtI$IPHt
HHPHt
HHRHt HHRAHD$PHGHG G(I$Ht$(HD$ E3LHIPDtPHL$8HtHPI$IPHt
HHPHt
HHRHt HHRAE3AFL9sDEHKHtHPHCIAIP8DE3A;tMHL$8I;tHPI$IPI;t
HHPI;t
HHRI;t HHRAjHL$8I;tHPADl$@D$-M;tI$IPI;t
HHPI;t
HHPI;t HHP3HA_A^A]A_^[HUH HHMPH]HUH HHM`tH]HUH HHMhtH]HUH HHMptH]HUH HHotH
]HUH HHM8RtH]HT$UH0HH
H0]H8HD$ HAHMLBHHI(H8HT$UH HHH]HSUVWLHhHD$8IHHIH HIS(ISIS@ISMKAP ukQua;_v

HgHt-DHD$0HHD$0\f/fDf
HIuf_t3fD$@fD$BHD$HHHLL$@AP0tHL$@SeD$@f=uHT$HHtAHfu2GHfHL$@SuLH3HL$@_S@Hh_^][HU
H HHM@H]HSVWH IHSu<8t2HLL$XD@AD$Xu8D$XtL3H)3H _^[ESUVWATAUAWH
MHL3AHIGLAxDu_E`PE3EtfMPHIHHHuH9$t9HtcH;$wYH8E3HtL$I+BA8u:IIL;rIKLBHHdH
A_A]A_^][AIE;sqHSVWH0HD$ IAHMHuwHHu3kHHPH
t?HD$XHLD$XHHHL$XHtHPHL$XHtHPHWXHtHuu3H0_^[HUH
HHMX&pH]HSUVWATH@HD$ IHHHT$(HHHK HL$(E3DcXHVHKHLC]AT$TH1A;u
$C\fDd$(fDd$*Ld$0HLD$(HP@A;tHL$(uP/f|$(@u
CXHD$0HC0HL$(OPD8cXu[fDd$(fDd$*Ld$0HLD$(
HP@A;tHL$(Pf|$(@u
CXHD$0HC0HL$(OfDd$(fDd$*Ld$0HLD$(HP@A;t>f|$(uHKHT$0'Dc(HC fD
BHLD$(HP@A;tHL$(yO3f|$(uHT$0HK
HL$(QOfDd$(fDd$*Ld$0HLD$(RHP@A;tHL$(Of|$(uHK8HT$0{HL$(NLH$HAS(A;LcpLchLc`AD9vqLDd$x
LLxLHxD8d$xtCYH$HC`DdxLLxAHCD8d$xtHChHCp;$rL9chtCZC[3H@A_^][HUH HHM(H]HUH
HHM(H]HUH HHM(H]HUH HHM(H]HUH HHM(zH]HSUVWATH 3IH9ZLv,3HH0ID$HHIuH;_rH`H
A_^][HSUVWH(HIHIHDLHauH?HLHyatHrLHcauHHSHHH#HC3@H(_^][HgHSUVWH(HcYHt>HHHHH,HM
HtHPHEHMHP u
uG(3H(_^][HSH AAHu"HIP5HKHtHPH3H [HSWHHD$
HHH9Q(HL$0?LC(LD$0HI+HD$8H{(H$3H;tHT$0HDHHK@HL$xHLhHLP}HL$@s-H9Q(v'39Q t I
HCLDA8P|tHI+I@H{(H_[HUH HHM0FH]HUH HHH]HSUVWH8HD$
HHHHHQ3;|AHD$pH;tHHBHHKLL^LL9[(sL[(H8_^][HUH HHMpfH]HSUVWATAUAVAWHH$IELE3L9qu
@A;u3HL$0AIL$HHT$@HT$(HTHHT LL$PAP
A;gAHT$PI;t|$@QuHL$0zfDt$XfDtZLt`I$HLL$XAP0A;D$Xf=uHT$`I;#HL$0fA;
Dt$8HD$0fD0HLXIDDDILHLLDDPA;D9HL0A;||SHL$0HQHL$xHD$pI;tHT$xHbIL3HHL$x;\$8s\$8
HD$0fD4HHL$0}A;}0HD$pI;tHT$0HIL3HA:toNHHC9CuHDCHfBB:EfF4BDCVHHTHD^HJ\I;tHHF|$DD;tkAD
9$ADt[IL$HHT$@HT$(HTHHT
LL$PAP A;HL$0`7HL$X:GA@HL$XGHL$0*@HA_A^A]A_^][HUH HHM0H]HUH HHMX|H]HUH
HHMxBH]HUH HHMpH]HUH HHMp~H]HL$SUVWATAUAVAWHXHD$(IDIX@H\$ 3I@0H$HHfAH(I@
fAHBAHIfA@DIpLcfID;t3HAHHH(H;tHH3D;uH_@NLOBAALImGBLCALAIEHHO !A$tLA?AI$@2@8+-
@ILHHLLAP8fD0fD$2HD$8IHLL0AAP0tD$0f=uHT$8HtHO0ft@a@2D$DwDAuG(HG fAGLG
AItMAItHL$0DRHL$0DAu@t[HxM;_(s
_(HO fYKHHHHO0tKHf<H\tf<H/t3AG@HLDGLAI6Au @VHH|LIxQSIRH$tSHf<Q\tf<Q/t3AHD$
;_s_HfXu2NHH\HtHHFHHO ^3HXA_A^A]A_^][HUH HHM0fxH]HSH0HD$
HAAu>HL$@HI@HtHPHK(HtHPHK HtHPHI3H0[HUH HHM@H aH]HUH HHM@H(`H
]HTHLSUVWATAUAVAWHH$LLH 3H$H;t HPIIOH;t
HPI_IOH;t
HPI_A_
A_!A_"A_#A_$A_(I_0A_@IG8fA_PIGHfAG,AIIHHL`f.H$;|HH$HHHL$`>LH\$xD$$$`IHHH8H;tHH$8I$A
9Xv8AI$;HrA8\$tID$
HXHX$h\$6Attp;|:DD$6HL$x9L$0>$`Ll$xD$3\$0\$8$h\$5HT$`f:ztf:Ztf:rtf:Ru=HJf;t1f-0f= w'Hff;uf:zt
f:ZtDD$5D$hI$E3D9PPMD$hH@ItE8uE8T$uHHNE3A:ID$0EPA;t"LAA;tA;}DAD;uA;HL$83:BEL$8HT$`H;3;}
LD:tHQHN3:u18L$5tHHN3:uHL$xR8E3EB/DHL$xvAAD;E3AAAII$;Xl$0Ll$xDID83H;u7A;t'H8I/HL$`$@6
ED$H;AHHE3E33P
;t"HIHL$`C6HH$H$H\$H\$P\$T9\$0HHL$`:HHL$`*:1HHL$`:HHHHHH`L$`HIL$83;t33HHIHL$`N5H9$`u8
3HHIHL$`5HD$(HD$ LL$H\$0DHT$xI$L$`L\$(Hl$
LL$HDHT$xI$W3CH$$$D;t7AHHHDD$LIH5N3LH$I$$h;PH$H{HH$HHxL$xHIL$8S3;t33hH_HVIMHL$
`B3HxRar$!$$$HH$LL+HB8uTHHr} suaE
t\$hATHL$HJ4ADD$0;|

D$0(!D;v&AA}tAUHL$H3IH+uHTHHLxRHL$HHLl$xDl0AH\$H\$P\$TD;t.AHHHDHDtPDt$TLIH
L3LHTHIH\$H\$P\$TD;t.AHHSHDHDtPDt$TLIHK3LHT$HI$;~;|ATADATAD3;t@8t$8u@$h\$0$h\$0HH$
I9t$8IL$8HL$3DBP 3;t"HIHL$`.1IL$8HE3E33P
;t"HIHL$`x0H$3IIHHHHH;tHHHI9pv@4(I$;HrH$8HcLpH@A8t$uA;M$t$8;ZIL$0AD@t$4HI$HHHH$9\
$8rHDD$4I9t$Ht#IL$HHL$3P3;3I9t$8t!IL$8HE3E33P 3;3H$ L$ EII3;H$
3H;u\$0H<IT$8H;tDD:u@8$`tHXHHHMLHL$HPDQH$PHL$PH~HH$PH;HIT$@PDH$PH;tHPEE3HI3;H
D;E3E8w#tAO$u@AE8t$tA:u
A(D8t$4tHHP\$03N)D9t$89L$0AAG,AG
AGXAG!AGYAG"AGZAG#AG[AG$AG\AG(AG`AG,AGdIG0IGhIW8IOpIWHIE8t$u'A:t&H$3@:3
333HHP\$0a3D;A@8t$6tMt$#@848tMtIDMt$@8$hLEAG
$@:t?A8t$t8MMII3;$3A8w"EIH;~B@8t$4u$@8$`tA8vuA8vtA8t$tI;L$ vHHP\$0@:t8H;~A8vu-
HHP\$0]A86uHHP\$0DH; @:H9$@H$y@:HHP\$0D$8D$8H$0HH$0;LpHvHmIdHL$`Y+HKHBI9HL$`.+H$
H;tHPHHIHL$`j+HHPHHIHL$`@0+HHPHHIHL$`*HHPHhH_IVHL$`K*HHPH.H%IHL$`A*HHPHHIHL$`T*H
HPIH;tHPIHHPHHIHL$`*I9t$8u0HuHlIcHL$`X);A8t$A8t$t;I9t$ H HIHL$`y)I$HHHHT$`H!;|Et7@8$`t-
HHIHL$`)8^LH$H;t.pH`IH6IFIvAv Av$Iv(Iv0LHLL;t
IIP0LH$H;tpHfIEH;IEIuLH$L$L;tIEIPI\$HH;t HHPIMH;tHPI]Iu
Iu(HHH@$`HH$H$H;sHH$$`3H;t*3H$H;tHHHHIL$8LE3E33AR 3;tRHIEIP3L;t
IIPHHHLxHL`7'HH L$ HIL$8m3;tRHIEIP3L;t
IIPH\HSHL$xHHL$`=&H$ 3H;uUH IEIPL;t
IIPHHHLxHL`T&$`H;BLpHHtHtPtTD0$;H|$xHHDtpHD/DE3E8|-
I$HHJ4HHN]A:tADl$0H$3NcHFI;tJH$ H3;*u D: E3HL$H~T$0H$D8~AtAAmD$`HH$HHNA:u2$`AA$`H$HH$
rAAAA:t.D9~Pt(AuAuAu
D$`D$`A:FPA;AD|$8A;MD$`Dd$p3}HFHMLMQDFDCH9$ s
D:uDJMI @;tT$8T$8I;VPrT$8H$HLXDDtpHD$`;VPt4E3HL$H
}T$0H$3NvD$`D$`3D:tAHL$HT0H$N?T$0H$,T$0HT0HT03HH$H+H$,;|D3HL$Hg|HD$HHDH`9t$P
H$8L$LPLHAHIHHHH$I9t$Ht#ILHHL3P3;T3IL$8HE3E33P
;HLEII;H$3H;uH$(A8tLl(IDHHD
L$(MD$8HA;uA(HHP3c3;E3E3HIm3;3I9~HHP#H$@KIH$@IHHA8AAD$H;}HH$HH$@A8t$A8@8t$6tID$
H$2@:tID$HIDIL$@8$hHEH$A8w <A8t$t.ILIIX3;rA8O"
3H$@0H9$@}H$@3:E@:3A(H9$@u[@8$uQIH9$Hr@HHxxH$`kH$P^HHPuL$@H$H3;H$@I&H$H$xH$`H
$PHHP3D$`AD$`HHHD;D$P8dHL$HH$IEIPL;t
IIPHkHbHL$xWHL$`LHL$H<H$.IEIPL;t
IIPHHHLxHL`gH$H;tHPHL$HH$IEIPL;t
IIPHHHLxHL`xHHPHLH^HPIEIP3L;t
IIPH+H"HLxHL`HHPHLHHIEIP3L;t
IIPHHHLxHL`H$H$xH$`sH$PfHHPHL$HQH$CIEIP3L;t
IIPHHHL$x
HL$`zIt$8E3I;t HHPIOI;tHPIwHHPII;tHPIH$H$xH$`H$PHHPHLHmH_IEIPM;t
IIPH<H3HL$x(HL$`AH$H$xH$`H$PHHPHL$HH$IEIP3L;t
IIPHHHLxHL`yHHLHaHXH$$$H$$3I9HLD3iAHMICIDDxP9xPv-
LH$uA;t$PrL$X3oII$;XrH8HHLPL$HLLH$LH6DCH$6;rsI<H$.H$ IEIP3L;t
IIPHHHLxHL`R3H\$H\$P\$THLH$L3H5$;6Ht$xH$LH$`LXHE3KLcE;AH$(IHHLE8qAAAEEE9qPv^IIAH
HHxs#HBEB!BF!DHE;YPrLXHL$`A:t'AHL$HKL$(CLL$`HH$L+L$`H$8LLPH$XHK8HE3E33P
3;I*HLHIHHHHP3L;t
IIPHHHLxHL`-
`vHHD$@3H;tPHHHVHVPHVXHHT$@H$H;tHHP3H[8H;tHHP3HNH;tHP3H^HHH(H$XH9PHHHH$3H;t1
HHTHH"HBHHBHJ

HJ(HJ@HH$H;tHHPH$HH$(H$XHHHH$P3H;tHPH$3HJ H;tHPHHPHB HHLB(HL$IH
HI@HH$X3H9PHHHHHL$3P3;H;tHHP3HL$@H;tHPIHL$HIHHH$HP3L;t
IIPH^HUHL$xJHL$`?Ht$@H~X@t.HNPH;t3HVPHVX@HFPHFX@E3L$AI$PAD$`MLMLMLHXD8Ju>A
9N u8IFHD8I|uL9 uLQL$AAAL$E3II+H$(A:I@@I+H$pOHJ8HE3E3IP
3;LD$@IPPH$HLpHXHH8kE3A;/H$pI;uAL$LKHAA$`Ht$@HXL
LLLHt$@HFPHF8HN@L$LFHH$XL$$`L;I+AI;w]IH+LD$@IHPHH+IL_/LH$L$ML+L$Ht$@H$X$`E3`ML$
ML$AIH+H=sfHL+L$LD$@IHPHM.L$LLHt@HXLL$$`E3L;DL$4I;AF L$IA0MQ838Bt
A~ wD$4D$4H$HL;rSIE33IP E3A;
LLHt$@H$XH$(L$L$E3KL$L;sMH$ML+L$ I
AM;HH%IL+D8JuH$XL;
I+L;LGLLVPM
L$OE3L$(D8$PujM;s&HAB< uLM;rL$H$8MM+L+L$(LMLM;L$$`E3A$B
L$0D$DHDHHhHXLHDL0P)HDL0DL0PDL$0DL$0E3A:t>$;D$Ps1H$hDDD$pAAHHhA
ALEDDpF+EDDL0PtC<(nHHHNAADL$L;H$$P:IAHNMPH$(IH;$
DL0PM3LL;v+IM+A8$PLHM;r3DDL$pL$HHHNL$03D:t
H9$tABDH;$uA8)H$$PoHH3D:u(ABDLH;H$2IH+H$3MBL;tmH$H;reH+L$I;sZIH+HNPAE3A;u
D8$`H$XLHXL$
E3E3E3D8D$6tHBH$0E89tHBH$HBHJD8$hHEHLLDDpIRE3A;S
H$I;H$HL$I+L$H$0EHAtKHD$@L@0L@ L@HP(HHHE33P
HD$@LE3E3H$HAS H$5HD$@LP0LP L@HH(HHHE3IP HHE3L$0I;tAF H$HB0HJ8H$L@
HH(D8D$4tHH$XHIHHD$(HL$ L$0LD$@HH0 L$LEDHI(D$83;H
L5Q;H$X8HuQH9$@}A8O#t
AW$
HHPHt$@H$XHLDL0P_A8O#tAW$uA8t I9uCHHPHt$@HXHLDL0$P
T$5;8H$HHHL$0AtIH$E3E3M9MML+IAD$XA:t"IHI;vMID$XI;YII;FE3D8L$5t$u AI$DIE3D$
$E3H$I;}HHILI;A:I;H$XD8@H$HHH$HHPHt$@HXHLDL0PSHXHXHXD8@t
H$E:t
HHHLIHD8E3A;
L$L;$H$XD8AE9F AN IFHDHI;H@HH$JH;w}AN IFHDH$PI;tTHHHaH$PHI8PH$PHH ?H$PHH.H$P!L$E3AF
E9F QHXHXD8D$5}D8AsD8D$XhD$AW $PA:tYD8AtSMML$HID$8E3A;<
$PE8G"AEL$HXDM;t&E:uXA:tL$ACTL$ACFA:uLLAH$@AAM$LDH$@}EE$b
3D:uNE3A:uFE:tM;u<E9F uE8C<
*A9F uIFHD8A\tH$XD8A
DpHIZH$=~H$0~H$#~H$~E3I;t7HHPL$E3AA*L$LL$E3
L$D8$t:D8$t0$PHt$@H$XL$L$$`D$PHt$@H$XL$L$$`E3I;t
HHPHL$@I;tHPI)}HL$H}I}H$}H$|H$HPM;t
IIPH|H|HL$x|HL$`|.I;tHHPE3HL$@I;tHPI|HL$Hx|Io|H$a|H$S|H$HP3L;t
IIPH*|H!|HL$x|HL$`|HHPHL$@3H;tHPI{HL$H{I{H${H${H$HPIIPH{H{HL$xz{HL$`o{D8J6A9F
PHI*E3E9N I;tHHPE3I;t
HHPI {HL$HzIzH$zHzHHPIIPHzHzHL$xzHL$`zD8$PuA9F KH$XHH8H$XI;t
HPE3IN0I;tHPH$XIF0IIPI3H;tHPM73AO
AO!AO"AO#AO$AO(IO0AO@IG8fAOPIGHfAAAIH;tHHP3H;t
HHPIyHL$HyIyH$yHtyHHPIIPHRyHIyHL$x>yHL$`3y3I;tHHPE3I;t
HHPIyHL$HxIx3I9u{H$xHxHHPL;t
IIPHxHxHL$xxHL$`xH$rxX H$^xHPxHHPL;t
IIPH)xH xHL$xxHL$`
xH$w3H$I;t HPE3I;tHHPE3HL$@I;tHPIwHL$HwIwHwHwH$HP3L;t
IIPH\wHSwHL$xHwHL$`=wI;tHHPE3HL$@I;tHPI

wHL$HwIvH$vHvHHP3L;t
IIPHvHvHL$xvHL$`v@HHP3H;t
HHPHL$@HPI[vHL$HPvIGvH$9vH$+vH$HPL;t
IIPHvHuHL$xuHL$`ut$8\D$pAG,AG
AGXAG!AGYAG"AGZAG#AG[AG$AG\AG(AG`AG,AGdIG0IGhIW8IOp.IWHIHHP3H;t
HHPHL$@HPIFuHL$H;uI2uH$$uHuHHPL;t
IIPHtHtHL$xtHL$`tFHHP3H;t
HHPHL$@HPItHL$HtItHttHftH$HPL;t
IIPH?tH6tHL$x+tHL$` tHtHsHsH$sHHP3H;t
HHPHL$@HPIsHL$HsIsHsHsH$HPL;t
IIPHXsHOsHL$xDsHL$`9s@H$!sH$sHsHrHHP3H;t
HHPHL$@HPIrHL$HrIrHrHrH$HPL;t
IIPHsrHjrHLx_rHL`Trt$8H$=rH$0rH$#rH$rE3I;t
HHPI;t
HHPHL$@HPIqHL$HqIqHqHqH$HPM;t
IIPHqHqHL$xuqHL$`jqt$8E3HI8H$XI;t HPE3IOI;t HPE3H$XIGI;tHHPE3II;tHPI7D$pAHIHpHpHpH$pE3I;t
HHPI;t
HHPHL$@HPIpHL$HtpIkpH$]pH$OpH$HPM;t
IIPH(pHpHL$xpHL$` pAHoHoHoHoHHP3H;t
HHPHL$@HPIoHL$HoIoHzoHloH$HPL;t
IIPHEoH<oHL$x1oHL$`&oH$o@HA_A^A]A_^][HUH
HHcH]HUH HHM`FH]HUH HHMx&H]HUH HH8H]HUH HHH]HUH HHMHH]HUH HHHH]HUH HH H
]HUH HHPH]HUH HHH]HUH HHH]HUH HHH]HUH HHH]HUH HHMHH]HUH HH#H]HUH HH@C~H]HUH HHH
]HUH HHcH]HUH HHCH]HUH HHMH&H]HUH HHH]HUH HHCH]HUH HH(#H]HUH HHH]HUH
HH#}H]HSUVWATHHD$ HHE3A;;L9#0HKI;t
HPLcHKI;t
HPLcHHLCHHHLCHL BH}A;LAH}A;L?H}A;L
@Hf}A;rL[HI}A;UL
]H,}A;8HHL$(bHL$8W{f.HL$(tA;|HHD$(HHHL$8|HDgHfD
A;HHEHHHD9c0u\HL$hzHHL$XzLLHT$(HL$HHH|HL$HjHL$XiHL$hiIHT$8H&nA;ALHC(LMHHT$(HL$hH
HZ|HL$hiHL$8iHL$(i3HA_^][HUH HHM(H]HUH HHM8H]HUH HHMhH]HUH HHMXH]HUH HHMHjH
]HUH HHMhJH]HSUVWATH0HD$ HL3H\$p3H|$h@8z`t=K^hHHtXHzH3Ht HHPH|$hH}@Hz8u~xhHD$xHt
HnH3Ht HHPH\$pIIzHK(I$uH6Ht HHR9H]8HIA$Ht
HHRHt HHPH0A_^][HUH HHMpbH]HUH HHMhBH]HUH HHMx>gH]HSVWH
3H@q@@qA@qB@qCqDqHHqPHAXq`f0HAhqpf0HA0q8f09qt)KHH|H;tH)HfC9suH
_^[HSUVWATAUAVAWHHH$HHYHF(A D9`r
@]3A@DD$0@l$8@l$9@l$:@l$;@l$<D$@D$A@l$B@l$C@l$D@l$EHlHD4D$5D$6D$7D$>D$?D$=HNH
T$0MDH^(Cr^9Gs+GHHHH$MHHHM@8$zHNH$M
D9gZW;H$0HVhH$wHVhHwHH$0j;dHD$PH;tH$0HHHH]H$0'jHLd9SEGfl$0fl$2Hl$8I$HLD$0P@;f|$0a\$8I
HHP(;};HL0DHIHLHH;H;Hl$
HLD$ PHL$;H;Hl$(HLD$(HHL$(;HH;?H$
L$IE3A;L$EEVI$JuA;HNHI;t>Ll$PHLD$PHHL$PI;tHHPHLPI;tHPLlXDl$`Dl$dHL$pX%HFXH$HHD$pH
L$xHVA ?D$DHDXHDHD$(H$HHPuL^HL$HFPH$HTpH HF(xA'LLII;KbHD$PH;tH$
HHHHZHaHLXaH$ HL(H;tHPHL$ H;tHPH$H;A
7HP,$G@$GAGBGCGDGHGLHHGPHOXH$sHOhH$sHO0HsH0HL0HL$0H$ #HL$(I;tHPHL$
I;tHPH$I;tHPpH$ #HL$(I;tHPHL$
I;tHPH$I;tHP&$@G@AGABGBCGCDGDHGHLGLH$PHGPHOXH$XrHOhH$htrHO0H$crH$_HL$X_
H$ "HL$(3H;tHPHL$ H;tHPH$H;8HP-H$F_HL$X;_H$ E"HL$(HtHPHL$ HtHPH$HtHPH$^HLX^H

!HL$(H;tHPHL$ H;tHPH$H;tHPvH;tHPHL$ H;tHPH$H;t@HP8H;tHPH$H;tHPH;tHP
HL$09oO(AHHA_A^A]A_^][HUH HH0 H]HUH HHMP]H]HUH HHM0j"H]HUH HHkH]HUH HHM NH
]HUH HHM(.H]HUH HH ? H]HUH HHMP
H]HUH HHMXnH]HUH HHMpH]HUH HHMP\H]HSUVWATH`HD$ IHHHA I\H$Ht
HH3H$Ht
HHPHHL$(lHL$8lH8u-
`u'LD$8HT$(HOhLD$8HT$(HCHKHHWhPH_HH_PHHM)tHL$8[HL$([393H<tiHLHT$(HL$HkL6[H$Ht
IHm3HMHSHL$H/[LI3HE
H;xHL$8[HL$(Z3HHPH`A_^][HUH HHZH]HUH HHH]HUH HHM(H]HUH HHM8H]HUH HHMHH]HUH
HHZH]HSWH(IeHt
HHRuH(_[HSUVWATAUAWH`HD$
IHLIcXHE3A;tHHHEHYA;uDmAD9n0AHHHf._<A;ALA;}~DD+DHT$(H&D9l$0D|$8Dl$@DlADlBDlCDl
DDHDIDlJDlKDlLDlMLlPD<D$=D$>D$?D$FDGDELD$8HT$(IsA:t[XHI;tHHT$8A
IHH6Q_HL$(X;^HL$(pX2HL$(bX2H`A_A]A_^][HUH HHM(H]HHHSH A A HuMHW3H [HSH0HD$
HAAu(HL$@HI0HtHPHK~HW3H0[HUH HHM@HZ~H]HcHWHKHgH(LH
sHDAMYAAJAJ#$E-ACA#$J-A#I$K-AACys$ SAA
s
$SAA s
$TAA AAA
tIQAAA
OH(HSVWH H@3@t
%?3H{rAADtD
A.tADA#HHrt HHvOHhH@tHK H _^[HSWH(3HHf:@u!HJ9Au9EH}A AA0t_A5tIA[f:JH,MH-
HOHTHH#yMef:uJRUf:uJ
f:u/HJHS0Cx"N.f:uHOcNf:t
HHR^H(_[HSH`f:HuHRhHL$ HT$ H[hH`[HSUVWATAUH(AHHAsHz`9:t HyrA;wAzjAzBsHHcpH
uCA;RLp3I9tHrHITu%uhRLr3I9tHrPuCu>E3HrE3LW3A9uHAIH|At7AH
ArHxH_u!uH`HRHHxHZ_Ed=CA9CuH
]DKLE7
M0BACACD9KDKuH\DKLAD$7A
AL$0BICAICDKnt:HC9CuH|\SH
-
SH_HHuH(A]A_^][HSUVWATH BMAD$pHHt5D$x4iIHD^;s+rf<3uT3H^H
A_^][HSVWH0IA@IB=Hf9tHw9QvHwH]DQLL$XD+AHHH6i9wHvHv]WDLL$XD+GHL
vDHHD$(D$ L
vDHHD$(D$
HhH]HXvH]H0_^[Lf9I;DD+AB<HABA;AH;DD+ArzB<HupABA;tWA@t'A9PsJDE+HAr=A@BLA;t)A@t%A9P
sA+PrA@BL;u2HSUVWATAUAVH`HD$
IHA@IfD$8D$<HL$@`HL$P_LLHHL$8Ott|$8tHuH"dHT$(HL$8HHcHL$(NHL$8uHJHcHT$PHcs@2D&nE;
v@2nf~t@2_3LmIxD9!tHrxHITH[c)H&tHJcHT$(AGHT$(H0cHsH!cH{cHfHE3AD6EAAC9CuHK\DKLAT$0A
D$7A
BfCHDfCHDKAD9KuH\DKLAV0AF7A
BfCHDfCHDKDHD;PHsH7bHL$PMHL$@M@H`A^A]A_^][HUH HHM8HbH]HUH HHM8H]HUH
HHM(&H]HSVATAUAVAWHHD$8Lzu37H$HLHHu3E3Ld$X3t$`t$dA^[HKH$Ht)LxL-Ll$ L
DVIE3L$E3Dl$0E;nbIJfDhfDjHD$pHHL$HT]{ugTPHDHDDPfA-
u3HLhTPfA+uHLhfTPf|$htpT$PHD$HfPXH$HKiHH$f8u({t"H;wHL$h:HL$hZHSHL$h6JHD$@HtHT$HH
m\3HHL$XHCKDmIHT$hHL$HJHL$hcADl$0t$`Ld$XE3Ll$HE3DLPDLT3\$0;s#IHHL$HB\$0DLPLlHH$
HMIPDtXIJt$4t&t$4IHtHIHIIIH$HtHPAvIIMt"L

[EGIIOIt$IHtHdIH\IuIPIH$HtHR3HA_A^A]A\^[HUH HHH]HUH HHMXvH]HUH HHHH]HUH HHMhj
H]HUH HHMHH]HUH HHM@jHH]HUH HHMHH]HT$SUH8HHHt HYL
DHH33H8][HSUVWATAUH(MAD?HA;DT$hwOA;sL$HHJA~LLHL$hIAyA$;vDT$hDH(A]A_^][HSUVWA
TH0HcYILvTH1HNMDHl$ =uH|HNMDHHl$ wH0A_^][LIHAHHH iHSUVWH(y3H;zHZv H
FHHH^fF~H;~t#HvH;rHLHH(_^][HSWH(yHt>KHHLH D[HJ|HtHFHFC{uH(_[HSH
HLA HQHD$0VHH [HSH E3HD$0cHH [HL$SH0HD$ HHKH0[~EHUH HHM@HH]HL$SH@HD$
HytHHL$(UHL$(t$C9D$0uHHL$(t HlHL$(DHH@[DHUH HHMPNH]HUH HHM(.H]ALrEtAtAuHA HIJ
HQHQMIBIBtIAB3HSUVWHHHD$ AHHVHVLF Hhu3^8HF0ff.HN Y3;N(;uF(N(HF fH^8HF0f\MHF
HHHL$(THT$(HN0I:t HT$(HN06V;n(sn(HF fh
^8HF0fHL$(hCHH_^][HUH HHM(H]HT$SWH8HD$ HHDPDP8t3C9CuHQSHfQ.fQSHW0HWHH8_[HUH
HEPt
ePHMXH]HT$SWH8HD$ HHDPiDP8t3C9CuHPSHfQ.fQSHW@HVHH8_[HUH HEPt
ePHMXtH]HT$SWH8HD$ HHD$PHQXHrSD$PHW HvV8t3C9CuH
PSHfQ.fQSHW0H=VHHrVHWhH"VHH8_[HUH HEPt
ePHMXH]HSH y$t238Y u
e:H [LL$ T$SUVWATAUAVAWHHHD$ IMLM L13AA<$fM+ff;t}=.u\fasf"fzwsfwfHfasffzwGfwf
f;tf\tf/u%f\tf/uIfC&fA<$f\$H$MfA<iu2IUf<jtQf<j\tJf<j/tCt?Ef<B\t5f<B/t.2YtSIUf<juHfA<i\tfA<i/u8EfA<Au-
LjIWHL$(OHH
RHL$(W?2HHA_A^A]A_^][HUH HHM(H]HI?HSWH(HHQHSHOQHS HO
QHS0HO0QHS@HO@rQD[PHSXHOXD_P]QHShHOhPQHH(_[HSH HHI>HKHtHPH
[HSUVWH(HIHIHLHt,H1LHtHLHu
HC3@H(_^][AAH(AAu
HP03H(HSWH(H~eHHHI HtHP@tH=HH(_[HSVWH0HD$ HH0G=HHD$PHtILLHVHPHVHHOD^ D[
F!C!F"C"F#C#FCF(C(F,C,3OHHGH0_^[HUH
HHMP<H]HSUVWH(HcYHHt,HHHH,HtHM<H<uHH(_^][<HSVWH
HI9Ku}tE<3H;Ht3F;|/HODBQQQDADADA
QHyH9StCHHLIfH<H;sH _^[HSWH8HD$ IHAHQ8HHt HHRHHtHPH3H8_[HT$UH HHH]HL$SUVWH8HD$
H
Hc{Ht,HHHH,HtHM6;H.;uHH8_^][;HUH HHM`H]HSUVWH(33H9Y0v'3HG(HH Ht
EH;_0rH(_^][HSUVWATH 3LH9Y0@v03HF(HH HtHI@"H;^0r@H A_^][HSUVWATH@AxIHHv2E3E9`tIED$
HHeD9cHKHT$ A D$ Dd$(Dd$)Dd$*Dd$+Dd$,D$0D$1Dd$2Dd$3Dd$4Dd$5Ld$8D$$D$%D$&D$'D$.D$/D$-
tHH
>A;C|H@A_^][HSUVWHhHD$
IHHHL$(I39~}HMHL$(L9NHCH@8y@uHT$HgHHL$(KHL$H8HT$(HL$8J@8tH7LHL$8yLHHT$8TKHHT$(
CKHNPDH89~@vEHHF8H(HK@HT$8KHKPHT$(KHKDHH;~@rHL$848@HL$(&8@Hh_^][HUH HHM(H
]HUH HHMHbH]HUH HHM8BH]HADHNHQAy tHAy!t
HBIHB HBIHBHIEA IQHH`pHSUVWATH HcyDHHt,HHHH,HtHM,7H$7uD;cCv)H7IHHC6DcHH
A_^][HSVWH0HD$ HH06HHD$`Ht9HHHOHKHtHPHWHK?HL_ L[ HG(HC(3HHH0_^[/HUH HHM`J6H
]HUH HHM`H
H]HL$SUVWH8HD$ HHcYHt;HHHH,Ht HM5HMHtHPH5uHH8_^][5HUH HHM`"H]HSVWH0HD$
IHHHOH@45HD$PHt
HHG3HOH-u@~
H0_^[HUH HHMP5H]HSUVWATAUHxHD$
IELHMtAES;S0tHD$@HL$HVEHL$(KESHL$(J|$0sf0HL$(ZK|$0rLCH3HL$(M0-
4HHt@H;2HHG3HH|$8HtHPHL$8HT$@HtHHPHL$8H|$@HT$(HA(HE3~H{X3H$HtHT$(HnE3HHK,HD$XH
D$`HT$(HL$H.FHK(HT$8HL$(m3HL$Hb3HL$@HHPHC(H<K@;rAHC8H,HCH;rFH+HCBCGjHL$(2HL$H2H
L$@HtHRHH;G

t HOHE3E3P HCHG ILL+g I;DBHOHL$EIPuj$HG $HHCHSHCHCH;C vHC H;W(vHW($HtH9o
uCHCuEt@3HxA]A_^][HUH HHM8HH]HUH HHM82H]HUH HHM(2H]HUH HH1H
]HSUVWH(3HH9q0HA(H(HHM(H;r
H+;s0r|HMHHP(H}(aK0HC(HlHMHtHPHEHM}K0HC(HlHt HM0HMHtHPH0C0;s0rHCCH{
HC3H(_^][HSVWH HIHIHHAtHHAHu
HC3@H
_^[HSUVWATAUAVAWHH$ILLIcXHE3A;t.HHHEH4I;tHN/H/A;uDeM.IEH$0IP(A;g$0HEHL$pO@L$DDH
$)@H$@D$D$$AD9$0HL$0?Dd$@Dd$ADdBDdCDdDDLLD$pIwA;>HT$pHL$0AD$D\$@DAAAvG3I
HHL$(HT$pt
$(t@AHE;grE3AE3@t$DLLCLD(InRA;LLBLD
IgQA;fDd$PfDd$RLd$XIELL$PA(IP0A;uxf|PuDXD$LA;|{vHL$P|HHT HHHL$0-;$0\HL$0-RHL$0-EHL$0-
8HL$PwHL$0- HLP_HL0l-@AHL$pCHA_A^A]A_^][HUH HHMpzH]HUH HHMpHH]HUH HHMpH bzH
]HUH HHMpH]HUH HHM H]HUH HHMPBH]HSUVWATAUAVAWHH$MI@LHD$ yL$@MtwAy
@jI]H$ Ht
HHPHLD$ HHtHRH@H sH$@HHP,LD$ HtHL$ HtHRHL$ HuHRH@HrH$@eHH$ P tHL$ HtHRD$
A\HFH3A8:t"uHL$ H;tHP@:A88t#uHL$
H;tHP@Ht$8t$@tDHt(t$0t$4HHHHHHHHH$
H$(H$0H$8H$@HHHPHXH`HhHpHxHHHHHHHA9P[$
L$PA9vLHt$`IIHL$x=:38K$iA9PSHIHL4(LL$xLCS
Ip3:8K!tHHK<D3;|6DHT$hHKLL$xLD$h3I)udHL$h(3HA;Pw$ Ld$`L$@L$P@$
Ld$`L$@L$P@$9$uHL$x7$HL$xfA:fQ$AL$HCHHHL$x<HL$hC($ Ld$`L$@L$P3$
LHLILJLKDLDPDTLXC(LILHDJLKD$P@:taD$ILL$KAIMx93;Nx'H$ H;tHT$xHB9HHHL$(
D$T3H$HL$HHHHAHBABHL$8D\$@KHD$8HH$ApD$@HL$x'$
ILd$`A;~TL$@H$H$`HL$x&Hc\$0H;t/HHHD$(H4HtH&H&3;uHL$(&HL$8&HL$
H;tHPHtHtPtTLLHLPIH`HuLML$L$H$H$H$pH$@MH$L#LD$8IHL$H}HL$H%L$@H$H$`;9HD$8L@
DA@@:A8pAxAHHG0HBPtA80t
HH@8r`t*A80t
H$ HHHA80t
HHHH$rA9p|lH$H$(@:HDAHIH@8p tA80tHB09H4@8p!tA80tHB@#HBHHB
A80tHB8HBHHBIIHH$HP(;thHc\$0H;t/HHHD$(H4HtH=$H5$3;uHL$(%HL8HL H;tHPJ#H$
H;tH3aLHLHL$L;tI$IPAAAA AA$
I$L;t-I]H;t HHPI$H;tHPIIHhIA8A$A<A$
A>AMMHD8IHXI$A9Pt
HD$(I$HHHL$HA8uqHL$hB3HT$HIPHT$hHZp@:@HL$Hi"@:t $@HhH$@CHL$hrHL$h/"HHHt$`H=
A9`qA8tX!LH$H;tpHIEIuLH$L;t
IEIPIL$0o!HH;tpH8H@HH$H;t HHPH$H;t HHPHH$HL$H1E3E8nPtCA;vINhDiHfD(6HT$hI%HHL$H3HL$h
&HT$xIHHL$H3HL$x HT$HLk(E3HKOA:u!PuVE8nPtPX@[H$ 3H;tHT$HH#2HHH$pLD$HHIH$xHL$H,
HHRHHRI$IRHc\$0HA;t.HHHD$(H4I;tHHA;uHL$(HL$8HL$ I;tHPHL$H
A8tvI$IPHc\$0H;t/HHHD$(H4HtHTHL3;uHL$(<HL$81HL$
H;tHP@^L;IIH;~vI$IPHc\$0H;t/HHHD$(H4HtHH3;uHL$(HL$8HL$ H;tHP@
`NHH$ H;t-pHPGHHs(s0s4Hs8s@sDHKH.HH\$`H;t HHPH$H;t HHPHH$IXHK8XHT$hIYHHKH0HL$hCP9CTu
HKHP,SPHKHfQ.fQSPH$pHCX3sHsHsHs IW(HL$;H;t
HHRH;t
HHPI$IPHc\$0H;t/HHHD$(H4HtHH3;uHL$(HL$8HL$ H;tHP*A8xH$ H;t
HHHHl$xH;tHEHPHM(IMHEH$xDHtHUHRHt
HHRHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHHuHL$(HL$8HL$ HtHPA3H$ LA9`uH;t HHPHH$ R0YLH;tpHkIEIELL;t
IEIPIL$ HT$hIHT$hIE(IME3LD$hHDH$xhDHL$hIUIRHtHUHRHt

HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHHyuHL$(kHL$8`HL$ HtHPA HL$h=HMHH$PDHt
HHRHUHRHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHHuHL$(HL$8HL$ HtHPAHHLIDHt
HHRHUHRHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHHuHL$(HL$8HL$ HtHPAE3HHDHt
HHRHUHRHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHHuHL$(HL$8HL$ HtHPA*MIDHt
HHRHUHRHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHVHNuHL$(@HL$85HL$ HtHPAdHt
HHPHEHP3H$A8&L$@L;I9IMHE3E33P
;H;t
HHRH;t
HHPI$IPHc\$0H;t/HHHD$(H4HtH\HT3;uHL$(DHL$89HL$ H;tHPiE3MHIM;H;t
HHRH;t
HHPI$IPHc\$0H;t/HHHD$(H4HtHH3;uHL$(HL$8HL$ H;tHPA8tH;t HHPHH$0HHt@H<HHF 3Ht HHPH$Ht
HHPHN HtHPH^ IHF(HFHFH;t HHPHHHL HMDD$@HPD$@Ht
HHRHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtH HuHL$(
HL$8HL$ HtHPA.A|pHxHMH'=IT$xHM >Ht
HHPHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHNHFuHL$(8HL$8-HL$ HtHP@ZAA$
$$E3D9|$@3L$PL$`HD$8|(tL(xID$0L,I|(t9L(x1IL,IMt!IH$ {yIEH$ AHD;|$@r$ LD@uH$ H tPH$HtHMH$
3~Ll$`8Ll$`Mt.H$ IFHLl`
HLl`H$@HtH$@HWL$@H$HtLXM] H$LHt
HDMtItDHt
HHPHt
HHPHt
HHPI$IPHc\$0Ht-HHHD$(H4HtHHuHL$(HL$8HL$ HtHPAHL$ HtHP@HA_A^A]A_^][HUH HHM nH]HUH
HH KH]HUH HHM8]H]HUH HHM(H]HUH HHMx]H]HUH HHMhn]H]HUH HH H]HUH HHMH.]H]HUH
HH H]HUH HHKH]HUH HH+H]HUH HHH]HUH HHMh\H]HUH HHMHn\H]HUH HHMHN\H]HUH
HHMh.\H]HUH HHMx\H]HUH HH gH]HUH HH GH]HUH HH H(H]HUH HH H8[H]HUH HHMhn[H]HUH
HH
H]HUH HHMxH]HUH HH kH]HUH HHMhZH]HUH HH+H]LL$
HL$SVWATAUAVAWHH$MMLH$P3@8t@8t
@+zv
@+9P\$p@:t~@t
@+@8@t?~@t
@+HF8HH9u
Hr
@t+

IEOIaIN9`v@8t
@;+@8HH Hz7H$A IHH$H;t%H$HHKH$ qHHN(HK9u#H$XHH6@DHf\H;}fH$
PLHH$ qH$3H$ q@:tH$ HAH$ D:uGHhq@:u:LH6H$X@H$
H$
)H$
H$
\$pA H$@8$pt;MML$0IH
@:tMIH@:u@rL$0HT$xHNP9`H$ HNPxHHL$xH$ D$D9$uHL$x`DHLxAfA.AfB<YDHHLxA9u-
9~H4HHPHeHfHHTxHEh@:ul@:t$H4H$@H)PH$@
9~MIH@:HHLx@tH#HHOH@8$t#H$HL$xt@@8@8~H$H$XHKH3BH$PH;tHT$xHHHKH;u@
;~ HHLx9`vu{H$PH;tHT$xHEHH$XHKH@HKH-|H$RHL$xG@rH$$$@8>t4IHH;tHHVMRHHH$H$$H$`
HF(HL`H$H$H$H$@$H$HT$xH$L$hI$HT$xI;t~H$FH$8Hc$L;t'IAAIH$IL;uH$H$HL$xL$`H$`H$D;u9$\A@
D;HHuHc$L;t'IAAIH$ILB;uH$1H$#HL$xH@I$Dl$
LLxLH$0IPxD;H$HHcH;t&HAAHH$H;uH$HrHLxgAD;HHH:Hc$H;t&HAAHH$H;uHHHLxA(H
$X@HH*H$HHcH;t&HAAHH$H[;uH$JH$<HL$x1@\$H$HT@8$H$H@8z H$X@HHo-
HHHc$H;t&HAAHH$H|;uHkH]HL$xR@}9~$H$HLFMH$0H@:HHHc$H;t&HAAHH$H;uHHHL$x@
HvHhLc$MD;t)IAEIH$I5D;uAH$@AH$@A9~}OHH$;HH$0FH$
9~}HL$x@$8D$q@:uDl$pD:tHL$x|@Dl$pHLhIIWHHH#HPHPH$`H$hHpHxIDDD$@$H$@8H$ptH$
HHHDDD@HHpLPHhLhL$`L$XANiH$H;tH$PH.HHHHHHpDDD:8D9~@,LF8IH8t(HrH98u
HrAAID;N@rD:IID;tIHpJH`=H$P0H$HL$xAD>$8$I:$L(L$
H$H}D;tf;tH$XHHHpH`H$PzH$LHL$xaAIH$I;tFH$p1H$`HPHHLx.H$H{RH$YZH$x
HHHp9uH*HfA.f<QHH$m[@:$HH0HH8HH@HHHD@88t%LHHDDH0HHH$H
$3D@$H$@8tB@8t9H!H$SHH
H$]vAH$$$@|$p@8ut@8~HtnHF8HHVPHK@8|$qu9tKD:uF9`u>D$pC`9tLkhHIL
LkhHS I:
Iu9~@sLHF8MtD:tIN H$
@8u<;w@8|$pu1H$ INH$ b@:uH$ I;^@rH$XPHKHw$H$PH;tH$ H{HHKHV;u@
;~ H$
HHQHaHSHpEH`8H$P+H$HL$xBH$$$D|$qD:$HLHTH$H{;Hc$L;t2IAIH$MtL;tINIx;uH$gHHPH$
=H$/H$p!H$`H$PHHLxLHH@8@tED$IL;t#H$ILHLD;v
L3I$9~@HH$L$XD:t$H$L|L$0H$0HF8L<L$H$@8tH!H$
D|$qIIOH$HH${
H$;u@8~Ht@8|$qtADH$hHEH$;H$@HI#L$IAIHHThHhHT`LtXHHTPHHTHHHT$@HL$8HHL
0HD$(H$0HD$ L$0AHd;KH$hHH$;H$$$HHH;^@D|qHbIYHc$L;t2IAIH$MtL;tIN#I;uH$
H$|H$RMHHHpH`H$PH$|HL$xH$~IuHc$L;t2IAIH$MtL;tIN?I7;uH$&H$H$nLH$H$H$pH$`HPHHL$xH
IHcL;t2IAIH$MtL;tIN[IS;uH$BHHKHH
HpH`HPHHLxLXD:
H$4;IHc$L;t2IAIH$MtL;tIN]IU;uH$DHHJHHHpH`HPHHLxHIHqHcH;tEHAHHHL<L;tInIfH$;
uD|q@8|$pHF8HHH$CD:HL$x=@:
LD$xHjIbDH$I$$H$;t.A$H4H;tHNHHHHHHiH$[H$pMH$`@H$P3HHLxH$A
HTxH>9@:YL$H*I:H$PH;tHT$xHfHINHAA;u@
;~ H$uIl$$H$;t.A$L,L;tIM5I-HHHfGHHHpH$`H$PHHLxH$#
H$@8H$HH$u2@:AINH\SA;u@
;~ H$1IHc$L;t2IAIH$MtL;tINI;uH$H$)H$EHHHpqH`dHPWH)HL$x>9HAH$H$H;lAINHA;u@
;~ H$0IHc$L;t2IAIH$MtL;tINI;uH$yHHDHOHAH$p3H$`&HPHHL$x LH$$$$9~@3HHF8HHH${H$
QH$ H$8@:PH$H;tH$ HHHHH <H$.$$H;F@ELaL$HINH$
HHc;t/H$L4AIM>L;tII;uH$H$b.IiHc$L;t1IAIH$N4(L;tIN4I,;uH$HHcBHHHpH`HPHHLxH;$
H$Qi;HML$LIHHH$0

HHIH$0H$33DB(HSDHHHH3DB`HPHHIH9tI3DB(H$$H$H$$HH|$
AL$P33AH$HHH$0H$IHuHPLLHMHc;t4M4AIIH$PH;tH~H$Pq;uIeH$G+@8@pHH$D$$H$$$D9$gILH
$J8EH$H$:rCPt7D$THHP*HPH.@8>u H9;HDH
HHjE@:tiHH9$u\HSH$:;uFHSH$$;u0H$hHE3H$;uVH$2H$H$AIHD;$DHHH3H$wInHc$L;t1IAIH$N4(L
;tIN9I1;uH$
HHh=HHHpH`HPHHLxHDD;HFHPDHHoHK@:t0H$hHAH$;u@H$,H$AHPHHPE;xH$3HI
Hc$L;t1IAIH$N4(L;tINI;uHzHH$;H$PHBHp4H$`'H$PHHLxH$hH;3HIHc$L;t1IAIH$N4(L;tINI;uH$yH$
H$:H$OHAHp3H$`&H$PH$HL$xH$~3HIHc$H;t1HAHH$L,0L;tIMI;uH$H$H$9H$ZH$LH$p>H$`1H$PHH
LxH3HA_A^A]A_^[HUH
HHO.H]HUH HHH]HUH HHH]HUH HH-H]HUH HH-H]HUH HH -H]HUH HHH]HUH HHMxr-H]HUH
HH O-H]HUH HH/,H]HUH HHPH]HUH HHPkH]HUH HH7H]HUH HH,H]HUH HH`H]HUH HH o,H]HUH
HHH]HUH HH0GcH]HUH HHP'cH]HUH HHkH]HUH HH*H]HUH HH+H]HUH HH+H]HUH HHCH]HUH
HHKH]HUH HH /+H]HUH HHPH]HUH HHH*H]HUH HH_H]HUH HHH*H]HUH HH*H]HUH HHo*H
]HUH HHO*H]HUH HHH+*H]HUH HHH*H]HUH HHH)H]HUH HHH)H]HUH HH)H]HUH HHH)H]HUH
HHPH]HUH HHHK)H]HT$UHpH33Hp]HUH HHH]HUH HHH(H]HUH HHH(H]HUH HHH]HUH HH(H
]HUH HH o(H]HUH HHHK(H]HUH HHH]HUH HH(H]HUH HHH]HUH HH'H]HUH HH0'H]HUH HH'H
]HUH HHo'H]HUH HHO'H]HUH HH/'H]HUH HHH]HUH HH&H]HUH HH%H]HUH HHH&H]HUH
HH&H]HUH HHHk&H]HUH HHHK&H]HUH HHH+&H]HSH AAHu%HIHHK8|HK(SHk3H [H8HD$
HHPH8HT$UH HHH]H8HD$ HHPH8HT$UH HHH]H8HD$ HHPH8HT$UH HHH]HSH@IE3fDD$
fDD$"LD$(tltLt6t ugHI;t[HHL$ hKHI;t?HHI;t-HHI;tPPHL$,hHL$ gHL$ HAfHL$ e3H@[t3I3A>H8HD$
HHPHH8HT$UH HHH]H8HD$ HHP`H8HT$UH HHH]H8HD$
HHPhH8HT$UH HHH]H8HD$ LLIHLE3F9LAASH8HT$UH HHH]Lu;rPIH3IHSUVWATH
HII9ODuCtHvHHtOHLIHIuHHwGLKdA+IL@AD$H@IIxLHEIHEIDHEIDGH A_^][HSVWATH8HD$
IIDHHHP upHOL Ht3C8Ht3C8DH$Ht+CDtLMt
HIHA(3H8A_^[HT$UH HHH
]H8L\$`ME3MEED8tE>u@D8u7HI;t2P\A;|#HHHxLIIA@AA3H8HI;tHHI;tHM>L\$ P
HSUVWH8A>HHIIEALt
AYHLHA|tJALt@ILXMt0IHt
HHI(IDIHl$(Ht$ P
{A|uq>MALIA0Hu&AtNz\|HIAxJ\HHHB)At(HJ@HyuHJ0HytHHAE3H8_^][HSUVWATAUHHhHD$
MHL$H@@@H@@@HPHHT$(I3|$0vB{3;|$@s/HD$8HHHD$(HH uH;|$0rHM^LuIMQLHInHc\$0Ht-
HHHD$(H,HtHMH{uHL$(mHc\$@Ht-
HHHD$8H,HtHM?H7uHL$8)I8HL$H{;|$0sH~HL$H;|$0rHc;\$@sXH;t1T$P9T$TuHL$H]T$PHL$HfA\fQT$PHD
$8HHL$HmH;|$@rHT$HI<HL$HyHc\$0Ht-HHHD$(H,HtHMFH>uHL$(0Hc\$@Ht-
HHHD$8H,HtHMHuHL$8IHhA]A_^][HUH HHM8VH]HUH HHM(6H]HUH HHMHH
]HSVWATAUAVAWHH$MADHHO$L*3f|$ f|$"H|$(C8|PA6D;q`uIHL$]HL$
\3]C9|CTHHH0HH$|$H|LHLPsHL$`h$LHL@8HP0HL$H@@:HT$pHL$HB|$LuHTpHL
]GDHHXHHH@:t1LHTpHL$8iHHL$ W]HL$8H$HIHL ;\HL$pHL$`HLPHL
[3HL$`HL$P}ZD;q`udAdH@HFPDHHDDHT$8HWHHL$ \HL$8$IHL$ [HL$ Z3bC9||IHL$ i[HL$
Z3<C8|t?t:t:?t0tHL$
\3HL$]u)CD;| HHHHHL$ [u*;|7A;u2HH;t&HL$ [x2uC9||C8|aC8|u@8tXt?uNCTtDLL;t8HH;t
HHQ(IMDIP0HL$ YGDALIA0JtntZtFt2t3R`HL$ v[HHL$ [HHL$ [vHHL$ [fRPHL$ e[WBPuH:HHL$ u[;RPHL$
[&HT$8IuUHHL$ /ZHL$8IHL$ /YHL$ X3HA_A^A]A_^[HUH HHM 2H]HUH HHMHHH]HUH HHMHsH
]HUH HHMpH]HUH HHsH]HUH HHSH]HUH HHM86H]HUH HHM8H]HUH HHM8H]HT$UH HHH
]HSVWATHHHD$(EAH@0HHtiHHH9DDAtPHT$PHrSHHL$@tHL$PD\HHH0JPP$$D$HML#D$@HHDAPXH
L$@gHL$0HD$ HHtHHHZH\$ B $HHLD$0tHL$0H\$0H\$ HL$Ht0HL$0cu"fD$`#HT$b1H\$`H\$ HuH7H\$
HHD$LAPXHL$0KHA_^[HUH HHM@H]HUH HHMPH]HUH HHM0fH]HT$UH HHyH
]HSVWATAUAVHxHD$0EEL3\$ HL$8.HH\$(uHL$8j@AIH;tHJHyH|$(:IH;t.LD$8A6;tHL$8H|$8H|$(IH;tJLL$

AA;t1HL$8UufD$H#HT$JAMH|$HH|$(8\$ IHLDAPPHL$8vHxA^A]A_^[HUH HHM8H]HT$UH HHH
]HSUVWH8HD$ AHHA!unH-T-Hl$xH^E3D9G0vFHG(90tAHD;G0r/AHG8HOxL A!HAQ@t
H
H
NH8_^][HUH HHMxH]HSUVWHHL3AY;tII9LDAK4@L;r%w(HBH;DEAAH;DDDD;tD}AAH;uH
HD$ LD$ HHJHHHJHHIHH_^][AHL$SWH8HD$ H3yHHHHHAHpHAH-HAHHA
HHA(HHA0HyHA8H^HA@HyPyXy\A`HyhypytHyxHHHHHHHHHHHH@@ @
@@@
@HlCLHH8_[HUH HHMPHPH
]HUH HHMPHhH]HUH HHMPHxv~H]HUH HHMPHKH]HUH HHMPH+H]HUH HHMPHH]HUH
HHMPHKnH]HSUVWH(HIHIHFLHcuHHLHctHLHpcu HSHPLHQcu HSHLH2cuHSnHLHcuHS RHLHbuHS(6H
LHbuHS0HLHbuHS8HHHH#HCH3@H(_^][AHAHHHHHHHHgHH OH
H(7H(kH0H0SH8H8;HL$SUVWH8HD$ HHHtHPH HHHcHt-HHHCxH,HtHMHuHKxHKhHKPH8_^][HUH
HHM`HPH ]HUH HHM`Hh
H]HUH HHM`HxzH ]HUH HHM`H
H]HUH HHM`H
H]HUH HHM`Hc
H]LDTSVWATAUAVAWHHD$XAMHILHO$L(C|u
@HHP GDExHHH0JXP!CDxHHHHX 2C|HL$0CDE3A;|!HHHHHHL$04GDE;|*HT$@H]GHHL$0_HL$@HHt$
ADHT$0P0A;tHL$0A:ub0=HI;tDxHHL{(IH$I;t
HHPL{L{ L{HK(I;tHPL{(IHL$03?HT$0HFHHHt$ E3DHP0DHL$0EtAt3tUtt3^HHt@HH3Ht HHPHHxHHt
HldH3H$Ht
HHPFdHO8HGHHH#HFhL~pGDHT$0H6EHO`D$AiBHD$@HtHT$0H\3HOpH8CTHHH0HHx8uHxHtnHN(D$(
D$ AE3HT$0w&HHDHT$0HS8HL$0HHRCHN(DHT$0*uzEDHL
DHT$0AQ8t5tu,AEANHL$0HHRHL$0HHRtSHNHHT$`Pu@$v6HD$hHD$@HD$pHDHDtP_PHOHHT$@;_
PuDwXG\Ht+H
!~G\HAH
v!XH$H0HL$03HA_A^A]A_^[HUH HHM0H]HUH HHM@H]HUH HH7eH]HUH HHM0H]HUH HHH
]HUH HHdH]HUH HHM0ZH]HUH HHM@H]HT$UH0HHH0]HSWHhHD$
IHJHT$H)HT$HHL$8|$@sf0HL$8
HHL$(L$09L$4uHL$(L$0HT$(fB.fJL$0HT$8HL$(HHL$(0HHt@HHHC3H$Ht
HHPHT$(HC(HKE3(u+HHRHL$(\HL$8Q!HHL$(@HL$853Hh_[HUH
HHM8H]HUH HHM(jH]HUH HHbH]HT$UH HHsH]HSUVWATH0HD$
HHL%Ld$hIE3D9C0HC(98tAHD;C0roApHK(ADC0D+IHHuXC0HC8H<HtHHLK8DC@D+III8XC@IOD$`ET3
HNHL$`WH0A_^][HUH HHMhJH]HSH AHAHHu!Hq3H [HHHH H(H0H8HSWHXHD$
IHHHL$(QHL$(HHL$(HL$(HHL$(HT$(HL$8kHHL$8VHUH HHM(H]H tHSVWH
HI9Kuft"HHt"FxHOQQHy3{tDCHHIYSHH;sH _^[HSVWH qH;qtY3;tBH;t ;|HHSQQHyHDGHHIRHGHGH
^[HSUVWH(I:H4H,HVHM/3;u'E :F t;u;;DH(^][LL$
DDHTHL$SUVWATAUAVAWLHH$HL3D$(E3M{E{E{E{MM{E{E{Da8Dd$HrIM{I;tHHpHH$A;vLI3HTD$A
;v53gLDHHLH$$$A;tAA;vIH;rLHH$hMH{L+H$(HAD=L4A$L<IVIO3;tE3AG A:F tE3E3AA;u
GIH;rHDdHL$
L|$PD|$XD|$\AHL$PDAE;v_DHT$8I:HH$I;tHHLMLXHDPLDXHL8A;rHHLPhAvZIH$*HL$PL$H*L,IU
I$iA;tDd$HA;rMIH
XED|$$AD|$ DtLML|$`LH\$0LMLthE;r;D$<D$@D$D3HE;sH$A,H$ H@0L<;sgE*H$(HJ<E;DHD$PJHWH
E3A;uO AGP$:A;}7DD$$L\$`HD$PL$D$8l$@ADD$$IL\$`A;~GT$ LL$0LT$hE3LgAD8GD8Dl$<T$
ILT$hILL$0aLL$0AA;HD$PN$D8G$l$@Dl$<D8G#$0,EW,IOA;tMH$mBHTxHO_BDD;\$xD;\$xAADLL$0\
H$|BHT$pHOnBD$D;\$pD;\$pAADLL$0HWzLL$0E3At1t"D8G"tHI9u
D$8D$8D$8D$8D$$HD$`L$

L$ HD$hILL$03L;tA8G`uE3I;D8!E3H$I;DGE9D$I$fB<@:u4H$ID|$(AD|$(HH3;u@
D|$(3@AtAD|$(H$E3A:t!D$tDDH8ADtLL$8IIAL$HIoD8ADT
DD$$DdHLL0LT$hL\$`PH$8}Hc|$XHA;t/HHHD$PH,I;tHM=H5A;uHL$P&HIHbL AH$(HLIHWH
|LGIH
$(?H8H$(MHA_A^A]A_^][HUH HHH]HUH HHH]HUH HHH]HUH HHMPfH]HUH HHM8rH]HUH HHH
]HSVWH qH;qtl3;tUtH;t/;|&HHSYYYQQQ
YHyHGHHLIIHNGHGH
_^[HSUVWATAUHH3MI9yLH3LMD$+D$4BBDFDALD$8D$,DD$0D$)D$(D$*tDtumED$+D$*^tuUHHHH
KuBDxHA|}PD$)D$(D$*HjFHT$(HHAH~HFExMtI$AIH;}HHHA]A_^][,HHHL$ HD$ JHHQHH%vHQ
H%vHHLD$8HLDHL$03HD$0D$ lHHuA
!3
HSH HHHH
suHpH1HL$0HD$0JHH [Hi3HH
}H%NsHSWH(HFHHH
W3'stHHH(_[H(HtHH0H(HSH Ht.HBHYH@HHAHB HBIHH%@H [HSUVWATH@HD$
ALHHtHHNHtH~tsHL$(EHH~HyHH
tHH~HVHL$(IHNH^H3H+HN
NH@A_^][HUH HHM(H]HSWHHHHHT$ HI[HT$ HrHHH_HH_[HSVWH LQHHHH
KH;DIiDJBD$Kr GHrMHHD$HHLD$HHT8HH)C9CuHSH
)
SH _^[Ht)SWH(HHHzHHH(_[HSWH(HHHHt*HHLuLDHAH)HWHL?HHLDkHWHcHt'HHWLH=HW
H5H(_[HSVWH HHIH0@t-HHW0H @2HLLDHH8t@@HtNHmXHW8H@2HLHLDH@t.@HtH&HW@LJHbH
^[HSWHHHD$ HHHyt=HL$(HHL$(WH[HHL$(yqHHL$(>HH[HUH HHM(H]HSVWH0HD$ HHH5
Ht$PHdnHt
H{(HK ~
@H+nH0_^[HUH HHMPBH]HSVWH0HD$ HHH5*
Ht$PHmHtHtHHC HK @HmH0_^[HUH HHMPH]HSUVWHHD$ IIHHHH
EoHH
eoHHHPHtdH}t]HL$(HUHL$(H\$(HH
oHH
oHH~oHHL$(CHtJE3HT$@H0t6HH
BoHH
2oHHL$@$oHH_^][HUH HHM(RH]HSUVWATAWH8HD$ IILHL=_L|$(IlB@tHtHO lHHtHHthHHH
%WnLLIHHHH
+nLLH$HHDAtvAtaAtLAt7At(AtAt
@@@@H$>H$.HHH$HHttH@IfjH8A_A_^][HUH HHM(zH]HSUVWATAUAWH0HD$ ALHL=XL|$pI
jHXIt-t tH5H5H5nH5 9rHHtAE2HHtH;uHO 3HH8
MtH8HHH8kPHHfMt"IHHHHHL8HHHH$Ht3HHH
vkHUHHH
xZkHtHHtZrHGPHGHfHO8AHwEuHMtIHOHHO m_@IhH0A_A]A_^][HUH HHMp"H]HSUVWH8HD$ HHH-
Hl$pHg@tHHHtHK HHtwHHt/HH
iHHHdH<f@HgH8_^][HUH HHMp*H]HSVWH ID3AA@IAtiAtTAtFAt8At*AtAtAucHZHQHHH?H6H-
H2H[HHHEHLHI HHt
HH|pHmH _^[HSVWATHHHHD$ AHL%L`@Ieu+H{@HC8@8{PHCHf8HC0HHHHtHK
HHt`HL$(:LD$(OHHHL$(g`t*HHH
gHXHHHHL$(T@IdHHA_^[HUH HHH]HUH HHM(~H]HSVWH AHt HPIHODHP@r%@H _^[H(H
93H(HSVWH0HD$ HHH5*Ht$PHcHOHHP HcH0_^[HUH HHMPH]HT$UH HHH
]HSUVWH(AHH%@HhpqHHHtH9uH 3HHtAHHH=H

N@HEyeHHNHHtHK8H83H(_^][HL$SUVWATAUHHHHD$
H@@N$3H-AADt#Lm{tHIHAA#Hrt1D$(0D$)xHT$*>{tHHT$(HHHHA]A_^][HUH HtHH
]EtVSVWH0HHHL$ AHHH
dH~HHcHjHL$ H0_^[HSVWH IHHHHHxHHHHRHH _^[wHSWH(HHHtH9uH 3>HHt5HHH
:cHtH}%@H(_[HSVWATHHHD$ HL%}Ld$pI/`Ht)HO G@HG8GPHGHfHHtHuF8pu>8qt HHxHHtjHH
DbHPHHHt=Ht.HHH
bHHHfHH(H@pHHtxHHH
auHHH
Jda'HL$(HHHL$(HH@IZ^IM^HHA_^[HUH HHMp^H]HUH HHM(H]HSUVWHHHD$ IHHHH
`HHHL$(F,9uHL$(bHHL$(-\HDyLHHGHLAHiHL$(3DExHGJHYLH%HL$(HT$(HrHHGH?HL$(HH_^][HUH
HHM(H]LL$ SUVWATAUAVAWHHHD$ IMLH3H9tH 4H9tHo0o@HG8@(oPHGHf(H9tHO
HH;tnDA9mCLIEI4^$@8n!t ;u 9n@HH;t)HD;tHHH$;t)HH;tDHvQHp9n@tLHHt0H^8HUH
CU^HHHHHp3HH;tHm^(@8n"u@8n t F$@u@;u 9nPHH;t)SD;tHHH/;t)HH;tDHq\Hq9nPtLHHt0H^HHUH
>`]HHHHHq3HH;t@8tHo9n,|3HH;t LIG@8tH>qAIE;eH$$;u4HH;MI;HHHH;tHH;HH
UG\HHHMIHB#;uttTuHHH
['HL$(HHpHL$(H9H;%@HHA_A^A]A_^][HUH HHM(H]HSWH(Ht$HHtHH
=[HHtHKHK H'%@H(_[HSH Ht2HBHYH0HBHHAHB HBIHBHRD%@H [HSVWH
HHHtHaHO0t?Ht5HpHHHHH@ZHH%@H _^[H(Ht
HQH%@H(H(HtHtHHH^P%@H(t9SWH(HC9CuHSH
-H
SuH(_[HHSWH(HH`HcE3L9t.D8tI;HK0HDYfDtH2HK%@H(_[HSUVWHHD$0AHHHHOXHL$
+G~HL$ rHuHHL$ XHL$ dHH
HHuXD$@HHT;DGHL$@pHHL$@KXHH_^][HUH HHM rH]HSWHHHHHT$"ID$:D$! HHHWHHL$
WH?HH_[HSUVWATAVH(Hp3HHGH9rv9E3DvtHCHEIQHA;ABM;urt5H
G9GuH/WH
-H
WutBHHG9GuHDGHA-AHADGuHHHVHH(A^A_^][!HSUVWATAUAWH@HD$
LHvHhxHpHHHtH9u33A9l$vhE3D}tHpID$I\HSHp}D[EE;EBD+[E~AHpHuMA;l$rtZHpyHHL$(
+D$0~HpLHuHT$(HpHL$(;t+HpHp
HHpcHHHpUHIT$HG%@H@A_A]A_^][HUH HHM(.H]DL$
HTHLSUVWATAUAWHHHHE3MHI;tH9u3`HpAHD{D8E9|$$hID8pIL,(D|$PtEEHHL$PHJT(lEmHL$PES(
HL$PL|PD(D;DBD+t
E I (CD=E3A;tHHT$PHHA;|$]HPHXD8HWD8$pH|$ tDHT$-HH|$-~{HL$-'
+A;~8H|$-DI+A;v(Hv'H D|$-HD$ D$(D$,HHID8tHHHHHHA_A]A_^][H%RHSH0HHt[DL$ IE3t5huHH
HRHH`HHt
HCHK%@H0[HSUVWATH 3HH9rve3HCAHL$(I
H{ uH{tAIHH{(tAIHHBH;srH A_^][HSVWH0HHHHtmHHVHHV0LFAHD$ HHHH~ uH~t+LFMtH6HLF
HHLF0HHLF(Mt"HHLF8HHH-HHj3H0_^[H H%OH(AwH +H
!cPH(HSUVWATH 3IID{$D;v"A+;ttu+HoHPH A_^][iHSUVWH(zHHt
HAHztHBAHAz(t$y(tH H MyHG C(HC HG0HC0H(_^][HSVWH@HD$
I3IAYf\$(f\$*H\$0HLL$(P0;tHL$(qL$(;tYtJt:t+tD$`NHsHL$`"HD$0H!D$0HD$0H
D$0H_HL$(
3H@_^[HUH
HHM(>CH]HSWH(HHHHINHH
tNHHHH(_[HSH@HD$ HHL$(,f
HL$(HHH

NHL\$(IAICft,f

uf8
uHf
f
HfHfuL\$(I+HT$0fACHT$(HHT$(H3HHH
MHT$(HHT$(H~H~HL$(()H@[HUH HHM(H]HSVWH@HD$ AIHHHHMHH
9Lt
HH;HHL$(HT$(H'HT$(HZ~H2~HL$(|H@_^[HUH HHM(H]HSUVWHhHD$
IIHHL$8E3DHHL$8U'39\$@HL$({HL$H`sHwHHL$(+H;tHHL$H\$0HD$(HL$(9\$0tHHL$(K
HT$HH]}HH
KHT$8HLHL$Hq{HL$(f{HL$8[{Hh_^][HUH HHM8H]HUH HHM(H]HUH HHMHH]HSUVWHHHD$
IALHfD$(fD$*HD$0HLD$(IP@tLL$(LH93HL$(t HH_^][HUH HHM(&?H]HSVWH H:HHH
pAgJHHUH
HEJJHH
:JHH{H
HHJHH _^[{HSVWH zDBHHtA H t/HH_H
LIHH
IHHWDGu?t
G@uA@HgHO0t/HH_(H
kmIHH
]IHHH _^[HSH HHyHKH [MxHL$SWH8HD$
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]HUH HHMPH@JH]HUH HHMPHP*H]HUH HHMPH`
H]HUH HHMPHpH]HUH HHM`HH]HUH HHMPH9H
]HSUVWH(HcYHHt5HHHH,HtHMvHMvHvuHH(_^][vHSUVWH(HcyHHt5HHHH,HtHMvHMvHxvuCHCXC`
H(_^][HSUVWH(3H9iv43HHK0;u3S4LCK(H;orH(_^][LDTHL$SUVWATAUAVAWHHD$xIHE3DiD$8D|$T
E3Lt$`D9qbHEJEu:J<39{0v{0L H7C0LH0(){ tHHKvEHSH
w;uLEuHH$EELM0LE@HU H
xEqH
}v`Dc4{HEHHHHTPHT(HT$0HT$
LL$XDP mD$0@>u1|$P"LE0HL$X"HHM0DYuRHLhLDhT$0HL$X$t@2LE0HT$hH
?wHL$ht@sD$0|$P@v)H|H
]DT$0H
dx6L$@tRL\$XEEAAP0A@7A
BA
AAQ0AA7A
BAJIIHuAHH$@Cf3fD$8fD$:HD$@tgtHt(HEHHLL$8DP0D$8N(tHHW HL$8fD$83t-
HWHL$8MfD$8tHHL$83fD$8; ft
f=f#T$@J<}PtEt
HY$A.A$$AKAK%AK $ACCEtHH3BYHdLIfu4E;J<3EtIM
H);Mf=@J<|$@u|$DtE3HHL$@ZEtHHAHL[4H;H+J<3tD H7Lf=HT$@HM@yHU@H
tEtLE0HU@H
yt:K,3D;eHv%A+}H3ttuU+LE0HU@H
0s8E3DHT$8H$EtHH$@N<Ik,3H$LD;v<A+3ttu3tD IkA.H+AEH$H9H3tD H1I+LH$ D$8Lt$`HL8(EtH
/qLTLTILt$`;MH$0@#@HL$86H
p3HA_A^A]A_^][HUH HHMhH]HUH HHM83H]HSUVWATAUAVAWHHD$
MMHLE3D9aL3IEH(K0@;u1s,[4D$@t
HT$@HfLD$@?;u2s,[4D$@tHT$@HOfLD$@;u>D$@(t!HO D$@9uyt
E3HT$@LD$@S4K,y;ubHL$(QxMHW0HL$(Mt%H3HL$(zLFIHL$(iLD$(3K,fHL$(mL0OS4K,GAHE;eH

HA_A^A]A_^][nHUH HHM(H]HSVWH`LBpL
HHHhtNL
hHW8E3HHL$ HA8&HW8HL$ L
AHT$ E3HH`_^[HT$SUVWATAUAVAWHHD$pMHHE3E9h-E3I$I<Ho HH
Lz<E3LHeH1D]ExUD;uHH
?<Hm2HFJHQHL$`}AHT$`HHL$`ky LHHFHLAE3HHHHHHt3HH
";HH
;HHEpHlH?E3EA,HHLHuHt3HH
aK;HH
};;HHLpHlHHT$@HPX39l$@veHD$0HH$HL$ LLDLD0HP`:LL$0DD$DHHHL$09;l$@rAD$D;HH
#:LHT$<HASHI$ILD39l$<HD$0HHHL
LL$8LD$0HPP3;fDHfDJHD$PHLL$HDD$8AHP0ujLL$HLD$0T$8HRHLHHL09;l$<eAIE;l$H$/HL$08HL
$H<HL$083HA_A^A]A_^][HUH HHM`H]HUH HHM0H]HUH HHM0fH]HUH HHMHv-H]HSVWH
Ax)IHHtHH
?AxL|.IP0mHjD_LHFJE3HHHH
Eo8HiHiHW@HG3H _^[HSUVWH(HHIXhHK@hHK0gHK
gHcsHt5HHHH,HtHMgHMgHguHH(_^][gHSVWH0HD$
HH8SgHHD$`HtAGCHWHKyHWHK5rD_(D[(G,C,G0C0G4C43HHH0_^[_HUH HHM`fH]HUH HHM`H^H
]HSUVWATH0HD$ AHHHL8mfHHD$xHtHH/wHKp3HH
_GCHKHWoqDD[(GC,GC0GC439_vHNX~;_r39ov,F`9Fdu HNX3pV`HNX
-
V`;orH I@H0A_^][HUH HHMxeH]HUH HHMxH"H]HSUVWHxHD$
AAHHHL$@vHL$Po|$8\$<`sHHHL$P^p0HtHHL$@nwDXHDPHL$PqHmHL$@z|$XtHUHL$PCqHL$(Do3L
$HtM3T$0HD$@4fs79T$4uHL$(nT$0HL$(@4
T$0HL$H;r;uHT$(HL$PoHL$(bdHT$8HHL$PJdHL$@@dHx_^][HUH HHM8HH]HUH HHM8JH]HUH
HHM(ZH]HSUVWLHHHD$8HHHIS8HPHu~39$vqHD$0HHL$xHL$
LLD0HPPu1E3DHT0HHL$02;$rHL$023HH_^][HUH HHM0H]HSUVWH8HD$
HHHHT$hHP(uh39\$hv^HD$xHLL$pLD$xHP0u+ADD$pHT$xHHL$x1;\$hrHL$x13H8_^][HUH HHMxH]DL$
LDHTHL$SUVWATAUAVAWHH$HD03LIHHH$ $($,H$@mrH$PlH$`QrH$xl@$3@:uDBH,H$
3H$$H$
$($8$0$4H$@H$HPHX$`$p$h$lH$xH$L$AOHH$H;t`HH$3AA9WvAA;OrLHT$`HHT$HDT$4D$8HT$@A9
Wm@Hl8
ILHTX@$x@:H$HpIH$(n.DL$4
ND$4H
niaH$2H8H5HcaHH
/HHhaH@aH$_IH
|dH=pHaHaIHX_HHt$@3J_$H
`H=H`HH
:,/IH
idH=HH
</Hz`Hr`IH^{H}Ht$@3JH$H|$XH|$HH^{H}H$@$HHHU3$H$$$HnHHpHHxH`H$$HH{pH$
HHH$`H$HhHHpH@HIH$ p$t>H5H^HH
\-IHbH^H^LHH$@E3A;uD9$IAp;,
H
YT^H=Ha^HH
,IH
bHHH
X,;uL$@H$`H

_it$4HMHH
m,uH.HH
N,-H$uHH
]H$[H
]I>H$[H$[H$[H$@>#HHt@3J$HA;v]H$@HHD9@PtH7D9@(uD8@"u D8@
tH7D9@,|H7D9@$uD8@!tI6D9@@tI6HH+uHt$`LL$D8$xuo$XLL$HD$HH$`HD$HAA;vCIDd$8HPHHv
E3A;|A;vH;$XrD8$L$@H$`H5HH \D$EtHH
l*E3?H$ H[HH$x=*H[E3D$$HH$@LlL$0D$8IuE:tOH$ HH$ E3A;IUI;tH$
E3A;LDLDDDDLLDLDLDD$ DDL$(L$0HHT$PHP(RH$i3H$$$H$]iH$OiE3D$D$$LL$
DL$(DL$,AD9L$PLD9
L$@IDE3D8$xt=WA;wE8
t/L$HFjDE3A;D8$D$E8t9L$HjDE3A;MD8$tD8$ZL$pHiDE3A;D8L$0D8$t@L$ID" HH/)E3A:LHT$
H$@$pALL$1HT$ H$0E3A:D8L$1LDLDDDDLL$AH)DL$D@HD3;c
$$$fLhfLjHL$pHLL$hDAHP0DtHL$h8D$hf=@uHD$pH$$ fHLhE3Et D8$pt
H$0H$H$H$$HDH$E3D8$t
D8$t&L$H$
TDE3A;
AA;|PDDAAH(H$HD8$xuKD8$uAD8$u7D9$XtLD$XL$`LD$XHHI#H$D$$I;AD$$I;AD$D8$E:u>HHx
H=HUHH E3H=D9$|THH
I3$E3L$pHHH`H@HHE3HkHHHHHL$0L$H

UHc|$(Lt-IIHD$ I4HtHWSHOSuHL$
ASHH%SHSHZRH$@H$LHt@3JD$8D$8HHt$@A;GSHL$hA@QHRHRHQH@HmR.HZRHLRH
QH@H(RHRHRHHQH@vHQHQHQHQH@/HQYHQHwQHPH@HSQHc|$(HA;t2HHHD$
L$M;tI$QIQE3A;uHL$ QH$ HPHPHPH@IHPuHc|$(HA;t2HHHD$ H,I;tHMPHwPE3A;uHL$ ePH$C
HIPH;PH$~OH$@HPAHc|$(HA;t2HHHD$ H,I;tHMOHOE3A;uHL$
OHHOHOHNH$@HwOA5Hc|$(HA;t2HHHD$ H,I;tHM?OH7OE3A;uHL$ %OHH
OHNH>NH$@lHNAHc|$(HA;t2HHHD$ H,I;tHMNHNE3A;uHL$
NHcHiNH$[NH$MH$@H7NAHc|$(Ht.HHHD$ H,HtHMNHMuHL$
MHHMHMHMH$@1HMAZHc|$(Ht.HHHD$ H,HtHMeMH]MuHL$ OMH$-
H$3MH$%MHhLH@HMAHc|$(H;t0HHHD$ H,H;tHMLHL3;uHL$
LHHLHLHKH$@HdLA"Hc|$(Ht.HHHD$ H,HtHM-LH%LuHL$
LHHKHKH0KH$@^HKAHc|$(HA;t2HHHD$ H,I;tHMKHKE3A;uHL$
wKHUH[KHMKHJH$@H)KA@$HlHDd4I8$@:A9Ow L;H=eHyLHVH$xH\LH$H$
H?LLD$`HHMHEHLH{H3I9uD;tHJJAH<J3H$ HA_A^A]A_^][HUH HH H]HUH HH H [H]HUH HH H0;H
]HUH HH H@H]HUH HH [H]HUH HHH]HUH HHH]HUH HHH]HUH HH@H]HUH HHH'HH]HUH HHH
]HUH HHH]HUH HHH]HUH HHH]HUH HHHH]HUH HHH H]HUH HHH]HUH HHM jH]HUH HHMh~H
]HSVWH 3HH8tLIA9uA;sD+AH HuHHH
^[H%HSVWH`HT$@AH\$@?38D$@tHL$@H9u;s+HH@3uHHH3H`^[HSUVWH(H=HIHHHHHHH
YHBzA HHHH;H.HYi+A0
IHHI;w3H;tHkd3HHHH#H
HKA HH%HH(_^][H%HSVWH H=HHHH
yHHHH
QHA H7HH
NH=tHdH
OHH _^[HSUVWATHPLLD8HHHHD$ H$AHHL$@M3SH
33H|$0HHHHHu*H
5Ht-HHhHHtHT$@EHH|H;HHtmHT$0H$$D$$$I L$H H IH$H H$HH+H

LtQHH
aMHH4CA Hd$0HHMHH
HH
sLHJ&H
ULutH$H
*iH
LHKtHT$HH
BH
DHA_^][7H[BHSUVWH(HcYHHtkHHHH,HtPHMxBHMhBHMPBHM@BHM0BHM
BHMBHMBHBuHH(_^][vBHSUVWH(HcYHHt,HHHH,HtHM=BH5BuHH(_^][!BHSUVWATAUAVH Hc
MMLHHt/HHHH4(HtHAHAu 0LLLH
A^A]A_^][HSUVWH(HHHxAHc@Ht0HH8HH,HtHMFAH>AuH8.AH("AHc
Ht0HHHH,HtHM@H@uH@H@H@HH(_^][@HSH HSHHHp@H`@HKH [HSUVWATH@HD$
HH39YveHLHIHEHH
4<VL^A+HL$(|HHAHAHL$(?IH;^rHH
HH@A_^][pAHUH HHM(6H]HSUVWATAUHXHD$ IILD3H$9@tlHtbHAHH
1H@H@I$8HA$@HH
KH-DH@ED9.D9nD9n A$ uP@8$A9$@HHtH0@HUH
]sH3@Ht}H!@HH
H @H@I$HHH
eHGCHH
KvHs0H?HHL$(N~tHVHL$(RHL$(R39^
v)3HFH(HL$(RHL$(QH;^ rt-HL$8HHL$(LRHL$8q=HL$(Q|$0tHH?HHT$(?HL$(9=HXA]A_^][HUH HHM(H
]HUH HHM8nH]HSH HHp<HKH [1HSUVWH(Hcy HHt-
HHCHH,HtHM<H<uHKs<HKH(_^][b<HSUVWH(HcYHHt5HHHH,HtHM)<HM
<H<uHH(_^][<HSWH(3H@9@y@y@yyyHL@{
@{!@{#@{%@{'@{(@{)C"C$C&C*@{0@{1@{2@{3HH{8{@{DH(_[HSUVWH(HcY0HHt6HHG(HH,HtHM
P;HMG;H?;uHO(H(_^][*;HSUVWH(HHX;HcPHt8HHHHH4(HtHN:H:H:uHH:H(:H:H:H:H:Hv:Hj:H^:HR:HKpI:
HK`@:HKP7:HK8&Hck0Ht5HHC(HH<HtHO:H9H9uHK(H(_^][9HL$SWH8HD$
HHHHHAHHAHYH\$XHHHKHHJ0HHHHGHpHGH=HH8CHHIHXIHH8_[HUH HHMXH8H]HUH
HHMPHvH]HUH HHMPH8H]HUH HHMPHHH
]HSUVWH(HIHIHLHuH[HLHutHLH_uHSHrLHCuHSHHHH#H03@H(_^][00H;HH#HHSH
00HuCHX<7HH07H8$7H 7HK _6H73H [HHHL$SWH8HD$ H3@@HAH5GH $H(04H8@DHHPTHH8_[HUH
HHMPb5H]HUH HHMPHoH]HL$SH0HD$ HH7HHHAHHHHHChHp3FHH0[HUH HHM@HH]HL$SH0HD$
HHKHH;HHH`EHpg?HH0[HUH HHM@H"H ]HUH HHM@H`H
]HSUVWH(HcY@HHt6HHG8HH,HtHMP4HMG4H?4uHO824HOH(_^][!4HL$SWH8HD$
H3@9A@y@y@y@y@yAA@y@y@y@yHy AA
AAAAAHy(y0y4Hy8y@yDAHHP<@H"D@@@HD@@@HCHC
H(C@8@9@:@;@<@=@>@?@@@AHHPTHX`dHH8_[HUH
HHMPH]HUH HHMPH8RH]HUH HHMPHPH]HUH HHMPHH]HUH HHMPH{H]HUH HHMPH[H]HUH
HHMPH;H]HSUVWATH HDHHHcyHt,HHCHH,HtH=Hb1uHKU1AtHG1HH
A_^][H(AAuq3H(HL$SUVWH8HD$ HHcYHt:HHHH4(Ht$hHtHNdH0H0uHH8_^][0HUH HHM`~H]HUH
HHMh}H]HSUVWH(HcyHHt-HHCHH,HtHM80H00uHK#0HH(_^][HL$SUVWATAUH8HD$
HHcHAt0HAHHH,HtHM/H/uH/Hx/HchHt0HAHH`H,HtHMe/H]/uH`M/HH5/HcHt9HAHHH,HtHM/HM.H.uH.H.
HcHt0HAHHH,HtHM.H.uH.HhHc{pHt-HAHHChH,HtHMS.HK.uHKh>.HKP4.HK8*.Hck(Ht,HAHHC H4HtH-H-
uHK -HKH8A]A_^][HUH HHMpHJH]HUH HHMpH8{H]HUH HHMpHPzH]HUH HHMpHhH
]HL$SVH8HD$ H3@q@q@q@qHqqqHq
q(q,H8=HKP=HshspstHHHs=H@@@@H+=@@@@@@@@@@@@@@@@HH`hl@p@q@rtHx_<HHH8^[HU

H HHMPHH]HUH HHMPH8xH]HUH HHMPHPxH]HUH HHMPHhzH]HUH HHMPH[H]HUH HHMPH/xH
]HUH HHMPH+H]HUH HHMPHOH]HUH HHMPH#H]HUH HHMPH`H
]HSUVWATAUAVAWHH$FE3L$DD-HH$:H$HH)A$A;vQH$H9I;tH)Hw)HDCIHQAA;H=&I;t'HH
zIHH
'p9$HcA;t1HAHH$H4I;tH(H(A;uH$(3H$\H$&L$HHDD H
$tI9$HDH-H$H=9$tI$HDHI$A;t H;HDD8$I;t!HH
YyHH
nHHHc$HA;t1HAHH$H4I;tHr'Hj'A;uH$X'3D8$tI;tHH
xZHHQAD9t9$tDAPI;t*HH$+A;t D$(&HI;t%D`HHELeDeDeDe
De!IH$I;tHEHP$E
$E!H2A;t$H<oH$5H$0DD9euEA:u$0tH$0A:tHlHHmHLDDLHxHA:u$H7lH$HlHLDDAD9$3H$$
HL$IH*$uxL$EE3$t'L$BE;t+E;}DAD;uEH$LII;$GE3$H,kHHkHvDDH$0H?E3I;HEH%HH
UH%H
~LHDD9})MHEI4HH
WV D^@A$# HAE;v*L-sAE HIA;rH ^ HVH=%s+H }HuH jH$3AD9~0MA;t
H$
<HF(M,IH$7E9}tmHUXH$7IUH$7D$D9uHH1DHAfA)AfF<YD$I;^0T$H$H-$
s
+H mHuH ZD9~Dt)HH
LVDH-'H +ED9~PIL$E;tHH
4HFHL<E3M9oMtH IB(A<^wHLDAAP0A@7A
BHS0C7
BHnIM;oxHAHHD;vPA'H"H$!D$AD$ID;mL$D$Ho"HH
~HW"E3ED95H5E~ HHH
RHAS uHAHl%HCH%AH]LI$$AHDHA8H$HKAH$H9HAHHKHt!AHD;-'A HP!HH
|H8!ED95H5MHHH
QHA~HKAHHHALH$HK3AH$HMHAHHKH AHD;-*SD$u^HE3I;HEL$H$wDH{I;tHH
{AE3D:
H$0E3A:D8$tD9$uH({H$1HwHHDLDDH$h.HH$HHHD8$HtD9$XtAHPH$0H$m$$D$I;tAE$$xD8
$HtD9$Xu
DHPH/DDLLHH$ DH'LDDD@HHD8HHD0HHD(HHD LL8H$HDL9$tH$ L
IM;HDD$(HD$ LH A5DHc$HA;t1HHH$H49I;tHHA;uHHHuH HZH
$0 H$I;tAE$LL
HHDDHM%LLH$`H$DDH%LDDHH$%L
_IM;HD
YD(HL$ LHADHc$HA;t1HHH$H48I;tHHA;uHHH$HuqH$HHpH$HI;t0H!HH
NHH
S`HHbH4H$$$H$$$8$tAIH$H;tH$H*HHH$dIH$H;tH$HS*HHH$*vHoH$HdHH:H#H$H0H$HAEHttHH
t,H3HHH$
H$(H$0H$8H$
rH$\(H$HL$0H$HL$(H$HL$ L$L3H$DH$^H9$tH$MD;u8$tHHuH$dD;D:(H$E3I;t
HHIH$I;t
HHP$HH H$PH)LH_H~HLHLhDpDqLxLLLLLLL$$DI;t
AEHPf$P$$R$$S$$T$$XHH`5(LpHHIAfACA
AC
$$$$$
$$0HHH%H'HLDDMD9$ptnL$HhHMA;t$H]H$LLLLLLHHHHHDXHHDPLlHH|$@
H\$8HPHD0HL$(H$HD$ LLH$HDL9tHKD9$tSH5}I;t;H$HXHH

pH@HHUH-@E;DDH5f}AI;t`H
HhvNHH
"ptHhHHHH
oMHxHZHL9t.I;t'HH
oHH#H{L9t2I;HH
IoHHH@I;tqL9t'HH
nHHHL9t8HL9t'HH
neHHrHL9t?I;t=HL9t,HH
KnHH*HA:t$I;L9eL9XE;H$0I;t0HH
mH$0HHH$0H$8uI;uL9$@HH
um_H$8HkHL9$@tPHH
+m-H$@H9HHH
lH$ HHiHH
lH$HHH>HH
plH$(HHM;tSH H$HE9HL9t(HH
XHZHHgHH$1H$H$PiHHPD$E3LLHHHHHDxHHDpHHDhHPHD$`H$HHD$X$`D$PDHHL$@
D8D0HHD(HHD DLHHDA:tEL9$v;HHlsH
=kHHHcL9$vNA:t4HEH"sH
cjH$HHAD$E3DHcHA;t1HHH$H4I;tH$HA;uH$
Hc$HA;t1HHH$H4I;tH
H
A;uH$
DDD8$atL95wt3E;tD$HsVHlH`
Hc$HA;t!HHH$H3
A;uH$!
HEHPHHHc$HA;t1HHH$H4I;tHHA;uH$AHA_A^A]A_^][HUH HHH]HUH HHYH]HUH HH/H]HUH
HHH]HUH HHH]HUH HHH]HUH HHCYH]HUH HH#YH]HUH HHYH]HUH HHH]HUH HHH]HUH
HHH]HUH HH
H]HUH HHH]HUH HHCXH]HUH HH
H]HUH HHcH]HUH HHPH]HUH HHWH]HUH HHH]HUH HHHK H]HUH HHH]HUH HHH]HUH HHH
]HUH HHH]HUH HHVH]HUH HHH]HSWH(HH
sHt
HsHt$HH
+g)HHH
H(_[HSLHHD$`H`mHrHBmHrIKRbD$ xD$ 1*#HDbwH$3bcH$bOH$b;H$a'\$ HaHaH[HUH HHaH
]HT$UH HH
heSHH]HT$UH HH
`e#HH]HT$UH HH
@eH
qHtHE@HHHH]HT$SUH(HH]0;uH
dHJk;@uH
dxH3MH=pt:H
dXHMhKHH
rp9HHMhcHH(][HUH HHMhSH]HT$SUWH HH
oHtH=oHt(HH
cH](H
HvH](HVH _][HT$UH HH=ot(H
bbIL]PIH

ko2H
HH]HT$SUH(HH=6ot,H
bL]8HoHIAHHH(][HT$SUH(HH=nt)H
aHnHHMHHXHxH(][HT$UH HH=wnt%H
Ra9HUXH
^n%HH1H]HT$SUH(HH'nHtEH
nHtH
nHH
a*U$HHH HH(][HT$UH HH
aHH
]Zm%@E3D8uNL9tEI;tHHADM;tI;IHuII;uHHAl%@H(E3D8uCL9t:I;tD8HHAtHHAM;tD8uIHAHl%@H(H(HtH
3H(HSWH(HHlHH%@uW8u3HtHjHH_u#HHH%H(_[3HAHHQHAQ HA(Q0fHSUVWATAUH(YXDHtVS;Qlv
H`uLg`A$
It$tL HH
HHHO`A+whHEtHHGPGXH(A]A_^][HSVWH@HHI3H@%uH@MHHtH3HkdHHT$ HPHL$ t@| D
;s.+CH9CLu HK@
SHHK@
H
SHuHK@HT$
H@_^[HL$SH0HD$ H3HHyHmHKpcHK`YHKPOHK@EHK(;HKH0[-HUH
HHM@H]HUH HHM@H@nMH]HUH HHM@HPNMH]HUH HHM@H`.MH]HUH HHM@HpMH]HUH
HHM@H/H]HUH HHM@HLH]HSUVWATAUAVAWHH3H9i8Dt ,D9oX@A9o8tA+O<;O8G
9uaHWH2;uMG09uBHW(H;u-HGH9u HGH9uHH9}AHw@HHn@(@:t"9FuHH;>HH HOH;t;HT$ aF9FuHAVH
A@,
VHT$ HT
9o t0F9FuHVH
A@,
VHWHk
9o09GHF9FuHVH
A@,
VHW(HOp"HHWpHW`HOp9GhGH;o0t|AHW(HOpD+DGxHOpD+L[HOp6HHWpvHW`HOpD_hD_HD;vuHw@
uHG`3oh@(3HW`HT
F9GXuHHOP;t>3HHHH@8tHHOPHHHWHHHGHHGHHW(H9o8tDo<HHA_A^A]A_^][HSWH(HHHtHH
ZHrHK(H[3H(_[HSWH(HHt0HBH0HBHHAHB HBIHA!HO0HOHH(_[H` HSUVWATH@HD$
AALHHt3HHHtHL$(`@HHHHYH
0@HEHHHH`HL$(IHHH7H/HH@A_^][HUH HHM(GH]HSUVWH8HD$ AHHH-
2dHl$xHE3DHHNHsH8_^][HUH HHMxH]HSVWH@HD$
HHHtHHOwHtHHL$(HHL$(iVHHHL$(HHHL$(E3H@_^[HUH HHM(FH]HSWH(HHHHtIHH
XHtHHHH
WHHV3H(_[HSWH(HHHHtbHHuWEH
WHEHtHHiHH

WHlHHH3H(_[HSWHhHD$ HHHHtHHL$(MHT$8HK
HWHL$(H;HL$(HT$8HL$(HL$(H`WHL$(HHL$(}SHL$(HHHHL$(vHL$(C3Hh_[HUH HHM(DH
]HSVWH@HD$ IHHHHtfHH
WHHHHL$(jHHL$(RHHHL$(H?HL$(3H@_^[HUH HHM(CH]HSUVWH(HHHHHHtH9u3"u,HHt HHH
+VHHVHH
HHHHHHHHHLHHtHuHt HNHF Ht%HN(H<U+HN8HHN3H(_^][HSH

HHHtHHttX3H [HSVWH@HD$ IHHHL$(HHL$(RH\$(HHHH
7HHuHHL$(:H@_^[HUH HHM(AH]HSUVWH(3HHH9HHH;tH9u3H9uHH9u?H9u6H9u-H9u$H9uH9uH9u
H9t&HHL+THHVHH9*u0H9ju*H9ju$H9juH9j uH9j(uH9j0uH9j8uH9j@tHLSgHLSHQH3H(_^][HSVWH0HD$
HHH5\Ht$PH8HtH{HK`H3H0_^[HUH HHMP&H]HSVWH0HD$ HHH5N\Ht$XHHtHtHKHH[@HH0_^[HUH
HHMXH]HSUVWATAUAWH0HD$ AMHHL=[L|$pI+tHHtAE2HHtH;u
3HzIHHtHB HHHcHfHt"HHQHHLHHH|tHKHtYrHG0HG(fHOAHwEuIHtHHO(H Z@IH0A_A]A_^][HUH
HHMpH]H(Lt3qHtf:uH&OD$Pt;tL)QL
QEt L!2LPL
PMEA9IJAH(H3HSH HH
tYHC H
cY3H [HSUVWHHHD$ IAHHHt"HHHtHL$(]LD$(rUHHHL$(HH
HYHHHH;H3HHL$(x33HH_^][HUH HHM(;H]HSVWH0IHtztmt`tStAt/tHT$"D$ oD$!p5LD$
ULwOdGLaO9LS+LAOL4OLOLNLN9HOHAH0_^[HSVWATH8HD$
ILH3I8@8hu'@8tHpHJ;u9h@8h@A<$HpsHH%H8A_^[HT$UH HHH]HSWH8HD$
HHHhuHpHu*hHpHH%H8_[HT$UH HHH]H(u0v'Htf:uHHKLMHA!3H(LL$
SUVWATAUAVAWH(HH3H9IMLtA9mDLIEI4@8n!^$t ;u
9n@HH;t%'D;tHH/H;tHH;tH\$D4W9n@t>HHt0HUH^8H
4$FHHHH3HH;tHl@8n"^(u@8n t F$@u@;u 9nPHH;t%RD;tHHZH2;tHH;tHw#D_V9nPt>HHt0HUH^HH
O#qHHHH3HH;t@8tH9n,|,HH;t LIfY@8tH]AIE;eH9Hu&H;MI|;usH,cH;tHH;tMHH
HH]HHMI;uH3H(A_A^A]A_^][HSUVWATH0HD$
AHHL%RLd$`I^HD$x3H;tHHtHH8QHHCqADHH2I3H0A\_^][HUH HHM`H]HUH HHMx>H
]HSUVWATH0HD$ AHHL%RLd$`IHD$x3H;tHHHHyH(kpADHHZI$H0A\_^][HUH HHM`:H]HUH HHMxfH
]HGHHSWHHHD$ H3H;tHH
IFH;tHH
?I1HHL$(cHT$(H
bK!:HNKHuxHL$(HL$(9\$0uH"KHuS|$0u/HD$(A<w at[ntOqtCst7ut+ytHL(_<1*#!HL$(HH_[HUH HHM(z3H
]HSUVWATH HHHtHH
hHHJwE2D$PHHt-Ht(HT$PHtT$PHAH
IHE@t
T$PHHtHH@uW1HIHt@t
~u@3H
A_^][HIHI3HH`HSWDILD;J3D;Dv"LII+AB9AIE;rEHD;JD;Dv0IxLRH_I+AB9utAB9ulAHIE;rA9H`Dv!LJXMX
XM+IK9u?AIE;P`rEH(D;J(u(D;vHR MP L+A9uHA;r_[2HSH HHIXHKHHK8HK HKHH
[HSUVWATAUH8HD$ IELHHYH H\$(HHOHWH;Qt$HIHE3E3P
tOHOHGHAD$pHGHHHLL$pEIPL$pHOHWHOHQHtL$pMHmH8A]A_^][HUH HHM(~H
]HSUVWH(HHIHHSIAH;Qt HIE3E3HP
uLHKHCHAHCDPLLPHHDHHPL$PHKHHKHSHQtL$PH(_^][HL$SH0HD$
HHH;H!H!HHKxHHHHK5HK+HK!H(H\$@HK8HK(HKHtHPHHtHPH0[HUH HHE@HU@H(HHH#H]HUH
HHM@b!H]HUH HHM@Hx.H]HUH HHM@Hc.H]HUH HHM@HK H]HUH HHM@H+ H]HUH HHM@HH
]HUH HHM@HH]HUH HHM@HH]HUH HHM@
H]HSUVWH(3HHzzz(Hj{`n{;kv#HKHH{H{kHCk9~Dv*LHNHSAAAADIADD;Nrn{;kv
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IHKXI I IID;^rF0C0n({(;k,v#HK HH{ H{,k,HC k(9~(vHHF HK
H;~(rH(_^][3HAAAHAA A$HA(A0A4HA@AHALHAPAXA\HA`AhAlQpHHHSH AAHu#H HKHtHPH3H
[HSH AAHuHI HtHPH3H [HSH HHI HKHHt HYL
DHH [HSUVWH(HcYHHt=HHHH,Ht"HMHMHH8uHH(_^][$E3H@LADADALA
DA(DA,LA0DA8DA<LAHDAPDATLAXDA`DAdLAhDApDAtLAxDDDDHH"@HALDDHLDDHSWH(HIHIH
bHAuH@H(_[HMHDL$(MMIH(})HL$SUVWATH0HD$

LHH|$hHc_Ht+HHHH,HtHgHuHI$ID$IHH#HKpeHK`\HKPSHK@JHK(AHK8HKH0A_^][%HUH
HHE`HU`HHHH#9H]HUH HHM`HOH]HUH HHMhB(H]LL$
LDHTSUVWATAUAVAWHXH$HIML3H$@8MQIAHLIHH|$@|$HH|$P|$XH|$`|$hLD$@IHEPALL$pD
D9|$HvFHL$`HLPHL@H;tHYL
DI{H@XH$$$H$$$H$$$H$$$H$$$H$$$H$$$HHL@H$@:H$HHHHHHL`HLPHL@H;tHYL
DIzH@LE8HU@HEPALBID$0H$H;H9HEH8H$+H$HHHL$`HL$PHL$@H;tHYL
DIyH@IH9D$0A8<$tITH@:I$H;tHPI$A8|$ptr<H;t3H5HHI$H;t
HHPI$H;tHPI$I$H;tHHI$rH$@H;t3H>HHI\$xH;t
HHPI$H;tHPI$ID$xH;tHHI$IHHHbHUHHH;H$.H$!HL$`HL$PHL$@HtHYL
DIwHi39|$HHH|$@H;H%H=HH L$3H;o1D;;uH$3H; H$ 3H;u9$,hI$HHPH
H;tHPH$H;tHPI;t$H!HHHHHHHHL`HL$P}HL$@HtHYL
tDImvHU@H$
HtHPH$HtHPH$H$ HHHHHL$`HL$PHL$@HtHYL
DIuHH$ H;tHHPH$H;tHPH$ZHMH@H$3H$&HHL`HLPHL@H;tHYL
DItH@^H;tHPH$H;tHPH$HHH$~H$qHdHL`YHL$POHL$@H;tHYL
FDI?tH'@HIL7ILHIL$(H$D$E\$8IL$@H$ILPHIL$`H$AIHP $HE@B$D@$$9|$H
HH$@Lt$@I$HPL(L;uLhD:@8$tSH|$8IELD$8H#IHL$8H;t$$H$P%HL$8H;tHPH|$8IELD$8Hy#IHL$8H;t%$H
H$
PvHL$8H;tHPH|$8IELD$8H#IHL$8H;t7J9D3HFD3JT3P=WDPHL$8H;tHPH|$8IELD$8H"IHL$8H;H$AD8L$L;
HIHIPH$D8H${Hf8H$HtH{H,?H3HH$H$
3HtH$ACfTCIH;rHL$8HDHP3HH$\HL$8H$H;tHPDH|$8IELD$8H!IHL$8H;t%DD$0EHD:PzHL$8H;tHPFl3H|$
8EtIH>HHD$83H$@EtIHHH$@E3ELLL+$Er33LDPTXtA9,tAI;rHHx!H$HI8AD$HI
M33T$htLD$`A9,tAI;rHH'H$HDH+KJI EIA$E;KAHHt;$uL HHP8LIHD|$ LP(HHAHH
H$;l$HDqLHL$8HtHPH$HHyHlH_HRHL`GHL$P=HL$@Ht HYL
4D +nHHL$8HtHPH$HHHHHHL`HLPHL@Ht HYL
D mHnHPHSHFH$9H$,HHHL$`HL$PHL$@H;t HYL
D lHXHL$8HtHPH$HHHHzHmHL`bHL$PXHL$@Ht HYL
OD FlH.
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D kH
HHL8HtHPH$_H$RHEH8H$+H$HL$`HL$P HL$@Ht HYL
D jHd HH$)HL$8HtHPHHHH{HnHaHL$`VHL$PLHL$@Ht
HYL
CD :jH"HL$8HtHPH$HHHHHHL`HLPHL@Ht HYL
D iH}HnHeHWHJH$=H$0H$#H$HL$`HL$PHL$@Ht HYL
D hH\L$L;tI$H@8|$0PH$0$8$<HaHH;txH{)HH~HN +HHt$8H;t
HHPDL$hAA;,H$LLNH$HEIE3HAR Ht
HHRH$0yH$HHHHHHL`HLPHL@Ht HYL
D wgH_HHPHN3H;tHPH^I$o
E@:AEELhHH$D;H$H$H}A;u4HE3E3HHP 3;HHPH$3A8l$pulD:ugM(fHH;thH'HHk HH;t HHPH$H;t HHPHK
H;tHPHs HsH{g(HH;thH/'HHk HH;t HHPH$H;t HHPHK H;tHPHs HsH{0HH;thHHHoHnHHt H3HH$0Ht
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ND EdH-$8HH$tHHHH$t*3DzH$0HHH:HI+uL$M3H$@H$HthI$uQK`HH$Ht!@H.$HHkHEHP3Ht
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HHRHHt
HHPH$0aH$HHHHHHL`{HL$PqHL$@Ht HYL
hD _bHGLHILx-H!Ht

HHRH$0H$HHHHHHL`HLPHL@Ht HYL
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H]HUH HH@
H]HUH HH0H]HUH HHM8jH]HUH HHjH
]HUH HHG
H]HUH HH@jH]HUH HHH]HSH HuoHW3H [HSVWH HHIIAguHt@*Ht3HHtHOHw@HOl3H _^[HSVWH
H;HHtRz;yv0H HCHCHw{H{tHHLI\HH _^[HSVWH HIHIHAHA^tHAH^u
HC3@H _^[HSVWH HHA(HI HHtHPHG Ht HHPHOHtHPH_HLG Hp
HH _^[H HSWH(HHHt HHPHKHtHPH{HC@H(_[HSVWH HHHL_@HOL\$@HOHtHLD$@HP3H
^[HSUVWH(HHID$PHILL$PAPHtL$PHHtHOOHo@HO9H(^][HSUVWH(HIHIH?LH\uH?H|LH\tHLH\uHHS
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]HAHAHHSH AAHuHHy3H
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D HX3HHtHYMD ?WH'H/wH@A]_^][HUH HHMpH]HSH AAHu#HhHKHtHPH3H [HSH0HD$
HAAu.HL$@HI HtHPHKHtHPHy3H0[HUH HHM@H>bH]HSUVWATH
ix3H;jHt&;LtHHLHOLgoFxD;vGLHVpLOA+ADHCHBLICLFxD;rVhHFhD;v=LLHA+AHHF`ALHIIIFhI
D;r;o(t&;LtHH&LHO FLg o(9v.LHHO AHAI;rH
A_^][H3HSUVWH(HcYHHt,HHHH,HtHMAHuHH(_^][HSUVWATAUAVH
HcYHteLIHII4HtGHcnt/LIHIM,MtIM%AI5uH)H!uGH A^A]A_^][HSUVWATAUAVAWHXHD$
LHE3D8aHt^I;tHHIH_I;t HHPHOI;tHPH_HGI;tHIHGbGH$I;tHxHIHI;t
HHPHOI;tHPH_HI;tHIHGHOHHW`A;ED9g(MHG
I,Ld$8Ld$@MA;|LD$8IA;<LD$8HMJA;GE9D$LbHt$8I;uHt$@I;uH$I;t
HHPLHLHIHH$I;tHWDPA;(H$I;tHPLHLH6HH$I;t"MHQyH$I;t?HP7ADEE;vHMHD8`t
HA;rAWA;LHLHHL9$ttGXD$AA#HHD$(H\$0E3D9OXv"3HGP*fL*AHD;OXrH$HEHPE3A;dHHOHHT$8P
H$I;tHPHHPHL$@I;tHPHL$8I;tHPAID;o(CHL$@I;tHPHL$8I;tHPHL$@I;tHPHL$8I;tHPHL$@I;tHPHL$8I;tHP
@@H$I;tHPHHPHL$@I;tHPHL$8I;tHP}HHPHL$@I;tHRHL$8I;tHRNHH$I;tHPHHPHL$@I;tHPHL$8I;tHP3H
XA_A^A]A_^][HUH HHH]HUH HHM8HH]HUH HHM8ZH]HUH HHM8ZH]HUH HHM8vZH]HUH
HHM8VZH]HUH HH3ZH]HUH HHZH]HUH HHM8YH]HUH HHYH]HUH HHM8YH]HUH HHYH]HUH
HHM(H]HUH HHM8VYH]HUH HHM86YH]HSUVWATAUH8HD$
HHH;uH];_v6HHHtDGIHH>LHH7_t^E3L HHD$pHt)HMIHHL[L^KNCF3OHH4GIIuHH8A]A_^][HUH
HHMpZH]HSUVWATAUAVH HcYHteLIHII4HtGHcnt/LIHIM,MtIM8IuHHuHH A^A]A_^][HSH HHI0HKHH
[@HL$SUVWATAUAVAWHXHD$
HE3D8t3ZA(A;D8iIu!HH,MD9i8t!HHMLl$(Dl$0Dl$4D$DALd$8t$@NHH$I;tHT$(HLcsCIHO
H;Ho`H}H31D3mED$AA;Ly`HG
ILY8uvG(;t%DEHOpH$ntHE3D;v,D$C<Hx0DD;rL`D$DD$II;o(TE3G8IA;AA;vyIHG0DTD;Whs];OhsULHW`
DLD;sCAA;vHL+uADHOpH$H;o8r
WAE;v9AD9oxvHOp9tH;GxrAA;uHo/A;rHo`EAD9mv?AD9mvHEH9tH;MrAA;}E:uU0D;UrE:u
WeHA:u
WN9HHED9,LE AID9mvHED9tHH;UrII;|HGpLHHED9,uOAD9m(vHM
D9tH;E(rAA;|*A;t%DIHBHJILAD8oIDwhEA;v>C&HALH$HOpDID;rE3DA;Lw
LLA$3HD$(D$0D$4DDt@Ld$8H HH$HtHT$(HLcsC3HIPHI<-3L+xH$Hc_(LA;t(IIHG J
HRHuE3hvW7E(EvW%HA:uWH@3HXA_A^A]A_^][HUH HHM(H]HUH HHH]HUH HHM(H]HUH HHH
]HSWH(HHbHVHJH>H2H&HHOpHO`HOPHO0HO HOHtHPH(_[LL$
HTHLSUVWATAUAVAWHH$IL3;hI9vuHI|;OINHP

HHHHHHt$0Dt$8t$<Ht$@tHtLH$$$ANhL$xAE9vZPH$H;tHNHHHL$0~HfLHLAE;rDd$8HD$0AE9L
cHHt#@HHHFHF3H$Ht
HHPIEHF
HHtH0HHR3HH$HtH03HHL$@HHPAIE;7l$H3Lc|$xEtINHD$ E3E3P(A;r0lL3H;t#pHIEHIEIuIu
LL$L;tIEIPHt$`HHXIL$3H$$$H$hI3T$|IEHH
H$INHPL8MuLxHD$hILD$hHIHL$hHtHPHD$XILD$XHIHL$XHto$HxHffHHALHPNH-
HL$XHtHPHD$pILD$pHIH$hLH$N|L|$XH|$p(LE3I;tDxHIM~M~M~ ML$M;t
IIPM~ HL$pHIPD`HT$XI}IIPL$PIHt!IIGHL$pE3I;tHPHL$hI;tHPT$|T$|H$
;TxH,HL$XHtHPHL$hHtHP3MtIEIPH$HctLl$@I\HHTuLl$@IAIcEt/Lt$0IDHH;HtHGHEuLt$0I$IIPHL$pHtH
PHL$hHtHP3MtIEIPH$*HctLl$@I\HH{uLl$@IhIcEt/Lt$0IDHH;HtHFH7EuLt$0I#AE3INH3PINA:HHI;t!DxHH
L{HKL{@II;t HHPHHH
aHI;tDxHHLLII;t HHPHLMt IIPHOHtHPLH_E3A;vLD$@DIHHZIIuE9 LHD$`Ht@HIIGE3L|$`Mt
IIPILLHLEMt IIPIOHtHPMwIGIHLPAE9vEE3LPHIHH$AIE;$rDd$8L$PHL$LEINHLD$PLD$
LHP0DH$Ht
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IE(HE
INHP8HH$pAIE;~hrl$HDd$8H$Ht
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@o!G$G(@o)HO0Hn`nhnlHnpnxn|HHHHHHH@9ku @8k)uD$hHHL$hw6HH!HSHOxD[D_
C!G!CGC(G(C)G)HS0HO0H.HnHnHH8_^][HUH
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HC3@H _^[HSUVWAUAVH8HD$ DHQ4LA8HHIHA(HtA;u
38YAAEt!uA;@sI88u~
ADHD$pHI@HDLD$pP(tHL$pHtHRHD$pHHtHHRHD$pHOHtHPHD$pHoGt~t~
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{L$ptA;AGL$pLS3MtIDLL$pIIPL$p{tDKILpCHt+LH)C uHuHuL$pu<u.3H(A^A_^][Hu9C0t3Hu&CH
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DADED$D;A:tEAEAI`;t_A;uD;sIX$$A;w#HIHLL$ AQD$AD$$ $0L$PHIHPA;tHHRHH$I;t
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s9D8$0tAH$BD9uA;rDQAD$E+AD$L$A;s#HIHLL$$D[4AH$HD8$0IEHK(Dk0DcDcHHDD$A;tVI;t
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$8H\$0HHD(D M8IH$0DK=@@$A:7E;txH$]H$I;tHPI;t
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H$E3I;tHPH$I;tHPH$I;tHPH$0H$I;tHPH$I;tHP@HA_A^A]A_^[HUH HH/H]HUH HHH]HUH HH.H]HUH
HH0H]HUH HH.H]HUH HHs.H]HUH HHS.H]HUH HHH]HT$UATHHH%
E3A;tH@HHA\]HT$UHHHjH]IApLH;w ;sIAhHI#y$u
HAIHA(H9AHGAI3HSUVWATH E3AII;HHt HHPHKHI;tHPHsHHk8{@DcD;{\DcXv
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SDHC8LD$`AQ8t
HL$`HHtHRHL$`HSHtHHPHL$`HsHu\CDHKHH3P@HHL$`HtHPC@9CDSHL$`HtHPxHD$hHLD$hHHL$hHt
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xH{`H{hH(_[HSWH(HaHH3H;txHHHPHXxHX
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0 C+tB.\$$8|ET$$8}tT$$;HL$8\$ 8xDT$ 8ztT$ 8{t T$ 8~tT$
HL$8sHhHL$8HpHL$8stHL$82_H(H;OHL$8?@HL$((\$T;@H8LHL(I!uZHHL$(0@uHT$X!mHL$XrHT$XH
L$(KIu2HiZHL$(s4HL$X7HT$XHL$(TMHT$(H\$T)HT$(HL$8HL$(t9\$P;sH94wT$P@@HL$8IHL$8HL$8V3H
pA]A_^[HUH HHM89H]HUH HHM(H]HT$UH HHH]HSLH@DHA@NLH IM+ICIKIKMCGtpHL$ EHL$
HT$0DL
DAvEAHIHIuAHHHT$0t%A tHL$
DGDL\$0u2H@[HSWLT$8L\$@3AIAAuJH9t?LL;t3HGHHH+HH;sIXIAAQ3_[HSUVWATAUAVHpHD$hMHI
;u3C$oLIL
LGIH+LD$ HD$(HD$0HL$ 4FDE2 HL$ kD@DAHT$0HL$ L
vAHAHIHuAHHT$0@tHL$ EHL$ E33@ t"HL$ EHT$0HL$ H4
HHT$0D$8H;oH=~VHL$8yDJL8DA]98AD+D+AtHRqUHtDH|R[UH4DHiR@U
!uIH=[uHT$8`hHL$8u_H=D$8oHT$9hE3D8\$9tIB|9uJL9:HcH+HuVDkuH=ZuZHT$8gIu H=8u
H=XZ'H=r?HT$8kg3E38tIA<8u38T$8tHD$8H8utEtA;+H+LTpA:Hut(JLDpA:HT$8HD$8L+ADHuEDDp

}HL$XeyHT$XH?EtDp L$`uHLpH+-A;wE+HctHTpLD$XLAIHIuHL$XnDA\7HL$XnnEt!rDp
Dp.Dp.Dp.HTpIHpA^A]A_^][HUH HHMXH
]HSUVWH(HIIAxHHHH{7tbt3;Hx38LH;`HX38LHh;H38LH&H`t?HX9,|LLAAAII+fHKM3HfHC8@
"HLD;H38LHHf@KH KtqtVt8;H38LHH)H`tufKkH`LH$V3;sH8Lt(38Ht-@fC H`HfffC3H(_^][HSH0HD$
HHHtHPHHHf3H0[HT$UH HHH]HSH HHXjHHjH8jH(jHjHjHjHwjHkjH_jHSjHGjH;jH/jHH
[~3AHAHAAAHA A(A,HA0HA8HA@HAHHAPHAXHA`HAhApAtHAxHHHHHHHHHH
$H(04H8@DHHHPHXHHL$SVWATHHD$@IMHHHP H\$HHt
HHPHD$0HtHLD$0HHHDPDX$xMHHL$PCDt8HL$PHtHPHL$0HtHPHt HHRAxHHLHHD$(HL$
LD$0HL$PLoDt5HL$PHtHPHL$0HtHPHt HHRApHt HHPHHtHPHHL$PHtHPHL$0HtHPHt
HHPH3HA_^[HUH HHMHH]HUH HHM0H]HUH HHMP~H]HT$UH0HH@HP
E8HTH0]HT$UH0HH/H0]HSH HHfHfHeHH
[HSUVWH(HH8Hc_0Ht,HHG(HH,HtHlHeuHO(H(_^][eHL$SVWH0HD$ HHHHHAH|HAHIHA3q
HY(H\$XH@@@@@@@@HHHHHGHHGHvHGHHHHPTHXH`H6uHH@@HH0_^[HUH HHMX>H]HUH
HHMPH(H]HUH HHMPHH]HUH HHMPHH
]HSUVWH(HIHIHLHuHwHLHtHLHuHS6HLHuHSHLHuHSHHHH#HC 3@H(_^][H/H#HHL$SUVWH8HD$
HHbHbHbH`bHXbHHsbHHHtHPHO`Hc_XHt,HHHGPH,HtHHbuHOPH8_^][bHUH HHE`HU`H(HHH#H]HSH
A A HuHa3H [HHH7HSWHHHD$ IfD$(fD$*HD$0HLL$(P0tJD$(f=@uL$4H
D$0HHH$ftWHH$3HL$(HH_[HUH HHM(%H]HSWH(IHA&t-
HQxLfA;su*3HwHH%uWHx3H(_[3HBHApHAxHSUVWATAUH8HHL$ qHL$ e39\$(DkHD$
HLhIK~LH$L;uHD$ Af<heDsI+H;|$(tqHD$ Af<Hfu
M;MBL^pGHbt.gt"ktmt
tu6H
IDL^x;|$(<WHL$ ^H8A]A_^][HSWHHHD$
HHtYttW}fzFHT$hHOJ#u0HWHL$(oHT$(H~HL$(V^5D$htHHCpHCxHCp3HH_[HUH HHM(zH
]HSWHHHD$(IHHL$0nHL$0c|$8u
W}HD$0f8suI3HL$0t|$8uHHMf;t
W=HT$0H)HT$ HL$0"HHL$0ctHHU"HOHL$0ctHH*"H
HL$0ct3D$xHT$xH!tD$xW#tHHL$0Vct(HH!tE37HHL$0ct!HHz!uHYHL$0bt!HHD!uHHL$0bt!HH!uHHL$0wbt
HH ubHHL$0DbtHH :HHL$0btHH} LHT$0HmHL$0-[HH_[HUH HHM0H
]HSWH(HHBH(AHBH(HAHBHAHBHAHBHA|lD[8D_8C<G<C=G=C>G>C?G?C@G@CAGACBGBCCGCHH(
_[HL$SH0HD$
HHHtHPHKxHtHPHK H0[/HUH HHM@H H]HUH HHM@HxH]HSH HHI0YHK YHKYHH
[YHSUVWH(HIH9KuCtHvHgY{HtKHLIHIHlYH;sCHH@EEDEDCHKH(_^][HSUVWH(HcYHHt,HHHH,HtH
M(XHXuHH(_^][XHSUVWH(HcyHHt,HHHH,HtHM(XHXuHCXHCCH(_^][HSWH(H#x)HHfyuA;B
;G$H
#;LM MiH(_[HL$SUVWATH0HD$ HHcyLt^IIHI,HtCHM WHMWHMHt HYL
rD HWHWuHH0A_^][jWHUH HHM`H]HSWH(ILJLBHHOCyW
HH#3H(_[HSVWH`HD$ HHu3@HD$(D$0D$4HL$8&aHL$H[gHcHL$8a gVHHt@f@f@
H@3HHL$(OC HKHDBHL$(a!AHL$(L!AHL$(7!
DBHL$($! UHHtH@@@3HH_NLD$(HHcHL$([H`_^[HUH HHM(H]HUH HHM(HH]HUH HHM(H
]3HAAHAAAHA A(A,HA0A8A<HA@AHALHAPAXA\HA`AhAlHApAxA|HHHHHHHHHHH
(,H08<H@HLHHL$SUVWH8HD$ HH@SH0SH SHSHSHSHSHwSHkSH_SHSSHcHt/HHHH4HtH"SHSuH
SHRHKpHK`RHKPRHK@RHK0RHK RHKRHH8_^][RHUH HHM`H]HUH HHM`H@H ]HUH HHM`HPbJH
]HL$SUVWATAUH8HD$ LHHAAZtHHQH_tf3L0QHHD$xHt2IMH)HHHSHN\HS HN
c3OHH4GHIuHH8A]A^][HUH HHMpH]HUH HHMxBQH]HUH HHMx2H]HUH HHMxHH]HSWH8HD$
H0PHHD$X3H;tHCCHK&[HK \aHHHCIHH8_[HUH HHMXvPH]HUH HHMXfH]HUH HHMXHH
]DD$SUVWATAUAVAWHHHD$
LHHIOHWPHL$(3L%@:\$0t+3HD$(HHyuIZ\$0H;ru;HL$(O@S;DHLDHHIZkAo

\$0xt93A9Wv3IGl,HA;WrLG`3HL$(<Ao!\$0E2E3E3HD$(I4HHO(o$H
NHHtH@@@3HIWGLHH]HCEHDEH!tH=twH=t)H=tH=HDYAQHxHHfzuDZ*HHDA
DMHYoDAHHH;sHH9vH@DI\$0D;yEHc\$0HHAAHHD$(H4HtHHLMuZuAHH#H@Hct1HHHD$(H4HtH~HLu
3HL$(LHHA_A^A]A_^][HUH HHM(~H
]HSVWATAUAVAWHpHD$0EMLH3H*HO\$$A;DKHL$`m]HL$`R9\$huHL$`:LW@KdMHT$`f:bfA9tHLW3H
L$`)cLD$<HT$8HL$`%D;tHL$`KAHL$`f9:tKW3HL$`b$L$HT$@HL$`D;tHL$`sKA{9$tHL$`XKW^9\$htHL$`>
KWDHTPHL8HHABHHT$PMHL$`K,HOD;tHL$`JAHL$`Jt$$Z\$(;WHsHG@LA9XuA9XuT$(;vo\$
;s)L@HB9TrB9rL$ WuD\$ D;sAH@H)T)ADD$ HO@|\$,;s(H[HWH9Ts
9s\$,W 3HpA_A^A]A_^[HUH HHM`FH]HT$UH HHH]LL$
DDHTSVWATAUAVAWH0H$0IAHE3ML9tLLt$PDtXDt\A:tD8tAEDpEDpAD\$eDE:tD8t
EDEDDT$dA:t D8uA:t
EDEDDL$cA:t D8u
A:tEED|$bM;tuE9tlA:uE9AD$pD\$eE:uE9ADDTdA:uE9ADDLcA:uE9 AD|bHXD$D;g
Hu\H$G\$XHLPt+$H<HtHO(GHGHL$P~G@HHHL
LLAHP(tYH$8G\$XHLPt+$H<HtHO(GHFHL$PFXH$W3@$@$@$@$@$9$$9$$$$D$@$H$$H$f0$L;fA;
YHHID$D:u#HL$D$$C
HHA;@sI8@84t
D$@$D:MA;s'I@84tIHHAH@$A;s'I@84tIHH$AH$@$A;s'I@84tIHHAH@H#HDHD\$XHLP;t+$H<
H;tHO(DHDHL$PDW@8$H$D:Oft$hftjHtpHLL$hA
AHP0trHLhHDH$D\$XHLPt+$H<HtHO(CHCHL$PC#D$h3f;u
@$f=tuHL$hvHCHrC\$XHLP;t+$H<H;tHO(ACH9CHL$P-CWDpDHLh@8pHHD$
L$A
AH^tgHBHB\$XHLPt+$H<HtHO(BHxBHL$PlB3@8HHD$
L$AAHtgH$BH$B\$XHLPt+$H<HtHO(AHAHL$PA.3@8HHD$
L$AAHtgH$yAH$kA\$XHLPt+$H<HtHO(:AH2AHL$P&A3fthftjHt$pHLL$hAAHP0trHLhH@H$@\$X$
HL$Pt+$H<HtHO(@H@HL$P@D$h3f;f=tuHLhGHQ@H$C@\$XHLP;t+$H<H;tHO(@H
@HL$P?W`HT$pH$"RH$5HLhfthftjHtpHLL$hAAHP0trHL$h|H$?H$x?\$XHLPt+$H<HtHO(G?H??HL$
P3?
D$h3f;u@f=tuHL$hH$>H$>\$XHLP;t+$H<H;tHO(>H>HL$P>W
f9tpAHL$hkft$hftjHtpHHLLhAAP0trHL$h0H$:>H$,>\$XHLPt+$H<HtHO(=H=HL$P=LD$h3f;u
@$f=tuHL$hH$=H$=\$XHLP;t+$H<H;tHO(j=Hb=HL$PV=Wf9tpHL$h@8$t-
@@$@$@$@$H$$@:u#@:t($A"$HHHHH$PO@8$H$@8$fthftjHt$pHLL$hAAHP0trHL$h6H$@<H$2<\
XHL$Pt+$H<HtHO(<H;HL$P;R
f|$htuHL$hH$;H$;\$XHLPt+$H<HtHO(;H;HL$Ps;W
HDpH3H;t@8$tuHL$h)H$3;H$%;\$XHLP;t+$H<H;tHO(:H:HL$P:WB
HL$hH:H$H;tH$HHHHL$P73H$:ED$$E3LDDLDDD$
D$!D$$D$(D$)H$0JL$PDXD\L$`D$hD$lD$pD$qD$tD$xD$yH$LJH_DGH$H[A;H$o9H$`b9H$PH$0G9H$:9
HH9\$XHLPA;t+$H<I;tHO(8H8HL$P8>H$PHA;H$8H$`8H$P<H$08H$u8HHZ8\$XHLPA;t+$H<I;tHO
((8H
8HL$P8yG$$$(DtxLLIL$HMID$)D$8H$0fD8H$I;8L$HLHpPA;HHI;tHPH$:7H$`-
7HPH07H$7H$H$6\$XHLPA;t+$H<I;tHO(6H6HL$P6
D9p)A:tH$I;tHH$0HH$@@t$`AD|a)A:u1D8itpD$)HpH$0H$)A:tHD8t\$a
i\$aADd$`A:t$yH$0H$jH@$ABL$`G(DH$H$D8uA:u
LHPH$A;tD8xtD8$!u
D$D$$ G,$$HHHH
$($)$*$+L$`D$hL$xL$L$L$L$L$H$`mH$7L$@IL$@HIH$@HD$@HHD8Lt$0H$xHD$(H$HD$
L$@LD$PIHA;H$@I;tHPH$H$I;tHPH$I;tHPH$`8
H$I;tHPH$z3H$`m3H$PH$0R3H$E3HH*3\$XHLPA;t+$H<I;tHO(2H2HL$P2IHLPLLHH$@>QH$@I;t

HPHHI;tHPH$I;tHPH$`H$I;tHPH$32H$`&2HPH02H$1H$H$1\$XHLPA;t+$H<I;tHO(1H1HL$P1H0A_A
^A]A_^[HUH HHMP.H]HUH HH~H]HUH HH}H]HUH HHMhH]HUH HHMhH]HUH HHMhH]HUH
HHMhbH]HUH HHMhBH]HUH HH_0H]HUH HHH]HUH HHH}H]HUH HHCH]HUH HHPH]HUH
HHPH?}H]HUH HHPH]HUH HHcH]HUH HHH]HUH HH@H]HUH HH/H]HUH HH@H]HUH HH@H _H
]HUH HH@HxH]HUH HH@H H]HUH HH@Hx_H]HT$UHPHHHP]yLtQH HApHPH@H;tAxIEP
BDJt#EAAH@L\LZpt
KIHD
ICA@H(DQLAu3H.wHL$0D$0KRIDIApABAAIApHL@H@H(H(HHIH;Jr3HvHL$0D$0vHHHJH(MtPSWH(HH
IIHCLH+L;v3HvHLPDP'HHIiH{H(_[H(LAHAI+H;v3H?vHL$0D$0IHAH(HSUVWATH
LYHLAM+MLALL+xLIIIJH A_^][MtPA9LL+AADtGMt*A@HLL+H@r
LHu3HpuHLPDPED$UL#ILH(H=vD$8HuHL$8D$8H(H(HQLBL;Av3HtHL$0D$0HLAH(H(HQLBL;Av3HtH
L$0D$0aHHLAH(Q3tH L9tHIuHSUVWH(IIHHLHHHuD$pEE H(_^][HL$SUVWATAUAVAWH8HD$ MLHA
MHI
3;A7D8euW}zuQ}uK}uE}'u?}u9H]HMV;tF@8;uHA;r@8}u@8}t3L;tI9>u)HHD$(MHHYLAL;tII+I;sD;$IHS
L$IP;$;DAH|HH;A7D8't,D8gtD8gtD8gtHH;vH
HHH;wQDD+?7u;zu5u/u)'u#uHO;G$2u+`LHA HL3IECA HH(AIH$HHIH E3E3IP H(H8A_A^A]A_^][HUH
HHM(uH]HSH HAH Ht
HPHCxH [HSUVWATH IH3HKDfH9XtUH9HtLHXA@N$H,I+Hs-};{v
H5HHHLD-J`{H
A_^][HSUVWH(HII+HXtmHHtcLXCI,IH+H@sESH~u8HHHHNHht"HA\f=/fADfHHu3H(_^][HSWH(YHH'HH
H&HH&HHH&3E3E33H9E3E3HA|Eu:tHAI;OHDHG0<uHHAuHG0AD;rHE3AIID;uEt;OHcsHHDH;OrH(_[3H
%oHL$@D$@HSWH(;QHAv$H %HHCb%{H{;{Cv+HKp%HHCHC)%{HC{H(_[HSWH(;QHAv$H
$%HHC${H{t
HL3TH(_[HSVWH0IHHHOp-tcHu3HnHLXDXHOp;Cr3HmHLXDXHHLHD$
MHM>H0_^[HSUVWH(IHHHH3LHDBP
ubH3LDBHP uHHHE3E3HP u.LHHuHHPHHtHPH3H(_^][HSWH(HHIpH_pHHHHOpHuH(_[HSVWH HHIpH
Ht-H_pHHH<HOpoH;uH _^[3HylHL$@D$@HSUVWH(zHH;zBv+HJ"HHCHC"{HC{HsHt38t
HMpHHHuH(_^][HSVWH A;PIHA@v$Ir"HHC0"{H22{Ht"u
HNpHHuH _^[HSVWH HHIpIcuLHZD;{Cv$H!HHC!{H{t
HLH _^[HSUVWATH@HD$ AIHLA`D$0D$1LIHL$(vG;wv+HOM!HGGHH!HGwwHoHt#H38t
IL$p~HEHHHuHL$(H@A_^][HUH HHM(H
]HSUVWATAUAVAWH(HL3D;u#$HIjH$$HjD;D$+HH.IDMD$IL$DI+L;;I$HMvIAHIHHuAKID$H+tIID+D+A
tTIDL;|+t0HL+H;t
H|+L;H|+tIgL|+HD+HT+MIHL+H;t
hH|+H|+$Dl+H A;$An;nt%;HtHHHNH^n;v,LDH^IIAIIADuD;si$H{hH$$$HXhH$$$H5hH$$oA+;^(t%;HtH(HHN
HHn ^(uaD;v29~vHV9
tH;Fr;| A;rHF A;uA$HgH$$;vH^
IHHuH(A_A^A]A_^][LHHLQXICMHA`IBIIIKIKI+ICL\$(L;\$0tHcHLXDXh3FHHHSUVWATAUHXHD$
LHHIp#D HIMIEAL$HIEEe33EtF3IEH4HOpHH;r
HA;r3HeHIMJ4HOpHH
ufHD$(D$0D$4HD$8D$@D$DLD$(AHHT$(HHL$8HL$(H_pH3HHHOpHkHXA]A_^][HUH HHM(H
]HSUVWATAUAVAWHHD$pIHHHMpDD$E3EDt$$DthDtiLHHL$`cL]pICIHD$0DfHNH2LvHAD$HHHFH
HNPLvPIHFPHNXLvXHHHFXHN@Lv@HHF@3H|$H|$P|TLt8Dt$@Dt$D3L$(Lep3T$
E3L|$X9$$HMpHVXHAH+D$0HJE3I]@D${IDITDIDH+L;I$L3tIHAHIHuIID$s<E3t#LVxBI;tI;sDAD;uLHNx
oAAt%IwD@IcEA@A
tkI?DIL$I+L$L;H!uu=HEpHHH8Vps*Vp%HuuHEpHHHT9VtsVtMD$$$;L|$XuD;u
3DwE;=AHL$HFHL$8:3EtTHMpvA;QHD$H<@HMpO;HL$8<A;rAA+t5E3t.IA;HL$H<AD;r3t
HL$89tH;r$$;HF@Dl$$F,DDl$$HFHL$(B+;L$(HFPB8T$ T$

IL|$X;$H|HLt8$$H`H$!$$H`H3H?`H$$$H|`H$$$HY`H$$$H6`H$w3$H_H$Y$$H_H$6$$H_H$$$H_H$$$H
_H$$$Hi_H3H^H$$$H(_H$iHEpHXLL$0I+HHF@DD,HFHHMpHVXHAI+HJH;^ht8HN`Ht
HF`HFhHt*HHF`H^hLL$0HtLIHN`-IHHL$`lH;HN8HF8AAHgHF8Et3HMpHN8HHHuHMpHtLH
u!LF$H+HVH#H]pH]HHHMpIHuHA_A^A]A_^][HUH HHM`H]HUH HHMHJaH]HUH HHM8*aH]LL$
LDHTHL$SUVWATAUAVAWHHHD$(MHLZHJ0^E3Lw0HHHG0AD9wvIHG0H;WrIOpLHH
u)AD9wvIIOpHO0H;wrH
t$H t(I;tI_pHHHH AID9wHG0D$E;tkAE;v,IOpHHH$LL;rTAE;rLG8HW@HGP0II;rNI+L$I
E3L$H;or<3$HV[H3H8[H$IOpHeAID9wvYHG0<w1<uLG8HW@HGP0II5
H;or3HZHfEDt$$AD9wv7LO0MIB<
u;Os
HGE84uF
IH;OrDT$$I;\H
Lt$0Dt$8Dt$<LD$0AIATHHEMMADt$ Lt$03HG0u2;ws-HG<t!AHEHG HMBAI]tYHHHC7IAH3t
HOpzHNBAIHuHt Ht
H;wRIL$E3I_pHHHIOpHI;DT$$LHPH$9SHEAMD9wvqMHG0EAu3;Ws.HGD84t"AHDHG
HKABAI"E;vAHD4HCF4AIIuI;WrHHA_A^A]A_^][HUH HHM0r\H]HSUVWATH
HHIpIMHHHu$HKpHHI$gHKpHHuLHHHKpHE3D9gt$L9guHOcAL$Lg)HGL
Hu#LLxLDpHHHKpZHgHO0_HLg0H
AHG0D9gv>IHG0HGPHG@HL$pHW8HmH;orI;t3HWHL$PD$PH A_^][LL$
LDHTSUVWATAUAVAWLHH$hIIHE3EEMMEEMEEMMMMMMMM
E(E,M0E8E<MEEMEEMEEIHD$(IHD$ M3pH$p-EMD9$]L$HI;t L(LhIHHAH$
HBHL,L;nt0HHtHHFMtI}HLn(lH3H;tPHvHHH$`H;tHHP3HHGHW
Lo$`L$hLH$D$xT$pL|$hH$HD$`H$HDXHHDPH`HDHHT@HT$8H|$0HT$(Dd$
LHUHp0ux8$`tExD;$s,H$A<tH$IHB;HHPAID;HpH$xA sHHPH$ps}H$!
H$
H$
H$T3$H1SHHI;tHHHp|H$ H$ H$~ H$3HA_A^A]A_^][HUH HHH]HUH HHVH]HUH HHH]HUH HHp#|H
]HUH HH`H
]HL$SUVWATAUAVAWHH$IMHHHIpHHuH`HHNpHE3Lt$@Dt$HDt$LHH$hHD$8H$`HD$0Hl$(Ld$
LL$@LHpHlDA;tHHc\$HHl$@A;t)H|HH7I;tHHA;uHAAHpHHNpHLDDLADDLDD$HuRH$HD$(H$
HD$ LLxHT$@H,LpLxHNp-
H$HHNpDSHrSHcE;vA;tSHJLt$hDtpDttLtxDDMLtXDt$`Dt$dHNpHHE3HNpHHVpHJH+JH;DDLBL
D$
LHH$H@ct$LEHT$@HD$DLD@HH$L^pI[I+[H;Pt:HHI;t[LHLPI;tHHHHPLHHHNpHXLXAL$HHXMEME;
vRHI+HLHOItfE9,RtHH;rH;vHXIHJMDPAIE;rHXIHJL;tFxHPLDXAH;LD$xAH&LD$hAHEL$pA;vHD$hD8
(tAHHuDDl`LEHT$@HCtKt-
I;HNpA:tFxHuLEHT$@HmLAHZD$DLD@HH$eHH*H$LEHT$@HwHHHNpHAHAHFpHHH@H;H$HNpHI
;hLl$XHNpHH$AA+;$t#$@$@H3LH$@tE3DtPDtTAL$`A;v^ID80tHHuA;tGD@D;Dv0H8)L8DDIH8DDD@
DD;v4HLDIHHDDEE;'MMLLth3HHH\$xI+LHDD;L$psA3uNDD
E2IEH$9D$TsE8t3DtDD$TIIHKD$9D$PsB3D
D$P;D$`sEE2D$PIHDtH8EAIHIE;!H$@HLlXE3IxHL$xmHL$hbZ3$@HIH$@A3$@HIH$@#3$@HdIH$@H
I;uHFpHPH@H;tHHHHctHA;t0Hl$@H\HH;I;tHHA;uHl$@Hq3HA_A^A]A_^][HUH
HHM@H]HUH HHLH]HUH HHH]HUH HHMhjLH]HUH HHMxJLH]HUH HHMX*LH]HUH HHSH]HUH
HH3H]HUH HHH]LL$ LD$SUVWATAUAVAWHHD$pHHHHHh`aE3D8`t AGHLDD9\I;SM;JE;AHHL$D3P
A;HHL$3DBP A;uH$HH+$AI;sDsHHEHI+E3E3P A;*MH$H6A;AD$D8tAL$Hcu
t 8u
t
A;tJH+D+I+HIDHLE3E3H$AR A;sHH HpH DI;DH@L;1M;uI;t"3D8uID$ HI,HA HHH+pH;s

HHE3DHP A;ISLHD$`Ld$hMHHA;tmIIA;t3HDH)D8uD|$XD|$YD|$
MMHHL$PML|$@AD|$HD|$LHNpLLH;`Ht3HCHLLt$8L|$0HHD(HHD
LL$@HpHDtOHc\$HHl$@t(H|HH7HtH.H&uHHL$PAl$Hu%HL$@HL$PI;v3$HCHHLP\Ld$@I$D$
LHLHHL$PfHNpH;t3$HBH$aHHLt$(L|$ LLHHqHct'IHH;HtHHuIHL$PIHA_A^A]A_^][HUH
HHM`.FH]HUH HHMPZH]HUH HHM@H]HT$SWHHHD$8HHHHD(HHD
xt=@uD$1D$0HAHL$0HH_[HT$UH0HHEhHH0]HT$UH0HHEhHH0]H(ytHAy tH AHAhH(HSUVWATH
3IL@8qHt LA@8q tFL;v.DC(B&HK AAD;C,DC(u HK HH;rKLITC<L;t7HIhHC`H+L;vH[=HLPDPHKXH{hH
A_^][HSWH(y@HtHAvy t@DA(H HA<AD;ADAu,S@2H
C3C0HIhH9K`uH<HL$@D$@$HCX@<HChH(_[HSVWH HHLHuH _^[HSVWH HHHHHuH _^[HSVWH
H23HADCAHH;rA
AAA?|
HH
H@HHH _^[HSVWH ypHtPAHN#t5+sHRHHt3H:HuH _^[D3A;t;uPA;t;uAA;t;u2A;t;u#(A;t;uHD;t;uHSH
HHWHL$0AHHT$0AD$6D$7H [HSWHHL3HHDD$HIH|HJHL,HHH|JHL4HH|HL$$/D HH|HHT$ AHH_[HSH
HHHtHPHHKxHtHPHCxH [HSH@3HHL$ DBHHT$
AH@[HSUVWATAUAVAWH8HRHA39~DLL>O<DBIHH;tA@rAAIctHD DHI;uC|<Ot<IH
HA
AT$ HT$ @:tCT<HHL;tIH|KT<MHAI D;n+9~Dv-LH^HA@ATH3AID;nr~(v"9~(vHHF H
H;~(rH8A_A^A]A_^][HSVWH H329_HAv3H<tA
Au
HA2;_rAtHH _^[HSWH(IHWHHpHHH(_[sHSUVWH(HHHtY
HA;{Hu 23)HH&39{v%3H<tHCH0H;{rH(_^][HSUVWATH 3MIA9XHHt_HHUH H9]vHHUHH2~H;]rIH3HjH
A_^][HSUVWATH DREAE;HHuE3AADD$pHAAEKHJlAHH;r
AAA?|CTHPAHHH;wHu 3HH3HH A_^][HSUVWH8HHAHtFDDHHD$ 39^v&3H<tHVHH:H;^rH8_^][HSVWH
AHH*Ht HHPHHtHP@Hu!HLGxHXCHHxu@-Hxu@HuHOx3LHDBP
H _^[HSUVWATAUH(3IH9ZHhH^UHG3HI9]Dv
LHUHJ"AID;mrH9^8vHHV0HH*H;^8rHH3HH(A]A_^][HTHLSUVWATAUAVAWHXHD$
MILE3D9C(vEHC 8uAHD;C(r-
Ir39s(v3HC 8INH;s(r39K(vdHC 8w
H;K(rN I(E3E3D9c(v83HC ,3t;tIJII;rAHD;c(rHD$(D$0D$4HD$8D$@D$D33E3H$9xvv3L$LHC
u;{sH<tI4t0LITHL$(+M_CHL$8tIIuHA;~rLHT(I3IHL$8HL$(HXA_A^A]A_^][HUH HHM(H
]HT$SUVWATAUAVAWHHD$0HHADqpE3IAD9HvHG@HAH;OHrIAHgD9xHSLOPLG@3HYLHWpHIL|$h
D|$pD|$tIL|$HD|$PD|$TML|$XD|$`D|$dED9vWHD8|+t*H+HL$h
T+HL$H)T+HL$XsAHD;rH\$hLlXHlHHDHHD
LL$hLHWpH3HqIHHAD9u3H>H/HML|$HD|$TD|$PA;v%3L|$HD|$THgLHD$H\$T\$PAED9IIHD8|tD9D1AEHI
D;rA;TLD$HHML|$hD|$tIL|$8D|$@D|$DD|$pA;v$3L|$hD|$tLHD$h\$t\$pD|$@A;v$3L|$8D|$DHHD$8\$D\$@E
AAD9LMML+HE8|uHE8|
C
EE8|
ED;HsH@D8<tAAAA:AEMAI;rE:tLD$hHA:tLD$8HHIIEIA;HLH@A;tELHHM+uE;IEIHH;rE?|APAHHHH3HE
D9vcIHL<(E3E3A}IAHafHSDIE;ovLEHH$D;rE3HAH~HAHlHAHZHAHHH ,A;t\D$ ADH HVAD9(v3IH
D8<tH0(HAH;(r3Hi3H_HA_A^A]A_^][HUH HHMhb3H]HUH HHMHH]HUH HHMH"3H]HUH HHMh3H
]HUH HHM82H]HSUVWATH ;QEAHr3HS$;{s@H@,tHK3m;{sHCD$H
A_^][HSUVWATH ;QMAHr3H#;{s@H@,tHK3];{sHCL$H A_^][HSUVWATAUHHHD$
IILHHME@$gHLKDC%QHLKDC&;HLKDC'%H DK DC(Dk);Hr3H@#;HsH@D,HH$3H;t
HrHHNAo,l0HHT$0HHA]A_^][HUH HHkH]HSWH(H0$H3H;tHCHCCHC
C(HHHHH(_[HSVWATAUHhHD$PIILL0HHt@HHHC(3H\$XHt
HHPHOHHCHK HCHK(HtHPHC(H$H!HWHyHNkL_L$L$M0HHt.H@H@@H@

@(3HHL^0HD@Hl$8Ll$0HHD(L\$ LL$HI>t3HHPHhA]A_^][HUH HHMXZH
]HSUVWATAUAVAWH(H$MIHHHhE3A:u
@D9}Hu+D9}(u%D9}xuD9uII33pxAI;t#D9~uF)HH#I;t
D8~)uE8>tHHG8HO vG_G LLD$0HH(A:H_HWLH$H$HOHGXHO`LhDD LD$0HHL_hL9_t
@LDDLDDD$D$$$DHxF)$HV0H$XH$E8>HEH$
KL|$8D|$@D|$DL|$HD|$PD|$TL|$`D|$hD|$lL|$pD|$xD|$|L$D$D$L$D$D$HD$`HD$(HDHHD LL$8L$H$
HA;HHHLpHL`HLHHL8H$!;HH~H$4IhG
D9|PuD0HHL0;H{LDDLDDLLD8H\$0HHRHHLD$`HT$HH}3HD\$@E;vHD$8IHHHuH$H$sH
L$piHL$`_HL$HTHL$8IH$ 9H$-H$ H$2I
H\$0HO A;uPwHO HH$$H$HOxHE3E3HP A;uH$HH(A_A^A]A_^][HUH HH)H]HUH HH2H]HUH HHH)H
]HUH HH_2H]HUH HH 8H]HUH HHM8b)H]HUH HHMHH]HUH HHM`H]HUH HHH]3;MHs(H3LH
GI;tH
rWAAHD$(IfI3HSUVWATH zAL4H;qv5H{HtDCHHI|HH+stKHI$LHI{{HH
A_^][HSWH(3LH9YvCHAH9t
H;Yr+IHDGD[E+JHI~GH(_[HSH DAE3HE;v7LIH9tAHE;rAAKD+II8~CH
[HSWH(H3S;v4LIH98tH;rAI+DII}CL3HH(_[hHSVWH@HD$
H3HD$(D$0D$4P;t/HHHHD$(|$0|$4LHHHkzHL$(HL$(HAHHL$(VAHHL$(CA HHL$(0DHHL$(DFHHL$(
~DHHL$(DF
HHL$(DFHHL$(DFHHL$(DFHHL$(HT$(H"V[HV\HV]HH/H%VHVHHL$(H@_^[HUH HHM(>%H]H8HD$
qHHD$@3H;tqH8HUH HHM@bH]H8HD$ HHD$@3H;tqHHHHH#H8HUH HHM@
H]HSWH(HHIIHLL$@PL$@HK(HtH(_[H(HIHu@HPH(HLAD8Qut9ELFulItu`AIt
uSA3tAQAAfffAQtHt<t7t9~~(t
+t%>t3utAAHML9At 9t9t93AAAtt<t,tt-t&uRtNtCu8EAtAAAt=wAwAA3HSWH,HL9RIFFuyyWAVEupyfmt
ugIAw\fAyuTAQ"AYuEs=s5DQE3AJH;w%C<
dataCL
twAET
A
|3_[sAAPHSVWH HIHHAA@rZf9MZuSDI<AsFAH;w:Au4J:PEu(JLtVttJtEt9<t-dt:HH6uHHtBIfzwJtuALHHSH
_^[HSUVWHHHD$ IHLQAAAEIh(H@HOf.H;F0GG;F0HL$(aHMGAHcGfH4T$0ftGC<w 9T$4uHL$(T$0HL$(
T$0HHEffr]H6HtE t7LD$(EIEu t, t'A:uHt tHHuWHL$(HHH_^][HUH HHM(zH]A8LA@
A@A@(HAIAAA@$HAI@ABA@%HAI@ACA@&A>3A@)AH'IA HAA?B
A8I?uA8I>uI9I tJHSH HIHHH%H [HSHDHGE;o$\$L$s5AH|u |u!|t9Trw
T9TvAAH|$u |u|t)D;rw!;\soAEHEE;vAHH[HSUVWATAUHHHHL$
DOdC$MA;JAHJDHAJDHAwjA;sL$H[HHJA~L$H[H4HL$
HAy)AD;H@HHHFHDHFHDvHT$ AH@HHHBHDHBHDHHA]A_^][HSVWH
A3H;At2;HtHhDCHHHpHzCH3CC;Ct:;t#HH-DCHSIHHpHK9CH{CH _^[HSVWH
A3H;At2;HtHDCHHH>pHCH3CC;Ct:;t#HHDCHSIHHoHKCH{CH _^[HSVWH HC(3;C,t=;Ht#HH7DC(HS
IHHoHK CC(Hs C,C8;C<t:;t#HHDC8HS0IHHZoHK0C8H{0C<H
_^[HSUVWATAUH(H9HHG?L:C?tPE3A:tG>:C>uHS(HO(x,E8(I;J;JADA;JHC(HHAL$HG(HH)A;MHC(HHAL$
HG(HHA;GCA:uD8kCA:t7D8kCu
A:t"D8kCtHOH;KH;KADA;ugHO H;K H;K
ADA;uKHS(HO(A;u9LMM$AHA;ILA;IADA;uAA;LA;ADH(A]A_^][LL$ SUVWATAUAVAWLH8HD$
MMDHAAAIICHIH($f.IN(@{E2@t
H;;~
HIF(L,HAN0IF(L,HAF8sIt@I~ C3H-9HUIu
HrA@{L$30H>I3@:t@{L$L$3@:H9+@H9st,H9st&HKH;t
HkHkHHCHsH$HHDL$AP;H$H;H$LHSHH;tHRHH$;L$H$HKD;9$ur$u$Ct*t"tu $$#$t3IF
HH;DED;u$$HHH$H;t+HP#H9+tD:t$D:EHIE3H8A_A^A]A_^][HUH HH/wH

]HIHH`E3LMLIAAH;rACID8t!AJ IB(HHD8YtHI3HSVWH QAHDHA 3AHHHAHDD HI(H;tHASEAP ;u&G8^t
8^
u_GHHG3H _^[H(QADHA EILyBAAt.AA9At#HI0HtHDJP(u 3H(HSH HI5SHC HHH<u$HuH\uKHC;Hr3H
[HL$SH0HD$ HHI0HtHPHK(HtHPH0[HUH HHM@H(6uH]HSUVWATAUH(MIALHtAE{
H;CrS3T$p@8ktHKHDLL$pHIPT$p{
tKDI,^T$pCHt+LH)CuHKDuHKuT$puCu53H(A]A_^][HKwuHCP9S$tHKnu0@HSUVWATAUAVH
 MIALHtAE{ H;Cs D|$p
DCDD$p{tMLsHD;DGDD$pHKPLL$pIHPDD$pKI]CD$pH)C{tL+HttkXH{uHKuUHKkuHD$puOu-
@5HKKu(HCP9S$tHKBu3H A^A]A_^][HSWH(HIHIHZKHAPhuHC@@H(_[A@A@HL$WHHH$H@
@HH8LALLHO(HDxDpHHDhHHD$`H$HDXLDPLLHHD@HD$8LT$0HD$(D$
LLH!HHHHOHHtHPHGHH_HUH HHH]HT$UHHH@HH]HSHDMLBoE3A;As HD8tHHAI
MAAA$A;s!ID8tIHAIIMQAAA%A;s!ID8tIHAIIMQAAA&A;s!ID8tIHAIIMQAAA'A; s ID8tI(AAI EQ
AAA(A;@s
I8D8uEEA)[HSH HHIHHtHPH [HSVWH HcYvUH1HDHGuHHHooFHDNHwH
_^[HSUVWATHPHcyILH1HNMDHl$ uHH\HHL$0HHCMHAHCDHAHHL$0HHFHCHFHHC(HHl$
HHAHFHAHNHFwHPA_^][HSVWH HI9Kuct:3H;HtF;|HOQQHyH9StDCHHItaHH;sH _^[HSVWH
HlCH3;CLt=;Ht#HHDCHHS@IHHaHK@CHHs@CLHKPCx;C|t=;Ht#HHeDCxHSpIHH`HKpqCxHspC|;tO;Ht)H
HDHIHHy`HH;tO;Ht)HHDHIHH`HHH}HqHeHYH H;LtD;t!H+DHH@HH_H@2HH@LH
_^[HSUVWATH@HD$0IMLHHE3E3IP P0HHt@HHHG3H|$pHt
HHPHHPHOHtHPH_HoHG G(H8HHtW@(HVHH$HFHHFHHFHHF HF0HF8HF@3Ht$8Ht
HHPLHHD(HD$ E3MHHARtHHRHF@H+H@HHPHHPH@A_^][HUH HHMpFjH]HUH HHM8&jH]HSH
MIMC>AP>:t3K?A:H?tuIP(HK({?t{>tH
 [HHAD9IAvdE3D9QEvHQH9tDEHD;ArHT$ DT$ DT$$D$(HLD$0IBHT$0HA@a3
A;ArWHHHL$SH0HD$ HH/HHIH+HKPHtHPHH\$@HK0HtHPHK(HtHPH0[HUH HHE@HU@HHHH#H
]HUH HHM@H(hH]HSH A@A@Hu-Hy3H [HSH AAHuHIUHM3H [HSWH(;QHAv'H $HC{H{H(_[HSH0HD$
HAAuIHL$@HIpHK`HKPHKHHtHPHKHtHPH3H0[HUH HHM@HVgH]HSH0E3ft$ HD9Av)
LA; uAA9Bu AA8Bt>AID;Cro2HD[HMB4CHKfot$ H0[AHSH AAHuHIHtHPH3H [HL$SVWH0HD$
HHIHHtHPHHKHHH#H|$PHO0HtHPHO(HtHP@tHTHH0_^[HUH HHEPHUPHHHH#uH]HUH HHMPH(eH
]HL$SWH8HD$ HHAHAHAH,HH(HHHP?HHt+H@0H@8@@H*HHCH3HHt HHPHHtHPH@HH8_[HUH
HHMPH]HUH HHMPH(*H]HUH HHMPHdH]HUH HHMPH_dH]HUH HHMPH?dH]HL$SH0HD$
HH+HWHHtHPHHtHPHHtHPHK(0)HHHHK?HK5HKH0[&HUH HHM@H]HUH HHM@H((H]HUH
HHM@H?cH]HUH HHM@HcH]HUH HHM@HbH]HUH HHM@HH]HUH HHM@HnH]HUH HHM@HNH
]HHH(A@A@u3H(HL$SUVWATAUH8HD$ LHHAAZtHHH_t]3L
HHD$xHt)IMH)HH&L[L^KNCF3OHH4GHIuHH8A]A_^][HUH HHMp2H]HUH HHMxH]HSUVWHHxHD$
HH@@@@H@@AHH\~DBxHL$PI~
DBHL$P6~DBHL$P#~E3APHL$P~n~uHvCD$,CD$(\$0HT$@HL$(HHABHT$@HNX;^r
H\HH$Ht&HT$PHg%L\$`L[D$hCD$lC3HHHHH$Ht&HT$PH%L\$`L[DhCDlC3HHHtCHc\$XHHHHD$PH,H
tHM@HuD$(l$0D$,HT$@HL$(HHABHT$@HNWD]D\$0D$,HT$@HL$(HHABHT$@HN|WHct$XHt.HHHD$P
H<HtHO?Hu3HL$PHx_^][HUH
 HHMPH]HUH HHH]HUH HHH]HSUVWATAUAVAWH(3EL8Y!HtJHHH9Au
HAD9at
@9^t
HR9u3 H;HtHXXhHL3H9^DAvHHFEDdD$HD;^rD:t2AHtH;uHGOHD=AADgHGE9.uEF3Hz9^t
HvDDE9.tA9^t5AFA;u(uA;uiuADD;;9^tFH@HFDdD;fHAH<HtH!uyHdy;}fDfHAQy;}SVHBy;}D;t@E9.u
DHyA;}D+ADAH~yDHnyAH(A_A^A]A_^][HL$SUVWH8HD$ HH3HGGGn;t+H\mHHHGooLHVHOF G
F!G!FGF(G(F)G)HV0HO0YHH8_^][HUH HHM`&H ]HUH HHM`HH]LL$
LDHTHL$SUVWATAUAVAWHH$LHLhID 3H;HDHHH$`HL$HHHHLHH3L;t
MhLL;vWA8L$2uPHL$ E3H$XHc3;t0H$3H;tHRH$H;tHPiFHH$

LH$HH$$HHX`dLHD$$$AD$$A:I$DDHD;vzHHHHH;wKA;w&A;tOtJtBt=t5t0 t&tttAHE;s
3$3L;A3E3L$ HHHH$
AE9vEL$AHA;rA3L$@A9Kv#LII9tAIA;Sr3D$LA9N#DIF0B<LH$8HJc;LHHA8Jt%EA9|AIH38A<u
AL233HID;r;LND($,H$$H$$H$$ L$AHL]PG#D$(E3AL$$A;v"IECH8t
AH ;rAA;@D8$uA:u
EDDHDLAA;vIH8tH ;rA$PE:IHIHHu*H9YHA$L$HH+t.Ht(H-t H-tH-tH-tH=ud D$Hu
$L=t*t"tAD$LLHHH$XA;rV3 H;tH0ppHpppHHH$cD$A;sH$
LIHH(3@:t+H$8L@HHHBBLII+H$BLH$I;IBH$$@:EHHHH;t4HYL
D
HHDL@3DL@3DDLL8E;VH$XH$$L$hL$$LhLHH$PH9$vI9L$(tDDDL$@A9JvZIE3AvH
D8q<t(HI LE:t
HH$PH;HBH$PDHE;BrLH E3H;HBHPH`HIHPDA;IcE;t;I<DHHI;tHKXHPHHE;uI6H-
HHI;tHPH$I;tHPA?HH$8I;tH1}LH$0L$0L$E3M;t
IIPAHI{LLLHhD8g3H$Y}D3D;H$HH3L;t
IIPIcE3E;t;I<DHHI;tHKHHE;uIHHH$I;tHPH$I;tHPA=W3H$38W3:H$D3;HHHH3L;t
IIPIcE3E;t;I<DHHI;tHKHHE;uIHHH$I;tHPH$I;tHPA<HHLHH$AD$$$9Wu$$$QH$H;tHHP3DBH$O;~&D$|
D$ AMHHXH$@9P2LHMA8P<A8P?A8P>I9P $$$8$t#L$H$3;H$X@)HH$XA;rV3
H;tH0ppHpppHHHzDA;sH$ L$IR)3IH$@;xLHHXHkH$E3I;tHPH$HHH$M;t
IIPIcE;t;I<DHHI;tHKHHE;uIHXHHI;tHPH$I;tHP9IvE3I;t HHPL$pIL$D8$ HHpH8I;tD`HHHOIL$pI;t
HHPHH$I;t HHPH$I;tHPHHhHD8b)tH0HOF6H$pI;I;t
HHPHHHHM;t
IIPIcE;t;I<DHHI;tHKHHE;uIHHH$I;tHPH$I;tHP@7LHHPD3;H$l3H;t
HHRHHHHL;t
IIPIcE3E;t;I<DHHI;tHKHHE;uIHHH$I;tHPH$I;tHPA6H$H;tHOHH$HkE3HXHHHxD3D;H;t
HHRHHHsHf3L;t
IIPIcE3E;t;I<DHHI;tHKHHE;uIHHH$I;tHPH$I;tHPA~5HDE3A;I;t
HHRHHHvHi3L;t
IIPIcE3E;t;I<DHHI;tHKHHE;uIHHH$I;tHPH$I;tHPA4ILADDEH@D9BIH$8HHD8A<tD8A?u&D8A>u
L9A u59t HHD8DuAH$o"H$@E3DHD;br$H$H$
LHVH$!R3;LDH$PH$8L$@E3hA$IEH<H$D8w=t3LH`H/D$nHW(H$,L$L$`HLHpLLH`HH$IL+FH$H$
DL0LHHe3In($`;vL$XIL$UH;rL$XH$X[$HHH$X;0HHIH$hHUH$p38Et
8E3tADHH$p13;3@8ot6@8$uFH$pH;tHPH$)$@$$H$f(H$pHGIH@HH(9h
L$8HHH$8DD$IC0IL$0B$9DxH$H;HDJP 3;L$8IHc,H9$vmHI8T8tCHHDJDP
$3;7$L$8H;$rH$0H$0MCISHDAJHI<I+<BMMxLt$ LHHHH03;I~(H0rH3H;tHEEHEEHE E(HH$PHOpH
LH8wH$8LXH3H$0Ic9U(v[Hw@HDL$8DjIFHyH8H+HEHD;}(rH$
DL0LH8H@@H$0<H$,H;LPL8IV8HIO0(HH;rHL0L$3H$$$$H$H;t,HDJP
3;8L83H$IHc9$HHL$$L$@E3AwAIH$8D8|t&$E9<$|A$IEHAD8z<D@H$A:tH$8L$LLHI;L$8qH$H$
DLXL$3H$P@HHH$88HH;thHHHkHGHoHH$H;t
HHPH$h@8h3H$HHHHH#H$@H$LHHmHjkHHH$HtHEHPH$HHHHtHPH$HhHH$03H;tHEHP3H$H;t
HPHH8HxH$H$$$H$3H;HP`=HH$8H$@3H;t/HH0HH8H@HHEHMPMXHEH3HH$8MXHEHPH$3H;tHPH$
HEH$@3@$@H`lH$@$H$ED$x@l$pH$`HDhHHD$`H$HD$XH\$PHHDHHHD@Hl$8Hl$0Hl$($D$
H$8LLxH$0H$\3;NH$H;HEHPH$8HHPHtHPH$8HhPH$3H;tHPH$`H$@H$8Hh
H$HtHEHPH$@HH(HtHPH$@Hh(H$HtHEHPH$@HH03H;tHPH$@Hi0H$8HH$0AHHA3AA3;H$8HG0H$$
G(H$HG
H;t
HEHPHO3H;tHPHoH$hx3t
HqHHxHHHH$0H3H;tHEEHEEHE E(HH$PHpHH$PHH@H0Lt$@HL$8H$HL$0H$xHL$(HD$
LL$PHH$$= H$hE3D8I3HHR3HH$H$H;tHPH$H$

$AA;tE3D8$tbE3H$I;tGH$8HLEAAH$0DAAR($E3A;`A;A;$A;L$L9$e2H$8iX;9A;iA;aLH@HBJ+H@AL9$x;
H;t
HHPH$H;tHPLc$IL$PE;AHs*HIA@HIN8DHE;AHrH$ H$xIF0H$3H$PH
H$8H$(T2DL$8IH$0Hc9HHLLDLPL@E3ID8T(OAD$E9;AIEH<D8W<&H$L$L$$H$838_=G8$G@$
HGHGAHGHGBHGHGCG>$!$G?$HW(H$LH8aLLH$I]H$$LDL$8E3$HIL$E;}H$DLXL$3D$
AEDH(IH$(H$@D;H:x$;u:H$H$"H$HpHHPTHHHhDX1D$;H$HDEH@L$@AH*IEEL*H$HHHHH
OH@HGHHM+uH$
DL0LXLD$$DLHH$HE3IL$@I;tHHHH$@E3$A;v/HH$HHAHHI+u$AADML0A;E3EZILEL8;J4ID
HhL$@AH;$IELIT
H$H;W(D8W0tgIL$(f.IL$(;AD$0ID$(HHE3E;uHH$EZHE3A:t.H$EZEL$8HC7;AADE;HH@H$
DL0sADL8AIA]DE3A;L3H;tFHHIHIGIOIOHIOPAOXAO\IO`AOhAOlIOpAOxAO|LL$8L;t
IIPLDH`IHPBHHHH$HHHB@H$(Lb0HJpLLt$@H$(HD$8Hl$0HHD(Ld$
LPIHDE3A;AG@A9GDHcLLPA;QHs7IH$(AZHJIN8IA;QHrH$H$ H$IF0AD$IL$@D$D$E;L$L$(H$0HH$
H$@HH,H$38E=t+LHHHU(H$g8L$LUH8eLUH8E[38$!v8$i$IGP8u&H$@HE
H$@H$8IG`H$($IGpHHHH;t$$$$$LLHHPH$$$H$(H$HHHA;VH$@$L$PII
DL8L$03H;t4H$H;r'H+HH`HPD3D;IIP$$;L$0aL$XL$HH$HH$H$H${H$p$H$HH$XHH$X;$`A/@
HHH$p3H;t
HHPHHFH$9H$,IIPIcE3E;t;I<DHHI;tHKH}HuE;uIcIZHQH$I;tHPH$I;tHPLHHHHp3H;t
HHPHzH0H$#H$IIPIcE3E;t;I<DHHI;tHKoHgH_E;uIMIDH;H$I;tHPH$I;tHP6HH~H$~H$p3H;t
HHPHdHH$
H$IIPIcD;t;I<DHHH;tHK\~HT~HL~D;uI:~I1~H(~H$H;tHPH$H;tHPHoH$}H$}H$pk3H;t
HHPHLHHHIIPIcE3E;t;I<DHHI;tHKA}H9}H1}E;uI}I}H
}H$I;tHPH$I;tHPH$|H$KH$|H$|H$pG3H;t
HHPH$(H$HHIIPIcE3E;t;I<DHHI;tHK|H|H
|E;uI{I{H{H$I;tHPH$I;tHPH$H;tHPH$`{3H;t
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t
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H;t
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H$I;#HDD$P(E3A;HHRH$I;tHRH$rH$`H$rHrHp\I;t
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I;tHHPE3H$I;tHPH$oHRHoHoHpN3H;t
HHPH$/H$HHIIPH$2I:oH1oH$H;tHPH$H;tHP, I;tHHPE3H$I;tHPHnHJHnHnH$pF3H;t
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HHRHtH*HHIIPH$(+IgHvgH$H;tHPH$H;tHPqH$P.H$H;HAA3P
;H;t
HHRHHQHDH7IIPH$O*IfHfH$H;tHPH$H;tHPH;t
HHPHHHHIIPH$)I fHfH$H;tHPH$H;tHP3HA_A^A]A_^][HUH HHH]HUH HHoH]HUH HH H]HUH HHXH]HUH HH(H]HUH HH7H]HUH HH8dH]HUH HHH]HUH HHH]HUH HH7H]HUH HHH]HUH HH/H]HUH HH8+dH]HUH HHH]HUH HHoH]HUH HHOH]HUH HHpH]HUH HHH]HUH HHH]HUH HH/H]HUH HHH]HUH HHH]HUH HH@bH]HUH HH@HH]HUH HH@HPH]HUH HH`H]HUH HHOH]HUH HHH]HUH HHHH]HUH HH@H]HUH HH8H]HUH HHOH
]H(H\$@H|$HH3HwHH|H|$HH\$@H(HS3H%>LILL; uLZL; uu
IA3[ML;v uMYL;v utML;v uMYL;v uuHt=HAIA3[ML;u uMQL;u
uuHtHAIA3[@[E3EAtAfs(tttWfA8uEXAwAIHE;rDYh3DDL3DA\33LDHTLHxAI[H$IkIsMcMkE3MsHi@Hq
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;>t'tLeD&tHHE2>AHHArHC@HH(HHHCHHH0HCPHH8HCXH H@HHD`L!D
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BDrXLXL(XD9DDhHLD+D$HHD@LHARDXD$t7XH$HEuHsXtyXEHtuHLH+XHD@HHH(yHS`LHL+tH$
MH=H$HS`KxHHHtHHt
HI+H;HGHH
HPMtZIH+K`HHHHL$ I+H=@r9HLD$ HT$(H+@H HIHD$(IPuLt$
HtL|$8H|$XLdPLlHHt$`Hl$hLt$@H\$pHxXXEHS`LHL+tH$Hh<utD8trD9hfXt SH$HtEHJHt$HxHNH+H+H9
H$IHHI|3WH\$pHxH8HLd$0Ll$(E3LLt
HHH\$@Hl$HH|$XLt$
HyLAAHt$PH_(ffffffffsHA;ABHt;3t pL/D+oHH3HHrAIDLt H|XHtPHlHIIIMD$xHID$
AIIID$(I$IID0IIffffffffDjM(D*HIIurM$
M$(Ll$(Ld$0H\$@H8,DBJH+3J+JIHHHT$HHHHD$XHXHhHpHxL`LhLpLxMLLA$HA
HD$PE3AHHx(Affffffffff_BHHt;t!&nL.D/mHHtHHr
l3t$ MtI$Ht
HH;F\$ Ht$(IHLD$(HIG@HHIO
HL$`AGHHH$IO(HL$hAGLHH$IO0HL$pAGPHH$IO8HL$xAGTHH$LHt$HHHt$8Ht$@HL$`{s$$AGp$$2$hL
HE2'fffff\$ $h\$ $h\$
,HL$(H$$$Ht$0HLD$0IPhLD0I(H;rH;wEu2H;L$8w3t/HIHt$0HL$(HLD$0IP2$htfIG@ML$t*HH+L+$L;iH)$$
L;wD+D$8$$D$&t!@7HL$(HtHPHt$(Et8$H+HHH+D8H;DHL`uM_@$HH+I+I+H$HL$8$$snHH\`I+\
LIT H$pH6$uH\$@HID HD`$ADHID H3$hH$I+L;tK3H$\$ EyLILLEGXDD$0H$XHHLL$0PjD$0uAA\$
CLHIG@IHLMHL$8HH+D$HH=HD$@HHD$0HL$HILD$0HT$8IP$h\$
HL$(HtHP@HL$(HtHPH|$`H$pH+Ld$Pfffffffffffl$L/L+HM<4uHIHH|HL$(HtHPlHL$(HtHP3XHL$(HtHPDHL$(
HtHP@-HL$(HtHPHL$(HtHPWH$@H$8H$0H$(L$ LLL$HHHUH HHM(H
]HXHD$@H$HD8D$0H$HD$(H$HD$ @HXHT$UH@HHH@]HL$HHHD$0H\$`H|$hHH*HH
HAHHAHHAHHA HHA(HHA03y8HH|ffffffH|@yHHH|HHHHCHHCHvHCHKHC
H HC(HHC0HHHD$ L
s(DBHHH\$`H|$hHHHUH HHMPH@H]HS3Hu-LILL; uLZL; uu
IA83[ML;v uMYL;v utML;8v uMYL;@v uuH)HAIA83[ML;(v uMYL;0v uuHHAIA83[ML;u uMYL;u
uuHHAIA83[ML;Xx uMYL;`x uuHtvHA IA83[ML;u uMYL;u uuHt=HA(IA83[ML;u uMQL;u
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HHt$`A@8tHAH+H;DGAHH|$hHLt$0H(H0ueU(+tMtA4$HH8R|pLD IHt 8HH;HD

BDr8L8L8D9DDHLL$ D+DD$ HHD LHAR8L$ t/8HL$ HuHs8ttHtl8tuHLpH+8HD HHH
d8pu8Dt1t(HH9uHu8tuAt38EH|hLt0Ht$`Ll$8Ld$@Hl$XH\$PHHH(AAH|$HHuMHH\$@3HHkHAH8HAfffHL
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H\$PH|$XHA8A8uJHL$@HL
~DB3ffffffHL@^HH|HeI3H\$PH|$XH8HUH HH}@tHE@H@HE(HE(HM(aH]H7H'HH H(H0H8HD$
pmHHD$@HtHW3H8HUH HHM@^HH]H8HD$ x
HHD$@HtH3H8HUH HHM@GH]3AAHSH0ALADALIHID$ kCH0[H(vGHLt$3H
-ACACACACIIH(3H(H(&G3HtHH,H@HHH(HH(A3HSH DIHADAHHSCH [HSE3H#MMLL; uLZL;
uAuIA[LL;u u
LBL;u tDEu
I
A3[@[AAH(AAu>F3H(H(E3Ht#HHHH
iHHHHH(HH(H(EHt/@H
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+kH|HHHH(HH(H(&EHt/@H
G@HH
j@HHH(3H(H(D3Ht#HHHH
KfHHHHH(HH(H(DHt/@H
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;fH\HHHH(HH(H(DHt/@H
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+E@I;sDE+E@D$IIU0L$IP$}Mt>3I+IU0L$ DLL$ IPT$ IU@uKt>$;rIM@u0MRI$ME@IU@IP8
3 @Hl$hH\$pHt$`H|$XLd$PL|$8Lt$@Ll$HHxHA H3HSH H'HHH'HAH['HAH0'HAH'HA HI0UHK8HtHPH
[H(HH\$@H|$HHHt HHPHK0HtHPH{0H|$HHC8H\$@3H(HSH HHI0HtHPHC03H [H8H\$PHHI(D$ HH|XILL
PL$ HK0HH\$PtH|$XH8HHHD$0H\$PHlXHt`H|hLd@IHLH+?HHtI3X(HK&HH&HGH%HGH%HGH%HG
H_0H_8H_@3HH|$8Ht
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I3[ML;u uMYL;u utML;v uMYL;v uuHt@HAI3[ML;hv uMQL;pv uuHtHAI3[@[HHHHH(u(:3H(HSE3HMMLL;
uLZL; uAu
I3[LL;u uLZL;u uAtLL;xv uLBL;v uAuHtLIM

3[@[H'HH(u93H(H(8Ht6@H
HH
kHHH
@HHH(3H(HwHgHgH(V8Ht+@H
gHH
3HHH(3H(DAP3DAT3Au<(v@A`33A\3HHHXHhHpHxLhE3L9iXIIHHLi`u&HRH
;VHHGXu
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DGEEDDDhHHHHHHH$HGxWhHOX$ HH$@H$L$IDLD$XLlPHDHH$
HD$@HD$PHD$8H$HD$0GlD$(H\$ D9d$hDv
$ OxHLPHO`uOuDtAt3Eu&D9olt
HtH9EEHtH$H9EEAWL$LHHHH$HHAHH3Q<3HQ83H8H\$PH|$XH3MItA AHL$ HO LL$
LD$(HD$(HL$ HO(HH|XtL(H\$PtyH83H8HS3H5LILL; uLZL; uu
IAP3[ML;v uMYL;v utML;xv uMYL;v uuHHAIAP3[ML;8v uMYL;@v uuHHAIAP3[ML;Xv uMYL;`v
uuHfHAIAP3[ML;u uMYL;u uuH)HA IAP3[ML;u uMYL;v uuHHA(IAP3[ML;u uMYL;u uuHHA0IAP3[ML;Xx
uMYL;`x uuHtvHA8IAP3[ML;Hv uMYL;Pv uuHt=HA@IAP3[ML;v uMQL; v uuHtHAHIAP3[@[APAPH(APAPu
HP 3H(H7HHHHHHHWHWH H 'H 'H(wH(H(H0GH0H0H8H8H8H@H@gH@gHHHH7HH7HHhHpHx
HHHHH(D_@GDL\$0HG0HD$8u#H{H
HHG(uJHWXH\$xH_`HG<W8HO(LD$0LH\$(D$ H\$xt I3HHHhH(HH\$@H|$HHHt
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tmt>H(f:u:BA@H3H(f=uBI@83H(f=uHBI@83H(WH(HHxHXHhHpHx L`LHHAIH3t^t'
tLD$ HuW1f;uyCD$h"f=u
CHD$Xf=uWHCHD$XHH;rIL$ HT$ mLdpHHHH$HxWH(EH\$0Hl$8H|$HIHHt9Ht$@Afffff?uf;u
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HCHHC(uHGpHL$0D$0HH [H8HD$])HD$@HtH3H8HUH HHM@N)H]H8HD$ 0(HD$@HtH3H8HUH
HHM@(H]DA`3DAX3H(H\$@L
pH|$HHHttetKt0t@H|$HH\$@H(WH|$HH\$@H(@H|$HH\$@H(H|$HH\$@H(H|$HH\$@H(GUHO@3HtGt9Gxt
0=Ox_t=HHG@tOxOtH|$HH\$@H(H(E3HLA8DA$DA LA0A.tHHA0LA@DA(HXt3H(AL3HAPH3LL$
LDHTHHHyHHXLLhHy]HhL`LLpLxHAhE3HHHD@LYpH|$xEofffCT9CPu)Eu$DC|HSHD{TD{PILKTIP
L[pDHSxHHI+H;vJ<"3H+8K^tHC`I+H;r
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HP 3H(HHHHHHHWHWHWH 'H 'H 'H(H(H(H0H0H0H8H8H8H(HH\$@H|$HHHt
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PHD$HAIHD$PHD$@HD$0HDHDd(HD$ HL$HKHK0DD$@C
HD$PHC8D+LHtEuSHTHKL|$hLtpLlxLHHHH$HH(H\$8MHHt$@H|$HA^3IHHK`tIHC`HKpKXHHLL

HH|HHt@H\$8H(HHH\$PH|hLd@Lt$0HHIH3HL|$(MELtG|9t)2_|2HHGHG|MtAELl8DHlXHt`fffwT9w
Pu.E_T_PucHDG|HWHHLOTPwTDt@OP+HIA;AGHWHDJwPH_hD+LMtA4$EuHt$`Hl$XALl$8L|$(Lt$0Ld$@
H|$hH\$PHH3HQ@HHHAHHQHHAHhQXHAH2Q\HA
HQ]HA(HQ^HA0HAxHA8Q|HHHQhQTQPHHH(H=H\$@HHHH|$HHAHHAHHoHAHtHHAH:HAHHAHHAH
KH0HHtHP@H|$HtHEHH\$@H(HS3HLILL; uLZL; uu
IA 3[ML;v uMYL;v utML;v uMYL;v uuHtvHAIA 3[ML;hv uMYL;pv uuHt=HAIA 3[ML;v uMQL;v uuHtHAIA
3[@[A A H(A A u
HP 3H(f:PHJBf=wfE3fHuFQBf=wffCu~Q0BupfD9QuiEPAP WAEfBuJQBf=wffTu2DIA0AAw fD9QuWAPEH
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A
IwGAP3A3AP3AP$3AP3AP3AP3AP3AP,3Wff

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OHCHHC(uHZHL$0D$0bHH [H8HD$
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LhE3HDiDiHIILhL9/u%HH
VHHu
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HP
3H(t?t,t tW
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T$ H,HD$ H;tHVHHtH
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HSHMh~HHHuHt$@H|$HHl$8H\$03H(HHHXHxHHHIHLXHHHL$ HHO HW(}u!HO HLL$
LD$8HT$XLD[DHHHuDhu
D$0uHHHHHHHH(HH\$@HHHcH|$HHAH3HAHI Ht|@H|$HtH,HH\$@H(HSH AHHHHH(HAHHAqHH
YHC zHHC uH4QHL$0D$0HH [HS3H5LILL; uLZL; uu
IA3[ML;u uMYL;u utML;u uMYL;u uuHt=HAIA3[ML;v uMQL;v uuHtHAIA3[@[H(AAu
HP(3H(H(HQ3HHBAH
mD@3H(HXH\$`YHlhHtpH|$xyLd$PHLEAEA@E
D
IDL$ utD3uOtCHVL
L$!HL$"*oHVHL, LHT$ DI4Ld$PH|$xHtpHlhH\$`HXE3H|$ ADQDQLQLQLADQMLMPu3H|$
H\$?ACuH\$WADH|$ wWH\$H|$ JDADBD;uHEEKt IKfffffffHHIAutfffffIHHA@uACv?tA@H\$H|$
AHHHHHwHSH HxHHHHAH@HAtHH [HH9?HpHx Hu}39qvH(ffffffAHA;WrH9w Lcv'ffff sHGIAL8'H;G r
HcA HL8(D+3HLDaALa HtIHDGHWHHLHOLG HWH5HL$ DoLo LIuHHL$ MH0"DIuAHArHuHW(HL$
#HLLHHHHHHHH` HHHHXHhHpHx L`LhDi3LpEHLt|zLafffI$;{u\9uV3tLHNL+A8u<H;rLF L;C
u)MtYHSHNAD$
u5IA;r2Lt$0Ll$8Ld$@H|$hHt$`Hl$XH\$PHHHS)HN(AH+fffHHIABAut"IVLJHJZM^IzH8HD$
H\$@HlHHtPH|XLd0IDHHHHD$(HttCGHCHGHCHGHGHG Hk HtHCHGLHSHHo
HC(HG(HC0HG0HC8HG8HC@HG@3N9Nu>lHHFtLIHHLHHnLDFE+IKAD$I"LK<FH\$@HlHHtPH|XLd
0H8HUH HHM(H]H8HD$ H\$HHt$PH|$XHHAH;yHt3HI@Ht
GHC@HCHHt$HHC@H{HHtLHHK@^3H\$HHt$PH|$XH8HT$UH HHH
]H(H\$0Hl$8HHI0Ht$@Hc]HtDHH|$HfffffffffHEHH<HtHOsHkuH|$HHMHt$@Hl$8H\$0H(FHHH\$PHlXHt`
H|hLd@Ll$8Di3HEL~ZHifLUA;Zu`A9uY3tMHOL+A8u@H;rLG M;B u-MIRHOAD$
unHA;rE;,$r+AL$ID$H\HtHKhH`AD$IL$L3[Ll$8Ld$@H|$hHt$`Hl$XH\$PHHtIT$LJHJ^M\$IH8HD$
H\$@HlHHtPH|XLd0HL%jLd$(IBHSHu`HSH
cqu HKK;r%HCHlHtHMqHiCHKLC3f@uHSH
VcIH\$@HlHHtPH|XLd0H8H%HUH HHM(H]H8HD$
H\$PH|$XHHHD$@HHLD$@HPtHL$@HtHRHL$@Hu
@HHSXA
PtHL$@HtHRWHL$@HHSxAP tHL$@HtHR(HHPHL$@HtHRH\$PH|$XH8HUH HHM@H]HT$UH HHH
]HL$H8HD$ H\$HHlPHtXH|$0HHHtHPHtH3HO0NHc_Ht-
HHHGH4(HtHN"HuHOH\$HHlPHtXH|$0H8HUH HH}@tHE@HHE(HE(HM(H]HL$H8HD$
H\$HHt$PH|$XH3qAHq q(q,HqHHqPq4Hq8Hq@HqxHHH$~HH}HAH}HH}HA0`HHD$(Ht0DF
HLO|LHu|HCH|HCHHt HHPHHtHPHHH\$HHt$PH|$XH8HUH HHM@H]HUH HHM(NH]HS3HLILL; uLZL; uu
IA3[ML;u uMYL;u utML;hu uMYL;pu uuHt|HAIA3[ML;hv uMYL;pv uuHtCHIA3[ML;xu uMQL;u
uuHtHIA3[@[AAHHHHHHHL$H8HD$ H\$HHt$PH|$XH3qAHq
q(q,HqHHqPq0q4Hq8Hq@HqxHHH{HH{HAHT{H`HHD$(Ht03DF HLayLHyHCH,yHCHHt
HHPHHtHPHHH\$HHt$PH|$XH8HUH HHM@H]HUH HHM(^H]HS3HLILL; uLZL; uu
IA3[ML;u uMYL;u utML;hu uMYL;pu uuHt@HAIA3[ML;xv uMQL;v uuHtHIA3[@[HHHHHD$
H\$PHlXHt`H|$hHAAuoHL$(HHtHPHKHHc{(Ht0HffHHC H4(HtHNHuHK H3H\$PHl$XHt$`H|$hHHHUH
HH}(tHE(HHE0HE0HM0H]HHHH8HD$ HD$@HtH73H8HUH HHM@H]H8HD$ mHD$@HtH3H8HUH
HHM@^H]HSH A(HHPHL4#3@8K0EH [HSH y0ALtEu3H [sH [A(HL4DAQ H [HSH AHuAA@w8A$tD;u,A
{)L
zHL<HR|IEC(3H [WH [H(At
WH(HHHHBHHHPA HL,#3H(H\$E3EHt$EHH|$ tuH5H=Ufff:uMfA8uhHAHDK!EHEHCuHYEHEHCu
H9=u7AIHE;r3H|$

Ht$H\$WH|$ Ht$H\$H|$ Ht$H\$@AQ1DA,A0HsHHsHAHjsHA+A(3HPHXHy1HEHA HHSH0HD$
`HHD$@Ht33DB H\L
sLH3sHCHrHCH3H0[HUH HHM@^H]HSH0HD$ `HHD$@Ht5A HL{rLHrHCHFrHCH3H0[HUH HHM@H
]HHHpHx HHHHHL0A$
HHL0A$
HHL(ptHT$(HL$0A
1HT$ HL$0AD$
dffHL$0HsHL$0 HL$0A HV
HuHuHL$0H?H$@w HHHHHHXHpHHx H
ZAH~ tHHL$0dHL$0A HHL$0H
HL$0:HT$ HL$0AD$ r}HL$0A HjH$HL$0X
E3H$t AHGHA r[H
ZHHH$HH%H8MH\$@HlHHtPH|$XAHIHt/fffffLD$ HHH\$ uHD$
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Ht$PH|$XAHIHt0fffffLD$ HHH\$ VuHD$
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HA LD$ HDHD$ HC0H9vHHK8H|$XAHK(H\$HHHt$PH8H(H\$8HHI0Ht$@3H+K(H|$HHIu-
I9t(HC(HC0HC@I9vIHC
HS8HHHK0H9vHHK8H|$HHK(H\$8HHt$@H(HQ(3H(H\$8HHI(Ht$@H|$HH{0H+ILuHC
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HH`HL2IEHH2HAH7HAHHAHAHH`HHHu3H%)H%Hu3HAA3H%H(Ht3AyH(H(H
?H8nH'Ht HtHHHH
THDH
TH(Hu3HH%vHH%NHu33ADIH%HSH HHu3H [H
STHt<H@w3HHH;v(HHHH#H;r3A DIHuAAH3cH [H(HHt3AH3H(H(HH;rHtmHLu3H(HHLXH(H(Ht
HJ%H(H(H\$8Ht$@HIH|$HH~ HH;HHBH;s HHH;rHFHHLu3H|HHt@H\$8H(HV
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HATA[A\A`HdfffffHHQHQH@HHQHQHQHQHQuA\Hw_AP
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BL;B8J`B\AI@HBJ`H
HB H+LB@LJHM+I;vMInz[LuDA$<uE7<At$<t'HII;tHII;t$<uALL+I;MD uDR[DQIHADJ[DBTLB@AHu A
AADJ\DR`DBLBdAD;sB\+ffBDBdD+D+DJ\DR`f+fBLBdBP z[ADLL;L A IAJBTHB@LBH+JTL+IrVHHB@J[-
BP3DZP3BP3ADBTLB@H;J B[ADH
BP3MJXtIJYvHIJZvHIHB@A@J[BPB\=s'LBL;B8tJ`B\AI@HBJ`33APA\ATAXAYHA`ApAtAxHAhA|HHfffffffH
HQHQH@HHQHQHQHQHQuAPHtu%HH;L eHABPHHt$H\$BPH|$ z\s^J`r
HB`H t8HJH;J8'HB`HH BYAHJhBYuHJJ`BYHBhb\HB`LB@HBHI+zTuHH HH
LJ L+I;vMIWzXNuEA$<uD?fffffA<t$<t(IHM;uIHM;tA$<uAML+R@M;uA@DRpH
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+JtBDA;sJ|B;DBEPAu 3Az\DZAEu'J\A+fAfZLB@DRX+HB`z\AfD+fDZABpA@DJpBXI@E3DAHB@AAJJ;D
AHDHJH\$H|$ Ht$BPHJzTskJ`r
HB`H t:HJ ffffH;J8tHB`HH BYAHJhBYuHJJ`BYHBhHB`rH\$H|$ Ht$BPH\$H|$ Ht$BPDBPDH\$H|$
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Ll$(Ld$0H|XHtPHl$HH\$@H8HALHEt2L;s_IAzuAB+AA%
ABL;s-IAzuABA+A+%
ABL+IHSHEALLL\

t~fffffM;ARABI#=rAJABIABAA+ARAB$ARABM;wnARABI#=rAJABIABAA++ARAB$ARABM+I[HALHLL;
sNAI$<HuAB$<uEAJtAA+A AA+A+HAJM+IHALHLL;scAI<@uABt<uAzrAJEtAA+A AA+A+AJM+IH8EHt$
H|$3Ld$AHLl$MEHLs3cHl$(l$`LLH\$0ffI;I;sfffff$<tHI;rHI+LL+H+I;HvGDDBA@BAC\#DBIADBADt;D9AEt
AwAtAHLuAAIDD+Et1AAAAuD3tDD+DADBBAAAABDBAAIHHl$(H\$0wAAAuILlLdH|Ht
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HtHlH\$IHSLAHD$0AA [HSL3AAXAHAPA@AXAHAP[HH\$03H|$8D$D\$L$%DAT$DHO\AE3AfffD9Pu
9xuD9tHrH|$8tQt7tH\$0HArYAuYsTAAH\$0HAr5Au5r*
#ArAut&t't
5t6uAAH\$0HHSH3L\$T$$\$T$H[3A{HH8HXHhHpHx L`LhE3IILAtHHL$ LIxHMt:At-HT$
fffL;s$A(IH*B;JAD(rL;rH$H;ADL$(+HctHT<
DIwtHHT$ DH+IwL$0H$XHPH@H8HH8HXHhHpHx L`LhE3IILAtHHL$ LIwHMt6At)HL$ fffL;s
HA(A(I;ArL;rH$H;ADL$(+HctHT< DIvtHHT$ DH+IvL$0H$XHPH@H8A+A)Q)Q)QHSH y Hu3ytu-y#t-
QI+H;C`vK`H)C`
CuC H [ICP+KHH+HC@HD$0tfffffffffHCHHLD$0HCtuHD$0HtCC+C;CXwKCP+KHH+HC@HD$0uH [HSH
DADITHD+AHHI@MI+xD[TL[@LH [38A#uQPHQ@H+IXH;H(y uA+A9AXrH(HSH
y#HuGLSPHI@CXI+E3HH;AEt%DCDKTD+CM+MIwD[TL[@L{ u3{tu-{#t-SK+H;C`vK`H)C`
CuC H [KCP+KHH+HC@HD$0tHCHHLD$0HCtuHD$0HtCC+C;CXwKCP+KHH+HC@HD$0uH [3HA<
A!A\LAxHQ@Q#HQ(Q"I 3% 3 3% 3 3% 3 3%
3A@dH(H\$@HH|$HHHQ(HW{#HC(uHS@HWHC@H|$HH\$@H(HXH\$PHt$@H|$8Ll$(EEHv3H$HQ(HW3@
8s#Hs(uHS@HWHs@3DAr DArDA$FDHl$HH$C*DATAL3Ld$0KXA@8s#F
tDcP;HS@HtD9cPtHUAHHs@DcPUHC@H9C@
S\DGDk$sluOH;vt
=vuC8vClvClvClCl@8s!{hCpDCAtHHS(HHt
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XH(DAlMtHI(I3sH(H(QlHA(DA8ILtI3IjsH(H(3H\$@HAHI@DCH|$HHCtC DCDC8C#t H9{`sK`H)C`ACufC
`CPH+HHD$0tPHCHHLD$0HCtu8HD$0Ht*CC+C;CXw KCP+KHH+HC@HD$0uC DCCK+CSXA+;BA+;wt
+;BC$;GA8H|$HCKH\$@H(H(QlHA(H\$@HHDC8ItI3rDClMtHK(I3qSHK@38C#H|$HCHSSCtC
tH9{`sK`H)C`
CujC dCPH+HHD$0tTfffHCHHLD$0HCtu8HD$0Ht*CC+C;CXw KCP+KHH+HC@HD$0uC
DCCK+CSXA+;BA+;wt
+;BC$;GA8H|$HCKH\$@H(E3MtB;w3+BIM;rH(H\$@H|$H9yHuHLxLA(+Qh3MtfffA;w3+AHI;r)S)S){
uC+C9CXuHC9CuC+CDCKSXA+;BA+;wt
+;BC$;GA8H|$HCKH\$@H(HHHHXHhHc$HpHx$L`LhLpLxELDpLxED+IALDLfffE;ALL+3D;AGA+E,A6
C8ucA8u[;LtHVL+ffA8uIH;u;s#A
ACI;ABMt,LxLDp LxLDpEE+]L$L|$LtLlLd$
H|$(Ht$0Hl$8H\$@IHHDD$HHXHXHhH$HpHx$L`LhD$LpMEE33Lx?D+EHtDDThLvDL$ffffAD;3D;GA
+H|AIH+D;D$hABB"8*uXA;t5B"8*u+A;Hct!LJ M+A8u
HA;uD$D;sAAAIA;ABDtTB"8*s_HwT$hADThLDL$p$$H$D+EOAL$AL|Lt
Ll$(Ld$0H|$8Ht$@Hl$HH\$PIHXDD$HHHHXHhHp$HxL`LhLpHT$pLxIDL$xE
E3E3H<D+LgD$fffE;3D;AGA+LAALL+E;AB*B8
u@A;HctMH(L+A8uHA;uAABA$VA;tDD$`*B8
sAZIzD
A$ADMED+tL$xHTpEAL|LtLlLd
H|$(Ht$0Hl$8H\$@HHHXDQH\$hHt$pAHHs'AAAH;Au\3Ht$pH\$hHXHHI(D$HD@Ht$@AH|$xDCBBC<LDC
D$8CAD$0CD$(HC0HD$
CKCHHH+H;KuHHt$pH\$hH|$xHXHXDQH\$`Ht$pAHHs'AAAH;Au|3Ht$pH\$`HXLLC(Hl$hAEKAKTxAH|$x
3IkLd$PD3SD#K8%A+<GACE3;{ADII+8KIHH;tB8uHH;uA+GAA;FuPC<LDCD$8CAD$0CAD$(HC0HD$

CKCH;KuHieDCT$HALHC<HL$@D$8CD$0CAD$(HC0AHD$
RCKCHHH+H;KuHH|xHlhLdPHtpH\$`HXHhH\$pHlxLlXDiHHAs,AAAH;Au3LlXHlxH\$pHhLK(DC
H$H1H$Ld$`NETxF3D3FD\xAG+$AD3AED#[8E+AGEE3ED;cs"AHH+8uAD$EUE;At)D;Ss#AHH+8uAJAELtq
J.EHIH;tB8uHH;u+A;TuAC<LDCD$8CAD$0CAD$(HC0HD$
2CKCH;K`DCA;HLC<ABLTHHL@D$8CAD$0CAD$(HC0HD$
CKCHHH+H;KuHHLd`LlXHlxH\$pH$HhHhH\$pHlxLlXDiHHAs,AAAH;AuG3LlXHlxH\$pHhLK(DC
H$H1H$Ld$`NETxF3D3FD\xAG+$AD3AED#[8E+AGEE3ED;cs"AHH+8uAD$EUE;At)D;Ss#AHH+8uAJAELtL
J.EHIH;tB8uHH;u+A;TuSHK0DCKCH;K`A;ALDC<ABLTHLD@DCD$8CD$0CAD$(HC0HD$
CKCHHH+H;KuHHLd`Ll$XHl$xH\$pH$HhHHH\$XHt$`H|$hH3ffffDSACCCH;CuKLxLC(+ShHMt!fffffA;w
+AHI;r)S)S)S@8s uC+C9CXuH4C9Cus+CDKSDCXA+;BA+A;wt
A+;BC$;GA CSHLC(HCAAC<LDCD$8CAD$0CD$(HC0HD$
CCCH;CuHLxLC(+ShHMtfffA;w+AHI;r)S)S)S@8s uC+C9CXuHC9Cus+CDKSDCXA+;BA+A;wt
A+;BC$;GA
CSH|hHt`H\$XHHHHH\$XHt$`H|$hH3ffffD[ACCCH;CuKLxLC(+ShHMt!fffffA;w+AHI;r)S)S)S@8s
uC+C9CXuHC9Cus+CDCSDKXA+;BA+A;wt
A+;BC$;GACS<HLK(DCJDTxBD3AA3#C8AEA%EC<LDCD$8CD$0CD$(HC0HD$
CCCH;CuDLxLC(+ShHMtA;w+AHI;r)S)S)S@8s uC+C9CXuHC9Cus+CDCSDKXA+;BA+A;wt
A+;BC$;GACSH|$hHt$`H\$XHHHHH\$PHl$XHt$`H|$hH3{CCCH;CuDLxLC(+ShHMtA;w+AHI;r)S)S)S@8k
uC+C9CXuHC9Cuk+CDCSDKXA+;BA+A;wt
A+;BC$;GACSWHLK(JDDxBD3ED3BLxEAA3DC#K8AEGGC<LDCD$8CD$0CD$(HC0HD$
8CCCH;CuDLxLC(+ShHMtA;w+AHI;r)S)S)S@8k uC+C9CXuHAC9Cuk+CDCSDKXA+;BA+A;wt
A+;BC$;GACSH|$hHt$`Hl$XH\$PHHH(H\$8Ht$@H|$HH3ffff{CCCH;CuDLxLC(+ShHMtA;w+AHI;r)S)S)S@8s
uC+C9CXuHC9Cus+CDKSDCXA+;BA+A;wt
A+;BC$;GA
CS6HJDDxBD3ED3BLxHS(EAA3#K8DKBBKHC0DCCCH;CuDLxLC(+ShHMtA;w+AHI;r)S)S)S@8s
uC+C9CXuHC9Cus+CDKSDCXA+;BA+A;wt
A+;BC$;GA CSH|$HHt$@H\$8H(HiHHHBHdHBy!uHCHBHxHB A\uHHBH9HB uHHBH]HB HHBHHB
H(HyH\$0H|$HHyHt$@qpu_yDAtHHCDHK0Hl$8HK(!;sptHK8HK0;spuHl$8C{Ht$@Ct
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LHK(CHK0:HK81H\$0H(H(9H\$0Hl$8Ht$@H|$HAHIHHH:ulHOG@VuXHO
IuKHO(<u>HO0Dzu,HO8D3huHOLHGt(H=H|HHt@Hl$8H\$0H(3HD\$HH\$H|$EIHt>LD$@fffffffffDWHAID
B+A@BAuH|$H\$HH(DT$XH\$ Hl$EH|$IDHtTLDPHtHt$`ffffffffOH3I3AA#+A@DAAuHt$H|$Hl$H\$
H(H(DTXHlHt$EH|$IDHt_LD$PH\$
H\$`ffffffffGWHG3I33AA#+A@DAAuH\$ H|HtHl$H(H(DT$XH\$
Hl$EH|$HItdL\$PHtHt`fffffffffDCCHAID3CAD3D3E#B+ACBAuHt$H|$Hl$H\$ H(HHhL`LpLxHXHhHpHx
LALhI$E3I$sI$ffffA$tAKIHtYI$p~I$b~H]HvH+]I$I)$I)$h0~I$"~uI$XH]v(+]hHSGUlHE(DE8HKI]I$8A+]A
\M\;rMHUxLL+MlHT$0UHE(A;LHMAG\$(LL$
DM8A$D[D]I$HELIEML|HLtPLlXLd`H$H$Hl$xH\$pHhHHHXHhHpHx L`LhLp3LxHLA
D+XDkHE\$PE;AGD@;GtPH+HHHg3%H|HH2H{L8
@DA@DABT$PH@AO\9HaHALAAHIKfHHHu.DXpD`+HD;DCA+DT$T;Bt+;BE;@LhAIH8A+A\@HCL$HH
D$@xD$8tED$0HPAl$(HD$
DT$TH+HAHDHhE;\T$P3`AD`+A+H;tDT$PpE;zE/L|$hLtpLlxLHHHH$HH(H\$0Hl$8Ld$
Ht$@HE3H|$HHKXAHK`8ffffffD9cHPD9cLHKpD9uHMyHC?HH;`v!DtHPA+M)`D9uHxDHKxOL9D9uD9tHx
DHvH;t$fHHF;uHKhLd$
H|HHt@Hl$8H\$0H(3H8HHHHHHA@HAPHAXHA`HAhHApHAxH(H\$@H|$HHHH0HO@'H8HSH\$@H8H
|$HH(H('3H(H3HD$(uY3HHhH\$`HlXHtPLd$@BLl$8=@LAEHQ,r
H8H|HHu'DHHH8uoHHCED IH|$(D$
tHHALuHK@DH@HLH|HLl8Ld$@Ht$PHl$XH\$`HhH(H\$8HHt$@3H|$HHqqD@3H%HLG,wHt$@GLHC(

HG
ClG(HCxHG0HC0HPC$XC\\C`HHhCpCtC<H\$8xH|$HH(H(HyPH\$8H|$HHtzyDutALtHtHKpHt$@HKh;t"ffffH
KxHKp;uHt$@CDH|$HH\$8H(H(yDH|$HHtE3H`AAAH\$@HOhHOXHO`PH\$@$Hs3HOpHOxHsLG?WWAO
WAOOBGr+O,DG(HW
bD_,AD_H|$HH(HHSH AH9AuCH [AH [HA0LY H\$YH|$ H9DOFD3AGCD;r-IH+89uA+AI@AXH|$ H\$H|$
H\$IHA0LY H\$YHt$H|$ H9DOFGD3EA3AGAED;AArIIH+89u;A+A@G8D9uAI@H|$
Ht$H\$AID;r"IH+89uA+AIAXH|$ Ht$H\$IHSDAHALBANADIALt0DCAAffffffA
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HOxHoLG?WWAOWAOOBGr+O,DG(HW
QD_,AD_WHGGGHDGH|HHt@Hl$8H\$0H(H(Hl$8Ht$@H|$HHH\$03G9G9oDt9HO`oDoLoHHOhHOXaHO`
"HnHOpHOxHnnLG?WWAOWAOOBGr+O,DG(HW D_,AD_OAGr'HHG0
DBHO
L3GABWHGGHDGH|HHt@Hl$8H\$0H(H(Hl$8Ht$@H|$HHH\$03G9G9oDt9HO`oDoLoH#HOhHOXHO`h"H
mHOpHOx;HlLG?WWAOWAOOBGr+O,DG(HW D_,AD_OAGrBLHG0L_
AAJDABWD3EAA3CAWHGGHDGH|HHt@Hl$8H\$0H(H HHHBHHBHHBLA@\t^t5Ax"HUL
IEHHHA8HHB HHHWHA8HLHB HHA8HHHB
HHBH((LwYuT$IIIMAD$JTLHTHDHDK9H(H(H((LwYuT$IIIMAD$JTLHTHDHDK:H(H(3AHAAA1
wBL
ByEMAAQux u
Awyu :QrAA AAAADAAAA$@sD98ADD+gfffAQD+CADADIf(ETmHT$HHxHXHhHpHxL`LhLpL$HI IA;
LxMMLH$,Hk8I;u$tBt=I$H;HHI$AU!II;HKII+<$H+E3H;H$r
HAKHvu3HKE3.H$H;HGHH$LHK(LIHK8k;H{8{TuK+CPH;wKT{PHLI$)CLCu)E3A3HKc.H$CH.HKH;L$
MHGLt$(D|$
H$q.H$I$)SLC8KL+LD)CukAt_HuMuuTD9CuNuJDL$A; H$%A3,A3!A3uA
L|$8Lt$@LlHLdPH|XHt`Hl$hH\$pHxHHH\$@Hl$8Ht$0I1H|$(Ld$ E3IIM!9 LLZl$pA,Mu-
t_tZHH;HIHHAuAJ:Mu MHH+ABu8HtCABH;HGI;IGIJ8HA)JABHL+A
aHABu
E9bt|uAD8'uZABH;HGHHA)Ju"ABIL;HGIJ8A)JL+E9buE"A: HABA 3Ld$ H|$(Ht$0Hl$8H\$@HHLL$
HT$HHxHXHhH$HxI8L`LeLpD$LxH3IHpHMMMLhffffHC0L$H9C8uHK8Hs8H+H;rHAH0HLHD$(L$
HMHHMHHk8L+HK(H+HH$LHDD7I/HH+EH$u!Ht!HtHL3=A3L|$8Lt$@LlHLdPH|XHt`Hl$hH\$pHx
HHAAHAHA HA(H(H\$8Ht$@HH|$HHx;HHuH|HHt@H\$8H(HHH03C H{hJ
HS(HHHffPHHPHHuH|HHt@HH\$8H(H(H\$0HtHQH0HLd$ E3HtHHPLD9hLtHpDhHl$8Ht$@
H|$HHHD8'tHOH*0D'HHuHt$@HHHtHHPL'LgHHuHCHHLd$ H|HHl8H\$0H(H`HHxHXHhHpHx
LxE3LLmIIE98HI
xExD8uDHL{(L{(i@%HHFHH7LLDDLLDD9HnPLdpLlhAEt!HH+`H;rD9HEDHOLt$`L|$@MLHL$0ffffffffL
GHHH+GLGH+S8LL$8HHD$8Dd$
HL$8HODuHVPKH+S8H;w0HL$0u,HC8HL$0HtH=@s0LHGHD$@AuEtDx
D8ktDkMLd$pt*At$AtEtHD$@E3Ls8AE3HGHGLt$`DHEL|$Xt
GXAHK8tCAuHOGH"AGtEtH+AH;HGHH`HO(HGLlhHHHH$HxH(H\$@H|$HH@HHH
(H
xH
uH|$HH\$@H(Ht*HH9sXHHPE3LLHHHHHuBH|$HH\$@H(HHHHS(HC0W`LHH|$HH\$@H(?HhH\$`Hl$XH|$
HHLd@L$H@Ll$8LHLt0H
8H
xII$IEMtHHt$PH$t t3H3HL$xHL$(L$HMD$
H$HHLt$0I$HK8Ld$@HIMLl$8uOH;ut,H;u HH9u-
HH9tHH9u3H|HHtPHl$XH\$`HhHXH\$PHHlHHHt$@H@Ld$0E3HALl$(H@HL$EMD"

A;tuEEu3H8HD9Hu=$tu,H@H90H8H9(D
L9KpLt$ AH|$8ffHCpHI;IGHHLHH;uEHH+tDH0E332tH|$8Lt$ Ll$(Ld$0Ht$@Hl$HH\$PHX
Ee3H(H\$@HuHHQ`LAHH|$HUurHHt
HC0H9tKHHPHHS0HHHHuH|$HH\$@H(HC0HHQ3H|$HH\$@H(T$HHHXHpHxHtNH
HHtHHPHHCHHuHpX$uHHW`LGHL$HHt
HG0H9tAHHPHHW0HHHHu
HG0HHHE3HHLlxLtpHDPHL|$hMLT$8HD$XILT$0MHl$@Er
ffffffHL$0L;t4HpHT$0-
E3HLMLT$8uD$ILT$0uGHLLD$0HD$0HGhIHMLT$8HL$0HttDHHW8HHH+H;vH*E3H+D9t!HH+H;rD9HEE
I+HO/HD(DL$ HLHLLHH3HLD$HLHLD$8H+DHHtH9wAE3E3A'Mu HD9$uH;ArhHHT$@LOpH+HILA
AH;HADH9uLHHT$@LOxHl$@Ll$8M$HHH+D$PH=@sHH+D$XH=@rILIAHHL0HD$PHE3HD$XpA$WZ
EtAP$A;u$D9t(D9tHH9tD9ut3DDALtpLlxH$L|$hLHHHHHxH\$pHt$`H$Ll$HE3(L.HvAELl$HHt$`H\$p
HxQ`IHlhHHA0I@HA8I@LdPLHA@I@HAHI@
HAPI@(HihHAXH$LIpHLaxLDtHH$H|XHLLDD/y@A:8tHQHq"DHHtHHPLD9hLL`uHphHCHpLHHD$
HHHCHHHC@xHHD$ HHD$(HHD$0H
HD$8HHuj8u`D9uOHHHD/HHuEH|$XHl$hLd$PLl$HHt$`H\$pHx(vQ`I3MHA0I@HA8I@HA@I@HAHI@
HAPI@(HHAXHD$0HAhtIHD$(HHHPHX7HHHXHhL`M
LhILp3I9LxMMLHt$HH+L;r9LEH$H$3HXH9Pu3u/HC0HLXHXHChHPHHPHXHD$@H+HD$0HDHHLHH
LD$PL
HIl$(Ld$PHD$ HTPHLHHPHIHIUL+LuH2H)L|hLtpLlxLHHHHHH8IH\$HHt$PILAH|$X3IHHD$
tL+AI;IGHD$ Ht>HALD$ HDHD$ H@HF~
HAH|XHtPH\$HH8Ht$PHH\$HH|$XH8HT$HHHXHxL`LhLpE3LxLI@ AMA8EMHHL$0M0xH;s
AFHIMHt$pH+H$"AU
IMHLHD(LD
m$"u7H$LHAH;s,Hw#M9uQ.A4Hl$xIHT$0;BH+MH;M9uHDEDEDIMDlH$$H+HDH!I]+HM$t)H$I<$LHIAH;u
eIHI<$BIMX#3Hl$xHt$pL|$HLtPLlXLd$`H|$hH$H E8u
tE8uAAAA$A
C@sCSA}tH$AE BH$MEHMAEAE
tLIAH;t
/I3HSH HHC8C@CDCHH [LHxI[IsI{ HIKMkHHHGHAHGHAHGHAHG HA
HG(HA(HG0HA0IKc_DwHO,A@A;EhAOANt$L;|$LuAA;~
AuAt$LHG8H$Ld$pHLg0O,tHtI;rIuHG0HHG8DG,HLg0uAAAHuA;
HG8yHu<WHH;rAHI;IGH;HBHH%HG8A;~3It-
HO83IHHI;tHH;sADALlhLdp_DH$o@H$wHHHHxH(Hl$8Ht$@HHH|$HHHuH|$HHt$@Hl$8H(HHH\$0Ld
$
!E3HOLg@GHGLGPH\$0HGXLg`HwhHopHGx@ffffffL HHuDH L L`L`H@HL`L`L`L`L`uHt$@Hl$8L
L`L`L`LLd$ HH|$HH(H(H\$0Hl$8Ht$@H|$HHYx@H3HHtLGpHWh%TH+HHu9tH EpH LffHHt
HGhHPH+HHuHGhHW`HHPHGhHHHo`H|HHt@Hl$8H\$0H(H`LHhHI[HICHBIKHAHBI{ HAHBHHAHB
HA
HB(HA(HB0HA0IKD\$(L$,A~HKHAPHK3H$H$HhHQXH(HCD3D;v(rH(LD$HL$HHHXHhHpHxL`H3MLxIL
LHHHtHH8HH$H9
uJHKhQHI$u
HD$8Lt$pL$MH
Lt$@HEHHD$8u%H9s`uHChHHHC`uEA8tuQIHSHD$08u/I$LD$0H$fuADAD$ MHLlx3MAD$
LIDHDPDXADHC@HD$Ht\HKXHH;tI;rHI+HtH;HGI$L[pLKhIHT8A L\$!cKLM+HtL;LGHIA
IHCpMHD$(HChHD$ cgL$HLEfffffffHH$tHH.H+H$HS`L$MIu`H$HHtH$H0ID$IH$HtLHu3HtH$H$c
I$ubM\$MuRL$MLEl$PM;u@MtDXHH$3L;u
HH>7$t+HtgH$HH;$r(HH3Ll$xLt$pL|$hLHHHHHHHAHHt LHHI[IkIsI{AH

McMkE3HIDHICLu(HAhHHHHuGH
HL$P]dKDdLHHHD$PLHDHE3D$@H$HHD$8H$Hl$0Ll$(L\$XLl$`HD$
Ll$h'H$HLlxLHHHHHH(H\$@H|$HHH HxHHt&LH3
H;DH|$HH\$@H(H;vH|$HH\$@H(HLH)HH|$HH\$@3H(HHHXHhHpHHxHHL`LhMILHtHu
HtMt
H@ffffffHHu{H9uH gE3MHHD$PHLxLLHD$XuHIEHMEHK@HCpLt$pH8 H$L|$hH0 H$H H(HD$PH H@ HHH
H
H LtL;sDLt$pL|$hLlxLHHHHHL9t4H
LHHtHChHPE3LHHuLCLH L HCXH yhMTHH+IMAH$HSxHHD$HHD@HD$8H|$0Ht$(LL$
MMHH8DY8DQ<HXHhHpq@Hx
L`LhDiXLpLxLyHIAJ<HEL|$AAD#AAC.DAGsH;AADHAD;m}DDMutIHU(HuHU
HEMDETEAAAD#AA*DCIM4FAsoAIfffffffHAF1sH;AADHAAD;sD
D+D+TH;rAMHHU(H;sLM E3HEH+AHFffffffDEAA#AA,FsH;TAADHAD;s
DA3
D+L D+;rACTo
D+D+AIsH;AADHAE3AD;sHLDEH;ATO8D+D+AD,$sH;AADHAD;s^=CGDsH;QAADHAD;s=s
H;"AD+D+ATOPD+D+AsH;AADHAD;sDHATOhD+D+AsH;AADHAD;sDD+D+HL$AHAsH;^AADHAD;sDJ,q
eQD+D+AsH;AADHAD;sDJlqDD+HD+AAIfffffffffHAD*sH;AADHAAD;sD
D+D+L H;rD+AABHt$A;ADBIANnHAF sH; AADHAAD;sD
D+D+TH@rsA#HVEAsH;AADHAAA+A#D+uIfffffffffAFfsH;sWAADHAAD;sLDHHD+D+Lu$$$AsH;r3L|Lt
Ll$(Ld$0H|XHtPHl$HH\$@H83A\Adt
A@ADA\EtA\3A\HA(AdA@ADLL$ HHhHXHhH$HxL`LhLpM1IEy\LxMMLHMt"CdsHAIDhCdIIuCdt{ht
ysE3fCjKiC8CdCkCl{\K<vFKtH{fCTCPCLCHCXC\DC\Et{AsrHS(LKL[
DSHIAH+I;B{DuK+C@;wKD{@D+DC\t,ffffffff3I;IBI+HHBBD
uHS({\Ht$P6ffffffH{(3I;r(C\u 9s<N9$QYpHckdHIruKt>&MIH"8t IL{0ffff{DIu+KDC@IA+H+H;vH9Eu
{<LHa9CDuC9C@rCDDC\EtuAslHS(LKL[DSHIAH+I;B{DuK+C@;wKD{@D+DC\t&fff3I;IBI+HHBBD
uHS(H{(I;sH9s0s
{\HC0I+I$LL+E3Hs"IAI;sHDgAHrkdrt$HShDHt HshHs0fff{DMu,S{@HK(I+H+H;vL
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{<rLIH
\9CDuC9C@rCDDC\EtqAshHS(LKL[
DSHIAH+I;B{DuK+C@;wKD{@D+DC\t"3I;IBI+HHBBD
uHS(L9k(sH9s0s
{\C0+h;+D;D+ALILL+Cd{\H${<udE3Ht$PL|$(Lt$0Ll$8Ld$@H|HHlXH\$`HhHHH$HKhMIH$DsdM4$r
HAHHQILL$ HT$HHxHXHhH$HpI0LhLmLpD$LxH3IHxHMMML`ffffHS
L$H9S(uHK(H{(HH+H;vH7AH$LMHL(HD$
|HHMHHk(L+HKH+HH$LHDI/HH+EH$u!Ht!HtHL39A3L|$8Lt$@LlHLdPH|XHt`Hl$hH\$pHxHSH
HHQHHPHCH
[H(H\$@HHQH|$HHHSHWHHGSH\$@HGH|$HH(H8AH\$HHt$PIHBDBADBADBADD;DB<DD$$DH|$X98A
D+gfffDD$ T$"HS+AL$!Ht;{`t/IAQHHHCHHCt?H
{`HCHD$
H|$XH3Ht$PH\$HH8Ht$PH\$HH8H|XHtPH\$HH8H8AH\$@Ht$PH|$XIHBzBB;B<|$$DHl$H98AD+gfffDD$
T$"HS+AL$!Ht;k`t3IAQHHHCHHCH
k`HC@r ?(H|XHtPH\$@H8@HCHSHHH#H;HBHtH;{ t8HVHHHCHHCuHSHVHCHD$ H{
H3Hl$HH|$XHt$PH\$@H83HA0A,A
AA$AA(AAAAADHEDHyDu'D;CLANBBAH;B0v=DJ0ffffAD;vD;vvABz}B39J}J9J}DR9J}AB9J}@A
AALB9J}9JB9J }B 9J$uB9JB$z,}ztzuADR,HhHHT$ HHAHBHAHBHAHBHA HB HA(HB(HA0HL$
HB0D$$HhHSH

ADHAHd%HHHd)A|CPHCTHdCXAC\HHHHHHHH$HH,HH4HH<HHDH$HLH,HTH4H\H<HdHDHlHLHtHTH|H\H_HH
AFHHd"A-HHd$AHShAAHMH [HSH
HAADHHd%Hd)HAHdCPHCTACXC\HHHHHHH$HH,HH4HH<HHDHHLH$HTH,H\H4HdH<HlHDHtHLH|HTHH\H HAfHd"HAMHd$HA4HAAHKhMH [
HhHLHL$ HHBHAHBHAHBHAHB HA HB(HA(HB0HA0HL$ dDT$(AL$,T$0D$$==`AD$8s
AA;AGACHA39T$4AL$<EAA)BtL$@}B;L|$LAdD$DADD$HA3AHhHhHH(y@H\$@Hu9HQ(HA0H|$HHyHH+LH;tC@ HC(H{8H|$HHCH\$@H(H(QH\$@H|$HHyHH HAruHAH|$HH\$@H(CHI@HAH;K
SHKuH3H{t/ffffHCH@xH;C
HCuHHKuH|$HH\$@H(H(H\$0Hl$8Ht$@H|$HAHHffffffDIFDM#A#HF+A+#fAfBLMsHrH|HHl8Ht$@H\$0
H(H8H\$@HlHHtPH|XLd0ALl$(AHALffffDAA#ADGDM3D#A##HF+A+#fAfCLMsHrLl$(Ld$0H|XHlH
Ht$PH\$@H8HH8HXHhHpHx 9LhLpEAHLL`ffffffDEGNAAAL$AA##HF+#A+fAfCNsHAuLt$
Ll$(Ld$0Hl$HH\$@>H|XHtPH8HH8HXHhHp2Hx
L`LhHLpALLEA+HGf+fsHAATMlrC+HGf+fAUsHDEI$H7?+ffAEsHAGDuOduN#A#HG+#A+fAfAL$sH^DnGlNA##HG+#A+fAfClsHBnEDDMA##HG+#A+fAfCLsHLt$ Ll$(Ld$0H|XHtPHl$HH\$@H8HH(HXHhHpHx
L`LhLpDMIHi@LlDALHI4PE3A,IffffffAEAAHBADFFHIHL3GHH3AAFEHHDAHAHAOAAOsL|$I}
H5HA,CHE3AD|fffAEAAHBADDFFHIHL3GHH3AADFEHHEAHAHAOAAOqA4$CL|$H5AHA,A;CtLtLd
v[I}@fffffffECE3AfffAABCHHH3EAsA)AHD;GrLl$H|$HHt$@Hl$8H\$0H(H(ALH\$0H|$HHH HWHH
G@W uH|$HH\$0H(Hl$8Ht$@
;wHtJ_@H;GW
LXII3IL+H;tA8uHH;uI+Hl$8Ht$@H|$HH\$0H(THLHHyLHXHh HpHxL`LxAHA4A0ALH WHH
G@W DD|$$u3hB
;wHu[_@HA;AGWB
LXHH+IIH;tfffff28uHH;uA+Dy<q8D|$$_@skHA;LLAGWDpLXE3E3E3HoPE3fffffffffMML+BL8E:0uIA@A8Cu>;vIKM+fA8u H;rBB;$vEMBAIHArOHADD;DT$ r!AJARDOLHW(D$ f;r)A
NOLHVW(9D$8IH+D$0)sD:tAsGDDgdDoDf$ASD#HGh#W`ARHHrpEE3DAAffAEAA#AKAEHAA3HD#H3D
ArDT$ HEE3AfffffffffAABCHHH3DAsEMAIILBGdHfABGdH5HDBoH5HA:E,uKWBG HHA;sfA;DCADT$
sD$0AD$<D$`D$@D$DIcHH8fffff@H sE3McH$LcAEODOu0HHIO
HHDDoDO4H5HADuIHD=OLIA+HHHHH3DIIG)HiIHH9LAA;JsJDBDfBAIH AsAHAD$ArAAD;'GD3GHD;
E4vH
HD;wD|$$MiIcLLiHHcL9NMH
]A$AAEMBsIIDCBIHH8d-HIDE;ZsCEZEJAfABD;MuHA;tAIII FHt$(H$9\$ L%%HGLWHH
G@W D$du3Hl$h{
;oHHlhtt(b_@H;GW
LXHH+IIH;tfff*8EuHH;ut$(D$dH$A+Hl$h;oHH;+uNH;8uB! l$0DD$@D$D+B!` l$0DD$@D$DD;;fEt
A+fAu63@(HD8sC C p
HH8AD$`suu'DAD$0D$8ADD$@D$`D$<AD$DmiD$<l$0uDAAl$8DD$@D$DGHDl$@lHDl$0l$8D$D#DAjD$
`D$<Al$0l$8DD$@D$DL$`f;;D;;;HW;D$DhL@GdAT$\A#ME3L+D$$E<HHLLDPDBWdLT$xHAPAL$4H
Gh#W`RH,HrvEE3EAAfffffAEAA#ATMAEHAA3HD#H3DArLT$xHEE3AfffffffffAABTEHHH3DAsEt$4A;;s,fD;;;A;BWdL$\L$H5HB_H5L$pHE:DDD$4u};r;tif;w^BW
B_HHA;;w*;;;f;E3O@DD$(A+;GL$\WH;FEl$HE:Ll$PAD$A8EAD$A8EA;MGA;v%IMM+fffffA8uAIHD;rLc|$
H- A` D$HLAD#_dHIO OdH5HHHH$BGIH5HBGDD$(HCID;s(HA+DH8D|$ fH
H@uMiHMB;:sYAA:::f:0Ll$PD|$ LlPD|$ Ll$PD|$ D|$ D|$
Lct$$HD$83Mct$XLHDx\$,L$ffDD$(ML+AA8EYACA8EJAA;v!ffffffffABB8)uAD;rCD;s@A+IHDH8D|$
LL$ffffffffH H@uH$Ewu6HHIG
HHHHDDlw4HH5HDuHHD9wLH+HHHH3DD$4IcA4It$$HiHHHHHHH8ffDD;QsDQDAfAAHH
AsH$uAqt$XGHEAAD$\;GIcD;s&MILM+fffffA
8uAHD;rE+AC$DdLghAHD$PEETADG\E3AD##G`,@fffffffffEAA#AEHADAHA3H#H3ArH$HA

GD\$(HHH$OdIHAFA#D|$ G!t$$DG HH5HHH5H&HE;sGL$IHH8AA+DLD|$ L$fffH
H@uL$AMiAHIAH;8s:\$,lHLlPL$fA88D8fD8\$,lHLlPLHDxH\$,HD$xoHL$h;v0L
33A;(vfffHA;,wA,T$dL
Dt$XA;T$pHt(GHD;IcT$ps-HA+DH8D|$ fffffH H@uE33E;0D\$,vfHAE;4wD\$,ETIADnBIH\$hHDT$4M8d-
Dl$HDHH$IcHDxHiA,H$l$$fffffAE~HMAFD$
AO;BAsAHHD!HAAHDCL=HD;9s%ABD9D99f9D;3GHLLPAD\MM+;EGIcD;s)IHD$PMJL+A
8uAHD;rLL$PE+A$HDDwdFFTAEH
3p E#AHAAHOdHDHD$PDHoh#G`4@fffffEAAA#AAEHTEAHA3HD#H3Art$XD\$
AEA#E0Gd!DT$(DGHH5HHH5H$HDE;sILl$xIHH8AA+DLD\$ Ll$xfffffffffH
H@uAMiAHIAAH;9s=D$4D\$,H\$hl$$Dl$Hf9D99f9D\$,H\$hl$$Dl$HAHD;\$dD\$,H\$htUDSADT$4MBIH8d-
DHTpHl$$ED|Xft(H$L%#H;t$ t$(H$iDFIcDG4HH8H8ffffffHD8D8D8A+EtHH AH A+AD
EuDA"H AH EJH AH D
DrDFG<o8IcDG4HH8H8fffffffffHD8D8D8A+EtHH AH A+AD
EuDA"H AH EJtH AH D
DvADG0DLLLLHHHHHHHXHXHhHpHx LhLpLxE3HD9yLAGAALH VHH
F@V Du AAGkB
;~HtW^@HA;AGVB
LXHH+IIH;tff:8uHH;uA+y8Di<Ld$PDf@AHE;EGVDPLXD|$ EELNPfffffffffA IH+D:uFBA8Cu<D;v IKI+fffff
8u
HA;r;^HsHA;vDD$ DAIAr;~HrFA
ONLHWV(KSDNLHV(ArZAA
Icv0H
A;u;vAHAwuAACAAArKAF;s AF;rsAN;r+r#D$ ANAVNLHV(AAHV<HDF8rM;N<
r;O;u
;D;AC;rr
;HVDPH^PLXffffffML+E:u0A@A8Cu%DOIKM+A;s>A8uHAHArEtHGWFLV(AL|$8Lt$@LlHLdPH|xHtp
Hl$hH\$`HXH8DIDH\$@YpHl$HIHt$PLH|$XHFIdAHAx+AfD+fFIdsHpvWDW+ffWsHOp<GDH
BHWpHd%DE3Ld0_pGDLl(X_pAAMfffffffMA=d"HGx+f+fA=d"sHOpl-
I@rLl$(ffffffffHGxsHOptuIfffffffffH/HGx+f+f/sHOp#CD$HDrLd$0HtPHlH_pH|$XH\$@H8u99t
|t
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Qd#Ht$@H|$HffffHKpWHuHKpH|HHt@u99t
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HHfffffffD^HEAAAAAAAHFBH3DBCHHH3ADBKHHH3CKD^DCEHHAHAAHOAAAAHAHAFBH3DBCHH
H3ADBKHHH3CKDCDHHAHAHOADOAHAHAAAH3ADBCFBHHH3ADBKHHH3CJDCHHHAHOAD^OEH
AAAAAHFBH3DBCHHH3DBKAHAHAHH3CKCDHHHAHOAFOH|$ Ht$H\$HH8HhHpHx L`LpAHLh-
L4AHt
HXffffffA$E3DDESAAAD+HUd$fAAACHFRHH3DAuDIIrL$(H|$HH+Ld"LHL$fffE38Lfffffffff

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U
HHl$`Ht$XH|$PL|$0DD|$pAHSuAHBHT$pHd{p+ffdsHKp{pHSSLHKpH+HShD8CLAD|$pHLd$HLl$@Lt$8SBt
HDD$ CK09K4uAHDD$ 'HHDADt$
C0D{dSD{pD#|$pDHEISdA+ffSdsHKpHspSKLH5LHChL+L$pASE#S`RHHKD1` CDsHKpED|$
wCPHKpEL+ED|$ V+HCxf+fSdsHKpSDSAc+HCxf+fSsHKpDCDBCE+ffBCsHKpDSDHIS At"HCx+H5f+fS
*H5+ffS CD0
CD+HCxf+fBCsHKpSDS4Au+ffS4KTHCx+f+fS4sHKpBSDSLAu+ffSLKXHCx+f+fSLCXK\C\CTCXCPH5~KP
CTsHKpA{pEFHSpHd)Eu(BuLHLd)ACDD|$ 0 CD+ffSsHKp.CDH
spp AHSpHd%EECDLu(BhMuLHdMLd%A[CXAAC\CTCXCPCTAD$CPsAd-%ABAHd-4HD|$
{pADn@ABHLd"fffEAIGNE#A#HCx+A+#fAfCNsHKpArA{pAs'DHKpD+Hsd$E
A+H#HKxsHKpWuDAANA##HCx+#A+fAfsHKpDnAFkNAA##HCx+#A+fAfBksHKpF,nAFkNAA##HCx+#A+
fAfBksHKp$BnADCDAAL$A##HCx+#A+fAfBKsHKp{pD|$ D|$pD){LD|$pJu%rHrH{HS|$xA+,;$s-HHH+HH
H;AAH+HVLt$8Ll$@Ld$HH|$PHtXHl`L|$0H$Hh=lD+HAHHXHHt$pH|$xLdPLt@L|$8MIDHAu$IAHHt:
HHH\$`3Ll$H9Dkt9u8)tAHWhHl$hHtH9t9taHVHHVH_hHHHHHHHGhHWhHHH*A;sED+t`DOHHEHL|$(D$
qHv@sE3HWHD*m|3eDOHHEHL|$(D$
Ht9HWHHSd3+HVHHVHGhHHl$hH\$`Ll$HL|$8Lt$@Ld$PH|xHtpHXAIH\$DQ\DQXDQTDQP3H|$
YDHApHYtHYxHHLLLLLHH
{fffffffffHHufBffBfB0HHuId"HfffffffLLXLXH@HLXLXLXLXLXuId$fffffffffLLXLXH@HLXLXLXLXLXuA
AAAEtIyhAfId%HffLLXLXH@HLXLXLXLXLXuId'HfffffffffLLXLXH@HLXLXLXLXLXuId)HfffffffffLLX
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Id+fffffffffLLXLXH@HLXLXLXLXLXuAAY4AAAAAY0AYLH\$+EQdAQ`HHH|$hHu
H~GHH\$PHlXHt`Ld$@Ll$83LdMDA$AEtILt$0L|$(fffffffLLd%HdMA;rL|$(Lt$0LLl$8t)fffffffLLd)I8;rLd$
@H|hHt`Hl$XH\$PHHHHH\$PHDffD;v r
Lt$0E3HDDHD9uHHCHHl$XHt$`H|$hLd$@Ll$8L|$(dMAt/fffffffLLd%HdM$;rL|$(At,fffffLLd)H;rLl$8Ld$
@H|hHt`Hl$XL3Lt$0H\$PHHILL$(EHPLAiALL$0LhLLD$(HH+&H(tHlH(H(HAH\$@H|$HI;IHs
HAHILH){H{H\$@HH|$HH(HSH HH SKLH+H [HhH\$`3H|Ht$PH|$HHD$ IIHLD$(\$8HD$0tmHHl$X
HHHHD
Gp_tH_xHHHuB2H$HD+HL$0H)|$8EH|$HHtPHlXH\$`HhH8H\$0LPLD$`HHt$(ILL$h3HSE33HH|$
uWffuKHt,LHHL+LLuE33Ht
tHjH|$ Ht$(H\$0H8I8DLsIAAA@
rAsLAA<fffD;vD;vvAAAADJBAADJB3L3IHtIH+QLIItIH+ALAHIL:HHHL$HHhHXHhHp
HxH9L`LhLHOXLpH_X3Lx/N,fffh%HHJE3E3l$0L9L/M9|$uHG8IHHID$u]mHD$pLgIHhE3Ht<ffffffLD$8HIH
t$8A$HD$8LHH+uHt1Hu3HItHtHH_l
N3E3L;/ADt$09HHO(HH+L;LGHG E3L<IH;HADt$0HO`39.tDDAF;r
AuHH t
D$0P|$0uI;sBH9wt<HH uHH D$0GhHOX-KMuzHDHBHt HD$pLxHLpl0HG@QHOHMEl$(Ll$ tHH$
l$0KvHDLco|E;u G|
A>HWE;tAu%3L|$@Lt$HLlPLdXH|$`H$H$H\$xHhGxDt$0IEu+At%HG@HOHAPtHHGxJAHD;rE33HAtAD/
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/Do|HmHO`l-u{t*HLmuEDD1EHOXH_X3Z+Ld$pH+H(H\$@HHIH|$H*uVf{ubH"H;tHH;u
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HC8HPL7H(HuH3HCPHS0HHL|$({lH{pL{pt
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Hd&t
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yHu)HHt!LAHQHtC
CH [AH [H(H\$0Hl$8Ht$@HH H|$HIH5H~H^~H|$Hu HHtLFHVHtF
^HN Ht$@Hl$8H\$0H(H(H\$@HH|$HHy H[HH|$HH\$@H(H(H\$8HHt$@H H|$Hn{usHK
H|HHt@H\$8H(H%?HSH HAxHHuHHA H HHuH [HCxH H [HL$HHHXHHhHp
HxL`3LhLpLxLHI0HHHDlD"
Dmfff{@
H{HHHGH$HCHeu-HHt!LHHt

tH;v
ADl$@l$@DHE3EADDPDHDDDpDLDxLD$XMoLIL+LD$hMu9HC HH HHLE3HLtHEH$ LhMIm
Hl$`t,HKxHH|$hHD
L|$XL|$xHD$`MH;HkIu
HMtEfffffffffffLD$HHHH|$HUDHD$HLHH+EuYHtYHuE3EHD$XHL$xH|$hIHAIDLL;#@HDXHLxl$HtE3E
3EEtDs`t$HEH+$HHH4u-HHt!LHHt

tH$H;vt$@3t$@DHE!HD$XIm H$Im H$
t$H3QHK0HH|$`$$HC(L$H$L$$$$D$DMH$tHT$`LHH9t$HI;MILl$hMmD5MLLAHE3DlHDlpAMAADl$H
D$tMHH>E3HLLt$Hl$@Dd$DL$
L$PAE3LD$HAADD$HAI;Dl$Pu!t$HE3HLLWE3MLLt#HD$`MH/HL+{H$E3IH+H$HD$`HH$HI+HD$XH|$ht
$HH|$xHMtH|$XHCxHu!HC HH HHtH$HCxHH
HuADlHDlpHE3sHD$`L+HL)d$XHMLHHLxH|$hHHDE3ADD$HHLxtHHt$HE3EhEHyD9tH$H;v
ADl$@3l$@DH3DdDL L$PE3Gt$Hl$@Dd$DL$ L$PE3)t$Hl$@t$Hl$@l$@DdDL
L$PEEDD$hu>t6u6HC(AWHK0PDD$ht-E3ADD$PD$H"tl$PD$H
l$PE3l$PLADDDCp;ClEHHH8HO8tuH0HO0tztuH(HO(u#H`L8t~
uCp|$DE3,KpuE3DL$HD$DADD$PK|$DKlE3AG;CpACHHH$HN0"E3DPAEMMILLE
H$ AHjtLL$xH|$XLH|$XHH+L;LGRIAH;H$HL$`HL$8H$DT$`LS(HL$0HHL(HK0LE D$ ARD$h
9D$`eH$H;ZH$HI+I+Hs
HHKHHu-HHt!LHHt

tH$H;v|$@3D$@DHEH$L$LHmH$ E3EPAnHztH$H;sH$HH/|$D|$D|$D
|$D|$DH$
HH8J#$39s@DT$`9shtT$@QD$htL$pShDCdL$ptSh{XtShSP9tD@ShSTD$@9t$PtHHHHHHI+I+@HHD
9u-HHt!LHHt

DH3V|$HD9$$Eut
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HC(L$AWHL$`HL$0H$HL$(H$HL$ HK0PDtAEDch$ukugDaH'tH$H;sHHDH3l$@L$
|$`t^|$Pt#EuD9shuD9t$huu
9uEt4u
AGHD$XHuu.HT$xIG

IWuH$H8ut5tH@u^AtSC@H8u=IO0H$HHDt$D4DmS3L$LLLHH$8H$0H$(HHUH Hl$ AH]HuH}
HHLe(HoH+HD$ HA@NHuCDd{DuCDH8C@H@ HuHH]HuH} Le(H]H(HQxHt$@Ld$ E3HHtHH9uHA
HPLfxHl$8H|$HAH\$0HFp;w;r++9w3HUHt*LeffffffffHEHL@ IAPHHuHH rD9H|HHl8H\$0u"HH9uALd$
Ht$@H(Ld$ Ht$@H(H8Hl$HHt$PH|$XLd$0Ll$(E3D9HHL9*LJHA HVL"HPL9n LfulMt4LnH\$@ffffffHI$IL@
IAPHLuH\$@tY;GpuDHHHNHtLnHF HH;HGH)N HMHFH MHHH+tHGxHMLHD LLmHWxHtHG
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rHH(Hl$8Ht$@H|HLd HA@E3H\$0H0 L9't"HO,HO#HHSHOJHOAHWHt"LgfffHGHL@
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Ll$(E3A;HHAHAGLDiPAlHA8DiTH9DiXA\DDHLDi`DidDihLLDDDipt_HIfffHHt"L/fffHGHL@
IAPHHuHHHuHVxHtHF HPLnxHHF8HFDLHLNpHDn@DnDHAFpL9k8t
HK8SHK8uL9k0HK0t 1t|HK8umHK0u^HtPDn\H0ffL9+t"HKHKH!H
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HHIuLd$0Hl$HH\$@~ t~XD9nPuD9nXu
D9nTuDn\D9n\Ht$PAELl$(H|$XH8g jAgArn<A:OARQAhAA[HA
HLHHTHLA0AfffA@HIIBA@BA@BA@BuDIDQDYT$@D$8DDtHYD$(D$pD$4D$lHiDDhDdHq
DD`D$,D$\H$yL$DD$0D$XLLD$$DTLDt$|D$DPDDLLD|xDDHDE3L-qL$ T$Et;3A3A3A
3DTT$333#3AAMAAD
3AAA#3AA#Et8L$3A3A3A
3L AA3A33A#3AML$ D
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3DLAA3A3A3A#3AML$D
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3D$4L$AA3A3AA3A#A3AML$D
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3AL$A33AA3#A3AAML$DA
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3DL0A333#3AAML$0DAA
3AAA#3AA#DDEt:L$$3
3D$3D$3D$ L$,A333#3AAML$,AD
3AAA#3AA#Et8L$03
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3D$3D$3D$$AA3A3A3A#3AM$$D
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DA@THD$8L$LLL$AAAAAAAAAAAH$AAHAAHAAH$AHH(MH\$0Ht$@H|$HIHHI LG
Hl$8?@+I;v
HL9(HL9(LH+vHH{H@reHHHHkHfffHHH@HG(HFHG0HFHG8HFHG@HFHGHHFHGPHFHGXHFHG`HuHt
HO(LHHl$8H|$HHt$@H\$0H(H(H\$8Y
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H|$HH(HSH HH Ht)uEH [H3H [H%THSH0HD$@MHLHD$(33D$ HHt3H0[OEH0[H(H t3H(EH(H(H
t3H(EH(H(E3H\$@AHH|$HE33$HHH\$@t3H|$HH(EH|$HH(HSH HE3E333HHt3H [AEH [HSH HE33HHt3H
[EH [HSH H E3wt3H [EH [H(N3H(3LfffffffffDIAHEAud3HAHAfHSH HHQHHPE3L[L[H [u3ttuHHAAHSH
Ht+tu,HH [HIwHCH [I~uCH [H(thtt3H(HlH(HAHHBHBHBHBHBHBHBH(AH(E3 H\$H;H|$
HGMEIt0EfffDAAAAHHH EyD;r3H|$ H\$EuAuEH|$ H\$AHHHH`ALA$uIu?A 3Iu?
t-tt%ttAI$3tttMu3H(3yHAHAHA(u H,H(H8AH\$PH|$XILHH
DHLA{tMLS HK,MLS HK,CDC$LK(HID$
gDKDC$HIRDKDC$HI==DKDC$HI(DKDC$HICDKDC$HIH{$H\$PHH|$XH8fKGGGGGGGLL$
LHxH$IsI0I{McMkH3HMsD$M{L8IHH$MLI[IkffffHH_H;t/H+H8,IH;HGL{HI$LH+H)GLGH8,H,~H$3HHGHG
@H8,H+I;IGLe{HHHL+H_H_0GHJHtW HO,L,LsW HO,L,LXGDG$LO(H,HD$
eHDODG$H,HVHnDODG$H,HHRDODG$H,H~H6DODG$H,HHDODG$H,HVH_$HH_u
EtHGHGH!HHlhH\$pHLlHLdPH9GHt$`H|$XuMuEtL|$8Lt$@3HxIIIIJ7JSJH(H\$8Ht$@HBHH|$HAHHHH
uc0AIAHuH|HHt@H\$8H(H
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HK(xp3H|HHt@H\$8H(H|HHt@H\$8H(H(H\$@H|$HHHtHs
HsHHHCH|$HH\$@H(HItMtp99;:HXHt$HH|$@HMItFIL$H\$PHY8MHHD$`HD$(D
+;HO8H+H\$PH'HD$`HD$0$D$(H$HD$ ?H$L$`H|$@Ht$H
HXLDHTHHHXHpHH$LhIE3AL
M(L)D9k0LLxHDptC4L9kHHLLLs8AL$L$$HHD$@$Lt$8L$LD0HLD@3|$(HD$
A>LHT@DC4H$AIHS(EHC(Au uEtD{0KA;uL$AI{0tVA;IcvCLl$pHfffffffHSHLW(Hu
t
L9k(uHC(AH0D;{rL$IA{tFL9ku9LIAHHCu
nHLHDlLDLL$AL\$`ffffD9kD$D|$HAAALILLT$@H{@uHD$xMH+HDXHLKHGLMH+ALHDXC
;uHD$pI+HD$P!HG H9G%HD$PHHSA$tHDhLDPHD8DLD0L$(I
HDXHD AR t ADLhAA;uD9{Dl$HuuK8uHL$XL\$`L$ILL\$`
HL$XL\$`HC;uHDPLIHHDPLoHG
AT$HuHtLT$@ADLT@$$$HLH$I0IHI;sLT$@8Ls8DHtLE>LLHHLLHHHH(DBBH\$@fHH|$
HHHJfDfDh;CH\$@tH|$HH(3f?H|$HFH(HHHl$XH|$h,Ld$@LLl$8LIlhB;D%t
AD$H\$P@AEIEDKEIEH;G3tGE3MT$EAAAIHI EEy;rcEuuu
KIEHtHHH;sEIEyxD IED+HcI DG3EtE3N EAAAIHI EEyA;rEu _W Ht$`Lt$0L|$(E}AAA4IufffDHHcD+I
DG3Et8E3N ffffffEAAAIHH EyA;rEuutD3D+ HcI DG3EtE3N ffffffEAAAIHHEyA;rEu xp+H;aHWHNLI~oAH E;s
AE<t,;Hcs#J ffffffffH;r3Lt$0Ht$`L|$(H\$PLl$8Ld$@H|hHlXHHLL$
HT$HhH\$`HlXLl8DjL|$(E3AMIAHAD;iMcubHAHtMu
HuOMuJEEEt?IHxHHTfffHH9uAHH
E;rHTxHTxAEHt$PH|$HLd$@HkL[Lt$0LAD{IIHHt HWL?H0HuHSHt
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HNx@3AHFLHuVIAHuh3H
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HNHP(HP3HtH$Hh(HH0HL$pH$AHH H0E;HL$pHHTxDkMcE3EAt;IIIttfffffffffDFLHHWuoH
H0A;rHK<HCHLxL8D9HHHu9SAv"HIATD>HI0;{rL{(D{0D{4D{83L|$(Lt$0Ll$8Ld$@H|HHtPHl$XH\$`H
hAMHH|!tAALI$tHt
uHtHtHh(LX0ADAk3AHAHHAPHAXHA@THPHhHXL`3HQhHApAxHHHH@HpHHXH`AHAHHAPHAXHA
@THPHhH(H\$0Hl$83Ht$@H|$HH}HixffffHHt
HVhSH+H0HuHVpH|$HH\$0HtHFhHPHnpHt$@Hl$8H(HSH HHA0HA(HCTH [HHH\$XHt$`HHL$ H|$hIHHL
HHT$ LxH^0HF(Ht$`H|$hH\$XHHLD$HHHXHhHpHxL`LhL$LpL$3LxIEHII8MI}H$ILH$A>L
-ffffffHHHH>DDHH+UHHI+MHDHL$@H$t*H+FH;rAHD$@HuHF H9 HHtH+F
H;rAH$EHXDD$8DL$0HD$@IHNhHD$(L$EHELd$ AHXHt
HhHIH$HhLHXuLHD$@HhIEHFH]H^ L!A>N&H~ HL$PFHHVHHLPHHT$PLH^0HF(3~~A>HHt
H;FHHtH;F L
L
IUH$H+u >HcH$A(fINsPsB A8$mP<A$I1FIEHHHfffV[;f~FHFHHH~0H~(nHH~L
NA$IFIETCHV(HHpFHN0HN8H;HDFHZH$F`H$3L
(GH$3FH$#~;tG+H1IH;GDyd~I]HHL3L
HHsDAAHH0!u80u.0(w$3EtHHfffHHuxt"HrH wHtuH
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FFt:H<HBIIEFAD$9H$H$NN+tUHH1IH;GDb~I]H$H$L;nL
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>~s2HBIEA$N1FH$H$IF`~;H$H$NI+H1H;GD,`~I]H$H$L~L
bHFP^HH~@HH9F0WU9fff;YZHHL
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HHHHHSXHC0HC8HCPS@CDHCHHJ
ffffPxHPHHHuH|HHt@HH\$8H(H(H\$0Hl$839HthHt$@H|$HPHuHHt
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E3HD9t^DPHA~fffffHHt
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HXSL3H0HuH`HtHXHPL`DHIuLl$(H|$XHl$HHHHtHF(HPL3LsHIuHFHLvXHLt$
Ld$0Ht$PH\$@H8H`HHxHhHpHx L`M`LhE3HiE9(IHMhMh(EhA@ -
D9@LLLDDDDDLLHH(HH0HH8HLDLLLXLLLLLDHDLLLLLDDDL@DHDLLLLLDDDLD9tWxfXLxLpH(
>HD>L>DHH>HH>HH>pD9>gHD$@>D>LMHD$8ED$0D$(HD$HH>HLD$P3HD$
LdHHDPKL$@>HL$H>HMH>L+H>H>-D9>>(@(H>
HH;GP>XH$H>>\HHH>HH>HH$H>H> H>>Xf>
fx>H>IH+>L;HGHMH>H>H;>L;u6D9mED9m>EHMHED8,tzEHWXH>(H;HBHHH9OPsDm
D9o|tHHH+H;>s>EH>H
D9mtMEE>H>HE(H>HH>HH>pHHLlhLdpHHH$HxH(H\$@H|$HHHiHHt/HH9suHA(HPE3LLHHHDHC(
HHuH|$HH\$@H(HHHH;CXvHCXHHHHHLLHt{@tt3H|$HH\$@H(HxH\$pHlhHHt$`H|$XLdPLHHiHLl$
HLIHIIEEHHH;s,HH+IL;HGHLH+H9
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ULHLHHI$HIMuL;uE33{@t&tHI$HIE3LlHLdPH|XHt`Hl$hH\$pHxLHH$I[IkI{3MsHH$M{(DHiEMC9'
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D$0D$(H$M3HD$ wH$$HX HX hlH 'D9u^H$HDDHu
nH+J?HL
NL8 H@ 3Gt
@8D9P HHHtIIL$H;u[D9P uRHL$HH$HC`IHD H$HH HtEt
P H+H$L7HL$8HAD0LD$@D$(H$H3Lt$@HD$ H$$HX HHX lhH D9tSL9t'H+H@rHx HX D9P EHHH$H8 H@
DD3Dt3x9t}Dt3t3HAHAHAHA AAAAA(A,A0A4A8LD$HHHX HpHxLxHDtLH
HHtHC(HPHHGHHuHHsXDHHt
HC8H9tCHC(HPHC(HS8HHHHu
$HC8HHHt
HC0H9t9HC(HPHC(HS0HHHHtHC0HH$H3Ld$xPHXH`HLlpHDPHLt$hE3E3H$HD$XjE3H$fffffL;Et,HH$E
3HLuE33H$uQHLL$H$HC`IHE3H$HHttH{8E3I+D9C|tHH+H;rD9CxHDE3HO.I+L;H|HHD@HIHHD8DD$
0L$(HD$@LD$HHHD$

hHL@HlHD$HHLLHuHtu3H;r+Mt&HChHMHHI;E3LKpMtZuVHHH+D$PH=@sHH+D$XH=@r+LIAuKHH
D$PHHD$XuAHHH A8 H$@8
p0LlpLdxHLthL|$`H$HHHHL\$XH\$LMKH|IDDPEACHAHICHAXAACACIC
A3uIJ@uAJIJ@D$@u,|$Hu3uAB@t2+Ett"uL;tHuI;tAC3H|$H\$L+MKAC(HHHHXHhHHpHHx3MLpL$H~H~
H~H~ @>@~@~@~@~~(~,~0~4~8HHHA0HBHA8HBHA@HBHAHHB
Hi`HAPH$LqpHy|HAhtA|IH9DIxHHHlhH@HHPH`HXHHHHHxH0H8H H(H0H, (H@,
(H@Ld$pLA<$A|$I|$HI|$PI|$XI|$@Ll$hL$A}yDp9HHHHtuH~HD$@HHHHCHLHCDHHHHD$@H\AEHD$H
HxHD$PHlHD$XrHX HNHX H P HH Hu$ tF0F8
9 HHFtxHiLH9plhLX LH Ht$8|$0T$(SxL$ IXuHtF8 3 tHH H9X
uN,F8E~8n4un8@LHA}LLlDhLF@~IL$HDHNIL$XFFHN A$HuID$@u
ADID@u#AuHuEL$@t(!uL;tHuM;tFL+EV(LFu5tHH9uDN,F8DE9~8D^4uD^8ALt$`Ll$hLd$pH|$xHHH$H
HH(HXHx L`ALH@D$!tHHL$"xCtHSHL CtoH$8H$@Hs ffffffffHVHL HL< rHVLHL< @H
HuH$@H$8@HctHD fffH@uHL$ D$ 6HT$ D GIDAL H$H3H;H$0 DH(HHH\$PHlXHt`HHHL$
IH|$hILd$@HHL< ~H^HNHH+DL;vKH\ H9H;ryHHUHHthLFMtHVH?HVHtHUH~H^HNHT$
MHNv?LfH3Ld$@H|$hHt$`Hl$XH\$PHHHHhHhHpL`LhLpLxDHHHHMHT$ D$ D5HT$ LIDA$3A
H;ADtH\$pH^H$H~AHL4LHIDA$AH;DtH~Ht$$AHD$!D$"D$#DH+HT4HL$,D|$1D$$HD=HD$,AfD$014MDH
ID$(D$2YD$3ZA$H;DDAH\$pH$L|$HLtPLlXLd$`H$Hl$xHhLHHHAI[IIkIsI{ McMkE3HILAHI[tH+A
H;HGI[HtWHAHtLD$ HH\$ HADo(HQ LHQI=HO0LITHD$ HG
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IAHHC@uH|HHl8H\$0H(3HKLHCPHCHCXHUD@Ht$@:uDEHKHULS u
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CPumL9kPuCXHCPLIH+CHH.LK@LKHHKHDHHD@HD$8CXDl$0D$(HD$HHD$ S0HL$HHKHL9.u
HtbHLlXLd$`H$Hl$xH\$pHh3A`ApAtAxHHAhA|APATAXA\HH$HHL$ HAP:|$hGxD$hHHL
HLlx_twxOHA@A;EhAOANt$h;|$huAA;~ AuAt$hHGhH$L$LOHI;rIuHG0H/A;~3It-
HOh3IHHI;tHH;sADwHLlxL_tHopHwxHHHHSHpHAhHHu/HHA0yHApAtAxAHHp[HHy8HHA0u
HAhHL$ HAPF8|$hCxD$hHL$ H{8D$`L\$XCpD$dCtL[huL[8L9[0v
ItL[0C@CDHp[HH;HK0wHuHHK0HK8HuHHC8Hp[}H;v
HtHC8HfHp[LDHTHHXHXHpHxL`LhL$LpL$I}PLx3L9AMHDH9HH$`u'HHHHu
HIHHHhHt]H$$H$`H$uG$$tG$$HHH!H3MHD$@H$Ld$HL|$PH\$`HD$XtHT$@HL$pAU`H`H$@H$H$(H$
0$8L$HLL$H@hML$IDHL$

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H;DtHL$`H+H9$hHNu]H$pHT$`HL$pHD$PHHT$@HDxHD$XH|$p@$u HD$`HF3HFMtT$8A3H$HLL
L$(L$0H$8H$@H$PHXL9$0AtHAHQLAHH
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HULc0HKHt LcHhHuD98Hl$8tH@GD8HLfffHHtHHPL#HHuH|$HH\$0LLd$
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pHDXHHDPHHDHHHT$@HD$8Ld$0Lt$(LD$ 3LuHT$pH
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HCIILIH[H\$Ht$H1H|$ 33HtbLYIfffffffffMS3E3Mt'IHLL;rIHIM;rHHH;r"HI HH;rHH|$ Ht$H\$H|$
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LMIDIEEHD$8fffffHH;KuOHHHtHHHUHHLHsMtHSIH&HSHUH{HHKLD$ IHH
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sE3LLt&HQDfffffffAHIHBuD;Ic'H|$(L$$M\H+L%VHl$I|ES3AufMAF! IHAB! DIIHAB! DAB B%
A3Dcv^uYIIHHF! HB DHHAB! DB ADAIA3HD;AC L$$H|$(Hl$Ht$ H\$HH(H\$@H|$HHAGH }DLTDHIHIB
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sA/I++ECECBAB+f<2E#K#IZE3E=sA/I++ECECBAB+f<2E#K#IZ=sA/I++CBAB+f<2L\$0L$XA#K4Lt$hAIL;|$
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Check charset encoding and -scs switch.Cannot find listfilebsobbbtbdba-helph?asut012sea0-
SeLockMemoryPrivilegeSeCreateSymbolicLinkPrivilegeSeRestorePrivilege

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tallerDeniedRODCPasswordReplicationGroupAllowedRODCPasswordReplicationGroupRASandIASServersGroup
PolicyCreatorOwnersEnterpriseAdminsSchemaAdminsCertPublishersDomainControllersDomainComputersDomain
UsersDomainAdminsKRBTGTGuestAdministratorCertificateServiceDCOMAccessEventLogReadersCryptographic

OperatorsDistributedCOMUsersTerminalServerLicenseServersWindowsAuthorizationAccessGroupPerformanceLogUsersPerformanceMonitorUsersIncomingForestTrustBuildersNetworkConfigurationOperatorsRemoteDesktopUsersPreWindows2000CompatibleAccessBackup OperatorsReplicatorsBackupOperatorsPrintOperatorsServerOperatorsAccountOperatorsPowerUsersGuestsUsersAdministratorsDomainsNetworkServiceLocalServiceLocalSystemIUserIIS16ThisOrganizationRemoteInteractiveLogonTerminalServerRestrictedCodeAuthenticatedUsersSelfEnterpriseDCProxyAnonymousServiceLogonInteractiveBatchNetworkDialup00xS-1-UNSUPPORTEDERRORd:s:
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...:REPARSE:Junction: 8

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GetFullPathName error7-Zip cannot find MAPISendMail functionMAPISendMailcannot load Mapi32.dllMapi32.dllcannot move the filecannot delete the filestdoutThe file already existsThere is some data block after the end of the archiveThe file is read-onlycan't find archivetype of archive is not specifiedcannot find specified SFX moduleSFX file is not specifiedrsfxHHHHHHH

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SISISIInternal file name collision (file on disk, file in archive):Duplicate filename on disk:Duplicate filename in archive: D

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XTIInternal collision in update action set|D|DESetConsoleCtrlHandler failsd}DXIxXII,IXXI@XI(XIXIWIWIWIWIWI`WIHWI0WIVIVIVIVIVIVIVIpVIPVI0VIVIUIUIWIUIUI CRC ErrorUnsupported featureUnsupported methodThere are data after the end of archiveUnexpected end of archiveUnconfirmed start of archiveUnavailable start of archiveHeaders Error in encrypted archive. Wrong password?Wrong passwordHeaders ErrorIs not archiveThere are some data after the end of the payload dataUnexpected end of dataUnavailable dataData Error in encrypted file. Wrong password?Data ErrorCRC Failed in encrypted file. Wrong password?CRC FailedUnsupported MethodNo files to processEverything is OkTesting archive: Extracting archive: Can't allocate required memory!TERROR: Scan

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Would you like to replace the existing file:

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SecurityProviderLocal NameLabelCluster SizeFree SpaceTotal SizeErrorSymbolic
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Total archives sizeArchives----------
opening : is not a filetI`jIXI@jIjIiIUnsupported archive type7-Zip cannot find the code that works with archives.Incorrect command lineUsage: 7zr <command> [<switches>...] <archive_name> [<file_names>...]

<Commands>
 a : Add files to archive
 b : Benchmark
 d : Delete files from archive
 e : Extract files from archive
 (without using directory names)
 h : Calculate hash values for files
 i : Show information about supported formats
 l : List contents of archive
 rn : Rename files in archive
 t : Test integrity of archive
 u : Update files to archive
 x : eXtract files with full paths

<Switches>
 -- : Stop switches parsing
 @listfile : set path to listfile that contains file names
 -ai[r[-|0]]{@listfile|!wildcard} : Include archives
 -ax[r[-|0]]{@listfile|!wildcard} : eXclude archives
 -ao{a|s|t|u} : set Overwrite mode
 -an : disable archive_name field
 -bb[0-3] : set output log level
 -bd : disable progress indicator
 -bs{o|e|p}{0|1|2} : set output stream for output/error/progress line
 -bt : show execution time statistics
 -i[r[-|0]]{@listfile|!wildcard} : Include filenames
 -m{Parameters} : set compression Method
   -mmt[N] : set number of CPU threads
   -mx[N] : set compression level: -mx1 (fastest) ... -mx9 (ultra)
 -o{Directory} : set Output directory
 -p{Password}
 : set Password
 -r[-|0] : Recurse subdirectories
 -sa{a|e|s} : set Archive name mode
 -scc{UTF-8|WIN|DOS} : set charset for for console input/output

-scs{UTF-8|UTF-16LE|UTF-16BE|WIN|DOS|{id}} : set charset for list files
-scrc[CRC32|CRC64|SHA1|SHA256|*] : set hash function for x, e, h commands
-sdel : delete files after compression
-seml[.] : send archive by email
-sfx[{name}] : Create SFX archive
-si[{name}] : read data from stdin
-slp : set Large Pages mode
-slt : show technical information for l (List) command
-snh : store hard links as links
-snl : store symbolic links as links
-sni : store NT security information
-sns[-] : store NTFS alternate streams
-so : write data to stdout
-spd : disable wildcard matching for file names
-spe : eliminate duplication of root folder for extract command
-spf : use fully qualified file paths
-ssc[-] : set sensitive case mode
-sse : stop archive creating, if it can't open some input file
-ssw : compress shared
files
-stl : set archive timestamp from the most recently modified file
-stm{HexMask} : set CPU thread affinity mask (hexadecimal number)
-stx{Type} : exclude archive type
-t{Type} : Set type of archive
-u[-][p#][q#][r#][x#][y#][z#][!newArchiveName] : Update options
-v{Size}[b|k|m|g] : Create volumes
-w[{path}] : assign Work directory. Empty path means a temporary directory
-x[r[-|0]]{@listfile|!wildcard} : eXclude filenames
-y : assume Yes on all queries

7-Zip (r) 18.05 (x64) : Igor Pavlov : Public domain : 2018-04-30

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Internal Error #Unknown ErrorERROR: Can't allocate required memory!Break signaledCommand Line Error:

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ai md cad dwg pps key sxi max 3ds iso bin nrg mdf img pdi tar cpio xpi vfd vhd vud vmc vsv vmdk dsk nvram vmem vmsd vmsn vmss vmtm inl inc idl acf asa h hpp hxx c cpp cxx m mm go swift rc java cs rs pas bas vb cls ctl frm dlg def f77 f f90 f95 asm s sql manifest dep mak clw csproj vcproj sln dsp dsw class bat cmd bash sh xml xsd xsl xslt hxk hxc htm html xhtml xht mht mhtml htw asp aspx css cgi jsp shtml awk sed hta js json php php3 php4 php5 phptml pl pm py pyo rb tcl ts vbs text txt tex ans asc srt reg ini doc docx mcw dot rtf hlp xls xlr xlt xlw ppt pdf sxc sxd sxi sxg sxw stc sti stw stm odt ott odg otg odp otp ods ots odf abw afp cwk lwp wpd wps wpt wrf wri abf afm bdf fon mgf otf pcf pfa snf ttf dbf mdb nsf ntf wdb db fdb gdb exe dll ocx vbx sfx sys tlb awx com obj lib out o so pdb pch idb ncb opt wav H^F^FnF\F PCTChFQF,jE8jEnFZFiE\bA\jFZF,jE8jEnFEiE\bAXiFE D0H^F^FhF]F p@ \ ^F , t P! !! 1!FGpF FFFG`FFFGpFFHHHHHHHHH l44X 44t4@ $540FFFF FFFFFFFpFFFF`F FFFFPFF`FpF@F0FFPFF@F
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p`P0xxxxy#y=yNy[y|yyyyezpzzzzzzzz2P2P2P2P2Pp`P0{<{\{|{{Bp0{|%|':|Q|`|b|e|2P2Pp`0||20
 }}}2PSp`P0 ~  N0202PUp`P0`  P0Bp00U2Pp020Bp0Bp0\5FHVt%*6AVacZGS2P2P2P2P2P2P2P2Pp`P0,Ll$D2P
0d|2P 04|2P0Mu2P2P2P
 p`P0 <\B20Bp0Bp0BR0  Bp`P0BBR0 Bp`P02p`0|M2P
 p`P0@|2p`P0p`P0Rp`0bbbbb2p`02p`P0Bp020 B
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p`P0xGLaft.9;mxz2P2P2P2Pp`P02020Bp0BBBBBBBFp`02p`0Bdw?ky"3_n/:Kq}S^2P2P2P2P2P2P2P2P2P
 p`P0|<\|2p`0 Bp`P0H,Ra*\g2P2P2P p`P0Bp0p`P0rp`0
(0Bp02020BBBp0Bp020202020Bp0Bp0Bp0Bp0Bp0Bp0Rp`0BBp00Bp02 p`P0B2p`P0  Bp`P0$Eq2P2P
bp`P0Bp0)2P2P bp`P0Dd|2P
R0 Bp`P02p`020Bp0Bp0Bp0Bp0:2P2P2P p`P0(@2P
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R0X H Bp`P0  Bp`P020*=D2P
 p`P0 BBp020':M2P2P2P2P2P2P2P2P2P
R0 h8Xx

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 p`P0Bp020202 p`P0pV}2PRp`0 Bp0202p`P020Bp02p`0Bp0  Bp`P0Bp0B20^2PRp`P0 Bp0BBp0Bp0Bp0Bp02Prp`0
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 p`P0 0L2P p`P0 ,D2Prp`0 20  Bp`P020B2p`0  Bp`P0202PR0 ,20202p`P0Rp`0202p`020T2P bp`P0 2p`0202p`P0
Bp`P0Bp0202p`P0Bp0202p`02p`P020 Bp`P02p`P02 p`P0202p`02020B 2P2P
R0 < X   2P
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p`P0H gB Bp`P0 Bp`P0tiXjj2PRp`0h j2p`P02p`P0lmn2P p`P0 n2p`020Tpmpp2P
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Q6Q;QxQQQQQQQQQ5RIR]RlRRRRRRRRSS+STTTUU$U:UNUbUlUUUUUUUUUUUVV0V:V<VPVdVnVV
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WW0W:W2P2P2P2P2P2P2P2P
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p`P0p. 0\P\l\\\]5]?]]]]^D^^_2_=_B_P_[_e_v_____N`Y`^`````````````
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?8@88`888@D89DI$9I998G@9`H90t9`9P969@h90M9M99V9`;9@a:a:9(:X:x@:x0::p:`::::C:Ph:{;;,;,qh;qX;0;
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domain8OriginalFilename7zr.exe,ProductName7-Zip0ProductVersion18.05DVarFileInfo$Translation <assembly
xmlns="urn:schemas-microsoft-com:asm.v1" manifestVersion="1.0">
<assemblyIdentity version="1.0.0.0" processorArchitecture="*" name="7z" type="win32"></assemblyIdentity>
<trustInfo xmlns="urn:schemas-microsoft-com:asm.v2">
<security><requestedPrivileges><requestedExecutionLevel level="asInvoker" uiAccess="false">
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<compatibility xmlns="urn:schemas-microsoft-com:compatibility.v1"><application>
<!-- Vista --> <supportedOS Id="{e2011457-1546-43c5-a5fe-008deee3d3f0}"/>
<!-- Win 7 --> <supportedOS Id="{35138b9a-5d96-4fbd-8e2d-a2440225f93a}"/>
<!-- Win 8 --> <supportedOS Id="{4a2f28e3-53b9-4441-ba9c-d69d4a4a6e38}"/>
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(08@HPX`hp (0 (08@HPX`hp8 (08@HPX`hpx (08@HPX`hpxhp 08HX`hpx (px (08@HPX`hpx (08@HPX`hpx 0
(08@HPXh (08@HPX`hpx (08@HPX`p(08@HPX`hpx (08@HPxhpH(@Xx(@Xp08`xX
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```
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CODE32

EXPORT |CrcUpdateT4@16|

AREA |.text|, CODE, ARM

MACRO
CRC32_STEP_1

ldrb    r4, [r1], #1
subs    r2, r2, #1
eor     r4, r4, r0
and     r4, r4, #0xFF
ldr     r4, [r3, +r4, lsl #2]
eor     r0, r4, r0, lsr #8

MEND


MACRO
CRC32_STEP_4 $STREAM_WORD

eor     r7, r7, r8
eor     r7, r7, r9
eor     r0, r0, r7
eor     r0, r0, $STREAM_WORD
ldr     $STREAM_WORD, [r1], #4

and     r7, r0, #0xFF
and     r8, r0, #0xFF00
and     r9, r0, #0xFF0000
and     r0, r0, #0xFF000000

ldr     r7, [r6, +r7, lsl #2]
ldr     r8, [r5, +r8, lsr #6]
ldr     r9, [r4, +r9, lsr #14]
ldr     r0, [r3, +r0, lsr #22]

MEND


|CrcUpdateT4@16| PROC

 stmdb   sp!, {r4-r11, lr}
```

```
 cmp     r2, #0
 beq     |$fin|

|$v1|
 tst     r1, #7
 beq     |$v2|
 CRC32_STEP_1
 bne     |$v1|

|$v2|
 cmp     r2, #16
 blo     |$v3|

 ldr     r10, [r1], #4
 ldr     r11, [r1], #4

 add     r4, r3, #0x400
 add     r5, r3, #0x800
 add     r6, r3, #0xC00

 mov     r7, #0
 mov     r8, #0
 mov     r9, #0

 sub
    r2, r2, #16

|$loop|
 ; pld     [r1, #0x40]

 CRC32_STEP_4 r10
 CRC32_STEP_4 r11

 subs    r2, r2, #8
 bhs     |$loop|

 sub     r1, r1, #8
 add     r2, r2, #16

 eor     r7, r7, r8
 eor     r7, r7, r9
 eor     r0, r0, r7

|$v3|
 cmp     r2, #0
 beq     |$fin|

|$v4|
```

```
 CRC32_STEP_1
 bne     |$v4|

|$fin|
 ldmia   sp!, {r4-r11, pc}

|CrcUpdateT4@16| ENDP

 END
```

This directory contains the LZMA SDK code used in building the self-extracting Windows installer.

The src/ directory contains an almost-unmodified copy of the 7-zip source code version 18.05, as downloaded from https://www.7-zip.org/a/lzma1805.7z (linked from https://www.7-zip.org/sdk.html) on August 30, 2018. As stated in DOC/lzma-sdk.txt and on the web page, this code is in the public domain. A few modifications have been made to this copy of the source code. Those modifications are contained in mozilla_customizations.diff, which should be found in the same directory as this file.

The firefox/ directory contains several things:

* SFXSetup.sln and SFXSetup.vcxproj
  These are Visual Studio 2017 conversions of the Visual C++ 6 workspace and project files from the src/CPP/7zip/Bundles/SFXSetup/ directory, with some changes and additions to support AArch64 and to include our resources.
* 7zSD.arm64.manifest and 7zSD.win32.manifest
  We need to add a manifest file to the binary for several reasons, and one isn't supplied with the source package (Visual C++ 6 doesn't support them), so we've written our own.
* setup.ico
  The icon group used for the installer package.
* resource.rc
  The resource script containing the above manifest and icon references, as well as other resources we need. This is derived from the supplied SFXSetup resource.rc, but heavily customized.
* 7zSD.ARM64.sfx
  AArch64 binary built using all the above files. Checking this into the tree in compiled binary form means that packaging the installer does not require having a compiler toolchain configured (which is the case for artifact builds). To compile your own version of this file, open SFXSetup.sln and build the ReleaseD configuration.
* 7zSD.Win32.sfx
  32-bit x86 binary built using some of the above files, but through a different build procedure that uses Visual C++ 6 in order to maintain compatibility with legacy Windows platforms (Firefox doesn't run on these platforms, but at least we can display a helpful error message). The exact procedure that generated

this file was:
1) Install Microsoft Visual C++ 6 Professional or Enterprise edition (the
Standard edition does not include the optimizing compiler, so it should
not be used). You'll likely want a virtual machine running Windows XP,
because running VC6 under more modern Windows versions is tricky.
2) Install an appropriate Windows SDK version. The Windows XP Service Pack 2
SDK is known to work well, but that particular version appears to only be
available from archive.org:
https://web.archive.org/web/20080209175723/http://www.microsoft.com:80/msdownload/platformsdk/sdkupdate/XPSP2FULLInstall.htm
3) Run the tool included with the SDK that configures Visual C++ to use it.
4) If using a fresh copy of the LZMA SDK sources, apply the patch in
mozilla_customizations.diff. The copy in the Firefox source tree already
has these changes applied.
5) Open
the workspace file at src\CPP\7zip\Bundles\SFXSetup\SFXSetup.dsw and
build it using the "SFXSetup - Win32 ReleaseD" configuration. The output
executable should be at the path ReleaseD\7zSD.sfx relative to SFXSetup.dsw.
6) Use Resource Hacker (available from http://angusj.com/resourcehacker/) to
replace the existing icon on 7zSD.sfx and to add the manifest as a new
resource. Also edit the version info resource to replace CompanyName and
LegalCopyright with "Mozilla" (or appropriate vendor name) and
FileDescription and ProductName with "Firefox" (or appropriate product name).
Be sure to recompile the resources using the green "play" button in the
toolbar (or by pressing F5) before saving the file.
In the future this file may be replaced with one built in the same manner as
the AArch64 version using a modern toolchain, if the loss of platform support
is deemed acceptable.
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Valid copyright Element With dir Attribute in span Element in text Element 2</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />

```
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with rtl
as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span dir="rtl"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: No text Element in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
```

```
 <meta name="assert" content="The license element does not contain a text child element." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family:
 "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID" /&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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wOF2OTTO> @j
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$D91P:P 1gF2k4tF%mihK$1*m_>OtVx|@ey]wso\t
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* https://github.com/marcoceppi
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liteFirewall is based on nsisFirewall.

nsisFirewall -- Small NSIS plugin for simple tasks with Windows Firewall
Web site: http://wiz0u.free.fr/prog/nsisFirewall

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One Language Tagged (using lang) text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-003-ref.xht" />
  <meta name="assert" content="The copyright element matches the schema. It contains one text element that has a language tag using the lang tag instead
 of xml:lang." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-003.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
```

```
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text lang="en"&gt;
   Copyright with "en" language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```


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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-015-ref.xht" />
  <meta name="assert" content="The license element contains a text element with rtl as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-015.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
```

```
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text dir="rtl"&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Two div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
```

```
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <link rel="match" href="metadatadisplay-schema-license-021-ref.xht" />
 <meta name="assert" content="The license element contains a text element that contains two div elements." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face
 {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-021.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div&gt;Paragraph 1&lt;/div&gt;
   &lt;div&gt;Paragraph 2&lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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89FNET_FW_SCOPE_WWW0]NET_FW_SCOPE_ALL0,\NET_FW_SCOPE_LOCAL_SUBNETWWW0NET_FW_SCOPE_CUSTOMW0>NET_FW_SCOPE_MAX0ScopeWWWRemoteAddressesW)remoteAddrsWEnabledW,
8 INetFwIcmpSettingsWW,#AllowOutboundDestinationUnreachableWallowWWW,
o'AllowRedirectWWW,AllowInboundEchoRequestW,iAllowOutboundTimeExceededWWW,nAllowOutboundParameterProblemWWW,PAllowOutboundSourceQuenchWWW,0AllowInboundRouterRequestWWW,AllowInboundTimestampRequest,AllowInboundMaskRequestW,AllowOutboundPacketTooBigWWW8INetFwOpenPortWWName
8NET_FW_IP_PROTOCOL_W0z8NET_FW_IP_PROTOCOL_TCPWW`0=NET_FW_IP_PROTOCOL_UDPWW
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KipProtocolWWPort
gportNumberWWaBuiltInWX8|pINetFwOpenPortsWX@0vCountWWWXrAddWXRemoveWWXzItemopenPortX
$xA_NewEnumjnewEnumW
8INetFwServiceWWW
 8NET_FW_SERVICE_TYPE_ 0HNET_FW_SERVICE_FILE_AND_PRINTWWW
09NET_FW_SERVICE_UPNPW 0jNET_FW_SERVICE_REMOTE_DESKTOPWWW
0NET_FW_SERVICE_NONEW 0wNET_FW_SERVICE_TYPE_MAXWType
hCustomizedWW"rGloballyOpenPortsWWW
.openPortsWWW88,&INetFwServicesWWsvcTypeWD]serviceW8INetFwAuthorizedApplicationWProcessImageFileNameh
imageFileNameWWWL8NINetFwAuthorizedApplicationstappW
8INetFwProfileWWW8kNET_FW_PROFILE_TYPE_X0hNET_FW_PROFILE_DOMAINWWW0ONET_FW_PROFILE_STANDARDW0NET_FW_PROFILE_CURRENTWW0ENET_FW_PROFILE_TYPE_MAXWEFirewallEnabledWfExceptionsNotAllowed\
KynotAllowedWWvNotificationsDisabledWWWdisabled,UnicastResponsesToMulticastBroadcastDisabled\RemoteAdminSettingsWpIcmpSettingsServices)AuthorizedApplicationsWW8
9appsx8INetFwPolicyxCurrentProfileWWprofileWx2GetProfileByType@;4profileTypeW
8INetFwMgrWWW~LocalPolicyWCurrentProfileTypeWWRestoreDefaultsW`
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RrestrictedWWIsIcmpTypeAllowedWWW@l8INetFwMgrPrivate@{GetScratchPolicy
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>

```
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Mixed text Element Language Tags 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element matches the schema. One text element does not have a language
tag. One text element has a language tag." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
  &lt;text xml:lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
<!-- DO NOT EDIT! Generated by `%(generating_script_filename)s --spec %(spec_directory)s/` -->
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```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Child Element in copyright Element text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element with an unknown child element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
```

```
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
81811405e2360d0019cdba9cf804776d
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Mixed text Element Language Tags 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element matches the schema. Two text elements have a language tags."
/>
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text xml:lang="en"&gt;
   License with "en" language.
```

</text>
<text xml:lang="fr">
License with "fr" language.
</text>
</license>
</metadata>
</pre>
</body>
</html>
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DEALINGS IN THE SOFTWARE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: More Than One copyright Element</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
<link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />

```
 <link rel="match" href="metadatadisplay-schema-copyright-006-ref.xht" />
 <meta name="assert" content="The copyright element occurs more than once." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face {
   font-family:
"WOFF Test";
   src: url("support/metadatadisplay-schema-copyright-006.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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%%%  @TeX-hyphen-file{
%%%     author          = "Dejan Muhamedagi\'c",
%%%     version         = "2.02",
%%%     date            = "22 June 2008",
%%%     filename        = "hyph-sh-{latn,cyrl}.tex",
%%%     email           = "dejan@hello-penguin.com",
%%%     codetable       = "UTF-8",
%%%     keywords        = "TeX, hyphen, serbocroatian, {latinic,cyrillic}",
%%%     supported       = "yes",
%%%     abstract        = "Serbocroatian hyphenation patterns",
%%%     docstring       = "This file contains the hyphenation patterns
%%%                       for the Serbocroatian language and the
%%%                       {latinic,cyrillic} alphabet."
%%%  }
%%% ====================================================================
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%
% NB:
% It is allowed to leave one character at the end of the row.
% If you find that awkward these patterns will work well with
% \lefthyphenmin=2.
% \lefthyphenmin=1 \righthyphenmin=2
```

%
% Osnovni skup paterna dobijen je na osnovu fonolo\v skih osobina
% srpskohrvatskog jezika. Pravila i preporuke sam preuzeo iz:
%
% M. Pe\v sikan, J. Jerkovi\'c, M. Pi\v zurica:
% Pravopis srpskoga jezika
% Matica srpska, Novi Sad, 1995.
%
% U drugom delu se nalaze izuzeci od fonolo\v skih pravila bazirani
% na boljem psiholo\v skom prelomu re\v ci. Izradjen je kori\v s\'cenjem
% slede\'cih re\v cnika:
%
% Re\v cnik srpskohrvatskoga knji\v zevnog jezika
%
 Matica srpska, Matica hrvatska, Novi Sad, Zagreb, 1967-1976.
%
% Milan Vujaklija: Leksikon stranih re\v ci i izraza
% Prosveta, Beograd, 1992.
%
% --
%
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<Project DefaultTargets="Build" ToolsVersion="15.0"
xmlns="http://schemas.microsoft.com/developer/msbuild/2003">
 <ItemGroup Label="ProjectConfigurations">
  <ProjectConfiguration Include="Debug|ARM64">
   <Configuration>Debug</Configuration>
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  </ProjectConfiguration>
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   <Platform>Win32</Platform>
  </ProjectConfiguration>
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    <Platform>ARM64</Platform>
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    <Platform>Win32</Platform>
  </ProjectConfiguration>
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  <SccProjectName />
  <SccLocalPath />
  <ProjectGuid>{B348342A-1787-4311-B120-EDAE3018868F}</ProjectGuid>
  <WindowsTargetPlatformVersion>10.0</WindowsTargetPlatformVersion>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.Default.props" />
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  <PlatformToolset>v142</PlatformToolset>
  <UseOfMfc>false</UseOfMfc>
  <CharacterSet>Unicode</CharacterSet>
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  <UseOfMfc>false</UseOfMfc>
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  <UseOfMfc>false</UseOfMfc>
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  <PlatformToolset>v142</PlatformToolset>
  <UseOfMfc>false</UseOfMfc>
  <CharacterSet>Unicode</CharacterSet>
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 <ImportGroup Label="ExtensionSettings">
 </ImportGroup>
 <ImportGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'" Label="PropertySheets">
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Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  <Import Project="$(VCTargetsPath)Microsoft.Cpp.UpgradeFromVC60.props" />
 </ImportGroup>
 <ImportGroup Condition="'$(Configuration)|$(Platform)'=='Debug|ARM64'" Label="PropertySheets">
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  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')"
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Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  <Import Project="$(VCTargetsPath)Microsoft.Cpp.UpgradeFromVC60.props" />
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  <IntDir>$(SolutionDir)$(Platform)\$(Configuration)\</IntDir>
  <LinkIncremental>false</LinkIncremental>
  <TargetName>7zSD.$(Platform)</TargetName>
  <GenerateManifest>false</GenerateManifest>
  <TargetExt>.sfx</TargetExt>
 </PropertyGroup>
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  <GenerateManifest>false</GenerateManifest>
  <TargetExt>.sfx</TargetExt>
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  <GenerateManifest>false</GenerateManifest>
  <TargetExt>.sfx</TargetExt>
 </PropertyGroup>
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   <SuppressStartupBanner>true</SuppressStartupBanner>
   <WarningLevel>Level4</WarningLevel>
   <TreatWarningAsError>true</TreatWarningAsError>
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   <PrecompiledHeaderFile>StdAfx.h</PrecompiledHeaderFile>
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   <ProgramDataBaseFileName>$(IntDir)</ProgramDataBaseFileName>
   <CallingConvention>StdCall</CallingConvention>
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   <FavorSizeOrSpeed>Size</FavorSizeOrSpeed>
   <WholeProgramOptimization>true</WholeProgramOptimization>
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   <SuppressStartupBanner>true</SuppressStartupBanner>
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   <MkTypLibCompatible>true</MkTypLibCompatible>
    <TargetEnvironment>Win32</TargetEnvironment>
  </Midl>
  <ResourceCompile>
   <Culture>0x0409</Culture>
   <PreprocessorDefinitions>NDEBUG;%(PreprocessorDefinitions)</PreprocessorDefinitions>
  </ResourceCompile>
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   <SuppressStartupBanner>true</SuppressStartupBanner>
   <OutputFile>
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  </Bscmake>
  <Link>
   <SuppressStartupBanner>true</SuppressStartupBanner>
   <SubSystem>Windows</SubSystem>
   <DelayLoadDLLs>user32.dll;shell32.dll;oleaut32.dll;%(DelayLoadDLLs)</DelayLoadDLLs>
<AdditionalDependencies>odbc32.lib;odbccp32.lib;delayimp.lib;%(AdditionalDependencies)</AdditionalDepende
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   <ProfileGuidedDatabase />
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   <LinkTimeCodeGeneration>UseLinkTimeCodeGeneration</LinkTimeCodeGeneration>
  </Link>
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    <TreatWarningAsError>true</TreatWarningAsError>
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    <CallingConvention>StdCall</CallingConvention>
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     <WholeProgramOptimization>true</WholeProgramOptimization>
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    <MkTypLibCompatible>true</MkTypLibCompatible>
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  </ResourceCompile>
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s)</AdditionalDependencies>
     <GenerateDebugInformation>false</GenerateDebugInformation>
    <LinkTimeCodeGeneration>UseLinkTimeCodeGeneration</LinkTimeCodeGeneration>
    <ProfileGuidedDatabase />
    <ImageHasSafeExceptionHandlers>false</ImageHasSafeExceptionHandlers>
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  </Link>
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   <ProgramDataBaseFileName>$(IntDir)</ProgramDataBaseFileName>
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   <BasicRuntimeChecks>EnableFastChecks</BasicRuntimeChecks>
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   <TargetEnvironment>Win32</TargetEnvironment>
  </Midl>
  <ResourceCompile>
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  </ResourceCompile>
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   <MinimalRebuild>true</MinimalRebuild>
   <DebugInformationFormat>EditAndContinue</DebugInformationFormat>
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   <AssemblerListingLocation>$(IntDir)</AssemblerListingLocation>
   <PrecompiledHeaderOutputFile>$(IntDir)</PrecompiledHeaderOutputFile>
   <PrecompiledHeader>Use</PrecompiledHeader>
   <PrecompiledHeaderFile>StdAfx.h</PrecompiledHeaderFile>
   <ObjectFileName>$(IntDir)</ObjectFileName>
   <ProgramDataBaseFileName>$(IntDir)</ProgramDataBaseFileName>
   <CallingConvention>StdCall</CallingConvention>
   <BasicRuntimeChecks>EnableFastChecks</BasicRuntimeChecks>
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   <TypeLibraryName>.\Debug\SFXSetup.tlb</TypeLibraryName>
   <MkTypLibCompatible>true</MkTypLibCompatible>
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  </ResourceCompile>
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   <ProfileGuidedDatabase />
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  <ClCompile Include="..\src\C\Alloc.c">
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  </ClCompile>
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 Include="..\src\CPP\Common\CRC.cpp" />
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  <ClCompile Include="..\src\CPP\Common\MyVector.cpp" />
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  <ClCompile Include="..\src\CPP\Common\UTFConvert.cpp" />
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  <ClCompile Include="..\src\CPP\Windows\Control\Dialog.cpp" />
  <ClCompile Include="..\src\CPP\Windows\DLL.cpp" />
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  <ClCompile Include="..\src\CPP\Windows\FileName.cpp" />
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  <ClCompile Include="..\src\CPP\7zip\Archive\Common\OutStreamWithCRC.cpp" />
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  <ClCompile Include="..\src\CPP\7zip\UI\Common\DefaultName.cpp" />
  <ClCompile Include="..\src\CPP\7zip\UI\Common\LoadCodecs.cpp" />
  <ClCompile Include="..\src\CPP\7zip\UI\Common\OpenArchive.cpp" />
  <ClCompile Include="..\src\CPP\7zip\UI\Explorer\MyMessages.cpp" />
  <ClCompile Include="..\src\CPP\7zip\UI\FileManager\FormatUtils.cpp"
/>
  <ClCompile Include="..\src\CPP\7zip\UI\FileManager\ProgressDialog.cpp" />
  <ClCompile Include="..\src\CPP\7zip\Bundles\SFXSetup\ExtractCallbackSfx.cpp" />
  <ClCompile Include="..\src\CPP\7zip\Bundles\SFXSetup\ExtractEngine.cpp" />
  <ClCompile Include="..\src\CPP\7zip\Bundles\SFXSetup\SfxSetup.cpp" />
  <ClCompile Include="..\src\CPP\7zip\Bundles\SFXSetup\StdAfx.cpp">
    <PrecompiledHeader
Condition="'$(Configuration)|$(Platform)'=='ReleaseD|Win32'">Create</PrecompiledHeader>
    <PrecompiledHeader
Condition="'$(Configuration)|$(Platform)'=='ReleaseD|ARM64'">Create</PrecompiledHeader>
    <PrecompiledHeaderFile
Condition="'$(Configuration)|$(Platform)'=='ReleaseD|Win32'">StdAfx.h</PrecompiledHeaderFile>
    <PrecompiledHeaderFile
Condition="'$(Configuration)|$(Platform)'=='ReleaseD|ARM64'">StdAfx.h</PrecompiledHeaderFile>
    <PrecompiledHeader
Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">Create</PrecompiledHeader>
```

```
  <PrecompiledHeader Condition="'$(Configuration)|$(Platform)'=='Debug|ARM64'">Create</PrecompiledHeader>
    <PrecompiledHeaderFile
Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">StdAfx.h</PrecompiledHeaderFile>
    <PrecompiledHeaderFile
Condition="'$(Configuration)|$(Platform)'=='Debug|ARM64'">StdAfx.h</PrecompiledHeaderFile>
  </ClCompile>
 </ItemGroup>
 <ItemGroup>
  <ClInclude Include="..\src\C\7zCrc.h" />
  <ClInclude Include="..\src\C\Alloc.h" />
  <ClInclude Include="..\src\C\Bcj2.h" />
  <ClInclude Include="..\src\C\Bra.h" />
  <ClInclude Include="..\src\C\CpuArch.h" />
  <ClInclude Include="..\src\C\Delta.h" />
  <ClInclude Include="..\src\C\DllSecur.h" />
  <ClInclude Include="..\src\C\Lzma2Dec.h" />
  <ClInclude Include="..\src\C\Lzma2DecMt.h" />
  <ClInclude Include="..\src\C\LzmaDec.h" />
  <ClInclude Include="..\src\C\MtDec.h" />
  <ClInclude Include="..\src\C\Threads.h" />
  <ClInclude Include="..\src\CPP\Common\CommandLineParser.h"
/>
  <ClInclude Include="..\src\CPP\Common\IntToString.h" />
  <ClInclude Include="..\src\CPP\Common\MyString.h" />
  <ClInclude Include="..\src\CPP\Common\MyVector.h" />
  <ClInclude Include="..\src\CPP\Common\NewHandler.h" />
  <ClInclude Include="..\src\CPP\Common\StringConvert.h" />
  <ClInclude Include="..\src\CPP\Common\TextConfig.h" />
  <ClInclude Include="..\src\CPP\Common\UTFConvert.h" />
  <ClInclude Include="..\src\CPP\Common\Wildcard.h" />
  <ClInclude Include="..\src\CPP\Windows\Control\Dialog.h" />
  <ClInclude Include="..\src\CPP\Windows\DLL.h" />
  <ClInclude Include="..\src\CPP\Windows\ErrorMsg.h" />
  <ClInclude Include="..\src\CPP\Windows\FileDir.h" />
  <ClInclude Include="..\src\CPP\Windows\FileFind.h" />
  <ClInclude Include="..\src\CPP\Windows\FileIO.h" />
  <ClInclude Include="..\src\CPP\Windows\FileName.h" />
  <ClInclude Include="..\src\CPP\Windows\PropVariant.h" />
  <ClInclude Include="..\src\CPP\Windows\ResourceString.h"
/>
  <ClInclude Include="..\src\CPP\Windows\Synchronization.h" />
  <ClInclude Include="..\src\CPP\Windows\System.h" />
  <ClInclude Include="..\src\CPP\Windows\Window.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\7z\7zDecode.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\7z\7zHandler.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\7z\7zHeader.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\7z\7zIn.h" />
```

```
  <ClInclude Include="..\src\CPP\7zip\Archive\7z\7zItem.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\Common\CoderMixer2.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\Common\ItemNameUtils.h" />
  <ClInclude Include="..\src\CPP\7zip\Archive\Common\OutStreamWithCRC.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\CreateCoder.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\CWrappers.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\FileStreams.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\FilterCoder.h" />

 <ClInclude Include="..\src\CPP\7zip\Common\InBuffer.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\LimitedStreams.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\OutBuffer.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\ProgressUtils.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\StreamBinder.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\StreamObjects.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\StreamUtils.h" />
  <ClInclude Include="..\src\CPP\7zip\Common\VirtThread.h" />
  <ClInclude Include="..\src\CPP\7zip\Compress\BranchMisc.h" />
  <ClInclude Include="..\src\CPP\7zip\Compress\Lzma2Decoder.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\Common\ArchiveOpenCallback.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\Common\DefaultName.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\Common\ExtractMode.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\Common\LoadCodecs.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\Common\OpenArchive.h" />
   <ClInclude Include="..\src\CPP\7zip\UI\Explorer\MyMessages.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\FileManager\FormatUtils.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\FileManager\LangUtils.h" />
  <ClInclude Include="..\src\CPP\7zip\UI\FileManager\ProgressDialog.h" />
  <ClInclude Include="..\src\CPP\7zip\Bundles\SFXSetup\ExtractCallbackSfx.h" />
  <ClInclude Include="..\src\CPP\7zip\Bundles\SFXSetup\ExtractEngine.h" />
  <ClInclude Include="..\src\CPP\7zip\Bundles\SFXSetup\StdAfx.h" />
 </ItemGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.targets" />
 <ImportGroup Label="ExtensionTargets">
 </ImportGroup>
</Project>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With class Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
```

```
  <meta name="assert" content="The copyright element contains a text element that contains a div element with
&quot;class1 class2 class3&quot; as the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div class="class1 class2 class3"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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- -

At the time this file was first modified, a complete, unmodified copy of
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http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

Portions of this file were originally made available under the following license
(copied verbatim from hyph-ia.lic.txt in the original work):

Hyphenation patterns for Interlingua

(more info about the licence to be added later)

% This file is part of hyph-utf8 package
 and resulted from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: iahyphen.tex (2005-06-28)
% Author: Peter Kleiweg <p.c.j.kleiweg at rug.nl>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%   and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested
 & integrated patterns into TeX Live
%
% However, the "copyright/copyleft" owner of patterns remains the original author.
%
% The copyright statement of this file is thus:
%
%   Do with this file whatever needs to be done in future for the sake of
%   "a better world" as long as you respect the copyright of original file.
%   If you're the original author of patterns or taking over a new revolution,
%   plese remove all of the TUG comments & credits that we added here -
%   you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;

```
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
%
 - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%------------------------------------------------------------------------------
%
% File: iahyphen.tex
% TeX hyphenation patterns for Interlingua.
% Version 0.2b. Released 3 July 2001.
% version 0.2c  Released 28 June 2005 (added LPPL header)
% Created by Peter Kleiweg, p.c.j.kleiweg at rug.nl
% About Interlingua: http://www.interlingua.com/
%
% \iffalse meta-comment
%
% Copyright
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%
% This file is distributed as part of the Babel system.
% ---------------------------------------------------
%
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% The latest version of this license is in
%   http://www.latex-project.org/lppl.txt
% and version 1.3 or later is part of all distributions of LaTeX
% version 2003/12/01 or later.
```

%
% This work has the LPPL maintenance status "maintained".
%
% The Current Maintainer of this work is Peter Kleiweg.
%
% The list of all files belonging to the Babel system is
% given in the file `manifest.bbl. See also `legal.bbl' for additional
% information.
%
% \fi
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CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION
OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR
IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
DEALINGS
 IN THE SOFTWARE.
MY_STATIC_LINK=1
PROG = LZMAc.exe

CFLAGS = $(CFLAGS) \

LIB_OBJS = \
 $O\LzmaUtil.obj \

```
C_OBJS = \
 $O\Alloc.obj \
 $O\LzFind.obj \
 $O\LzFindMt.obj \
 $O\LzmaDec.obj \
 $O\LzmaEnc.obj \
 $O\7zFile.obj \
 $O\7zStream.obj \
 $O\Threads.obj \

OBJS = \
 $(LIB_OBJS) \
 $(C_OBJS) \

!include "../../../CPP/Build.mak"

$(LIB_OBJS): $(*B).c
 $(COMPL_O2)
$(C_OBJS): ../../$(*B).c
 $(COMPL_O2)
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One No Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-001-ref.xht" />
  <meta name="assert" content="The copyright element matches the schema. It contains one text element that does not have a language tag." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-001.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
```

```
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Copyright without language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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here. Please keep the list sorted by first names.

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Jackson <preston.a.jackson@gmail.com>
Rainer Klaffenboeck <rainer.klaffenboeck@dynatrace.com>
Russ Cox <rsc@google.com>
Russ Rufer <russ@pentad.com>
Sean Mcafee <eefacm@gmail.com>
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LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM,
OUT OF OR IN CONNECTION
WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE
SOFTWARE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Valid license Element With One div Element in text Element</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />

```
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <link rel="match" href="metadatadisplay-schema-license-020-ref.xht" />
 <meta name="assert" content="The license element contains a text element that contains a div element." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face
 {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-020.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>

```
 <title>WOFF Test: Unknown Child Element in license Element</title>
 <link rel="author" title="Tal Leming" href="http://typesupply.com" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <meta name="assert" content="The license element contains an unknown child element." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family:
 "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$x.)7TzU%OU)x)r[|d5MZPKEJ[?{60$t#(a*L!
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: No text Element in copyright Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />

```
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <link rel="match" href="metadatadisplay-schema-copyright-007-ref.xht" />
 <meta name="assert" content="The copyright element does not contain a text child element." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face
 {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-copyright-007.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Child Element in license Element text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element with an unknown child element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
```

```
  pre {
   font-size:
 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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- -

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http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

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Italian hyphenation patterns

(more info about the licence to be added later)

```
% hyph-it.tex
%
% Italian hyphenation patterns
%
%
 This file is part of the hyph-utf8 package.
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
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%
% This work consists of the single file hyph-it.tex.
%
% \versionnumber{4.8i}    \versiondate{2011/08/16}
%
% These hyphenation patterns for the Italian language are supposed to comply
```

```
% with the Recommendation UNI 6461 on hyphenation
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% ChangeLog:
% - 2011-08-16 - Change the licence from GNU LGPL into LPPL v1.3.
% - 2010-05-24 - Fix for Italian patterns for proper hyphenation of -ich and Ljubljana.
% - 2008-06-09 - Import of original ithyph.tex into hyph-utf8 package.
% - 2008-03-08 - (last change in ithyph.tex)
%
<?xml version="1.0" encoding="utf-8"?>
<Project DefaultTargets="Build" ToolsVersion="15.0"
xmlns="http://schemas.microsoft.com/developer/msbuild/2003">
 <ItemGroup Label="ProjectConfigurations">
  <ProjectConfiguration Include="Debug|Win32">
   <Configuration>Debug</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
  <ProjectConfiguration Include="Release|Win32">
   <Configuration>Release</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
 </ItemGroup>
 <PropertyGroup Label="Globals">
  <SccProjectName />
  <SccLocalPath />
  <ProjectGuid>{EF903B79-AD97-45E0-BC6E-4FF846D6A2ED}</ProjectGuid>
  <WindowsTargetPlatformVersion>10.0</WindowsTargetPlatformVersion>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.Default.props" />
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <PlatformToolset>v142</PlatformToolset>
   <UseOfMfc>false</UseOfMfc>
  <CharacterSet>Unicode</CharacterSet>
  <SpectreMitigation>false</SpectreMitigation>
 </PropertyGroup>
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <PlatformToolset>v142</PlatformToolset>
  <UseOfMfc>false</UseOfMfc>
  <CharacterSet>Unicode</CharacterSet>
  <WholeProgramOptimization>true</WholeProgramOptimization>
  <SpectreMitigation>false</SpectreMitigation>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.props" />
```

```
 <ImportGroup Label="ExtensionSettings">
 </ImportGroup>
 <ImportGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'" Label="PropertySheets">
  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  <Import Project="$(VCTargetsPath)Microsoft.Cpp.UpgradeFromVC60.props"
 />
 </ImportGroup>
 <ImportGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'" Label="PropertySheets">
  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  <Import Project="$(VCTargetsPath)Microsoft.Cpp.UpgradeFromVC60.props" />
 </ImportGroup>
 <PropertyGroup Label="UserMacros" />
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <OutDir>.\Release\</OutDir>
  <IntDir>.\Release\</IntDir>
  <LinkIncremental>false</LinkIncremental>
 </PropertyGroup>
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
  <OutDir>.\Debug\</OutDir>
  <IntDir>.\Debug\</IntDir>
  <LinkIncremental>true</LinkIncremental>
 </PropertyGroup>
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <ClCompile>
   <RuntimeLibrary>MultiThreaded</RuntimeLibrary>
    <StringPooling>true</StringPooling>
   <Optimization>MinSpace</Optimization>
   <SuppressStartupBanner>true</SuppressStartupBanner>
   <WarningLevel>Level3</WarningLevel>
<PreprocessorDefinitions>WIN32;NDEBUG;_WINDOWS;_USRDLL;%(PreprocessorDefinitions)</PreprocessorD
efinitions>
   <FavorSizeOrSpeed>Size</FavorSizeOrSpeed>
   <DebugInformationFormat>None</DebugInformationFormat>
   <ExceptionHandling>false</ExceptionHandling>
   <BufferSecurityCheck>false</BufferSecurityCheck>
   <RuntimeTypeInfo>false</RuntimeTypeInfo>
   <IntrinsicFunctions>true</IntrinsicFunctions>
  </ClCompile>
  <Midl>
   <SuppressStartupBanner>true</SuppressStartupBanner>
   <PreprocessorDefinitions>NDEBUG;%(PreprocessorDefinitions)</PreprocessorDefinitions>
   <TypeLibraryName>.\Release\WebBrowser.tlb</TypeLibraryName>
   <MkTypLibCompatible>true</MkTypLibCompatible>
   <TargetEnvironment>Win32</TargetEnvironment>
  </Midl>
```

```
    <ResourceCompile>
     <Culture>0x080a</Culture>
     <PreprocessorDefinitions>NDEBUG;%(PreprocessorDefinitions)</PreprocessorDefinitions>
    </ResourceCompile>
    <Bscmake>
     <SuppressStartupBanner>true</SuppressStartupBanner>
     <OutputFile>.\Release\WebBrowser.bsc</OutputFile>
    </Bscmake>
    <Link>
     <SuppressStartupBanner>true</SuppressStartupBanner>
     <LinkDLL>true</LinkDLL>
     <SubSystem>Windows</SubSystem>
     <OutputFile>.\Release\WebBrowser.dll</OutputFile>
     <ImportLibrary>.\Release\WebBrowser.lib</ImportLibrary>
<AdditionalDependencies>odbc32.lib;odbccp32.lib;urlmon.lib;%(AdditionalDependencies)</AdditionalDependenci
es>
     <GenerateDebugInformation>false</GenerateDebugInformation>
     <OptimizeReferences>true</OptimizeReferences>
     <LinkTimeCodeGeneration>UseLinkTimeCodeGeneration</LinkTimeCodeGeneration>
     <EnableCOMDATFolding>true</EnableCOMDATFolding>
     <EntryPointSymbol>
     </EntryPointSymbol>
    </Link>
  </ItemDefinitionGroup>
  <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
    <ClCompile>
     <RuntimeLibrary>MultiThreadedDebug</RuntimeLibrary>
     <InlineFunctionExpansion>Default</InlineFunctionExpansion>
     <Optimization>Disabled</Optimization>
     <SuppressStartupBanner>true</SuppressStartupBanner>
     <WarningLevel>Level3</WarningLevel>
     <DebugInformationFormat>EditAndContinue</DebugInformationFormat>
<PreprocessorDefinitions>WIN32;_DEBUG;_WINDOWS;_USRDLL;%(PreprocessorDefinitions)</PreprocessorD
efinitions>
     <BasicRuntimeChecks>EnableFastChecks</BasicRuntimeChecks>
     <ExceptionHandling>false</ExceptionHandling>
     <LanguageStandard>Default</LanguageStandard>
    </ClCompile>
    <Midl>
     <SuppressStartupBanner>true</SuppressStartupBanner>
     <PreprocessorDefinitions>_DEBUG;%(PreprocessorDefinitions)</PreprocessorDefinitions>
     <TypeLibraryName>.\Debug\WebBrowser.tlb</TypeLibraryName>
     <MkTypLibCompatible>true</MkTypLibCompatible>
      <TargetEnvironment>Win32</TargetEnvironment>
    </Midl>
    <ResourceCompile>
     <Culture>0x080a</Culture>
     <PreprocessorDefinitions>_DEBUG;%(PreprocessorDefinitions)</PreprocessorDefinitions>
```

```
  </ResourceCompile>
  <Bscmake>
    <SuppressStartupBanner>true</SuppressStartupBanner>
    <OutputFile>.\Debug\WebBrowser.bsc</OutputFile>
  </Bscmake>
  <Link>
    <SuppressStartupBanner>true</SuppressStartupBanner>
    <LinkDLL>true</LinkDLL>
    <GenerateDebugInformation>true</GenerateDebugInformation>
    <SubSystem>Windows</SubSystem>
    <OutputFile>.\Debug\WebBrowser.dll</OutputFile>
    <ImportLibrary>.\Debug\WebBrowser.lib</ImportLibrary>
<AdditionalDependencies>odbc32.lib;odbccp32.lib;urlmon.lib;%(AdditionalDependencies)</AdditionalDependenci
es>
    <OptimizeReferences>false</OptimizeReferences>
    <ImageHasSafeExceptionHandlers>false</ImageHasSafeExceptionHandlers>
  </Link>
 </ItemDefinitionGroup>
 <ItemGroup>
   <ClCompile Include="CustomFunctions.cpp" />
  <ClCompile Include="exdll.cpp" />
  <ClCompile Include="main.cpp" />
  <ClCompile Include="Timers.cpp" />
  <ClCompile Include="WebBrowser.cpp" />
 </ItemGroup>
 <ItemGroup>
  <ClInclude Include="exdll.h" />
  <ClInclude Include="resource.h" />
  <ClInclude Include="WebBrowser.h" />
 </ItemGroup>
 <ItemGroup>
  <ResourceCompile Include="resource.rc" />
 </ItemGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.targets" />
 <ImportGroup Label="ExtensionTargets">
 </ImportGroup>
</Project>
```

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in div Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&amp;lt;?xml version="1.0" encoding="UTF-8"?&amp;gt;
```

```
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div dir="ltr"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
# -*- Mode: python; indent-tabs-mode: nil; tab-width: 40 -*-
# vim: set filetype=python:
# This Source Code Form is subject to the terms of the Mozilla Public
# License, v. 2.0. If a copy of the MPL was not distributed with this
# file, You can obtain one at http://mozilla.org/MPL/2.0/.

HOST_SOURCES += [
  'bsdiff.c',
]

HostProgram('mbsdiff')

if CONFIG['HOST_OS_ARCH'] == 'WINNT':
  HOST_OS_LIBS += [
    'ws2_32',
  ]
  USE_STATIC_LIBS = True

LOCAL_INCLUDES += [
  '/toolkit/mozapps/update/updater',
  '/toolkit/mozapps/update/updater/bspatch',
]
```

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*
**/
wOF2OTTO? @k
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$n6LQ$7Y~Sfm`!(Hk4j7,En4E
_lE}"`Deh5[*3
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>

```
  <title>WOFF Test: Valid license Element With class Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a div element with
&quot;class1 class2 class3&quot; as the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div class="class1 class2 class3"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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- -

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the LPPL Work was available from:
http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

Portions of this file were originally made available under the following license
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Swedish hyphenation patterns (Jan Michael Rynning, 1994-03-03)

(more info about the licence to be added later)

% This file is
part of hyph-utf8 package and resulted from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: sehyph.tex (2003-09-08)
% Author: Jan Michael Rynning
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,

% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
% and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested
 & integrated patterns into TeX Live
%
% However, the "copyright/copyleft" owner of patterns remains the original author.
%
% The copyright statement of this file is thus:
%
% Do with this file whatever needs to be done in future for the sake of
% "a better world" as long as you respect the copyright of original file.
% If you're the original author of patterns or taking over a new revolution,
% plese remove all of the TUG comments & credits that we added here -
% you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in
 UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
% and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
% they belong to loadhyph-foo.tex,
% and no \lefthyphenmin and \righthyphenmin,
% they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%

```
%    http://tug.org/tex-hyphen
%
%----------------------------------------------------------------------
%
% Swedish hyphenation patterns.
%
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 version of this license
% is in http://www.latex-project.org/lppl.txt and version 1.2 or later is
% part of all distributions of LaTeX version 1999/12/01 or later.
%
% Last update: 1994-03-03 (March 3, 1994).
% Revision history:
% 1991-01-08: First version available for anonymous FTP.
% 1991-08-08: Changed \aa to \aa0 at end of line, to stop TeX from
%           concatenating the patterns.
% 1991-09-03: Cleaned up lots of inconsistencies in the dictionary.
%           As a consequence, the patterns shrunk a lot.  Also
%           added some 4000 one-syllable words, some of which were
%           hyphenated by the old patterns, and some 1500 compound
%           words, about half of which were incorrectly hyphenated
%           by the old patterns.
% 1991-11-01: Added another some 6200 compound words, all of which were
%           incorrectly hyphenated by the old patterns.
% 1991-11-13: Added another some 6500 compound words, all of which were
%           incorrectly hyphenated
 by the old patterns.
% 1992-01-30: Changed macros to ^^, for use with LaTeX and dc fonts.
% 1994-03-03: The hyphenated dictionary now contains about 118,000 words.
%           The hyphenation now works much better for compound words.
%           Patgen parameters: 1 2 20, 2 1 8, 1 4 7, 3 2 1, 1 10000 4.
%
% This file contains Swedish hyphenation patterns for TeX.  It assumes
% that you have fonts with the Swedish letters in the positions where
% they occur in ISO Latin 1 (ISO 8859/1):
% Letter:   \AA  \"A  \"O  \'E  \aa  \"a  \"o  \'e
% Position: "C5  "C4  "D6  "C9  "E5  "E4  "F6  "E9
%
% Load this file into initex after plain.tex (or lplain.tex, splain.tex,
% or whatever).
%
% The patterns were generated in such a way that they will hyphenate
% correctly if \lefthyphenmin>=1 and \righthyphenmin>=2.
%
% These hyphenation patterns work quite well for simple words, but not
```

% quite as well for compound words. I'm working on improving the quality,
% by adding more words. If
 you know any Swedish words which are not
% correctly hypheneted using these patterns, or if you have questions or
% comments, please contact me:
%
% Jan Michael Rynning <jmr@incolumitas.se>
% --
%
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====

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in span Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a span element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
```

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span dir="ltr"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Nested div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains nested div elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
```

```
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div&gt;
    div 1 start
    &lt;div&gt;
     div 2 start
     &lt;div&gt;
      div 3
     &lt;/div&gt;
     div 2 end
    &lt;/div&gt;
    div 1 end
   &lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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- -

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- -

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(more info about the licence to be added later)

% dehyphn-x-2011-06-01.pat

\message{German
Hyphenation Patterns (Reformed Orthography, 2006) `dehyphn-x' 2011-06-01 (WL)}

% TeX-Trennmuster fr die reformierte (2006) deutsche Rechtschreibung
%
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% archives in directory macros/latex/base/lppl.txt; either
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%

```
%
% The word list is available from
%
%   http://repo.or.cz/w/wortliste.git?a=commit;h=2d246449f5c4f570f4d735d3ad091f6ad70f6972
%
% The used patgen parameters are
%
%   1 1 | 2 5 | 1 1 1
%   2 2 | 2 5 | 1 2 1
%   3 3 | 2 6 | 1 1 1
%   4 4 | 2 6 | 1 4 1
%   5 5 | 2 7 | 1 1 1
%   6 6 | 2 7 | 1 6 1
%   7 7 | 2 13 | 1 4 1
%   8 8 | 2 13 | 1 8 1
<?xml version="1.0" encoding="utf-8"?>
<Project DefaultTargets="Build" xmlns="http://schemas.microsoft.com/developer/msbuild/2003">
 <ItemGroup Label="ProjectConfigurations">
  <ProjectConfiguration Include="Debug|Win32">
   <Configuration>Debug</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
  <ProjectConfiguration Include="Release|Win32">
   <Configuration>Release</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
 </ItemGroup>
 <ItemGroup>
  <ClCompile Include="InetBgDL.cpp" />
 </ItemGroup>
 <ItemGroup>
  <ClInclude Include="InetBgDL.h" />
 </ItemGroup>
 <PropertyGroup Label="Globals">
  <VCProjectVersion>16.0</VCProjectVersion>
  <ProjectGuid>{B9B76BC4-5C6C-4808-AC23-2C10126CDFC0}</ProjectGuid>
  <Keyword>Win32Proj</Keyword>
  <RootNamespace>InetBgDl</RootNamespace>
  <WindowsTargetPlatformVersion>10.0</WindowsTargetPlatformVersion>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.Default.props"
 />
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <UseDebugLibraries>true</UseDebugLibraries>
  <PlatformToolset>v142</PlatformToolset>
  <CharacterSet>Unicode</CharacterSet>
 </PropertyGroup>
```

```
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <UseDebugLibraries>false</UseDebugLibraries>
  <PlatformToolset>v142</PlatformToolset>
  <WholeProgramOptimization>true</WholeProgramOptimization>
  <CharacterSet>Unicode</CharacterSet>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.props" />
 <ImportGroup Label="ExtensionSettings">
 </ImportGroup>
 <ImportGroup Label="Shared">
 </ImportGroup>
 <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
 </ImportGroup>
 <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
 </ImportGroup>
 <PropertyGroup Label="UserMacros" />
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
  <LinkIncremental>true</LinkIncremental>
 </PropertyGroup>
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <LinkIncremental>false</LinkIncremental>
 </PropertyGroup>
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
  <ClCompile>
   <PrecompiledHeader>NotUsing</PrecompiledHeader>
   <WarningLevel>Level3</WarningLevel>
   <Optimization>Disabled</Optimization>
    <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>WINVER=0x601;_WIN32_WINNT=0x601;WIN32;_DEBUG;INETBGDL_EXPORTS;
_WINDOWS;_USRDLL;%(PreprocessorDefinitions)</PreprocessorDefinitions>
   <ConformanceMode>true</ConformanceMode>
   <PrecompiledHeaderFile>pch.h</PrecompiledHeaderFile>
   <ExceptionHandling>false</ExceptionHandling>
  </ClCompile>
  <Link>
   <SubSystem>Console</SubSystem>
   <GenerateDebugInformation>true</GenerateDebugInformation>
   <EnableUAC>false</EnableUAC>
   <AdditionalDependencies>wininet.lib;%(AdditionalDependencies)</AdditionalDependencies>
   <LinkTimeCodeGeneration>UseLinkTimeCodeGeneration</LinkTimeCodeGeneration>
   <ImageHasSafeExceptionHandlers>false</ImageHasSafeExceptionHandlers>
  </Link>
 </ItemDefinitionGroup>
```

```
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <ClCompile>
    <PrecompiledHeader>NotUsing</PrecompiledHeader>
    <WarningLevel>Level3</WarningLevel>
     <Optimization>MinSpace</Optimization>
    <FunctionLevelLinking>true</FunctionLevelLinking>
    <IntrinsicFunctions>true</IntrinsicFunctions>
    <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>WINVER=0x601;_WIN32_WINNT=0x601;WIN32;NDEBUG;INETBGDL_EXPORTS;
_WINDOWS;_USRDLL;%(PreprocessorDefinitions)</PreprocessorDefinitions>
    <ExceptionHandling>false</ExceptionHandling>
    <RuntimeLibrary>MultiThreaded</RuntimeLibrary>
    <OmitDefaultLibName>true</OmitDefaultLibName>
    <BufferSecurityCheck>false</BufferSecurityCheck>
    <FavorSizeOrSpeed>Size</FavorSizeOrSpeed>
  </ClCompile>
  <Link>
    <SubSystem>Console</SubSystem>
    <EnableCOMDATFolding>true</EnableCOMDATFolding>
    <OptimizeReferences>true</OptimizeReferences>
    <GenerateDebugInformation>false</GenerateDebugInformation>
    <EnableUAC>false</EnableUAC>
    <AdditionalDependencies>wininet.lib;%(AdditionalDependencies)</AdditionalDependencies>
    <LinkTimeCodeGeneration>UseLinkTimeCodeGeneration</LinkTimeCodeGeneration>
     <ImageHasSafeExceptionHandlers>false</ImageHasSafeExceptionHandlers>
    <EntryPointSymbol>DllMain</EntryPointSymbol>
  </Link>
 </ItemDefinitionGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.targets" />
 <ImportGroup Label="ExtensionTargets">
 </ImportGroup>
</Project>
```

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```
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CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION
OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR
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PROG = 7zxr.dll
DEF_FILE = ../../Archive/Archive2.def
CFLAGS = $(CFLAGS) \
 -DEXTRACT_ONLY \
 -D_NO_CRYPTO

COMMON_OBJS = \
 $O\CRC.obj \
 $O\CrcReg.obj \
 $O\IntToString.obj \
 $O\NewHandler.obj \
 $O\MyString.obj \
 $O\StringConvert.obj \
 $O\StringToInt.obj \
 $O\MyVector.obj \
 $O\Wildcard.obj \

WIN_OBJS = \
 $O\PropVariant.obj \
 $O\Synchronization.obj \
 $O\System.obj \

7ZIP_COMMON_OBJS = \
 $O\CreateCoder.obj \
 $O\CWrappers.obj \
 $O\InBuffer.obj \
 $O\FilterCoder.obj \
 $O\LimitedStreams.obj \
 $O\MethodId.obj \
 $O\MethodProps.obj \
 $O\OutBuffer.obj \
 $O\ProgressUtils.obj \
 $O\PropId.obj \
 $O\StreamBinder.obj \
 $O\StreamObjects.obj \
 $O\StreamUtils.obj \
 $O\VirtThread.obj \
```

```
AR_OBJS = \
 $O\ArchiveExports.obj \
 $O\DllExports2.obj \

AR_COMMON_OBJS = \
 $O\CoderMixer2.obj \
 $O\HandlerOut.obj \
 $O\ItemNameUtils.obj \
 $O\OutStreamWithCRC.obj \
 $O\ParseProperties.obj \


7Z_OBJS = \
 $O\7zCompressionMode.obj \
 $O\7zDecode.obj \
  $O\7zExtract.obj \
 $O\7zHandler.obj \
 $O\7zHeader.obj \
 $O\7zIn.obj \
 $O\7zProperties.obj \
 $O\7zRegister.obj \


COMPRESS_OBJS = \
 $O\CodecExports.obj \
 $O\Bcj2Coder.obj \
 $O\Bcj2Register.obj \
 $O\BcjCoder.obj \
 $O\BcjRegister.obj \
 $O\BranchMisc.obj \
 $O\BranchRegister.obj \
 $O\ByteSwap.obj \
 $O\CopyCoder.obj \
 $O\CopyRegister.obj \
 $O\DeltaFilter.obj \
 $O\Lzma2Decoder.obj \
 $O\Lzma2Register.obj \
 $O\LzmaDecoder.obj \
 $O\LzmaRegister.obj \

C_OBJS = \
 $O\Alloc.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
```

```
 $O\Delta.obj \
 $O\Lzma2Dec.obj \
 $O\Lzma2DecMt.obj \
 $O\LzmaDec.obj \
 $O\MtDec.obj \
 $O\Threads.obj \

!include "../../Crc.mak"
!include "../../LzmaDec.mak"

!include "../../7zip.mak"
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: More Than One copyright Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
```

```
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <meta name="assert" content="The copyright element occurs more than once." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF
 Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
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  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
```

</metadata>
</pre>
</body>
</html>
MZ@ !L!This program cannot be run in DOS mode.

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rAqA PQqAYYUQQ=At
@USVWRjYGuUUE~^tRjYGU;VuU;Vu;u;t9UFVUNVUvvwAKvv _^3[tt
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_AW2QVuEtj+v Ev=MYYNM^d
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%
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SVWf=|W6IDx0N3vff0rf9vG@@;rW6xL$PK_^[DaA4SVu3WMF;t`}f9uMM5M]*EUPEJMEPEPuuyYY};}Wk4|
A]f]f]]EEztUH;t%fEPuWMEPMM_^[d
XaAVWff=u0N@s^j XsP3RP 3;f=u0MveMMfuhYWM_^d
laA5SVWCc{f: EuMF3;M|;us
Eufp9MvEf8s'A@@;Mru7uMY}3,uYWE{DpPWM_^[d
aApVuW~tzAu
uVo?uf>tW/MveMEPuYM_^d
aAQVWjYuetEHNPN=3MV[M_^d
VWjvY3;tHfHfH
H3V_^UQSVuWU>&t+;sURPuSQMM+uu3_^[UVuEuPu;u^]UVuEuPu+E%@^]USVW}t-
;sURPSVQ]+}u9Eu@3_^[]DVtH+H;v;v3vWP|$P
9A;r_pD$t03^UVuWF~+;}v}t0~tNWuQ~tUNWF~Et8_+E^%@]Vt$W|$~tT$NWFD$t8_3^U
EPEPEPEPXpAP8pAt(MUSV33W}u33_^[<pAjh'jPwV9FV6FVffVjF8V<^<pA333SUl$VWjY+SEU!+C8_^S<EU
][U8SVuNH3;9^Pu3EWPN]]FH8Xt7EN(V,SPEMN0UV4PMNPUURP5EN0V4SPEMN(UV,PMNPUURP;t NHW
^[VV0pAD$VF4pA^VWV0pA~V4pA^U
AUEAEAEAEEAEAEAMEgUM\EjEXueEMMueEuuuunjh@BRP`uuRPuuRPSVW@BVw;v*jYVWjYWVws_^[
U
AUEAEAEAEEAEAEAMEyUMnEjEXueEMMueEuuuuuuRPuuRPfuuRPqqq1ttUQQEUEEMEEEueEuuuuuuR
P-AAAU9EsEA38t3MO3-QPQPjjRPjYfuuuuu uRPkRP*]SUVWjXj_3p;vBr@ r _^][USVWuujvu$u
jvu,u(SWuuuuRP_^[](aA~$SVW;Ms
@r3;P29}uJ}KeMEGEEvM)MUA;MMrEEG3
uuq9~tjq;YtxxxA3P-utU9u-j;YtxxxA3Pj`u

H`@x}F};tPvM}EURhwAPE9}t#MEEP}uW;t
aE3P;vyttC;r}EURhvAPEE;thQPR;tUEE;tPQEe;tPQEM;tPQEM;tPQW}EURh`vAPEE;j[VdSRPQ3E;tLEE;tPQEE;
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QEEtPQEetPQEMtPQ^tSPEtWPEEtPQEEtPQEetPQEMtPQ3M_^[d
V#WiiQ#iPF_Q^VW|$Nt9>uf&cfFt
>jX_^t3U SVW}j3[EGEI]EM]
j[MH#EMEmCMMjX;s
jAX;rME;EvM}EwEMjXH#EmMx;}EwjXH#Em;r
ME}@EE+;vE0+EvA@KuM1F;uEEG_^[VjHXH#^Vt$jhAt$&u
D$@3@^D$@@Vt$NFut
VY3^#HdVt$NFut<VdY3^aAB0SVW3~_(_(_$_,]]]FE;tPvMEURhpvAPj;YtXAE]E;EtPQEE;]]]V4j
RPQ;Et:EE;tPQE];tPQEM;tPQEXEVTjRPQ;Et:EE;tPQE];tPQEM;tPQE9^,]]w 9^(;t4G W$+EUUu=vP;EG
EG$EV(N,9Eu9MuEE]t8^ttu9^,tE]MHHMXH9^t[6FPQN?;E=;EPHXNFQSSuPR;E7UUAuE8]t 9EKIO
_$G(_,E]E;F,twE;F(fF;t PQ^F;t PQ^EE;tPQE];tPQEM;tPQ3M_^[d
EE;tPQE];tPQEM;tPQEEE;tPQE];tPQEM;tPQEEE;tPQE];tPQEM;tPQEIEE;tPQE];tPQEM;tPQ@
SVt$Wt!;sPSWQ;wt+u_^[@bAHSVWj3Y;tXAE];EtPQU]]MEE;t;P1M7]EURhvAPE9]u9Xj;YtX@A3;EEtPQE]|_(
_0_,_4EURhvAPEE;tovPQ;Et\EE;tPQEE;tPQEE;tPQE];tPQEM;tPQE
E;EtPQ]EURhwAPE9]EEP]u;Et\EE;tPQEE;tPQEE;tPQE];tPQEM;tPQEm]EURhpvAPEM;ppQR;EtmEE;tPQEE;tP
QEE;tPQEE;tPQE];tPQEM;tPQE]EURh`vAPEE;VdjRPQ;Et~EE;tPQEE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQ
EE;EtPQE];]V4j
RPQ;EtmEE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQEnEVTjRPQ;EtmEE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;t
PQE9^,]]~w 9^(s;t4G(W,+EUUu=vP;EG(EG,EPE]HMPXIEEt
8^uu3jXAE9tl;Pp`EPQE2Mh;E)ELEQMQSuuPR;EE8Xt@;[G(_,G0_4E]E;F,wE;F(MgEM_EMEUMEIME=ME1E
];tPQMM3M_^[d

EE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQEEE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQ@EE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQEEE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQE<EE;tPQEE;tPQEE;tPQEE;tPQE];tPQMM
jXS\$Vt'W~hp@7hVtWY_tVY^[bAQVuE
EtPQVVEtPQEtPQh%C@jjPE@h%C@jFjPE*FEtPQFetPQMhrq@jjVM^d
IUVvvYY#A^QSUVW38\$jYu;w28\$u:t39SjVWL$(T$
LSDUPQ_^][YQSUVWGv3UjQPG3QjRP1`UjUWjYx_^][YS\$Vt$W~hrq@7j
VtWY_tVY^[S\$Vt$W~h<s@7j8VItWUY_tV=Y^[bAQVuF(etPQM^d
cAS3VW]]hM]-u30@;r=s)ucEPEUE;]3E]<0UDUf;u0E} rE}rMCSM_^[d
2V3v#SW<3< A3F;r_[^SVW3vL$F;r_^[$cAeMVMe;FtPWAWMNIMPMhA}Mj6<MWhMjv&_MEPuLEYM^d
U VuMu:EP^DAhDApD$At3VNXPub8D$tZW|ww
w4w0RPwNww7v\NQNhvtvpQNXRPvx~`_uNXP3^UVWv$v v4v0RP9t$j
tYRPj ZZtEuUuOEUt uUuuuuPuuu tE}Su3>u^SWFuuVSWFuuVSWF[V_^USVWj`Ej
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jh@BQPRPd_^UQSVWjjduMuEjUYruuSW1MRP_^[U@SKXP8EVuWKv4v0v,v(v$v
vvv6s{`EUtKX5ASdKXj}YuuuuSuEUuuu<EC8PChstKXeUspUEPuus|U_^3[IX`VWV38t@<0uT$+~W_^V3W8t
@<0uT$+~.V_^UHVWf g EPTpAU/hAhA,pAPLpAtMQU_^8cASVWAPjMYePMufYfu ~Jt%f= u ~!thAvhAF
RP?hAhF"RP%~NjXjEX3T;u9Ut#}u~@thAjvhAvm~thAV,~thAV
hAFv3;tjP7hA3%uWVM_^[d
U$EVM#3u}EUSj
Y3#EKu6j
YEM#u"j
YEG#3uj
YETRMPXMEP}[tuMuMu^Vfu
Rh A^
R^UeVM;MsuUEP^LcA9,VMf Lt1MReUMHEPIMuYhA=Ath$AM^d
U8SV3sW EPEPEPU}uK}swEMuMue3}UEPEPEPEKuj_FOuE}|}OUj(EPj)_^[UQSVW3]}tuF|_^[`cAVWf
3W(pAt0<AMWj!ePMuoYGArM_^d
EdA?SVWU>ujXtj3X@Dt;t@EePEEH}}}}}EE}@;EM4MVMRMpA9~MvMExEMPr9}[r
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;Y>MTALM}|M`QETEu;Y@9}&Wh@BWu;EUhLA3WuPhzAMutyEMP
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Y`ql3}Th}|WEPjh@BlhqHRP2}}0E9Ew(rE9EsEUjYEUE3PP$AuhAD@P<tPE
EtYP3}}EP9,EuhA(9}tQpppQUuSjuPPuu%(TPpTAE,thATmT(PT};uTYj^
}uEE'}}AvMMEsM:E;E|%EMDMPE;Es
ME$TM(tg9}tEMAAt"hAuYYEt98tu4tU4EFE.u*uuEEYAYucuEWYY}uTDY@cPhAU3'EjZWPmU9Uj^us'"EjZ
WPIUF9UruPPPYEE;EvE&f~39}}}}E
3MjX;EPp}38pt@pup:@pp
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uTY (HA(eAEtEE}E}Eu}6 8}Et+} E}EurE
t5tP}vPP}EPt7v2ujZM;Drw;@vNwjX}Et#;thAjZjVPMUQhAuRPjAPP}teEEe3}}}tE}EtE*t7Au@5AAu%P}Aux
A3j
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DAZjOjDj9}tj(j}t}u5AE}|PPEMtEM}e}39}tPj@AztEEu3jXlQMQMuuPuut'MEtMEu
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E}$TMtGxtkh<AtYYOt 8tBt4u-
tuEEYAY,8Fte|FFuEtYY}9}txe}E=@}uj^jZ;vvNj_;_#38t@u:j0P<AEtgffhzA$EVEQP0$OY*E
}tE=rxUQtQSjuP0Puu|P}E0tFj_;}EE;EXu-PL}t5A-
V0P}tS4AUPuu?5AUPuuP,A5U\`dhlpTTPXuu\PE}T8K@TYF0ETYM1MWM1MM_^[d
VW|$j'|fxu @2_^Vj|fxu@^jLX^3I^UQQEVuUUM&Fwr= =s
}u =3rw=r}t!}tRPjujYaV^cdAQV3uFFNENEM^d
dAWSVWMu
l3PEP,];p}
@j_9lt
W8]}utN}uE9]u@;u }Wj
;v;vuEuMu9]E}]VM\#M$`HM \FQpPE{;E\9u 9^\E)E(jSxNhHHFiFHPj F(SPjFXSP$9]]v}ELPLP2uEu5;E
LM;9uE9$LEpEEE;ErEE;EMDU;H;] tU
E 9]] NPEFEFE FDP uP!;EEE;Er\EM];tjFuVE\/LEqM];tju%ELLM];tj@M M];tj]tC9]E]E]uuiu3PE(uSpRPF(FV,E
EjXb;YtXX8X<XPxAE]EM
PAME MtAHu9]uFM$HPMH8HX<}v
EViP%@F0B;+EE;E}v9]vEEMuu;]]OEPM]]]]G(EG,;EvPU]U]IuM$UjRP;E]E9]]E]?uEi9]uwM(ua}aMuWQM$F(
FV,HPMH8HX<1tP$pAM];tjt1H(N(@,F, <;jX}Fvg39]vrEEE9]u ;ujY3UiPD0xPPH#@x0d;E
G;}rS;;EE;EE}](9]E}E9]v/EEE0\E0;tE(MuEMu9](tLtP$pAM];tj7](tP$pAM];tj] u;lOEP-
]]]]w,w(Su6ME;UvPU]U]Iuu$MSQP;t"tP$pAM];tju5MjQPt;P$pAM];tj<M nM_^[d
$Vj
e|fxu@^^V%uj|
3A^jX^Vj|fxu@^3^D$@@dAQQSVueWF0e$2E@@uEtPFHHrMe3d
_^[UVuWF$H>v<Ne_3^]dASVW}i3P3YE;uthp@h@pWhV8M3_^[d
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tjU}jXwr9EseEuuuuuj3X]eA|QVW}WueGPNNWEM_^d
VW|$WGNP9NW_^Q$pA
T$3333333333333333333333333333333333333333333333333333333333333333333333IUQQSVWUj
MZu>^uj3_uuSWvv.RPSWvvRPSWvvMRUP_^[UVWhAW}tEUjYRPjZhAhA=AthAhAujZjud_^]j3HHpH`HdHh
HtYAHxH|3AA
A(A0AA$A,A4A8A@AHAPA<ADALATeAHSVW}UM~AVOXjFMGGFG3EfEfEEvME)MMEQPMEuY8]Eu
9^uNUOxNO|OQWu2vuPEPuu`=@u`u
u=MMMA3M_^[d
MZMMseAFE S3VEW;UXuj_EUjYEUu
E]]]]]PYE ;Ethrq@h7@pWj V83;]uv+3Eu JpupuXp@uh=;] v)3E|@Q;KEE 9} rM3;vzE;u
B ;rdhEM5pA;MEw;v3MP;5pAEUu9]wE
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UQQSVuW=pAF^Ewv,NPFu 5pAVUu~_^3[SUW339ovV7yC ;_r^g_][V^tjfA6SV3;WuMuEE UHu
@]]M3]u;t3QM/u
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;u[EE89}rR@uYlEE;tPQuY]MMM1MY3;hE,;u
B8;rOUMEPj0]MEKuUMEub;Et"uEY;EtPQuE;u$uEY;EtPQ@m]URhwAPE9]t$M EPuu]K;t EE'u$UMuuuu;E;Et-
;tPQu@EY;EPQ;tPQuEY;EtPQp0P]]uuSu
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$VPF #NPvveF,3^]SUW339ovV7/C8;_r^g_][V^tjQVWt$_^
`(<fAPVWUMAEjHXMefefee6ME>MMEQPMEu1Yu|u$EUMu uuuPFPEvvPuu=@tu=MMMMA53M_^d
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PRPV_^sAI@@;v|VhAHpAthAPLpAu3^VjX^ZfASVW3u>~~E}X;t?;wPY^;}vQjY}Et#EMHOPO3Fe<FE9]rM_^
[d
nfA.QQVW};ukSW}3vLjY}et EHOPOT3FM<FC;]r[M_^d
VWwtSNtK~SYu[7Y_^VWwtSNt{S[Yu[g_^Vt PQ&^ S\$V;^v<?;wWPFYtP6W6Y>^_^[UQVjh@A
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wA3uD$_^H [AH 3vA3u D$4H_
H A^H 3[vA3u D$H_
H A^H 3[vA3u D$H_
H A^H 3[vA3u D$H_
H A^H 3[vA3uD$tpH_
H A^H 3[vA3uD$tBH_
H A^H 3[wA3u/D$tH_
H A^H 3[3_
H A^H 3[_^@[D$H AH L$A HA u
tjP3VD$t V<~^jhfAdPd%Vt$|AFlAF\AFLAF8AF$AFAFA@AN\D^{N}tVPL$^d
DLHH3DLH@3VN$t
FXVT;t('NXFTFu^NXNT3^DTVth@APNXe_uNF1^w"$@3W@@I@@@@@@T$S3;Y8:A6Y<tA8RQ<YH
YLY0\V[D$3H(H,HHL$QH3DLA,3DLP4@8A3,SUVWF$F5vF@ND^tD$,L$0\$D$~HV(N,F,;u,L$uL@P
FTPF$F(PQRD$FpNPnt+;v+D$ F6T$(t4F8NH^L~H+N<3;rw;rF4T$(tD$
F(N,+^$L$$L$QL$$QL$,QPN\TT$^(N@V0T$$D$ ^(N@FDNtFD+//o8^6ot^Ln<~H;rw n8;rDD\$
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W^T$NFRPQD$LTPQRu+N4tTtN@2W9;_u
Rq;t^Vt$W|$tVPtPQ_3^Vt$tPQ3^D$3;tSUVtWTFt1Fn,N
^0+l$(;rw;rFtD$l$(\$$T$(~F;u.D$(u&FV8WRVRPQD$(F+T$L$ L$RT$$QRVN@PD$4Pl$8TLT
NNDNFF$N(N(L$$N,F0D$,+tD$uBt9_^][D$(_^][l$lvlvl$l$FlFlVlll$&l$llllllllVl$VjhfAdPd
%Vt$F{AF|AFAF@AD$AFAFAFAF1FuD$hAPD$L$^d
TDSVWA3uD$_^H[AH3 wA3uD$_^H[AH3wA3uD$tpH_
HA^H3[vA3uD$tBH_
HA^H3[wA3u/D$tH_
HA^H3[3_
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RQH,%Wv3uf8L$PQ0@A43f8@$@D$P3L$3T$B3L$A$3T$B3L$A3T$B3L$A3T$B3L$A,3Wm@@@@@@@
@@@@@@@@@@ff=arf=zwf=HuGff=arf=zwf=Cuz3f0|mkfyudL$f=BuJff=arf=zwf=Tu-3f0|
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MWKu_^]3[3[LDPTID@uTLRT$~D$<L$VPzL$LQL$,"{L$t$XVFzT$h8A#h@AVtPDPT$8NT$
D$$VFD^uD,u
D$uy<l$lllflflfl6l$6l$6jhfAdPd%QhnDDtL$d
L$3d
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L$3d

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tB;r+++;vA3JABBBL$,|$$t-+twtD@uGf+t0CuK3|$$t+\$3lBJJB
l$G[vT$3_2^]T$_2^]Q$X3$PX3R!$$YSVL$333D$D$\$LTDL T$D$$LT(D$^[VstC
et5FPNQVR3lFPNQVRVS^3^SVWy3PA39zuZ;Yu;Yt@$r_^[
DPLQTRT3DPLQTRTL$V%WL$'t7Ht#HuruJsE_^
ru*r
rut&t't5t6t_3^ W~D~@|$~t~<_V4t9~<tCF4~<F439F4_A+A)Q)Q)QQ~u-
~\u'~t#NFVL+H;v)FLNuFY+VF<V+F4$tIN8$PF\u$tFF+F;FDw+VN<V+N4$uYVNF@+NQ+F4RPIN4N@^yt3A
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3 3% 3 3Bh^VWGV$~F$uV4GF4_^SVWv-|VG3^$8^u
V4G^4_^3[r
rLTUlV@T$*^D\$ P
VHn 3_nTuX3;lrw;hvh;tI
=vuF,@vFTvFTvFTFT~~PFX^tV$<;t9`t>\$ Cn$`;t3F$;tNXV(]_^[3V$C~n$u
V4Cn4]_^3[Wy$ITt3_ATQ$I,AW<t3_UVF4FFFFF\FtcFn+F+;sV+W~D;wt +_;sF
;vnV^]UVN$FTW<N,At3NT~$t3FV4FFFF\FFn+F+;sV~D+;wt +;sV
;v_nF^]W|$3tVd$4;w3+4@;r^_~SWu:`~$+FP3%t;w3+A;r)F)F)F~uF+F9FDuN9NuF^+N+;sV~D+;wt +;sV
;v_^N[TLSDUlLVt(+|$,W|$4t$,D$(L$0;+;#+D$$D$:+uU:EuND$;t$+}T$ T$
::uFG;u|$4;sI7O|$4;t|$4L$+L$|$0__^][$D$$SLL,UVpW|$,t$3+\$t$
t$9t$@D$0L$8L$@;|$<;#\$<+L$4,L$+l$$;r:uWF;t$t/:u'F;t$t
,+):EuFE;tuD0l$$9tHsLDO1ytHLD;t$te:sL$]tt \$L$\$Ult |$,+|$@#LDD_^][UD$_UD$^][D$
SUVt$0HWD$(33+L$|$\$l$9\$<IL$<;D$8;#L$8+t$0<L$,+;rt$,:0uE@;D$$t4+I.:u#@F;D$$uDOT_^]
[;D$$tt$,:0sL$\$|lLl|$|$\$D$(+|$<t
t$4)D$_^][SVNsFFF;Fu^3[BV$W~j8F0SPFPFPF(PPFPFFF+;FuA_^[VFTDs!FFF;Fu3^N$SUW>W3T`G3#F,V
T$+l$l$,3;V+ :{L$G;t(:u@;u\$+JS;uMV0FRVPF(RPFRT$,PQ:FFF;Fu
_][^L$\$PF0RVPFRV(PRVPRT$8FFF+;Fu_][^VFT$D$s!FFF;Fu3^A3D`QSUL$IW|`N33F$#~,l$ +T$$+TT
T$$T$33;^s+ :
uL${y;t$;ns+ :
u\$MLL$tz,
\$$;t\$$:u@;u+T$D;uEF0VPFRV(PRVPRT$4QQFFF;Fu7_][^sPD$F0RVPFRV(PRVPRT$<+\$FFF;Fu_][^VFT$D
$s!FFF;Fu3^A3D`QL$IL`SU3W~3F$#N,+\$ l$$+ll
<l$$L$33;Vs+:uD$jh8;t$;^s+:uD$STtjT$$T$;t\$$:uG;u+L$|;u5VF(L$FFF;Fu[_][^TsDF0QNPFQN(PQNPD$4Q
PR+|$FFF;Fu_][^SUVWI~r8A
V$NF0NPFQN(PQNPQW^F^;Fu{+u_^][QSUVWT$InrRAI3D`~$^3#N,%F0NPFQN(PQNPQUFFF;FuL$u_^][YS
UVWTIFDrrA3D`QIL`^33#N,F$V0FNRV(PQNRT$$PQRRFFF;Fu8L$^_^][QSUVWT$I~rXAy3D`IL`3~3#N,F
$<VF(,FFF;FuL$u_^][YP@B@@B0@yuB`@B@IHuB`@B@@uB@B@Bp@B@@W39~t2NFH~~~uNuNuNXu
F0P4pAFHN ~,>vN$6uN0Q0pAF,_~W~HtX~uR~,FtF0P4pAF,SN uNt;~HtN$GtN
u;~HuF[_V~Wt*~FtNtNtNLt~(tF0P$pAF(N-tN%tNtN tN$
t_^>uN0SutNF(tuNtuNtuWN
tuWN$3tuD$T$PNFsu3S\$t:UVt$W|$+$D,+,nBKu_^][|$tIS\$UVt$W|$AAi3)3#D$+,A.BL$
u_^][|$tVS\$UVt$W|$$i3i3i3#D$+,A.BL$
u_^][|$tRS\$UVt$W|$A)3i3i3#D$+,A.BL$ u_^][QSUV
$I<D$
r@Rrx$044
xIt<-
0pA_\SXP.+.54pA)/)SXPH~qF=v$+FPXF,NTV$@Pl$^+^D$E]FH;r@+vN,V`RVTSEPF$QVtQKM^LqT$Dpq^][
YS +UW|$$3Tl\$G;w

/_][3;99,tN@p,40pD~p<Sp@ol$(XQ4pApHTpLoXR0pATpH
@@T$W;RT<Iu_][+T$;rD$+@;sD$ +;sT$;T$LILt}L$<@+THP$OQ PQUPQL$0SxL$+P|$$ECL$;L$t+L$+;
u3\l;/_][V,WuXP0pAT?~W=v
+Q),uXR4pAT_^3(PT8<@DHLA,ATAXA<A@ADAHALAPVW(/F,'G_^W|$v3_3D$DDUV$WN@3plNDl~4~
8uNLHl_NPG6m~t8pt{,urT4tXP4pATHlDk;pt LGkHl;pu,NHk _^]3D$SUxW{ @r_][Vt$ uDt0CLTD$V
QRP#u^_][S(h@uSs,h@x@t ^_][^_]3[SVWx33^^aN ^ANW$VGTFO`N$W(G OHWOW
G0_$^[Vq,^Vw,WGtOH?GG@GWOO@G^rW+O RWIG
@GWG;GuG_SYUVqW9/GI$3%4;r%+::D$u+N_p^][D$_^][QSYUVqT$Q$W9O3G3%4\$;r?+D=:u3+HD$HL=:O
u_^][YD$;r+9:u+Np_^][YAVWy4l|ytSUhE>^:ZMu][A_^SUVFNW;T8Vu/FHNrRV+V
F(UF+_^][VN+SF(UK+\Xu+F_^][QSU-
0pAVWT$$F;F39~0t.ND^t~0~8~4gNHgN@YgNDgN\Q4pA^tNL~XgNPfV\R^XFtN+?FF@FVNN@FrV+N
RV+F FFN^)\$NDF&_^][YQSU-
0pAVWT$$F;F39~0t.ND^t~0~8~4fNHfN@YfNDfN\Q4pA^tNL~XfNPeV\R^XFtN+?FF@FVNN@FrV+N RV+F
FFHNr@N$3NV%F^)\$NDF_^][YQSUV3WT$]F;F9n0t.ND^tn0n8n4keNHceN@;eNDdN\Q4pA^tNLnXeNPdV\R
0pA^XFtN+?FF@FVNN@FrV+N
RV+F FFHNr8H@n$3L~3N%3F^)\$NDF_^][Y@B
@B@B@VxFHtCHt&~@u@tA(@Bp@^tP@A(@@B@@^t@A(B@@B`@^sVt$FAVAVAVA VA
FAFAFA
F3^QS\$VWs_^[YD$U(~]_^[Yh(AL$ L$QjjPRL$0QP&T$uuPD$D$Pj3]_^[YPSVWL$$|$
\$D$+'D$hL$lD$$L$(s_^[P\$d3N|$pNN N
N3NU^N
D$ltMt-D$lu]_^[PL$dSQPeL$ljT$tRjD$|33LtLhLpDD;T$T$$DLpTTD$ ~&H;u!|$htu
D$ly3tD$lD$dh(Ah(AjjL$0QVRL$@QSPNT$8_t%u6D$;D$tv|tDt|hDD$p@D$p;DDD|hDtL$$t
LlD]_^[PLT$PJ0B@L$J8D$B4SL$4JD$0B<UjVD$DJW:JNL$4J+D$PBD$8B(D$(BL$J,D$B
RlDt$X|$TLLD0\$(#\$$\$]=st$>Ft$;1+|$(f]u|$Lt.LuL8\$4\L$(#\$PL$T[4NL$D$(rl$+L$Ns}El$;s+f~+++fN
3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=
s}El$;s+f<3+++f3C,.=s|$G|$;s)+f<.t$|$4>Ft$+++|$4f.t$M>FtL
+\$,LL;#l$8+L$4,)Ns|$?D$;s#+fN+++fN|$ #<~|$?l$$=slmD;s+|$3L$ f/,+++l$f}l|$$#\$
|$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$
f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-
|$$#~4~=s|$G|$;s*+f>t$|$4L->Ft$
+++|$4ft$L->Ft$
+f]L$LM ++=st$>Ft$;s%+L$ftM |$+++fL} t}8=sL$ D$;+|$ft}8]sL$ D$;sR+t$f]L$,l$4;#\$8D$(++.Ft$L$=
+++f]+++ft}8L}P=stFt;s+L$<ft}Py+++fL}PL}h=st$Ft$;s+L$@ft}h+++t$@fL}hL$HtHt<t$@t$,t$<L$,>L$=s
LAL;#+|$$f4~~sLAL;s+fN+++f~
3=s|$?D$;s+f<3+++f3C<1=s\$D$;s+f1+++f<1A+++f>~=sL$AL$;+|$$f^t~~sLAL;s+fN+++f~
3=s|$?D$;s+f<3+++f3C<1=s\D;s+f1+++f<1A+++f~
$I
3=s|$?D$;s+f3+++f3Kr|L0prAt)=s|$G|$;s+fy+++fql$64=s}El$;s+f<+++f4C4=s}El$;s+f<+++f4C4=s}El$;s+f<++
+f4C4=s}El$;s+f<+++f4C4<=s]El$;s+f+++f<F@+IFI|$Dw=s\$D$;s +|$Dfw+++\$DfsIu+l$=s
}E+tw#Iu|DOl=s]El$;s+f_+++fOO=smD$;s+f,O+++fOO=slmD;s+f,O+++fOO=slmD;s+f,O+++fOL$@|$L$
HL$<L$@L$,L$<NL$,
LLLuL$(;\$0L$d|$+;r;|$,#t$8L$(+t$,+\$0\$8+;w\$4,+)|$.ME;ul$4./FG;t$8u3Iu|$L$;L$dsSL$;L$hsItXlDl$4D$
`T$_^]P[LL$`y_^][Lt$0L$`|$=sGQ$T$A
D$(QT$,A(D$<y_Q0T$<qDA4DD^Q8T]A<Q@3[LQS^DtbsZNFUn0L$+W~;s~,uV+V(;wVV,N(+^Dt;#T$+:8
@Iu_F][YUlVW~,u'V(FN++;v<u ~sBlUWVu,9F,uF9F(rF,.9^s9ns ~Dr3_^]_^]SUVWL$(o@W
wGLOD$K#_(+HL$
D$s&D$(;D$r_^]3[@D$(D$;~D$,D$u(t=OuO_*OH#G(i\$sa$D$<HsD$(;D$K@D$(;s++L rO_0;sG3+Il$D$

#DEDsD(;D$(@D$(D$;s
3|$ ++L rDD(s
;DbD_^][|$Lo ++L$L$(s;L$-AL$(D$;sD$ D$D++Do8D$s;L$9AL$(D$;sg|$ xD$(s;D$@D$(;s
;D$W_^][|$(++LhPL$s;|$%G|$(L$;s?Dhh++s;|$G|$(;s++|L(D$
s;L$)AL$(D$(;s_3D$^o++s;D$N@D$(;s_l$++D$s;D$@D$({;s++|?D$;sD$(+|$
0rD$(ol$\}sD$(;D$n@D$(;s++|?@r@IL$sL$yD|$s;@+K#+IuLD(DLL$is"D$9D$(D$(8@D$(;s++L$u3AALA
(A,ADD$SUD$$VWT$\$9nDt.$NLsD$ @T1PFLD$ D$$Ku\$FLt
~P^NQVRFSVT~DN$F FLvLN~tfF<F8F4F0F@FDT$9nDu)~T,_^]3[I\$L$3D$9Nr/FDu
9F$=|$(HVDD~Lupru
|$ D;,|$ W63|$t
|$ \$P~yFL$$+)D$|$ 3s;sT$ *L7PGEr~Lrt%VPR|$t \$FPFPuyF+P;+;D$$+(l$
)l$FL~D{D$,_^]3[L$,_^]3[T$,_^][SFPWPGD0L$8^L_^]3[D$$(D$,_^]3[L$,_^][_^][(D$,SUVL$@W9|$$3T$08
T$(9N9Nu~F+D$4;v
L$|$,DDHD$,|$L\$@l$$3~D\$lL;v"FLsT0PFLCAMu\$LlFLt~Pt
sFQNRVSNT~DF$F FLvRNVt f|$LF<F8F4F0F@FDT$~Du%~$$\$l$F3L$;D$r.FDu 9N$=9L$,;;D$ L$ FLufr
ul+$S|$
t ^\$UL$F+D$)DL3sI;l$s+T7PGEr~Lrt%FPP|$ t
\$FPFPIF+P;+;+ll)l$FL~D|$L`XMUNPSQCl$nL.T$LlDL3LDDD$4L$@)L$$~Nl$0+WQU*CD$H)|$48l$0
ut9\$(tT$(3q_^][(_^]3[(VFWWWGFG_^|$sBVrrr=sAr ^ ^AQ3D$VPL$ludS\$WVt;~Ht0Ul$ EFE?F]t
F~HL$_N3[^_[^D$VPLDUVW|$t;nHt*GFT-Ft$
FnHS\$@r?_]^@r,#;sVt;nt=GFFu
VG[_]F^L$^[_n3]^hSUVt$|.3WL$$s_^][h$D$$D$$RPL3;uq$T$.UPL4LhLDLHVT$0QL$$\$8DhT,u
}up$T$A_^][h3Q$QA0A4A,A AAAAAAQ(V0yx0u*
L63JPP3;H4rFw;P0v?P0W;v;vAvP_x}@x}@x}@x}3Hx}3J@Px^}39HHx }@
x$uP39HP$x,}xtxuH,SVWfItVWS?Gr_^[8SVW|Lbt$T$LD==`D$$s=vC039D$
D$(utD$,}}D$4T$03|$8_^3[8_^[8D$T$~0u*V N$W~+W;tF0 ~(N NV,_SVVW~
:F3Fr;uF_F^[NF@~F;FuhVVt1$F@F;Fu=~F~Fu_^[SUl$V7LAAD##+GAW%+#T$fs
r7^][SUV7DL#LTJ3lT#*D$##+GAW%+#T$fs
2i7^][VW$3q$=rd$A=sNu+r_^3S$W3s[SUV3#\$k33#r^][QUl$V3WD$TELL##+CGS%+#LlfDMs
TLD$u_3^]YL$SUVW/|$+wW+fs'AY|$rF+wW+fsLDPQ/p_^][l$+fs
DlCD$##+GEW%#L$+fCs
@ElDCD$##+GEW#L$%+L$fKs
T$DUCl$D$##+GEW%+#L$fKs
w7_^][U3VLC3k%3DsL$,t$5,Dr^]L$S\$UWi@DDPD$PKFT$,
RQVL$,T$lDD;vFK3IN3sTLC;\$r_][SVFF4F(FH L$u^[W@ ;~0t_^[Un(vVH9+D 4)];t:u@;u_^+[WyTu,|$y
5HH_HVyluH^_y+3HH^_SUVD$W{,x00,*+t5 O +u) O Bt O D$)L$y_^][hSUl$tV3uu9u4uD$ PD$M U$LT
](\$$s^][hv
\$$MEHHt$0t$Lxt4W$<T/8+T<XT$|:uCP:Qu;;vp+:1\$(uBF;rt$L$8T<h;Thvt$4|$8D<hFtrT4|hM0|$D;r#MEW
U4W_^][ht$;r1L$$D MFE4EV_^][h+L$X
Ls8L$|ts_^][hfU,$uL],#f(@#EHMPt$(@<ArD$L$|WHT$|Hdt$(MHD3fJPMH]<HD$<D$|t$@8D$uGM
]THH;DsD3PfJL$ D$D;rL$D$sP_^][hT$XL$\4T$`<8L$d@L*$@H s3D$thr^|$(E,\$W4iD$@y|$)0;QsQ3q9fQN
sD$@D$r\$hrC;\$ E,E<H|$<3L$||$D;H v$;H wL$|D$(i4D$(*0T$(L D$ Crs9/,GL$|D$;PsP3X0fP;H u
L$|;L$$tD$(|$DC JD$9D$i|D8PD$P;E0\.,+u40((uA|$lA|$*0ft+|$u2"*(s
AxA|$(4($sPuHT$XP@L$\T$`D$dJT$\PuHT$XP*T$`TDT$XDdLXHT$\PT$dL$`D$XL$\T$`4D$df(8<@MU$
XD$|+D$XT$,D$@ELL$$A#D$0D$<EHD$,$D$$K#EHL$|@EPP\$
D$(T$DrD$RHt|$@D$4T$DJ3D$4HD$4sL$$L$$;Ds(D3LPfJD$(T$<U}<T$|5HD$,HDTLH8T$usLrPtafJwWD
$<E
}THH;Dw%D33LPfJD$(u(+D$t$4;vD$4}0t$,;v|$,|$(S8T$I@CT$@:B0K:J$G;vt$|;vB+:uF@;rt$|DlAA#MLE

E5D$DH|$DHU<UTH|HTD$$;s..tD+|$|$D N@ut$|iNy;*$*$s
rBfBD$|L$|D$@\$
+DXD$<:S:Ps|$,;vT$ H+
:uFA;rLTD$D;s +)$T$ H@uT$0D$\$|RL$0D$HiLLyLDD$$D$|)0T$$;QsQ3yfQO suFD$@M0T$4F;v;s
\$<T$ <+::u@G;r+D$([T$<T$ <T2$#]L#UH|$DHRUPJQLHTAML$LHyL$$C#ELE
E<HHFH^HT$L$$L$(L$;s%/$+T$I O@uT$(iJyD$$)T(;sD$|FQfqAL$|AL$|WT$PL$,;v&3;H v;H wH
D$8D$@;kL$M<|$t$H\$T:\$P;s(+*t I@u3L$,;H v;H wL$,)L L$@T$(.,NL$<$L$$L$0ip4(0L$0
F|$DrsD$<L$|;OsONOB3fOL$@F;)H \$ M0+T$4H;v;s+\$T\$T:uGB;rN+|$@Y$L$
\2TD#]LT$TT1T$$#UHHRUPJQLXTxAML$|HC#ELE EHHDH\HL$|L$@LL$9L$s.\$*$+\$
J@uT$@iJyD$|;)$)$sQT$(fAQD$,D$,;D$8tU|DD@(L =D$(T$((,HL$<L$0D$$\$P FtD$@D$;DDU_^][hT$8E
UU$_^][hS39^4uD$PID$N V$LTF(D$s
[=vD$FN8UH\$\$LWT+
:uAP:Qu99|$vP+I
:*uGB;|$r;~0sO;|$v\$ |DCrl$;n0rLL$D EF4FU_][FWV4W_][3\$rcT$D L$v0<
HA;u|$;vML$wl$u|$rDl\$|$rHW;sG;rsO;r+r#T$ GF4FW_][|$~$W`F rHD ;r;M;u;;@;rr
;VN8HD$ LT+
:u2P:Qu*]P+d$;sM
:*uGB\$lL DAL
rtFUV4U_][_][SF,UFW~X+N`Vd+fsFX(V,V<V<+fsFXF,xASj$RFXN,~X\~Xd$3!+N`Vd+f3!sFXC@rI~`VdsFXT
Ku
$I3+N`Vd+f3sFXCr~X_][u99t
t
t
SVtGL#D$
wXIiKu!^[u99t
t
t
SUVWd$w\$3l$3HH\$\$\$3H\$XHYH1w3\$l$H3H\$\$\$3H\$XHYHq\$3l$3HH\$\$\$3H\$XHYHwQ3\$l$3HH\$\
$\$3H\$XHYHqW_^][SUVWt$I2,ID$B#|$+\$,{3\$HI\$<xut$\,Ft$rL$0+$!\$DLD$3l$(t$
F D$$4G3HG3HG3HG_33HHD$$G\$Ht$ 5,Hl$(Dt$;D$;v4A;rHKHKKH$
,4l$l((l$,(hrDlL$\$D$S_^][8SVW3X~|~xxT$PD$|$0|D<D$@D$8D$,D$(D$$D$ DDD$|$4h,H~P$_^[8V@t
I^S\$VW(XVPG$G3^P$VxG^xG_^[Ul$ tME9t
t
t]SVW\$,\$MUuS_^[]D$,PiuX}X+fs
17MU+E4MPPQM4C\$,MUt
D$9M9MuD$,PUD$ *$P@AT$M}L#|$,M,uXM
|$+f:=s
EXTMUuX+E4L$,P#UHMP}XRQM,lAu,sPR$+E8QR+E`Ud+f
sEXM,M<
+E`Ud+f
sEXM,MT
+f=s
EX>U,U U |$t+M`Ud+f+f
U,AE,+E`Ud+f
sEXM,MlMlu+f
E<}+M`Ud+fsEX\U,UUu+f
E@+M`Ud+fU@EDUDM<M@U8U<E8sEX|uXDWP(VEXSuxuWxHQU,AE,+f:=s
EXgM,D$uXxAL$QPD$($VEXU,uD4uT$R4HQ*U@E<M8UDSE@M<U8s(,*,HuXD$ L$@|$L$r(!L$ DL$

<QQT$$D$##+E`AUd%+#T$$fsEX|$ruXT$Hs#+SPU#P]X
+$#M`UdsEXu##+E`GT$$Ud#T$$%+fsEX"GDED##+E`GUd#L$%+L$fMsEXT$WD$ED$##+E`GUd#L$%+L$fMsEXBT$<W}##+E`CUd%+#f}sEXuXD$D$,)E4u#rrMEt:D$,+D$|$0tT,;D$4s +MxUhMp
;D$0YD$,+L$P_^[]=%D$,+D$_^[]QU3WD$9nxuFx;t(Ft9t#9uut
_]Y3VPS;t9$t9t\G$GnP$FPVP3$;;D$v;s+D$tdD$N0PD$hQP>vr3Vv"[_3]YL$F0QL$hPXQ
t2V>[_3]YGGnP[_]YQS3Z,UVWzDz@z<z8BxBpBXZ\Z`ZdZhZl
$(,048T D$
$ff(+ufqf1fqfq0)|$u!#@zP;vf$&(*_HBLZZZ4+^]BH[YSUVWuG0HxL$34t$U4PHE;r3tHDLDSQ(C;r_^][VW;v4;v;v;v;v rB
LTQD$3R
;u3_^VFW|$;s FFWRP)~~_^WjjVUuWuNt3Fp+FxFhVlRPPQujjVt
t_DVLV|T$QRjsuS\$E[^VL$9Ws
^S[=@r=sA%5d$;v;vAvGGOW_3^D$UVTT(D$F|W|$(T$8wD$4R3PLL,UD$@QAl$$(;u$S\$4[;u9u9tTL)9l$^]tS\$,TLV@trTUWl<u>L$,T$(Q0u(T$4D$0L$ USRT$(PD$
QRT$,PU(_]^[$^[$Vt"PtpAupAu^3^jQpAVD$PjL$QRjj^t3pAuPpAt3pAuPpAt3pAuVjjjjpA^t3pAuD$VjPRjpA^t3pAujjPpAt3pAuUjhAh[AdPd%SVWeEQpAE3Md
_^[]eEMd
_^[]D$h AP Ah AP AVt$;tT$W933A;u_^3% 3 3% 3 3% 3 3% 3 3 ABf$AVH3 AH3 AH3 AH3 AJu]A]AA A5Aru5A^@s s333VD$tV-Y^%qAjPdPD$d%llP@s s333DLL$u
DSDdD[SVDuLD$3D$A\$TDud$D$r;T$wr;D$vN3^[%pA%pA%pAUjhAh[AdPd%SVWeuEEeMx)uMUEMMd
_^[}uuuuuUjhAh[AdPd%QQSVWeeMxM+MMUuYeMMd
_^[D$8csmt3UjhAh[AdPd%SVW3EEEE;E}uUuuEEMMd
_^[}uuuuu%pASDuLD$3D$3P\$TDud$d$r;T$wr;D$v+DT+DT[%pA%pA%pA%pA%pA%pAQ=L$r-=s+@P%pA%pAUjhAh[AdPd%
SVWeejpAY
$A
(ApA
AqA
ApA,A=Auh\ApAYhAhAAEEP5AEPEPEPpAhAhAXpAMuuuo0EPpAE
MPQYYeupA%pA%pA%pA%pAhhYY3%pASUVW|lt2B33DOu|$+3:L:t:3333tL:3333u3:|$+t2B33DO_^][SUVW|lt2B33DOu|$+3:%3L:33L33D:33D3u3:|$+t2B33DO_^][M7U@AzM4\|
MPMMuTYMuBYdd*Yd AMAuYAuYMQ0AM?`AMMMAtM-MM AIMA6uQYA
u=Y@AMAMAM(AME#$?MNPAMMAuYAtMMAZMMMA6M@A"MMMGhAMAMAMkAuYAM}MuMmMeM]MU@ArMAM9M1ANMMM
MMMMMAhrq@jjuMMh%C@jjEPh%C@jjEPMuMgMMM=(AZMvAFMgA2MAMA
M8AM`AM
MdMZ`LtFM>M6M.M&MtttTMMMMt0P
$AM2M`A
M=\Lt=AAuY0A|hrq@jjuyM8M-h%C@jjEPMh%C@jjEP5MMM'MMXAMz1M`AMM>M;AMECu
Y0AtMMIuYMM#MMM`A(MSMMAMuYAuY(A}E EEM
9PAM8xAwEPYAWEPhYA7zdF.vfR@*r\L<V.|dD2(
DsA,sA sAsAsAsAsAsArArArArArArArArArArArArA
+F86SOSIEOSMTMFPBLPLCMCFBDAXMMH?Write errorRead errorCan not allocate memoryUsage: lzma <command> [inputFile] [outputFile] [<switches>...]

<command>
e : Encode file

d : Decode file
b : Benchmark
<switches>
-a{N} : set compression mode : [0, 1] : default
= 1 (max)
-d{N} : set dictionary size : [12, 30] : default = 24 (16 MiB)
-fb{N} : set number of fast bytes : [5, 273] : default = 128
-mc{N} : set number of cycles for match finder
-lc{N} : set number of literal context bits : [0, 8] : default = 3
-lp{N} : set number of literal pos bits : [0, 4] : default = 0
-pb{N} : set number of pos bits : [0, 4] : default = 2
-mf{M} : set match finder: [hc4, bt2, bt3, bt4] : default = bt4
-mt{N} : set number of CPU threads
-eos : write end of stream marker
-si : read data from stdin
-so : write data to stdout
i#@'i#@'i#@'i#@'5i#@'4i#@'1i#@'&i#@'%i#@'$i#@'#i#@'"'i#@' i#@'i#@'i#@'i#@' i#@'i#@'i#@'i#@'i#@' ->
MiBUnknown Error
ERROR: File closing errorOutput size: incorrect uncompressed size in headerDecoding errorSetDecoderProperties errorIncorrect size of processed dataEncoding errorincorrect encoder properties"@@b@@Can not use stdin in this modeDecode errorincorrect processed sizeUnpack size is too bigdata errorEncode
errorCan not readFile is too bigInput size: can not open output file @@!@G@G@H@ !@Can not get file lengthcan not open input fileJ!@@y @hH@!@J!@@y @NE@ @eBenchmark errorbmmtBT4xdUnsupported Windows versionError code =)
(
LZMA 18.05 (x86) : Igor Pavlov : Public domain : 2018-04-30

Incorrect commandError code = 0xUnunsupported parameter

Break signaled
zA--Too long switch:Incorrect switch postfix:Too short switch:Multiple instances for switch:Unknown switch:`*@p{ACRC32t+@~+@+@)@*@3+@@+@*@)*@H*@Z*@ZAZAZAZA{Aout of memorykernel32.dllGlobalMemoryStatusExI@
I@I@F@H@H@H@+F@H@H@H@F@C@MD@ZD@D@E@vD@ZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZAZA}AzA}A}A}A}A}A}A}A}A}AzA}AzA}A}AyA}A}A}AmemusefiltercheckexpectreduceeosapassmcmffblplcpbcomemOFFON(eAPAPxA
|(lAx(`A
:(TA
(DA
NEE8A/zz,A
/zzA
zzA
~A
w&AAA
AAA
.A
SA
A

.y-A
$ +%LA|AAAAAA|AxApAhA`A\ALADA<A,A
AAAAAAAAAAAAAAAAAAAAtAlA\ALA@A4AAAAAAAAARDTSCPARM_V8_CRC32ARM_V8_CRYPTO
ARM_V8RDRANDARM_FMACARM_EXTERNAL_CACHEARM_64BIT_LOADSTORE_ATOMICARM_DIV
IDEFASTFAILRDWRFSGSBASEVIRT_FIRMWAREL2ATARM_NEONARM_VFP_32XSAVECHANNELSC
MP8XCHG16CMPXCHG16BSSE3NXSSE_DAZSSE2PAERDTSC3DNOWSSEALPHA_BYTEPPC_MOVEME
M_64BITMMXCMPXCHGFP_EMUFP_ERRATAARM32_ON_WIN64ARM64NEUTRALIA32_ON_WIN64x64
MSILALPHA64IA64ARMSHXPPCALPHAMIPSx86
|
BLAKE2spSHA1SHA256CRC64CRC32:8CRC32:4CRC32:1AES256CBC:2AES256CBC:1BCJDelta:4PPMD:x5P
PMD:x1BZip2:x7BZip2:x5:mt2BZip2:x5BZip2:x1Deflate64:x5Deflate:x7Deflate:x5Deflate:x1LZMA:x5:mt2LZM
A:x5:mt1LZMA:x1J!@@b@V@J!@@b@U@ub@b@b@+V@ : / kernel32.dllGetNativeSystemInfo- gran: page:
cpus: act:. K
(LP)Tot:Avr:x5CPUKiB/s%EffecE/URatingR/UUsageSpeedDictMethodDecompressingCompressingusage:Benchm
ark threads: Dictionary reduced to: hashAvg:Size8crc32CRCLZMACPU hardware threads:size: -CPU Freq: -
Windows*freq=freqtimefile size =fileJ!@@b@W@, # LP MB ?RAM
]u@wu@x@RtlGetVersionntdll.dllSetConsoleCtrlHandler fails@@%@3@@@@@P@`@p@@
@0@@@@@@@@@@@@@@`@@@@@@`@p@@@@ @@@0@ @ZAZAZAZAZAZAZAZA@
@0@@@@@@@@@@P@`@p@
@@ZAZAZAZA@@@A@@@@@@@@0@GenuineIntelAuthenticAMDCentaurHauls

F(A/XAYAZA
ZAZA\A\AAAAAA `AxA^AA AN@\@0A AAAAA @AA^A^A^A^A^A_A_A
_A _A
'_A
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<_A
I_AA@ A`_A (At_A PA_A_A A_A A_A_A A_A_A_A A`A 8A`A `A,`A@AhAAPAAA A@`A AT`A HAh`A
pA|`A`A A`A`A A`A A`A`A (A`A`AaA `AaA A,aA4aA<aA APaA AdaA AxaA 8AaA `AaAaAaAaAaAaA
AaAaAaA AbAbAbAbA bA(bA0bA8bA
HALbA^bAibAtbAbAbAbAbAbAbA AbA AcA AcA 0A0cA XADcA AXcA AlcAwcAcAcAcAcAcAcAcAcA
cAcAcAcAcAdA dAdAdA$dA/dA:dA APdAXdA ApdA{dAdAdAdA AA A@ PAdA
xAdAdAdAdAeA&eA4eABeAPeA^eA AxeAeA AeAeAeA PAeAeA AeAeAeAeAeAfA
fAfA A$fA,fA4fA AHfAPfA HAdfA pAfA AfA AfA AgAVA$VA`pqXpLpzdF.vfR@*r\L<V.|dD2(
OLEAUT32.dllUSER32.dllZfputs_iobI__CxxFrameHandlerYfputc_setmodeA_CxxThrowExceptionmemcmpmemc
py_purecallmemmovestrcmpmalloc^freestrlenmemset_beginthreadex_except_handler3MSVCRT.dll??1type_info@
@UAE@XZ.?terminate@@YAXXZ_exitH_XcptFilterIexitd__p___initenvX__getmainargs_initterm__setusermath
err_adjust_fdivj__p__commodeo__p__fmode__set_app_type_controlfpGetVersionExWiGetLastErrorkMultiByteTo
WideCharWideCharToMultiBytePCreateFileW.CloseHandle[GetFileSizeSetFilePointerReadFileWriteFileSetEndOf
FileGetProcessAffinityMask:GetCurrentProcessGetSystemInfoGlobalMemoryStatusGetProcAddresszGetModuleHa
ndleWGetStdHandleZGetFileInformationByHandleGetTickCountGetProcessTimesGLeaveCriticalSectionEnterCriti
calSectionwGetModuleHandleA2IsProcessorFeaturePresentzDeleteCriticalSectionSetConsoleCtrlHandleruVirtualA
llocxVirtualFreeWaitForSingleObjectJCreateEventWSetEventResetEventfCreateSemaphoreWReleaseSemaphoreIni
tializeCriticalSectionKERNEL32.dll)@e+@@A.PADA.PAXA.HA.?AVCNewException@@@A.?AVtype_info@
@n0 H`4VS_VERSION_INFO?StringFileInfo040904b08CompanyNameIgor

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p_0000>1R1112E2f2s222222H3g3}333F4456a66.7Y7w778*888899993:a:::::::k;>?0hX22R5_5w555556t667"778899d9q9:5:::f;;/<;<@<|<<.=n====>@>G>r>>>>>>??@h000000000&1X1h1111E2Q2222`3g3n3333333334444445m5t555=666l788=9Z9::5;;4<9<< =*?A??P80122"334m4667e78888;<A==>>O??`
}23d3~3778<8r899Y:pXP0011W123=3s3b44449*:9:>:D:K:h:|::::A;Y;x;;;;<=t===>>>??d01V111223+33455667778~888D94;;;<<==========>>$>?>e>j>q>>#?8]000]112234=4h467:`;<;==>>>??TZ000000001
11W23"4-
474A4L444456666\7788!9A:F:::;<??0M1S1\1c1x1)233333334444,444V4|44444444444444515^55556M6{66C7a7h7o7v7}77777N88888888??????\000+0a00001111111i233333333
3$3(3,3034383<3W5^5C667c7~777:$>>>>>>>>>>>>P?p??(002244647899E==E>>??,00000000 111#11181#244[;k;;;
?014(:@4'6x77PS3|4444455"5=5M5o5555556U6\6s66K7]7x7777777777N8J9P9V9`9e999T:Y::X;^;d;j;p;v;;;;;;;;;<<<<<!<,<4<:<E<J<T<a<s<x<}<<<<<<<<<<U?i??????`l
0#070I0]0q00000
1!1E1Y1m11111A2M2u22223%393M3a3F4d4444445m55556=6[6o66667pT(1,1014181D1P1\1h1t111111111111228888t9x9|999999999999999:X;h;x;|;;;;;;;;;;;;;;;;<<<<<<<<<
<$<(<,<0<4<8<<<@<D<H<L<P<T<X<\<`<d<h<l<p<t<x<|<<<<<<<<<

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element matches the schema. It contains one text element that has a language tag." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {

```
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text xml:lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Mixed text Element Language Tags 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-008-ref.xht" />
  <meta name="assert" content="The license element matches the schema. Two text elements have a language tags."
/>
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-008.woff2") format("woff2");
   }
   body {
```

```
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text xml:lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
  &lt;text xml:lang="fr"&gt;
   License with "fr" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
liteFirewall 1.0 -- based on nsisFirewall 1.2

http://liangsun.info/portfolio/nsis-plugin-litefirewall/
http://nsis.sourceforge.net/LiteFirewall_Plugin

------------------------------------------------------------
liteFirewall resolved the issue nsisFirewall exists on Vista/Windows 7 platforms.
It support the profiles (private, domain, public) of firewall rules.
It support Unicode NSIS, while another firewall plugin SimpleFC not.
------------------------------------------------------------

Usage
------------------------------------------------------------
liteFirewall::AddRule "<application path>" "<rule name>"
liteFirewall::RemoveRule "<application path>" "<rule name>"
```

<application path> is the full path to the application you want to be authorized to
access the network (or accept incoming connections)

<rule name> is the title that will be given to this exception entry in the firewall
control panel list

Notes
---
1) Your installer must be run with administrator rights for liteFirewall
to work
2) When compiling with more recent compiler than VC60, you need to choose the compilation
option to use static MFC library.

Sample scripts

```
; Add NOTEPAD to the authorized list
liteFirewall::AddRule "$WINDIR\Notepad.exe" "liteFirewall Test"
Pop $0

; Remove NOTEPAD from the authorized list
liteFirewall::RemoveRule "$WINDIR\Notepad.exe" "liteFirewall Test"
Pop $0
```

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Requests includes some vendorized python libraries to ease installation.

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==========

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wOF2OTTO> @j
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,wnV_)yOzv!c
[*ijm^""Ne_qEg;.+`O 0
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wOF2OTTO8 @d
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf s$,
^4}c)j&bd'd-O"!fClJs9X&l:![
OTx C
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Unknown Child Element in copyright Element</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />

```
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <meta name="assert" content="The copyright element contains an unknown child element." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test
 {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,wZ 3q5O6J*{SlwS/t@4VnxTZjI"r
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%   (2002-11-20)
% Adaptation for hyph-utf8:
% - Mojca & Arthur (see below), October 2008
%
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%
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% we would be grateful if you could send
 it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%

% For more unformation see
%
%   http://tug.org/tex-hyphen
%
%--
%
% A note about lefthyphenmin/righthyphenmin (by Sigitas, February 2009):
%
% At the moment the lefthyphenmin & righthyphenmin are both set to 2.
% The Lithuanian patterns were build earlier then the latest grammatical rules
% took place in 1997. At that time it was forbidden to leave one character at boundary.
% One may change it to one now, but from typographical point of view there is no need to do
% any changes. Even more, changing lefthyphen and righthyphen to 1 requires
% to review and maybe rebuild the patterns file.
%
%--
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- -

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the LPPL Work was available from:
http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

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Welsh hyphenation patterns

(more info about the licence to be added later)

```
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 1996 Yannis Haralambous
%%     <yannis dat haralambous at telecom-bretagne dot eu>
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%
% The Current Maintainer of this work (patterns) is Yannis Haralambous.
%
% This work consists of the file hyph-cy.tex and is part of hyph-utf8
% maintained by tex-hyphen team which also contains derived files.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
% CHANGES:
% - 1996 - last change to patterns
% - 2008 - inclusion into hyph-utf8 and file rename (cyhyph.tex -> hyph-cy.tex)
% - June 2011 - added LPPL licence
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid copyright Element With dir Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element with INVALID as the value for the
dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size:
```

```
20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
 <div class="test">P</div>
</body>
</html>
PROG = 7zcl.exe
MY_CONSOLE = 1

CURRENT_OBJS = \
 $O\Client7z.obj \

COMMON_OBJS = \
 $O\IntToString.obj \
 $O\NewHandler.obj \
 $O\MyString.obj \
 $O\StringConvert.obj \
 $O\StringToInt.obj \
 $O\MyVector.obj \
 $O\Wildcard.obj \

WIN_OBJS = \
 $O\DLL.obj \
 $O\FileDir.obj \
 $O\FileFind.obj \
 $O\FileIO.obj \
 $O\FileName.obj \
 $O\PropVariant.obj \
 $O\PropVariantConv.obj \

7ZIP_COMMON_OBJS = \
 $O\FileStreams.obj \

!include "../../7zip.mak"
```

```
PROG = 7zS.sfx
MY_FIXED = 1

CFLAGS = $(CFLAGS) \
 -DNO_REGISTRY \
 -DEXTRACT_ONLY \
 -DNO_READ_FROM_CODER \
 -D_SFX \
 -D_NO_CRYPTO \

CURRENT_OBJS = \
 $O\SfxSetup.obj \
 $O\ExtractCallbackSfx.obj \
 $O\ExtractEngine.obj \

COMMON_OBJS = \
 $O\CommandLineParser.obj \
 $O\CRC.obj \
 $O\IntToString.obj \
 $O\NewHandler.obj \
 $O\MyString.obj \
 $O\StringConvert.obj \
 $O\TextConfig.obj \
 $O\UTFConvert.obj \
 $O\MyVector.obj \
 $O\Wildcard.obj \

WIN_OBJS = \
 $O\DLL.obj \
 $O\ErrorMsg.obj \
 $O\FileDir.obj \
 $O\FileFind.obj \
 $O\FileIO.obj \
 $O\FileName.obj \
 $O\PropVariant.obj \
 $O\ResourceString.obj \
 $O\Synchronization.obj \
 $O\System.obj \
 $O\Window.obj \

WIN_CTRL_OBJS = \
 $O\Dialog.obj \

7ZIP_COMMON_OBJS = \
 $O\CreateCoder.obj \
 $O\CWrappers.obj \
 $O\FileStreams.obj \
 $O\InBuffer.obj \
```

```
 $O\FilterCoder.obj \
 $O\LimitedStreams.obj \
 $O\OutBuffer.obj \
 $O\ProgressUtils.obj \
 $O\PropId.obj \
 $O\StreamBinder.obj \
 $O\StreamObjects.obj
 \
 $O\StreamUtils.obj \
 $O\VirtThread.obj \

UI_COMMON_OBJS = \
 $O\ArchiveOpenCallback.obj \
 $O\DefaultName.obj \
 $O\LoadCodecs.obj \
 $O\OpenArchive.obj \

EXPLORER_OBJS = \
 $O\MyMessages.obj \

FM_OBJS = \
 $O\FormatUtils.obj \
 $O\ProgressDialog.obj \

AR_COMMON_OBJS = \
 $O\CoderMixer2.obj \
 $O\ItemNameUtils.obj \
 $O\OutStreamWithCRC.obj \

7Z_OBJS = \
 $O\7zDecode.obj \
 $O\7zExtract.obj \
 $O\7zHandler.obj \
 $O\7zIn.obj \
 $O\7zRegister.obj \

COMPRESS_OBJS = \
 $O\Bcj2Coder.obj \
 $O\Bcj2Register.obj \
 $O\BcjCoder.obj \
 $O\BcjRegister.obj \
 $O\BranchMisc.obj \
 $O\BranchRegister.obj \
 $O\CopyCoder.obj \
 $O\CopyRegister.obj \
 $O\DeltaFilter.obj \
 $O\Lzma2Decoder.obj \
 $O\Lzma2Register.obj \
```

```
 $O\LzmaDecoder.obj \
 $O\LzmaRegister.obj \

C_OBJS = \
 $O\Alloc.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\DllSecur.obj \
 $O\Lzma2Dec.obj \
 $O\Lzma2DecMt.obj
 \
 $O\LzmaDec.obj \
 $O\MtDec.obj \
 $O\Threads.obj \

!include "../../Crc.mak"
!include "../../LzmaDec.mak"

!include "../../7zip.mak"
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element matches the schema." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size:
```

```
200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in div Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-020-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element with ltr as
the value for the dir attribute."
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-020.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
```

```
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div dir="ltr"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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wOF2OTTOK @w
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$bMNnI:g3"`B3Fa#tFsm'2S$(bv*|_^'Uq_fzC%bY;;?S>@F`f
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wOF2OTTO- @Y
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$nx*h
<3/ sK:`T-%7'ZG(LyKF6GrEfSC7
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- -

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Icelandic hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: icehyph.tex (2004-03-08)
% Author: Jorgen Pind <jorgen at lexis.hi.is>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby
 modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
% and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated patterns into TeX Live
%
% However, the "copyright/copyleft" owner of patterns remains the original author.
%
% The copyright statement of this file is thus:
%

```
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%    "a better world" as long as you
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%    If you're the original author of patterns or taking over a new revolution,
%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%
  they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-------------------------------------------------------------------------------
%
% The Icelandic Plain TeX hyphenation tables
% as of Nov 2 1988.
% Jorgen Pind, Institute of Lexicography, Univ. of Iceland
% jorgen@lexis.hi.is (Internet)
%
% Mar 2 2004: Uploaded a new version to CTAN, with a clarified license
% statement, at the request of the author, Jorgen Pind.
% Kristinn Gylfason (kristgy@ieee.org)

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```

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OF CONTRACT, TORT OR OTHERWISE, ARISING
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PROG = 7zCon.sfx
MY_CONSOLE = 1
MY_FIXED = 1

CFLAGS = $(CFLAGS) \
 -DEXTRACT_ONLY \
 -DNO_READ_FROM_CODER \
 -D_SFX \
 -D_CONSOLE \

CURRENT_OBJS = \
 $O\SfxCon.obj \

CONSOLE_OBJS = \

```
 $O\ConsoleClose.obj \
 $O\ExtractCallbackConsole.obj \
 $O\List.obj \
 $O\MainAr.obj \
 $O\OpenCallbackConsole.obj \
 $O\PercentPrinter.obj \
 $O\UserInputUtils.obj \

COMMON_OBJS = \
 $O\CommandLineParser.obj \
 $O\CRC.obj \
 $O\IntToString.obj \
 $O\MyString.obj \
 $O\MyVector.obj \
 $O\NewHandler.obj \
 $O\StdInStream.obj \
 $O\StdOutStream.obj \
 $O\StringConvert.obj \
 $O\Wildcard.obj \
 $O\UTFConvert.obj \

WIN_OBJS = \
 $O\DLL.obj \
 $O\ErrorMsg.obj \
 $O\FileDir.obj \
 $O\FileFind.obj \
 $O\FileIO.obj \
 $O\FileName.obj \
 $O\PropVariant.obj \
 $O\PropVariantConv.obj \
 $O\Synchronization.obj \
 $O\System.obj \

7ZIP_COMMON_OBJS = \
 $O\CreateCoder.obj \
 $O\CWrappers.obj \
 $O\FilePathAutoRename.obj \
 $O\FileStreams.obj \
 $O\InBuffer.obj \
 $O\FilterCoder.obj
 \
 $O\LimitedStreams.obj \
 $O\OutBuffer.obj \
 $O\ProgressUtils.obj \
 $O\PropId.obj \
 $O\StreamBinder.obj \
 $O\StreamObjects.obj \
 $O\StreamUtils.obj \
```

```
 $O\VirtThread.obj \

UI_COMMON_OBJS = \
 $O\ArchiveExtractCallback.obj \
 $O\ArchiveOpenCallback.obj \
 $O\DefaultName.obj \
 $O\LoadCodecs.obj \
 $O\Extract.obj \
 $O\ExtractingFilePath.obj \
 $O\OpenArchive.obj \
 $O\PropIDUtils.obj \

AR_OBJS = \
 $O\SplitHandler.obj \

AR_COMMON_OBJS = \
 $O\CoderMixer2.obj \
 $O\ItemNameUtils.obj \
 $O\MultiStream.obj \
 $O\OutStreamWithCRC.obj \


7Z_OBJS = \
 $O\7zDecode.obj \
 $O\7zExtract.obj \
 $O\7zHandler.obj \
 $O\7zIn.obj \
 $O\7zRegister.obj \

COMPRESS_OBJS = \
 $O\Bcj2Coder.obj \
 $O\Bcj2Register.obj \
 $O\BcjCoder.obj \
 $O\BcjRegister.obj \
 $O\BranchMisc.obj \
 $O\BranchRegister.obj \
 $O\CopyCoder.obj \
 $O\CopyRegister.obj \
 $O\DeltaFilter.obj \
 $O\Lzma2Decoder.obj \
 $O\Lzma2Register.obj \
 $O\LzmaDecoder.obj \
 $O\LzmaRegister.obj
 \
 $O\PpmdDecoder.obj \
 $O\PpmdRegister.obj \

CRYPTO_OBJS = \
```

```
 $O\7zAes.obj \
 $O\7zAesRegister.obj \
 $O\MyAes.obj \

C_OBJS = \
 $O\Alloc.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\DllSecur.obj \
 $O\Lzma2Dec.obj \
 $O\Lzma2DecMt.obj \
 $O\LzmaDec.obj \
 $O\MtDec.obj \
 $O\Ppmd7.obj \
 $O\Ppmd7Dec.obj \
 $O\Sha256.obj \
 $O\Threads.obj \

!include "../../Aes.mak"
!include "../../Crc.mak"
!include "../../LzmaDec.mak"

!include "../../7zip.mak"
```

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A) The U.S. District Court for the Eastern District of Virginia has ruled that the Netscape Navigator code does not infringe Wang's U.S.

Patent No. 4,751,669 ("the '669 Patent") because: 1) HTML is not Videotex as defined by the '669 patent; 2) web servers are not central suppliers; and 3) Navigator does not "connect," as defined by the '669 Patent, to web servers on the Internet. Wang may appeal this decision to the Federal Circuit. Wang contended that its Patent disclosing a "Videotex" system, is infringed by the following functionality in the Netscape Navigator code: 1) the animated logo and status line indicators --See Claims 1,8 and 9; 2) the "File Save As" function --See Claims 23-27; 3) Bookmarks and Rename Bookmarks in the Properties window --See Claims 20-22; 4) storing HTML, GIF, and JPEG files and adding filename extensions --See Claim 38

B) Intermind owns pending U.S. patent applications on communications systems which employ
metadata ("channel objects") to define a control
structure for information transfer. The Netscape code does not infringe as released; however, modifications which utilize channel objects as described by Intermind should be considered carefully. The following is a statement from Intermind: "Intermind's claims fundamentally involve the use of a control structure to automate communications. ...The essence of Intermind's top claim is that two devices sender and receiver have persistent storage, communicate over a network, and exchange a control structure including metadata which describes: 1) what information is to be updated, 2) when to update this information, and 3) how to transfer the updated information. In addition, at least the receiving device must be able to process the metadata in order to perform the update determination and transfer. Any digital communications system which incorporates all of these elements will be covered by Intermind's patents." See Intermind.com.

C) Stac,
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- -

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- -

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Dutch hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted
 from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: nehyph96.tex (yyyy-mm-dd)
% Author: Piet Tutelaers
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros

```
% - Hans Hagen, who did the unicodifisation of patterns already long before
%             and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated patterns into TeX Live
%
%
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%
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%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only "allowed" TeX commands
 in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-------------------------------------------------------------------------------
%
% PURPOSE: 8-bit hyphenation patterns for TeX based upon the new Dutch
%          spelling, officially since 1 August 1996. These patterns follow
%          the new hyphenation rules in the `Woordenlijst Nederlandse
%          Taal, SDU Uitgevers, Den Haag 1995' (the so called `Groene
```

% Boekje') described in section 5.2 (Het
afbreekteken).
%
% The main differences with our earlier patterns based upon the
% CELEX dictionary are:
% (1) provide 8-bit patterns based upon the T1 character encoding
% (the encoding for DC/EC compatible TeX fonts)
% (2) don't hyphenate if this results in a syllable of one letter
% before or after the hyphen (hence hyphens in a-drenaline,
% studi-o, mensa-pen and vide-oachtig are invalid)
% (3) handle words derived from other languages (`bastaard
% woorden') according to the new rules
%
% Like the CELEX patterns these patterns don't hyphenate:
% (4) in a changing syllable (menuutje can not be hyphenated as
% menu-tje)
% (5) in words that have different hyphenations according to
% there meaning (buur-tje/buurt-je)
%
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%
% --
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% VERSION: 1.1 (November 1996) Copyright changed March 2000
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!<arch>
/ 1516160955 0 0 0 270 `
R$$

__C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a_iname__head_C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a_RemoveDemoAppLicense@4__imp__RemoveDemoAppLicense@4_AddDemoAppLicense@8__imp__AddDemoAppLicense@8doxebt.o/ 1516160955 0 0 100666 628 `
L.text
0`.data@0.bss0.idata$4@0.idata$5@0.idata$7@0OEMLicense.dll.filegfake.text.data.bss.idata$4.idata$5.idata$7J__C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a_inamedoxebh.o/ 1516160955 0 0 100666 741 `
L6.text 0`.data@0.bss0.idata$2@0.idata$5@0.idata$4@0
.filegfakehnamefthunk.text.data.bss.idata$2.idata$4.idata$5I__head_C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a__C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a_iname
doxebs00001.o/ 1516160955 0 0 100666 691 `
L
.text,X 0`.data@0.bss0.idata$74b0.idata$58l0.idata$4<v0.idata$6@ %RemoveDemoAppLicense
.text.data.bss.idata$7.idata$5.idata$4.idata$6:_RemoveDemoAppLicense@4__imp__RemoveDemoAppLicense@4__head_C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a
doxebs00000.o/
1516160955 0 0 100666 681 `
L|
.text,T 0`.data@0.bss0.idata$74^0.idata$58h0.idata$4<r0.idata$6@ %AddDemoAppLicense
.text.data.bss.idata$7.idata$5.idata$4.idata$64y_AddDemoAppLicense@8__imp__AddDemoAppLicense@8__head_C__Users_Peter_Code_winapi_rs_i686_lib_libwinapi_oemlicense_a
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Valid copyright Element With Two span Elements in text Element</title>

```
 <link rel="author" title="Tal Leming" href="http://typesupply.com" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <meta name="assert" content="The copyright element contains a text element that contains two span elements." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size:
 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span&gt;span 1&lt;/span&gt; and &lt;span&gt;span 2&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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with Files("**"):
  BUG_COMPONENT = ("Core", "DOM: Core & HTML")

EXPORTS.mozilla.dom += [
  'Permissions.h',
  'PermissionStatus.h',
]

UNIFIED_SOURCES += [
  'PermissionObserver.cpp',
  'Permissions.cpp',
  'PermissionStatus.cpp',
  'PermissionUtils.cpp',
]

MOCHITEST_MANIFESTS += ['tests/mochitest.ini']

FINAL_LIBRARY = 'xul'

include('/ipc/chromium/chromium-config.mozbuild')
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With class Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-017-ref.xht" />
  <meta name="assert" content="The license element contains a text element with "class1 class2 class3" as the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import

```
 url("support/test-fonts.css");
  @font-face {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-017.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text class="class1 class2 class3"&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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- -

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- -

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Ukrainian hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted
from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: TODO:WRITEME (yyyy-mm-dd)
% Author: Maksym Polyakov <polyama at auburn.edu>, <mpoliak at i.com.ua>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,

% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
% and helped with testing, suggestions and bug reports
% - Norbert Preining,
 who tested & integrated patterns into TeX Live
%
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%
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% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns
 should be in UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
% and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
% they belong to loadhyph-foo.tex,
% and no \lefthyphenmin and \righthyphenmin,
% they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%

%   http://tug.org/tex-hyphen
%
%--
%
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% Please, send bug reports via e-mail:
%   polyama@auburn.edu
%
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Nested span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains nested span elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {

```
   font-size: 20px;
  }
  pre
 {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;span&gt;
    span 1 start
    &lt;span&gt;
     span 2 start
     &lt;span&gt;
      span 3
     &lt;/span&gt;
     span 2 end
    &lt;/span&gt;
    span 1 end
   &lt;/span&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Nested span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains nested span elements."
/>
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
    &lt;span&gt;
     span 1 start
     &lt;span&gt;
      span 2 start
      &lt;span&gt;
```

span 3

span 2 end

span 1 end

</text>
</copyright>
</metadata>
</pre>
</body>
</html>
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wOF2OTTO5 @a
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,mc,yr-}~%]|tsRw`I!>>0L}R+htf8dd$P?Q1do[(X8

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 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
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  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-011-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-011.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  Test passes if the word PASS appears below.
  The Extended Metadata Block is valid and may be displayed to the user upon request.
  P
  The XML contained in the Extended Metadata Block is below.
  <pre>
<?xml version="1.0" encoding="UTF-8"?>
<metadata version="1.0">
 <copyright>
  <text dir="ltr">
   Copyright without language.
  </text>

```
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-014-ref.xht" />
  <meta name="assert" content="The license element contains a text element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-014.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
```

<license url="http://w3c.org/Fonts">
<text dir="ltr">
License without language.
</text>
</license>
</metadata>
</pre>
</body>
</html>
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wOF2OTTOA @m
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$wRnD/y.g'^jN
\Fj^(jZiT_t?qR;Xq'3:|jO
"I
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THE SOFTWARE OR THE USE OR OTHER DEALINGS IN
THE SOFTWARE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Content in copyright Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains content." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {

```
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF
 Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Two span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-025-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains two span elements." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-025.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
```

```
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span&gt;span 1&lt;/span&gt; and &lt;span&gt;span 2&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
# Contributions to the urllib3 project

## Creator & Maintainer

* Andrey Petrov <andrey.petrov@shazow.net>


## Contributors

In chronological order:

* victor.vde <http://code.google.com/u/victor.vde/>
 * HTTPS patch (which inspired HTTPSConnectionPool)

* erikcederstrand <http://code.google.com/u/erikcederstrand/>
 * NTLM-authenticated HTTPSConnectionPool
 * Basic-authenticated HTTPSConnectionPool (merged into make_headers)

* niphlod <niphlod@gmail.com>
 * Client-verified SSL certificates for HTTPSConnectionPool
 * Response gzip and deflate encoding support
 * Better unicode support for filepost using StringIO buffers

* btoconnor <brian@btoconnor.net>
 * Non-multipart encoding for POST requests
```

* p.dobrogost <http://code.google.com/u/@WBRSRlBZDhBFXQB6/>
 * Code review, PEP8 compliance, benchmark fix

* kennethreitz <me@kennethreitz.com>
 * Bugfixes, suggestions, Requests integration

* georgemarshall <https://github.com/georgemarshall>
 * Bugfixes, Improvements and Test
 coverage

* Thomas Kluyver <thomas@kluyver.me.uk>
 * Python 3 support

* brandon-rhodes <http://rhodesmill.org/brandon>
 * Design review, bugfixes, test coverage.

* studer <theo.studer@gmail.com>
 * IPv6 url support and test coverage

* Shivaram Lingamneni <slingamn@cs.stanford.edu>
 * Support for explicitly closing pooled connections

* hartator <hartator@gmail.com>
 * Corrected multipart behavior for params

* Thomas Weischuh <thomas@t-8ch.de>
 * Support for TLS SNI
 * API unification of ssl_version/cert_reqs
 * SSL fingerprint and alternative hostname verification
 * Bugfixes in testsuite

* Sune Kirkeby <mig@ibofobi.dk>
 * Optional SNI-support for Python 2 via PyOpenSSL.

* Marc Schlaich <marc.schlaich@gmail.com>
 * Various bugfixes and test improvements.

* Bryce Boe <bbzbryce@gmail.com>
 * Correct six.moves conflict
 * Fixed pickle support of some exceptions

* Boris Figovsky <boris.figovsky@ravellosystems.com>
 * Allowed to skip SSL hostname verification

* Cory Benfield
 <https://lukasa.co.uk/about/>
 * Stream method for Response objects.

 * Return native strings in header values.
 * Generate 'Host' header when using proxies.

* Jason Robinson <jaywink@basshero.org>
 * Add missing WrappedSocket.fileno method in PyOpenSSL

* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
 * Fixed a race condition

* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
 * Added HTTPS (CONNECT) proxy support

* Stephen Holsapple <sholsapp@gmail.com>
 * Added abstraction for granular control of request fields

* Martin von Gagern <Martin.vGagern@gmx.net>
 * Support for non-ASCII header parameters

* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
 * Support for separate connect and request timeouts

* Peter Waller <p@pwaller.net>
 * HTTPResponse.tell() for determining amount received over the wire

* Nipunn Koorapati <nipunn1313@gmail.com>
 * Ignore default ports when comparing hosts for equality

* Danilo @dbrgn <https://dbrgn.ch/>
  * Disabled TLS compression by default on Python 3.2+
 * Disabled TLS compression in pyopenssl contrib module
 * Configurable cipher suites in pyopenssl contrib module

* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
 * Account retries on proxy errors

* Nicolas Delaby <nicolas.delaby@ezeep.com>
 * Use the platform-specific CA certificate locations

* Josh Schneier <https://github.com/jschneier>
 * HTTPHeaderDict and associated tests and docs
 * Bugfixes, docs, test coverage

* Tahia Khan <http://tahia.tk/>
 * Added Timeout examples in docs

* Arthur Grunseid <https://grunseid.com>
 * source_address support and tests (with https://github.com/bui)

* Ian Cordasco <graffatcolmingov@gmail.com>
 * PEP8 Compliance and Linting
 * Add ability to pass socket options to an HTTP Connection

* Erik Tollerud <erik.tollerud@gmail.com>
 * Support for standard library io module.

* Krishna Prasad <kprasad.iitd@gmail.com>
 * Google App Engine documentation

* Aaron Meurer
 <asmeurer@gmail.com>
 * Added Url.url, which unparses a Url

* Evgeny Kapun <abacabadabacaba@gmail.com>
 * Bugfixes

* Benjamen Meyer <bm_witness@yahoo.com>
 * Security Warning Documentation update for proper capture

* Shivan Sornarajah <github@sornars.com>
 * Support for using ConnectionPool and PoolManager as context managers.

* Alex Gaynor <alex.gaynor@gmail.com>
 * Updates to the default SSL configuration

* Tomas Tomecek <ttomecek@redhat.com>
 * Implemented generator for getting chunks from chunked responses.

* tlynn <https://github.com/tlynn>
 * Respect the warning preferences at import.

* David D. Riddle <ddriddle@illinois.edu>
 * IPv6 bugfixes in testsuite

* Thea Flowers <magicalgirl@google.com>
 * App Engine environment tests.
 * Documentation re-write.

* John Krauss <https://github.com/talos>
 * Clues to debugging problems with `cryptography` dependency in docs

* Disassem <https://github.com/Disassem>
 * Fix pool-default headers not applying for url-encoded
 requests like GET.

* James Atherfold <jlatherfold@hotmail.com>
 * Bugfixes relating to cleanup of connections during errors.

* Christian Pedersen <https://github.com/chripede>
 * IPv6 HTTPS proxy bugfix

* Jordan Moldow <https://github.com/jmoldow>
 * Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
 * Bugfix for ``ConnectionPool.urlopen(release_conn=False)``.
 * Creation of ``HTTPConnectionPool.ResponseCls``.

* Predrag Gruevski <https://github.com/obi1kenobi>
 * Made cert digest comparison use a constant-time algorithm.

* Adam Talsma <https://github.com/a-tal>
 * Bugfix to ca_cert file paths.

* Evan Meagher <https://evanmeagher.net>
 * Bugfix related to `memoryview` usage in PyOpenSSL adapter

* John Vandenberg <jayvdb@gmail.com>
 * Python 2.6 fixes; pyflakes and pep8 compliance

* Andy Caldwell <andy.m.caldwell@googlemail.com>
 * Bugfix related to reusing connections in indeterminate states.

* Ville Skytt <ville.skytta@iki.fi>
 * Logging efficiency
 improvements, spelling fixes, Travis config.

* Shige Takeda <smtakeda@gmail.com>
 * Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response

* Jess Shapiro <jesse@jesseshapiro.net>
 * Various character-encoding fixes/tweaks
 * Disabling IPv6 DNS when IPv6 connections not supported

* David Foster <http://dafoster.net/>
 * Ensure order of request and response headers are preserved.

* Jeremy Cline <jeremy@jcline.org>
 * Added connection pool keys by scheme

* Aviv Palivoda <palaviv@gmail.com>
 * History list to Retry object.
 * HTTPResponse contains the last Retry object.

* Nate Prewitt <nate.prewitt@gmail.com>
 * Ensure timeouts are not booleans and greater than zero.
 * Fixed infinite loop in ``stream`` when amt=None.

 * Added length_remaining to determine remaining data to be read.
 * Added enforce_content_length to raise exception when incorrect content-length received.

* Seth Michael Larson <sethmichaellarson@protonmail.com>
 * Created selectors backport that supports PEP 475.

* Alexandre Dias <alex.dias@smarkets.com>
 * Don't retry on timeout if method not in whitelist

* Moinuddin Quadri <moin18@gmail.com>
 * Lazily load idna package

* Tom White <s6yg1ez3@mail2tor.com>
 * Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

* Tim Burke <tim.burke@gmail.com>
 * Stop buffering entire deflate-encoded responses.

* Tuukka Mustonen <tuukka.mustonen@gmail.com>
 * Add counter for status_forcelist retries.

* Erik Rose <erik@mozilla.com>
 * Bugfix to pyopenssl vendoring

* Wolfgang Richter <wolfgang.richter@gmail.com>
 * Bugfix related to loading full certificate chains with PyOpenSSL backend.

* Mike Miller <github@mikeage.net>
 * Logging improvements to include the HTTP(S) port when opening a new connection

* Ioannis Tziakos <mail@itziakos.gr>
 * Fix ``util.selectors._fileobj_to_fd`` to accept ``long``.
 * Update appveyor tox setup to use the 64bit python.

* Akamai
 (through Jess Shapiro) <jshapiro@akamai.com>
 * Ongoing maintenance; 2017-2018

* Dominique Leuenberger <dimstar@opensuse.org>
 * Minor fixes in the test suite

* Will Bond <will@wbond.net>
 * Add Python 2.6 support to ``contrib.securetransport``

* Aleksei Alekseev <alekseev.yeskela@gmail.com>
 * using auth info for socks proxy

* Chris Wilcox <git@crwilcox.com>

 * Improve contribution guide
 * Add ``HTTPResponse.geturl`` method to provide ``urllib2.urlopen().geturl()`` behavior

* Bruce Merry <https://www.brucemerry.org.za>
 * Fix leaking exceptions when system calls are interrupted with zero timeout

* Hugo van Kemenade <https://github.com/hugovk>
 * Drop support for EOL Python 2.6

* Tim Bell <https://github.com/timb07>
 * Bugfix for responses with Content-Type: message/* logging warnings

* Justin Bramley <https://github.com/jbramleycl>
 * Add ability to handle multiple Content-Encodings

* Katsuhiko YOSHIDA <https://github.com/kyoshidajp>
 * Remove Authorization header
 regardless of case when redirecting to cross-site

* James Meickle <https://permadeath.com/>
 * Improve handling of Retry-After header

* Chris Jerdonek <chris.jerdonek@gmail.com>
 * Remove a spurious TypeError from the exception chain inside
   HTTPConnectionPool._make_request(), also for BaseExceptions.

* Jorge Lopez Silva <https://github.com/jalopezsilva>
 * Added support for forwarding requests through HTTPS proxies.

* Benno Rice <benno@jeamland.net>
 * Allow cadata parameter to be passed to underlying ``SSLContext.load_verify_locations()``.

* Keiichi Kobayashi <abok.1k@gmail.com>
 * Rename VerifiedHTTPSConnection to HTTPSConnection

* Himanshu Garg <garg_himanshu@outlook.com>
 * DOC & LICENSE Update

* Hod Bin Noon <bin.noon.hod@gmail.com>
 * Test improvements

* Chris Olufson <tycarac@gmail.com>
 * Fix for connection not being released on HTTP redirect and response not preloaded

* [Your name or handle] <[email or website]>
 * [Brief summary of your changes]
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WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE
SOFTWARE.
wOF2OTTOJ @v
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$xs^C_D]&T[OY[(?Al4u|Pl%+_bTpNpg&p,@FAgTk{w}
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid license Element With dir Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a span element with INVALID as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
 ]]></style>
 </head>
 <body>
  If the UA does not display WOFF metadata, the test passes if the word PASS appears below.
  The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test

fails.</p>
 P
</body>
</html>
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wOF2OTTOO @{
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$vsKA7a5O]uqKJRB`#v97
!5}x)0c;/Z">#>IOU9(2IXe
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Two div Elements in text Element</title>

```
 <link rel="author" title="Tal Leming" href="http://typesupply.com" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <link rel="match" href="metadatadisplay-schema-copyright-019-ref.xht" />
 <meta name="assert" content="The copyright element contains a text element that contains two div elements." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face
 {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-copyright-019.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div&gt;
    div 1 start
    &lt;div&gt;
     div 2 start
     &lt;div&gt;
      div 3
     &lt;/div&gt;
     div 2 end
    &lt;/div&gt;
    div 1 end
   &lt;/div&gt;
```

</text>
</copyright>
</metadata>
</pre>
</body>
</html>
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$O\7zCrc.obj
!IF "$(CPU)" == "IA64" || "$(CPU)" == "MIPS" || "$(CPU)" == "ARM" || "$(CPU)" == "ARM64"
C_OBJS = $(C_OBJS) \
!ELSE
ASM_OBJS = $(ASM_OBJS) \
!ENDIF
$O\7zCrcOpt.obj
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At the time of preparation, the original work was available from the texhyphen
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- - - - - - - - - -

Polish hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%

```
% Source: plhyph.tex (1995-06-26)
% Author: Hanna Koodziejska, Bogusaw Jackowski, Marek Ryko
%
% The above mentioned file should become obsolete,
% and the author of the original
 file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%             and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated patterns into TeX Live
%
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%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin
 and \righthyphenmin,
%   they have no influence here and belong elsewhere
```

% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%   http://tug.org/tex-hyphen
%
%---
%
% This is PLHYPH.TeX - the Polish hyphenation patterns
%   version 3.0a, Wednesday, May 17th, 1995
% to be used for the inclusion of Polish hyphenation patterns in any format,
% not necessarily in the MeX or LaMeX ones. The patterns are exactly
% the same as in the version 3.0 being the part of the MeX package,
% only the surrounding of the \pattern command is changed. The authors
% claim the upward compatibility, i.e., the version 3.0a can also be used
% with the MeX or LaMeX formats.

% The history of development of the Polish hyphenation patterns:
%
%
 The first version of the patterns was developed
% by Hanna Koodziejska (1987).
%
% The adaptation to the LeX format (see below) and extensive modification
% were done by Bogusaw Jackowski & Marek Ryko (1987--1989).
%
% The hyphenation rules were further improved and adapted to the
% TeX 3.x requirements by Hanna Koodziejska (1991).
%
% Lone-standing version (3.0a) of patterns was prepared (under pressure
% from LaTeX users) by Bogusaw Jackowski and Marek Ryko, following
% Mariusz Olko's suggestions, 1995.

% The LeX format mentioned above was the first version of the adaptation
% of TeX to the Polish language. The next version is called MeX.
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```
PROG = lzma
CXX = g++
LIB =
RM = rm -f
CFLAGS = -c -O2 -Wall -D_7ZIP_ST

OBJS = \
 LzmaUtil.o \
 Alloc.o \
 LzFind.o \
 LzmaDec.o \
 LzmaEnc.o \
 7zFile.o \
 7zStream.o \


all: $(PROG)

$(PROG): $(OBJS)
 $(CXX) -o $(PROG) $(LDFLAGS) $(OBJS) $(LIB) $(LIB2)

LzmaUtil.o: LzmaUtil.c
 $(CXX) $(CFLAGS) LzmaUtil.c

Alloc.o: ../../Alloc.c
```

```
 $(CXX) $(CFLAGS) ../../Alloc.c

LzFind.o: ../../LzFind.c
 $(CXX) $(CFLAGS) ../../LzFind.c

LzmaDec.o: ../../LzmaDec.c
 $(CXX) $(CFLAGS) ../../LzmaDec.c

LzmaEnc.o: ../../LzmaEnc.c
 $(CXX) $(CFLAGS) ../../LzmaEnc.c

7zFile.o: ../../7zFile.c
 $(CXX) $(CFLAGS) ../../7zFile.c

7zStream.o: ../../7zStream.c
 $(CXX) $(CFLAGS) ../../7zStream.c

clean:
 -$(RM) $(PROG) $(OBJS)
wOF2OTTO< @h
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,wRW]'lDy[%X[&9OXBZ][[hNFIgW9RV\Q#hf]CaMW2m}T8 S]#
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in license Element text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-018-ref.xht" />
  <meta name="assert" content="The license element contains a text element with an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
```

```
 {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-018.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid copyright Element With dir Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with
INVALID as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
```

```
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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```
# -*- Mode: python; indent-tabs-mode: nil; tab-width: 40 -*-
# vim: set filetype=python:
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with Files('**'):
  BUG_COMPONENT = ('Core', 'General')

with Files('7zstub/**'):
  BUG_COMPONENT = ('Firefox', 'Installer')

with Files('atk-1.0/**'):
  BUG_COMPONENT = ('Core', 'Disability Access APIs')
```

```
with Files('bsdiff/**'):
  BUG_COMPONENT = ('Core', 'XPCOM')

with Files('ia2/**'):
  BUG_COMPONENT = ('Core', 'Disability Access APIs')

with Files('nsis/**'):
  BUG_COMPONENT = ('Firefox', 'Installer')

with Files('ply/**'):
  BUG_COMPONENT = ('Core', 'JavaScript Engine')

with Files('snappy/**'):
  BUG_COMPONENT = ('Core', 'XPCOM')
```

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With class Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a span element with "class1 class2 class3" as the value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {

```
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span class="class1 class2 class3"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
```

```
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid copyright Element With dir Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-013-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element with INVALID as the value for the
dir attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-013.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
```


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wOF2OTTOH @t
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,QJI>?FDcnor?}U,Y6/qXFD L%O6{z;#RqczkN#FE 9|Es
o

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element Without id Attribute</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-003-ref.xht" />
  <meta name="assert" content="The license element does not have an id attribute but it still matches the schema." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-003.woff2") format("woff2");
```

```
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a span element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
```

<text>
Text with span.
</text>
</license>
</metadata>
</pre>
</body>
</html>
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ht-internal.h:
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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Child Element in copyright Element text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-016-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element with an unknown child element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-016.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
```

```
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Child Element in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-012-ref.xht" />
  <meta name="assert" content="The license element contains an unknown child element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-012.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
```

wOF2OTTO; @g
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,wYPEOX2zP|0s(VT_o6 #bqG{'()
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in div Element in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a div element with rtl as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  Test passes if the word PASS appears below.
  The Extended Metadata Block is valid and may be displayed to the user upon request.
  P
  The XML contained in the Extended Metadata Block is below.
  <pre>
<?xml version="1.0" encoding="UTF-8"?>
<metadata version="1.0">

<license url="http://w3c.org/Fonts">
 <text>
  <div dir="rtl">Paragraph 1</div>
 </text>
</license>
</metadata>
 </pre>
</body>
</html>
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With class Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-023-ref.xht" />

```
  <meta name="assert" content="The copyright element contains a text element that contains a div element with
&amp;quot;class1 class2 class3&amp;quot; as the
 value for the class attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-023.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&amp;lt;?xml version="1.0" encoding="UTF-8"?&amp;gt;
&amp;lt;metadata version="1.0"&amp;gt;
 &amp;lt;copyright&amp;gt;
  &amp;lt;text&amp;gt;
   &amp;lt;div class="class1 class2 class3"&amp;gt;Paragraph 1&amp;lt;/div&amp;gt;
  &amp;lt;/text&amp;gt;
 &amp;lt;/copyright&amp;gt;
&amp;lt;/metadata&amp;gt;
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One No Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
```

```
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-004-ref.xht" />
  <meta name="assert" content="The license element matches the schema. It contains one text element that does not
have a language tag." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-004.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2 and above				
2.1.1		2001-now	PSF	yes

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element Without url Attribute</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element does not have a url attribute but it still matches the schema." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
  12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
```

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
</body>
</html>
wOF2OTTOW @
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,,C3*

CaY^tnB*sZFM'aZ1v*s_lLz7H]pGgyfPBJ8
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-005-ref.xht" />
  <meta name="assert" content="The license element matches the schema. It contains one text element that has a
language tag." />
```

```
 <style type="text/css"><![CDATA[
  @import
 url("support/test-fonts.css");
  @font-face {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-005.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text xml:lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in span Element in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />

```
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-030-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a span element with ltr as
the value for the dir attribute."
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-030.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span dir="ltr"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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<?xml version="1.0" encoding="utf-8"?>
<Project DefaultTargets="Build" ToolsVersion="15.0"
xmlns="http://schemas.microsoft.com/developer/msbuild/2003">
 <ItemGroup Label="ProjectConfigurations">
  <ProjectConfiguration Include="Release|Win32">
   <Configuration>Release</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
 </ItemGroup>
 <PropertyGroup Label="Globals">
  <VCProjectVersion>15.0</VCProjectVersion>
  <ProjectGuid>{5058AAED-D02A-4F86-B011-31516AB5CD63}</ProjectGuid>
  <Keyword>Win32Proj</Keyword>
  <RootNamespace>BitsUtils</RootNamespace>
  <WindowsTargetPlatformVersion>10.0.15063.0</WindowsTargetPlatformVersion>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.Default.props" />

```
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <UseDebugLibraries>false</UseDebugLibraries>
  <PlatformToolset>v141</PlatformToolset>
   <WholeProgramOptimization>true</WholeProgramOptimization>
  <CharacterSet>Unicode</CharacterSet>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.props" />
 <ImportGroup Label="ExtensionSettings">
 </ImportGroup>
 <ImportGroup Label="Shared">
 </ImportGroup>
 <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
 </ImportGroup>
 <PropertyGroup Label="UserMacros" />
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'" />
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <ClCompile>
   <PrecompiledHeader>NotUsing</PrecompiledHeader>
   <WarningLevel>Level3</WarningLevel>
   <Optimization>MaxSpeed</Optimization>
   <FunctionLevelLinking>true</FunctionLevelLinking>
    <IntrinsicFunctions>true</IntrinsicFunctions>
   <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>WINVER=0x601;_WIN32_WINNT=0x601;WIN32;NDEBUG;CANCELBITSJOBS_E
XPORTS;_WINDOWS;_USRDLL;%(PreprocessorDefinitions)</PreprocessorDefinitions>
   <RuntimeLibrary>MultiThreaded</RuntimeLibrary>
   <BufferSecurityCheck>false</BufferSecurityCheck>
   <OmitDefaultLibName>true</OmitDefaultLibName>
   <ExceptionHandling>false</ExceptionHandling>
  </ClCompile>
  <Link>
   <SubSystem>Windows</SubSystem>
   <EnableCOMDATFolding>true</EnableCOMDATFolding>
   <OptimizeReferences>true</OptimizeReferences>
   <GenerateDebugInformation>false</GenerateDebugInformation>
   <EntryPointSymbol>DllMain</EntryPointSymbol>
  </Link>
 </ItemDefinitionGroup>
 <ItemGroup>
  <ClCompile Include="BitsUtils.cpp" />
 </ItemGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.targets" />
 <ImportGroup Label="ExtensionTargets">
 </ImportGroup>
</Project>
```

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
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1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3	2.1.2	2002	PSF	yes
2.2 and above				
2.1.1		2001-now	PSF	yes

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%
% The used patgen parameters are
%
%   1 1 | 2 5 | 1 1 1
%   2 2 | 2 5 | 1 2 1
%   3 3 | 2 6 | 1 1 1
%   4 4 | 2 6 | 1 4 1
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LZMA specification (DRAFT version)

Author: Igor Pavlov
Date: 2015-06-14

This specification defines the format of LZMA compressed data and lzma file format.

Notation

We use the syntax of C++ programming language.
We use the following types in C++ code:

```
unsigned - unsigned integer, at least 16 bits in size
int      - signed integer, at least 16 bits in size
UInt64   - 64-bit unsigned integer
UInt32   - 32-bit unsigned integer
UInt16   - 16-bit unsigned integer
Byte     - 8-bit unsigned integer
bool     - boolean type with two possible values: false, true
```

lzma file format
===

The lzma file contains the raw LZMA stream and the header with related properties.

The files in that format use ".lzma" extension.

The lzma file format layout:

Offset Size Description

```
 0     1   LZMA model properties (lc, lp, pb) in encoded form
 1     4   Dictionary size (32-bit unsigned integer, little-endian)
 5     8   Uncompressed
size (64-bit unsigned integer, little-endian)
13         Compressed data (LZMA stream)
```

LZMA properties:

```
  name  Range         Description

    lc  [0, 8]        the number of "literal context" bits
    lp  [0, 4]        the number of "literal pos" bits
    pb  [0, 4]        the number of "pos" bits
```

dictSize [0, 2^32 - 1] the dictionary size

The following code encodes LZMA properties:

```
void EncodeProperties(Byte *properties)
{
 properties[0] = (Byte)((pb * 5 + lp) * 9 + lc);
 Set_UInt32_LittleEndian(properties + 1, dictSize);
}
```

If the value of dictionary size in properties is smaller than (1 << 12),
the LZMA decoder must set the dictionary size variable to (1 << 12).

```
#define LZMA_DIC_MIN (1 << 12)

 unsigned lc, pb, lp;
 UInt32 dictSize;
 UInt32 dictSizeInProperties;

 void DecodeProperties(const Byte *properties)
 {
   unsigned d = properties[0];
   if (d >= (9 * 5 * 5))
     throw "Incorrect LZMA properties";
   lc = d % 9;
   d /= 9;
   pb = d / 5;
    lp = d % 5;
   dictSizeInProperties = 0;
   for (int i = 0; i < 4; i++)
     dictSizeInProperties |= (UInt32)properties[i + 1] << (8 * i);
   dictSize = dictSizeInProperties;
   if (dictSize < LZMA_DIC_MIN)
     dictSize = LZMA_DIC_MIN;
 }
```

If "Uncompressed size" field contains ones in all 64 bits, it means that
uncompressed size is unknown and there is the "end marker" in stream,
that indicates the end of decoding point.
In opposite case, if the value from "Uncompressed size" field is not
equal to ((2^64) - 1), the LZMA stream decoding must be finished after
specified number of bytes (Uncompressed size) is decoded. And if there
is the "end marker", the LZMA decoder must read that marker also.

The new scheme to encode LZMA properties

If LZMA compression is used for some another format, it's recommended to
use a new improved scheme to encode LZMA properties. That new scheme was
used in xz format that uses the LZMA2 compression algorithm.
The
LZMA2 is a new compression algorithm that is based on the LZMA algorithm.

The dictionary size in LZMA2 is encoded with just one byte and LZMA2 supports
only reduced set of dictionary sizes:
(2 << 11), (3 << 11),
(2 << 12), (3 << 12),
...
(2 << 30), (3 << 30),
(2 << 31) - 1

The dictionary size can be extracted from encoded value with the following code:

```
dictSize = (p == 40) ? 0xFFFFFFFF : (((UInt32)2 | ((p) & 1)) << ((p) / 2 + 11));
```

Also there is additional limitation (lc + lp <= 4) in LZMA2 for values of
"lc" and "lp" properties:

```
if (lc + lp > 4)
  throw "Unsupported properties: (lc + lp) > 4";
```

There are some advantages for LZMA decoder with such (lc + lp) value
limitation. It reduces the maximum size of tables allocated by decoder.
And it reduces the complexity of initialization procedure, that can be
important to keep high speed of decoding of big number of small LZMA streams.

It's recommended to use that limitation (lc + lp <= 4) for any new format
that
uses LZMA compression. Note that the combinations of "lc" and "lp"
parameters, where (lc + lp > 4), can provide significant improvement in
compression ratio only in some rare cases.

The LZMA properties can be encoded into two bytes in new scheme:

Offset Size Description

0	1	The dictionary size encoded with LZMA2 scheme
1	1	LZMA model properties (lc, lp, pb) in encoded form

The RAM usage
==========

The RAM usage for LZMA decoder is determined by the following parts:

1) The Sliding Window (from 4 KiB to 4 GiB).
2) The probability model counter arrays (arrays of 16-bit variables).
3) Some additional state variables (about 10 variables of 32-bit integers).

The RAM usage for Sliding Window

There are two main scenarios of decoding:

1) The decoding of full stream to one RAM buffer.

If we decode full LZMA stream to one output buffer in RAM, the decoder can use that output buffer as sliding window. So the decoder doesn't

need additional buffer allocated for sliding window.

2) The decoding to some external storage.

If we decode LZMA stream to external storage, the decoder must allocate the buffer for sliding window. The size of that buffer must be equal or larger than the value of dictionary size from properties of LZMA stream.

In this specification we describe the code for decoding to some external storage. The optimized version of code for decoding of full stream to one output RAM buffer can require some minor changes in code.

The RAM usage for the probability model counters

The size of the probability model counter arrays is calculated with the following formula:

size_of_prob_arrays = 1846 + 768 * (1 << (lp + lc))

Each probability model counter is 11-bit unsigned integer.
If we use 16-bit integer variables (2-byte integers) for these probability model counters, the RAM usage required by probability model counter arrays can be
estimated with the following formula:

RAM = 4 KiB + 1.5 KiB * (1 << (lp + lc))

For example, for default LZMA parameters (lp = 0 and lc = 3), the RAM usage is

RAM_lc3_lp0 = 4 KiB + 1.5 KiB * 8 = 16 KiB

The maximum RAM state usage is required for decoding the stream with lp = 4
and lc = 8:

RAM_lc8_lp4 = 4 KiB + 1.5 KiB * 4096 = 6148 KiB

If the decoder uses LZMA2's limited property condition
(lc + lp <= 4), the RAM usage will be not larger than

RAM_lc_lp_4 = 4 KiB + 1.5 KiB * 16 = 28 KiB

The RAM usage for encoder

There are many variants for LZMA encoding code.
These variants have different values for memory consumption.
Note that memory consumption for LZMA Encoder can not be
smaller than memory consumption of LZMA Decoder for same stream.

The RAM usage required by modern effective implementation of
LZMA Encoder can be estimated with the following formula:

Encoder_RAM_Usage = 4 MiB + 11 * dictionarySize.

But there are some modes of the
encoder that require less memory.

LZMA Decoding
==========

The LZMA compression algorithm uses LZ-based compression with Sliding Window
and Range Encoding as entropy coding method.

Sliding Window

LZMA uses Sliding Window compression similar to LZ77 algorithm.

LZMA stream must be decoded to the sequence that consists
of MATCHES and LITERALS:

- a LITERAL is a 8-bit character (one byte).
  The decoder just puts that LITERAL to the uncompressed stream.

- a MATCH is a pair of two numbers (DISTANCE-LENGTH pair).
  The decoder takes one byte exactly "DISTANCE" characters behind

current position in the uncompressed stream and puts it to
uncompressed stream. The decoder must repeat it "LENGTH" times.

The "DISTANCE" can not be larger than dictionary size.
And the "DISTANCE" can not be larger than the number of bytes in
the uncompressed stream that were decoded before that match.

In this specification we use cyclic buffer to implement Sliding Window
for
LZMA decoder:

```
class COutWindow
{
 Byte *Buf;
 UInt32 Pos;
 UInt32 Size;
 bool IsFull;

public:
 unsigned TotalPos;
 COutStream OutStream;

 COutWindow(): Buf(NULL) {}
 ~COutWindow() { delete []Buf; }

 void Create(UInt32 dictSize)
 {
  Buf = new Byte[dictSize];
  Pos = 0;
  Size = dictSize;
  IsFull = false;
  TotalPos = 0;
 }

 void PutByte(Byte b)
 {
  TotalPos++;
  Buf[Pos++] = b;
  if (Pos == Size)
  {
    Pos = 0;
    IsFull = true;
  }
  OutStream.WriteByte(b);
 }

 Byte GetByte(UInt32 dist) const
 {
```

```
  return Buf[dist <= Pos ? Pos - dist : Size - dist + Pos];
 }

 void CopyMatch(UInt32 dist, unsigned len)
 {
  for (; len > 0; len--)
    PutByte(GetByte(dist));
 }

 bool CheckDistance(UInt32 dist) const
 {
  return dist <= Pos || IsFull;
 }

 bool IsEmpty() const
 {
  return Pos == 0 && !IsFull;
 }
};
```

In another implementation it's possible to use one buffer that contains
Sliding Window and the whole data
 stream after uncompressing.

Range Decoder

LZMA algorithm uses Range Encoding (1) as entropy coding method.

LZMA stream contains just one very big number in big-endian encoding.
LZMA decoder uses the Range Decoder to extract a sequence of binary
symbols from that big number.

The state of the Range Decoder:

```
struct CRangeDecoder
{
 UInt32 Range;
 UInt32 Code;
 InputStream *InStream;

 bool Corrupted;
}
```

The notes about UInt32 type for the "Range" and "Code" variables:

It's possible to use 64-bit (unsigned or signed) integer type
for the "Range" and the "Code" variables instead of 32-bit unsigned,
but some additional code must be used to truncate the values to
low 32-bits after some operations.

If the programming language does not support 32-bit unsigned integer type
(like in case of JAVA language), it's possible to use 32-bit signed integer,
but some code must be changed. For example, it's required to change the code
that uses comparison operations
for UInt32 variables in this specification.

The Range Decoder can be in some states that can be treated as
"Corruption" in LZMA stream. The Range Decoder uses the variable "Corrupted":

(Corrupted == false), if the Range Decoder has not detected any corruption.
(Corrupted == true), if the Range Decoder has detected some corruption.

The reference LZMA Decoder ignores the value of the "Corrupted" variable.
So it continues to decode the stream, even if the corruption can be detected
in the Range Decoder. To provide the full compatibility with output of the
reference LZMA Decoder, another LZMA Decoder implementations must also
ignore the value of the "Corrupted" variable.

The LZMA Encoder is required to create only such LZMA streams, that will not
lead the Range Decoder to states, where the "Corrupted" variable is set to true.

The Range Decoder reads first 5 bytes from input stream to initialize
the state:

```
bool CRangeDecoder::Init()
{
 Corrupted = false;
 Range = 0xFFFFFFFF;
  Code = 0;

 Byte b = InStream->ReadByte();

 for (int i = 0; i < 4; i++)
   Code = (Code << 8) | InStream->ReadByte();

 if (b != 0 || Code == Range)
   Corrupted = true;
 return b == 0;
}
```

The LZMA Encoder always writes ZERO in initial byte of compressed stream.
That scheme allows to simplify the code of the Range Encoder in the
LZMA Encoder. If initial byte is not equal to ZERO, the LZMA Decoder must

stop decoding and report error.

After the last bit of data was decoded by Range Decoder, the value of the "Code" variable must be equal to 0. The LZMA Decoder must check it by calling the IsFinishedOK() function:

```
 bool IsFinishedOK() const { return Code == 0; }
```

If there is corruption in data stream, there is big probability that the "Code" value will be not equal to 0 in the Finish() function. So that check in the IsFinishedOK() function provides very good feature for corruption detection.

The value of the "Range" variable before each bit decoding can not be smaller

than ((UInt32)1 << 24). The Normalize() function keeps the "Range" value in described range.

```
#define kTopValue ((UInt32)1 << 24)

void CRangeDecoder::Normalize()
{
 if (Range < kTopValue)
 {
   Range <<= 8;
   Code = (Code << 8) | InStream->ReadByte();
 }
}
```

Notes: if the size of the "Code" variable is larger than 32 bits, it's required to keep only low 32 bits of the "Code" variable after the change in Normalize() function.

If the LZMA Stream is not corrupted, the value of the "Code" variable is always smaller than value of the "Range" variable.
But the Range Decoder ignores some types of corruptions, so the value of the "Code" variable can be equal or larger than value of the "Range" variable for some "Corrupted" archives.

LZMA uses Range Encoding only with binary symbols of two types:
 1) binary symbols with fixed and equal probabilities (direct bits)
 2) binary symbols with predicted probabilities

The DecodeDirectBits() function decodes the sequence of direct bits:

UInt32

```
 CRangeDecoder::DecodeDirectBits(unsigned numBits)
{
 UInt32 res = 0;
 do
 {
   Range >>= 1;
   Code -= Range;
   UInt32 t = 0 - ((UInt32)Code >> 31);
   Code += Range & t;

   if (Code == Range)
     Corrupted = true;

   Normalize();
   res <<= 1;
   res += t + 1;
 }
 while (--numBits);
 return res;
}
```

The Bit Decoding with Probability Model

The task of Bit Probability Model is to estimate probabilities of binary
symbols. And then it provides the Range Decoder with that information.
The better prediction provides better compression ratio.
The Bit Probability Model uses statistical data of previous decoded
symbols.

That estimated probability is presented as 11-bit unsigned integer value
that represents the probability of symbol "0".

```
#define kNumBitModelTotalBits 11
```

Mathematical probabilities can be presented with the following formulas:

```
    probability(symbol_0) = prob / 2048.
    probability(symbol_1) =
  1 - Probability(symbol_0) =
                    =  1 - prob / 2048 =
                    =  (2048 - prob) / 2048
```

where the "prob" variable contains 11-bit integer probability counter.

It's recommended to use 16-bit unsigned integer type, to store these 11-bit
probability values:

```
typedef UInt16 CProb;
```

Each probability value must be initialized with value ((1 << 11) / 2),
that represents the state, where probabilities of symbols 0 and 1
are equal to 0.5:

```
#define PROB_INIT_VAL ((1 << kNumBitModelTotalBits) / 2)
```

The INIT_PROBS macro is used to initialize the array of CProb variables:

```
#define INIT_PROBS(p) \
{ for (unsigned i = 0; i < sizeof(p) / sizeof(p[0]); i++) p[i] = PROB_INIT_VAL; }
```

The DecodeBit() function decodes one bit.
The LZMA decoder provides the pointer to CProb variable that contains
information about estimated probability for symbol 0 and the Range Decoder
updates that CProb variable after decoding. The Range Decoder increases
estimated probability
of the symbol that was decoded:

```
#define kNumMoveBits 5

unsigned CRangeDecoder::DecodeBit(CProb *prob)
{
 unsigned v = *prob;
 UInt32 bound = (Range >> kNumBitModelTotalBits) * v;
 unsigned symbol;
 if (Code < bound)
 {
   v += ((1 << kNumBitModelTotalBits) - v) >> kNumMoveBits;
   Range = bound;
   symbol = 0;
 }
 else
 {
   v -= v >> kNumMoveBits;
   Code -= bound;
   Range -= bound;
   symbol = 1;
 }
 *prob = (CProb)v;
 Normalize();
 return symbol;
}
```

The Binary Tree of bit model counters

LZMA uses a tree of Bit model variables to decode symbol that needs
several bits for storing. There are two versions of such trees in LZMA:
 1) the tree that decodes bits from high bit to low bit (the normal scheme).
 2) the tree that decodes bits from low bit to high bit (the reverse scheme).

Each binary tree structure supports different size of decoded symbol
(the size of binary sequence that contains value of symbol).
If that size
 of decoded symbol is "NumBits" bits, the tree structure
uses the array of (2 << NumBits) counters of CProb type.
But only ((2 << NumBits) - 1) items are used by encoder and decoder.
The first item (the item with index equal to 0) in array is unused.
That scheme with unused array's item allows to simplify the code.

```
unsigned BitTreeReverseDecode(CProb *probs, unsigned numBits, CRangeDecoder *rc)
{
 unsigned m = 1;
 unsigned symbol = 0;
 for (unsigned i = 0; i < numBits; i++)
 {
  unsigned bit = rc->DecodeBit(&probs[m]);
  m <<= 1;
  m += bit;
  symbol |= (bit << i);
 }
 return symbol;
}

template <unsigned NumBits>
class CBitTreeDecoder
{
 CProb Probs[(unsigned)1 << NumBits];

public:

 void Init()
 {
  INIT_PROBS(Probs);
 }

 unsigned Decode(CRangeDecoder *rc)
 {
  unsigned m = 1;
  for (unsigned i = 0; i < NumBits; i++)
    m = (m << 1) + rc->DecodeBit(&Probs[m]);
  return m - ((unsigned)1 << NumBits);
 }
```

```
 unsigned ReverseDecode(CRangeDecoder *rc)
  {
   return BitTreeReverseDecode(Probs, NumBits, rc);
 }
};
```

LZ part of LZMA

LZ part of LZMA describes details about the decoding of MATCHES and LITERALS.

The Literal Decoding

The LZMA Decoder uses (1 << (lc + lp)) tables with CProb values, where
each table contains 0x300 CProb values:

```
 CProb *LitProbs;

 void CreateLiterals()
 {
   LitProbs = new CProb[(UInt32)0x300 << (lc + lp)];
 }

 void InitLiterals()
 {
   UInt32 num = (UInt32)0x300 << (lc + lp);
   for (UInt32 i = 0; i < num; i++)
     LitProbs[i] = PROB_INIT_VAL;
 }
```

To select the table for decoding it uses the context that consists of
(lc) high bits from previous literal and (lp) low bits from value that
represents current position in outputStream.

If (State > 7), the Literal Decoder also uses "matchByte" that represents
the byte in OutputStream at position the is the DISTANCE bytes before
current position, where the DISTANCE is the distance in DISTANCE-LENGTH
 pair
of latest decoded match.

The following code decodes one literal and puts it to Sliding Window buffer:

```
 void DecodeLiteral(unsigned state, UInt32 rep0)
 {
```

```
  unsigned prevByte = 0;
  if (!OutWindow.IsEmpty())
    prevByte = OutWindow.GetByte(1);

  unsigned symbol = 1;
  unsigned litState = ((OutWindow.TotalPos & ((1 << lp) - 1)) << lc) + (prevByte >> (8 - lc));
  CProb *probs = &LitProbs[(UInt32)0x300 * litState];

  if (state >= 7)
  {
    unsigned matchByte = OutWindow.GetByte(rep0 + 1);
    do
    {
      unsigned matchBit = (matchByte >> 7) & 1;
      matchByte <<= 1;
      unsigned bit = RangeDec.DecodeBit(&probs[((1 + matchBit) << 8) + symbol]);
      symbol = (symbol << 1) | bit;
      if (matchBit != bit)
        break;
    }
    while (symbol < 0x100);
  }
  while (symbol < 0x100)
    symbol = (symbol << 1) | RangeDec.DecodeBit(&probs[symbol]);
  OutWindow.PutByte((Byte)(symbol - 0x100));
 }
```

The match length decoding

The
 match length decoder returns normalized (zero-based value)
length of match. That value can be converted to real length of the match
with the following code:

```
#define kMatchMinLen 2

  matchLen = len + kMatchMinLen;
```

The match length decoder can return the values from 0 to 271.
And the corresponded real match length values can be in the range
from 2 to 273.

The following scheme is used for the match length encoding:

Binary encoding sequence	Binary Tree structure (binary + decimal):	Zero-based match length

```
 0 xxx            LowCoder[posState]      xxx
 1 0 yyy          MidCoder[posState]      yyy + 8
 1 1 zzzzzzzz     HighCoder               zzzzzzzz + 16
```

LZMA uses bit model variable "Choice" to decode the first selection bit.

If the first selection bit is equal to 0, the decoder uses binary tree
LowCoder[posState] to decode 3-bit zero-based match length (xxx).

If the first selection
bit is equal to 1, the decoder uses bit model
variable "Choice2" to decode the second selection bit.

If the second selection bit is equal to 0, the decoder uses binary tree
MidCoder[posState] to decode 3-bit "yyy" value, and zero-based match
length is equal to (yyy + 8).

If the second selection bit is equal to 1, the decoder uses binary tree
HighCoder to decode 8-bit "zzzzzzzz" value, and zero-based
match length is equal to (zzzzzzzz + 16).

LZMA uses "posState" value as context to select the binary tree
from LowCoder and MidCoder binary tree arrays:

```
  unsigned posState = OutWindow.TotalPos & ((1 << pb) - 1);
```

The full code of the length decoder:

```
class CLenDecoder
{
 CProb Choice;
 CProb Choice2;
 CBitTreeDecoder<3> LowCoder[1 << kNumPosBitsMax];
 CBitTreeDecoder<3> MidCoder[1 << kNumPosBitsMax];
 CBitTreeDecoder<8> HighCoder;

public:

 void Init()
 {
  Choice = PROB_INIT_VAL;
  Choice2 = PROB_INIT_VAL;
  HighCoder.Init();
  for (unsigned
 i = 0; i < (1 << kNumPosBitsMax); i++)
  {
    LowCoder[i].Init();
```

```
    MidCoder[i].Init();
  }
 }

 unsigned Decode(CRangeDecoder *rc, unsigned posState)
 {
  if (rc->DecodeBit(&Choice) == 0)
    return LowCoder[posState].Decode(rc);
  if (rc->DecodeBit(&Choice2) == 0)
    return 8 + MidCoder[posState].Decode(rc);
  return 16 + HighCoder.Decode(rc);
 }
};
```

The LZMA decoder uses two instances of CLenDecoder class.
The first instance is for the matches of "Simple Match" type,
and the second instance is for the matches of "Rep Match" type:

```
 CLenDecoder LenDecoder;
 CLenDecoder RepLenDecoder;
```

The match distance decoding

LZMA supports dictionary sizes up to 4 GiB minus 1.
The value of match distance (decoded by distance decoder) can be
from 1 to 2^32. But the distance value that is equal to 2^32 is used to
indicate the "End of stream" marker. So real largest match distance
that is used for LZ-window match is (2^32 - 1).

LZMA uses
 normalized match length (zero-based length)
to calculate the context state "lenState" do decode the distance value:

```
#define kNumLenToPosStates 4

  unsigned lenState = len;
  if (lenState > kNumLenToPosStates - 1)
    lenState = kNumLenToPosStates - 1;
```

The distance decoder returns the "dist" value that is zero-based value
of match distance. The real match distance can be calculated with the
following code:

```
 matchDistance = dist + 1;
```

The state of the distance decoder and the initialization code:

```
#define kEndPosModelIndex 14
#define kNumFullDistances (1 << (kEndPosModelIndex >> 1))
#define kNumAlignBits 4

CBitTreeDecoder<6> PosSlotDecoder[kNumLenToPosStates];
CProb PosDecoders[1 + kNumFullDistances - kEndPosModelIndex];
CBitTreeDecoder<kNumAlignBits> AlignDecoder;

void InitDist()
{
  for (unsigned i = 0; i < kNumLenToPosStates; i++)
    PosSlotDecoder[i].Init();
  AlignDecoder.Init();
  INIT_PROBS(PosDecoders);
}
```

At first stage the distance
decoder decodes 6-bit "posSlot" value with bit
tree decoder from PosSlotDecoder array. It's possible to get 2^6=64 different
"posSlot" values.

```
  unsigned posSlot = PosSlotDecoder[lenState].Decode(&RangeDec);
```

The encoding scheme for distance value is shown in the following table:

posSlot (decimal) /
zero-based distance (binary)

0 0
1 1
2 10
3 11

4 10 x
5 11 x
6 10 xx
7 11 xx
8 10 xxx
9 11 xxx
10 10 xxxx
11 11 xxxx
12 10 xxxxx
13 11 xxxxx

14 10 yy zzzz
15 11 yy zzzz
16 10 yyy zzzz
17 11 yyy zzzz

```
...
62    10 yyyyyyyyyyyyyyyyyyyyyyyyy zzzz
63    11 yyyyyyyyyyyyyyyyyyyyyyyyy zzzz
```

where
"x ... x" means the sequence of binary symbols encoded with binary tree and
"Reverse" scheme. It uses separated binary tree for each posSlot from 4 to 13.
"y" means direct bit encoded with range coder.
"zzzz" means the sequence of four binary symbols encoded with binary
tree with "Reverse" scheme, where one
common binary tree "AlignDecoder"
is used for all posSlot values.

If (posSlot < 4), the "dist" value is equal to posSlot value.

If (posSlot >= 4), the decoder uses "posSlot" value to calculate the value of
the high bits of "dist" value and the number of the low bits.

If (4 <= posSlot < kEndPosModelIndex), the decoder uses bit tree decoders.
(one separated bit tree decoder per one posSlot value) and "Reverse" scheme.
In this implementation we use one CProb array "PosDecoders" that contains
all CProb variables for all these bit decoders.

if (posSlot >= kEndPosModelIndex), the middle bits are decoded as direct
bits from RangeDecoder and the low 4 bits are decoded with a bit tree
decoder "AlignDecoder" with "Reverse" scheme.

The code to decode zero-based match distance:

```
unsigned DecodeDistance(unsigned len)
{
  unsigned lenState = len;
  if (lenState > kNumLenToPosStates - 1)
    lenState = kNumLenToPosStates - 1;

  unsigned posSlot
= PosSlotDecoder[lenState].Decode(&RangeDec);
  if (posSlot < 4)
    return posSlot;

  unsigned numDirectBits = (unsigned)((posSlot >> 1) - 1);
  UInt32 dist = ((2 | (posSlot & 1)) << numDirectBits);
  if (posSlot < kEndPosModelIndex)
    dist += BitTreeReverseDecode(PosDecoders + dist - posSlot, numDirectBits, &RangeDec);
  else
  {
    dist += RangeDec.DecodeDirectBits(numDirectBits - kNumAlignBits) << kNumAlignBits;
    dist += AlignDecoder.ReverseDecode(&RangeDec);
```

```
  }
  return dist;
 }
```

LZMA Decoding modes

There are 2 types of LZMA streams:

1) The stream with "End of stream" marker.
2) The stream without "End of stream" marker.

And the LZMA Decoder supports 3 modes of decoding:

1) The unpack size is undefined. The LZMA decoder stops decoding after
  getting "End of stream" marker.
  The input variables for that case:

    markerIsMandatory = true
    unpackSizeDefined = false
    unpackSize contains any value

2)
 The unpack size is defined and LZMA decoder supports both variants,
  where the stream can contain "End of stream" marker or the stream is
  finished without "End of stream" marker. The LZMA decoder must detect
  any of these situations.
  The input variables for that case:

    markerIsMandatory = false
    unpackSizeDefined = true
    unpackSize contains unpack size

3) The unpack size is defined and the LZMA stream must contain
  "End of stream" marker
  The input variables for that case:

    markerIsMandatory = true
    unpackSizeDefined = true
    unpackSize contains unpack size

The main loop of decoder

The main loop of LZMA decoder:

```
Initialize the LZMA state.
loop
{
 // begin of loop
 Check "end of stream" conditions.
 Decode Type of MATCH / LITERAL.
   If it's LITERAL, decode LITERAL value and put the LITERAL to Window.
   If it's MATCH, decode the length of match and the match distance.
      Check error
 conditions, check end of stream conditions and copy
      the sequence of match bytes from sliding window to current position
      in window.
 Go to begin of loop
}
```

The reference implementation of LZMA decoder uses "unpackSize" variable to keep the number of remaining bytes in output stream. So it reduces "unpackSize" value after each decoded LITERAL or MATCH.

The following code contains the "end of stream" condition check at the start of the loop:

```
  if (unpackSizeDefined && unpackSize == 0 && !markerIsMandatory)
    if (RangeDec.IsFinishedOK())
      return LZMA_RES_FINISHED_WITHOUT_MARKER;
```

LZMA uses three types of matches:

1) "Simple Match" - the match with distance value encoded with bit models.

2) "Rep Match" - the match that uses the distance from distance history table.

3) "Short Rep Match" - the match of single byte length, that uses the latest distance from distance history table.

The LZMA decoder keeps the history of latest 4 match distances that were used by decoder. That set of 4 variables contains zero-based match distances and these variables are initialized with zero values:

```
 UInt32 rep0 = 0, rep1 = 0, rep2 = 0, rep3 = 0;
```

The LZMA decoder uses binary model variables to select type of MATCH or LITERAL:

```
#define kNumStates 12
#define kNumPosBitsMax 4
```

```
 CProb IsMatch[kNumStates << kNumPosBitsMax];
 CProb IsRep[kNumStates];
 CProb IsRepG0[kNumStates];
 CProb IsRepG1[kNumStates];
 CProb IsRepG2[kNumStates];
 CProb IsRep0Long[kNumStates << kNumPosBitsMax];
```

The decoder uses "state" variable value to select exact variable
from "IsRep", "IsRepG0", "IsRepG1" and "IsRepG2" arrays.
The "state" variable can get the value from 0 to 11.
Initial value for "state" variable is zero:

```
 unsigned state = 0;
```

The "state" variable is updated after each LITERAL or MATCH with one of the
following functions:

```
unsigned UpdateState_Literal(unsigned state)
{
 if (state < 4) return 0;
  else if (state < 10) return state - 3;
 else return state - 6;
}
unsigned UpdateState_Match   (unsigned state) { return state < 7 ? 7 : 10; }
unsigned UpdateState_Rep     (unsigned state) { return state < 7 ? 8 : 11; }
unsigned UpdateState_ShortRep(unsigned state) { return state < 7 ? 9 : 11; }
```

The decoder calculates "state2" variable value to select exact variable from
"IsMatch" and "IsRep0Long" arrays:

```
unsigned posState = OutWindow.TotalPos & ((1 << pb) - 1);
unsigned state2 = (state << kNumPosBitsMax) + posState;
```

The decoder uses the following code flow scheme to select exact
type of LITERAL or MATCH:

```
IsMatch[state2] decode
 0 - the Literal
 1 - the Match
   IsRep[state] decode
     0 - Simple Match
     1 - Rep Match
       IsRepG0[state] decode
         0 - the distance is rep0
           IsRep0Long[state2] decode
             0 - Short Rep Match
             1 - Rep Match 0
```

```
      1 -
        IsRepG1[state] decode
          0 - Rep Match 1

      1 -
            IsRepG2[state] decode
              0 - Rep Match 2
              1 - Rep Match 3
```

LITERAL symbol

If the value "0" was decoded with IsMatch[state2] decoding, we have "LITERAL" type.

At first the LZMA decoder must check that it doesn't exceed
specified uncompressed size:

```
    if (unpackSizeDefined && unpackSize == 0)
      return LZMA_RES_ERROR;
```

Then it decodes literal value and puts it to sliding window:

```
    DecodeLiteral(state, rep0);
```

Then the decoder must update the "state" value and "unpackSize" value;

```
    state = UpdateState_Literal(state);
    unpackSize--;
```

Then the decoder must go to the begin of main loop to decode next Match or Literal.

Simple Match

If the value "1" was decoded with IsMatch[state2] decoding,
we have the "Simple Match" type.

The distance history table is updated with the following scheme:

```
    rep3 = rep2;
    rep2 = rep1;
    rep1 = rep0;
```

The zero-based length is decoded
with "LenDecoder":

```
    len = LenDecoder.Decode(&RangeDec, posState);
```

The state is update with UpdateState_Match function:

```
    state = UpdateState_Match(state);
```

and the new "rep0" value is decoded with DecodeDistance:

```
    rep0 = DecodeDistance(len);
```

That "rep0" will be used as zero-based distance for current match.

If the value of "rep0" is equal to 0xFFFFFFFF, it means that we have
"End of stream" marker, so we can stop decoding and check finishing
condition in Range Decoder:

```
    if (rep0 == 0xFFFFFFFF)
      return RangeDec.IsFinishedOK() ?
          LZMA_RES_FINISHED_WITH_MARKER :
          LZMA_RES_ERROR;
```

If uncompressed size is defined, LZMA decoder must check that it doesn't
exceed that specified uncompressed size:

```
    if (unpackSizeDefined && unpackSize == 0)
      return LZMA_RES_ERROR;
```

Also the decoder must check that "rep0" value is not larger than dictionary size
and is not larger than the number of already decoded bytes:

```
    if (rep0
 >= dictSize || !OutWindow.CheckDistance(rep0))
      return LZMA_RES_ERROR;
```

Then the decoder must copy match bytes as described in
"The match symbols copying" section.

Rep Match

If the LZMA decoder has decoded the value "1" with IsRep[state] variable,
we have "Rep Match" type.

At first the LZMA decoder must check that it doesn't exceed
specified uncompressed size:

```
    if (unpackSizeDefined && unpackSize == 0)
      return LZMA_RES_ERROR;
```

Also the decoder must return error, if the LZ window is empty:

```
    if (OutWindow.IsEmpty())
      return LZMA_RES_ERROR;
```

If the match type is "Rep Match", the decoder uses one of the 4 variables of
distance history table to get the value of distance for current match.
And there are 4 corresponding ways of decoding flow.

The decoder updates the distance history with the following scheme
depending from type of match:

- "Rep Match 0" or "Short Rep Match":
    ; LZMA doesn't update the distance history

- "Rep Match
 1":
    UInt32 dist = rep1;
    rep1 = rep0;
    rep0 = dist;

- "Rep Match 2":
    UInt32 dist = rep2;
    rep2 = rep1;
    rep1 = rep0;
    rep0 = dist;

- "Rep Match 3":
    UInt32 dist = rep3;
    rep3 = rep2;
    rep2 = rep1;
    rep1 = rep0;
    rep0 = dist;

Then the decoder decodes exact subtype of "Rep Match" using "IsRepG0", "IsRep0Long",
"IsRepG1", "IsRepG2".

If the subtype is "Short Rep Match", the decoder updates the state, puts
the one byte from window to current position in window and goes to next
MATCH/LITERAL symbol (the begin of main loop):

```
        state = UpdateState_ShortRep(state);
        OutWindow.PutByte(OutWindow.GetByte(rep0 + 1));
        unpackSize--;
        continue;
```

In other cases (Rep Match 0/1/2/3), it decodes the zero-based

length of match with "RepLenDecoder" decoder:

```
    len = RepLenDecoder.Decode(&RangeDec, posState);
```

Then it updates the state:

```
    state = UpdateState_Rep(state);
```

Then the decoder must copy
match bytes as described in
"The Match symbols copying" section.

The match symbols copying

If we have the match (Simple Match or Rep Match 0/1/2/3), the decoder must
copy the sequence of bytes with calculated match distance and match length.
If uncompressed size is defined, LZMA decoder must check that it doesn't
exceed that specified uncompressed size:

```
  len += kMatchMinLen;
  bool isError = false;
  if (unpackSizeDefined && unpackSize < len)
  {
    len = (unsigned)unpackSize;
    isError = true;
  }
  OutWindow.CopyMatch(rep0 + 1, len);
  unpackSize -= len;
  if (isError)
    return LZMA_RES_ERROR;
```

Then the decoder must go to the begin of main loop to decode next MATCH or LITERAL.

NOTES

This specification doesn't describe the variant of decoder implementation
that supports partial decoding. Such partial decoding case can require some
changes in "end of stream" condition checks code. Also such code
can use additional
status codes, returned by decoder.

This specification uses C++ code with templates to simplify describing.
The optimized version of LZMA decoder doesn't need templates.

Such optimized version can use just two arrays of CProb variables:
1) The dynamic array of CProb variables allocated for the Literal Decoder.
2) The one common array that contains all other CProb variables.

References:

1. G. N. N. Martin, Range encoding: an algorithm for removing redundancy
from a digitized message, Video & Data Recording Conference,
Southampton, UK, July 24-27, 1979.
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<signature of Ty Coon>, 1 April 1990
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```
PROG = 7zS2con.sfx
MY_FIXED = 1
CFLAGS = $(CFLAGS) -D_CONSOLE

C_OBJS = \
 $O\7zAlloc.obj \
 $O\7zArcIn.obj \
 $O\7zBuf.obj \
 $O\7zBuf2.obj \
 $O\7zCrc.obj \
 $O\7zCrcOpt.obj \
 $O\7zFile.obj \
 $O\7zDec.obj \
 $O\7zStream.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\DllSecur.obj \
 $O\Lzma2Dec.obj \
 $O\LzmaDec.obj \

7Z_OBJS = \
 $O\SfxSetup.obj \

OBJS = \
 $(7Z_OBJS) \
 $(C_OBJS) \
 $O\resource.res

!include "../../../CPP/Build.mak"

$(7Z_OBJS): $(*B).c
 $(COMPL_O1)
$(C_OBJS): ../../$(*B).c
 $(COMPL_O1)
```

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THE SOFTWARE.
wOF2OTTO: @f
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,xs.K^7M
1c/
"3#z=hR+p8nHhF)cR5V|+f]$6LT$<FGUD
This packge contains a modified version of ca-bundle.crt:

ca-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#
This is a bundle of X.509 certificates of public Certificate Authorities
(CA). These were automatically extracted from Mozilla's root certificates
file (certdata.txt). This file can be found in the mozilla source tree:
http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#
It contains the certificates in PEM format and therefore
can be directly used with curl / libcurl / php_curl, or with
an Apache+mod_ssl webserver for SSL client authentication.
Just configure this file as the SSLCACertificateFile.#

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@(#) $RCSfile: certdata.txt,v $
 $Revision: 1.80 $ $Date: 2011/11/03 15:11:58 $
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">

```
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element matches the schema. It contains one text element that has a
language tag." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text xml:lang="en"&gt;
   Copyright with "en" language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
Copyright 2001-2011 Xiph.Org, Skype Limited, Octasic,
                    Jean-Marc Valin, Timothy B. Terriberry,
                    CSIRO, Gregory Maxwell, Mark Borgerding,
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Erik de Castro Lopo

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>

```
  <title>WOFF Test: Valid license Element With class Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-026-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a div element with
&amp;quot;class1 class2 class3&amp;quot; as the value
 for the class attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-026.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&amp;lt;?xml version="1.0" encoding="UTF-8"?&amp;gt;
&amp;lt;metadata version="1.0"&amp;gt;
 &amp;lt;license url="http://w3c.org/Fonts"&amp;gt;
  &amp;lt;text&amp;gt;
   &amp;lt;div class="class1 class2 class3"&amp;gt;Paragraph 1&amp;lt;/div&amp;gt;
  &amp;lt;/text&amp;gt;
 &amp;lt;/license&amp;gt;
&amp;lt;/metadata&amp;gt;
  </pre>
 </body>
</html>
```

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%
%
% The word list is available from
%
%   http://repo.or.cz/w/wortliste.git?a=commit;h=2d246449f5c4f570f4d735d3ad091f6ad70f6972
%
% The used patgen parameters are
%
%   1 1 | 2 5 | 1 1 1
%   2 2 | 2 5 | 1 2 1
%   3 3 | 2 6 | 1 1 1

% 4 4 | 2 6 | 1 4 1
% 5 5 | 2 7 | 1 1 1
% 6 6 | 2 7 | 1 6 1
% 7 7 | 2 13 | 1 4 1
% 8 8 | 2 13 | 1 8 1
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 $O\BenchCon.obj \
 $O\ConsoleClose.obj \
 $O\ExtractCallbackConsole.obj \
 $O\HashCon.obj \
 $O\List.obj \
 $O\Main.obj \
 $O\MainAr.obj \
 $O\OpenCallbackConsole.obj \
 $O\PercentPrinter.obj \
 $O\UpdateCallbackConsole.obj \
 $O\UserInputUtils.obj \

UI_COMMON_OBJS = \
 $O\ArchiveCommandLine.obj \
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 $O\ArchiveOpenCallback.obj \
 $O\Bench.obj \
 $O\DefaultName.obj \
 $O\EnumDirItems.obj \
 $O\Extract.obj \
 $O\ExtractingFilePath.obj \
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 $O\SetProperties.obj \
 $O\SortUtils.obj \
 $O\TempFiles.obj \
 $O\Update.obj \
 $O\UpdateAction.obj \
 $O\UpdateCallback.obj \
 $O\UpdatePair.obj \
 $O\UpdateProduce.obj \

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Unknown Child Element in copyright Element</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
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<link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
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<meta name="assert" content="The copyright element contains an unknown child element." />
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@font-face
{
font-family: "WOFF Test";
src: url("support/metadatadisplay-schema-copyright-009.woff2") format("woff2");
}
body {
font-size: 20px;

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  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
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  <div class="test">P</div>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
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  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-027-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a span element." />
  <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face
 {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-027.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
```

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN
CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.

```
LIBS = $(LIBS) oleaut32.lib ole32.lib

!IFNDEF MY_NO_UNICODE
CFLAGS = $(CFLAGS) -DUNICODE -D_UNICODE
!ENDIF
```

```
# CFLAGS = $(CFLAGS) -FAsc -Fa$O/Asm/

!IFNDEF O
!IFDEF CPU
O=$(CPU)
!ELSE
O=O
!ENDIF
!ENDIF

!IF "$(CPU)" == "AMD64"
MY_ML = ml64 -Dx64 -WX
!ELSEIF "$(CPU)" == "ARM"
MY_ML = armasm -WX
!ELSE
MY_ML = ml -WX
!ENDIF


!IFDEF UNDER_CE
RFLAGS = $(RFLAGS) -dUNDER_CE
!IFDEF MY_CONSOLE
LFLAGS = $(LFLAGS) /ENTRY:mainACRTStartup
!ENDIF
!ELSE
!IFNDEF NEW_COMPILER
LFLAGS = $(LFLAGS) -OPT:NOWIN98
!ENDIF
!IF "$(CPU)" != "ARM" && "$(CPU)" != "ARM64"
CFLAGS = $(CFLAGS) -Gr
!ENDIF
LIBS = $(LIBS) user32.lib advapi32.lib shell32.lib
!ENDIF

!IF "$(CPU)" == "ARM"
COMPL_ASM = $(MY_ML) $** $O/$(*B).obj
!ELSE
COMPL_ASM = $(MY_ML) -c -Fo$O/ $**
!ENDIF

CFLAGS = $(CFLAGS) -nologo -c -Fo$O/ -W4 -WX -EHsc -Gy -GR- -GF

!IFDEF MY_STATIC_LINK
!IFNDEF MY_SINGLE_THREAD
CFLAGS = $(CFLAGS) -MT
!ENDIF
!ELSE
```

```
CFLAGS = $(CFLAGS) -MD
!ENDIF

!IFDEF NEW_COMPILER
CFLAGS = $(CFLAGS) -GS- -Zc:forScope -Zc:wchar_t
!IFNDEF
 UNDER_CE
CFLAGS = $(CFLAGS) -MP2
!IFNDEF CPU
# CFLAGS = $(CFLAGS) -arch:IA32
!ENDIF
!ENDIF
!ELSE
CFLAGS = $(CFLAGS)
!ENDIF

!IF "$(CPU)" == "AMD64"
CFLAGS_O1 = $(CFLAGS) -O1
!ELSE
CFLAGS_O1 = $(CFLAGS) -O1
!ENDIF
CFLAGS_O2 = $(CFLAGS) -O2

LFLAGS = $(LFLAGS) -nologo -OPT:REF -OPT:ICF

!IFNDEF UNDER_CE
LFLAGS = $(LFLAGS) /LARGEADDRESSAWARE
!ENDIF

!IFDEF DEF_FILE
LFLAGS = $(LFLAGS) -DLL -DEF:$(DEF_FILE)
!ELSE
!IF defined(MY_FIXED) && "$(CPU)" != "ARM" && "$(CPU)" != "ARM64"
LFLAGS = $(LFLAGS) /FIXED
!ELSE
LFLAGS = $(LFLAGS) /FIXED:NO
!ENDIF
# /BASE:0x400000
!ENDIF


# !IF "$(CPU)" == "AMD64"

!IFDEF SUB_SYS_VER

MY_SUB_SYS_VER=5.02

!IFDEF MY_CONSOLE
```

```
LFLAGS = $(LFLAGS) /SUBSYSTEM:console,$(MY_SUB_SYS_VER)
!ELSE
LFLAGS = $(LFLAGS) /SUBSYSTEM:windows,$(MY_SUB_SYS_VER)
!ENDIF

!ENDIF


PROGPATH = $O\$(PROG)

COMPL_O1   = $(CC) $(CFLAGS_O1) $**
COMPL_O2   = $(CC) $(CFLAGS_O2) $**
COMPL_PCH  = $(CC) $(CFLAGS_O1) -Yc"StdAfx.h" -Fp$O/a.pch $**
COMPL      = $(CC) $(CFLAGS_O1) -Yu"StdAfx.h" -Fp$O/a.pch
 $**

COMPLB    = $(CC) $(CFLAGS_O1) -Yu"StdAfx.h" -Fp$O/a.pch $<
# COMPLB_O2 = $(CC) $(CFLAGS_O2) -Yu"StdAfx.h" -Fp$O/a.pch $<
COMPLB_O2 = $(CC) $(CFLAGS_O2) $<

CFLAGS_C_ALL = $(CFLAGS_O2) $(CFLAGS_C_SPEC)
CCOMPL_PCH  = $(CC) $(CFLAGS_C_ALL) -Yc"Precomp.h" -Fp$O/a.pch $**
CCOMPL_USE  = $(CC) $(CFLAGS_C_ALL) -Yu"Precomp.h" -Fp$O/a.pch $**
CCOMPL      = $(CC) $(CFLAGS_C_ALL) $**
CCOMPLB     = $(CC) $(CFLAGS_C_ALL) $<


all: $(PROGPATH)

clean:
 -del /Q $(PROGPATH) $O\*.exe $O\*.dll $O\*.obj $O\*.lib $O\*.exp $O\*.res $O\*.pch $O\*.asm

$O:
 if not exist "$O" mkdir "$O"
$O/Asm:
 if not exist "$O/Asm" mkdir "$O/Asm"

$(PROGPATH): $O $O/Asm $(OBJS) $(DEF_FILE)
 link $(LFLAGS) -out:$(PROGPATH) $(OBJS) $(LIBS)

!IFNDEF NO_DEFAULT_RES
$O\resource.res: $(*B).rc
 rc $(RFLAGS) -fo$@ $**
!ENDIF
$O\StdAfx.obj: $(*B).cpp
 $(COMPL_PCH)
```

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{23170F69-40C1-278A-0000-00yy00xx0000}

00 IProgress.h

05 IProgress
// 050002 IProgress2

01 IFolderArchive.h

// 05 IArchiveFolder // old
// 06 IInFolderArchive // old
07 IFileExtractCallback.h::IFolderArchiveExtractCallback
08 IFileExtractCallback.h::IFolderArchiveExtractCallback2
// 0A IOutFolderArchive
0B IFolderArchiveUpdateCallback
0C Agent.h::IArchiveFolderInternal
0D IArchiveFolder
0E IInFolderArchive
0F IOutFolderArchive
10 IFolderArchiveUpdateCallback2
11 IFolderScanProgress

20 IFileExtractCallback.h::IGetProp
30 IFileExtractCallback.h::IFolderExtractToStreamCallback

03 IStream.h

01 ISequentialInStream
02 ISequentialOutStream
03 IInStream
04 IOutStream
06 IStreamGetSize
07 IOutStreamFinish

08 IStreamGetProps
09 IStreamGetProps2

04 ICoder.h

04 ICompressProgressInfo
05 ICompressCoder
18 ICompressCoder2
1F ICompressSetCoderPropertiesOpt
20 ICompressSetCoderProperties
21 ICompressSetDecoderProperties
//
22 ICompressSetDecoderProperties2
23 ICompressWriteCoderProperties
24 ICompressGetInStreamProcessedSize
25 ICompressSetCoderMt
26 ICompressSetFinishMode
27 ICompressGetInStreamProcessedSize2
28 ICompressSetMemLimit

30 ICompressGetSubStreamSize
31 ICompressSetInStream
32 ICompressSetOutStream
// 33 ICompressSetInStreamSize
34 ICompressSetOutStreamSize
35 ICompressSetBufSize
36 ICompressInitEncoder
37 ICompressSetInStream2
// 38 ICompressSetOutStream2
// 39 SetInStreamSize2
// 3A SetOutStreamSize2

40 ICompressFilter
60 ICompressCodecsInfo
61 ISetCompressCodecsInfo
80 ICryptoProperties
88 ICryptoResetSalt
8C ICryptoResetInitVector
90 ICryptoSetPassword
A0 ICryptoSetCRC
C0 IHasher
C1 IHashers

05 IPassword.h

10 ICryptoGetTextPassword

11 ICryptoGetTextPassword2

06 IArchive.h

03 ISetProperties
04 IArchiveKeepModeForNextOpen
05 IArchiveAllowTail

10 IArchiveOpenCallback

20 IArchiveExtractCallback
21 IArchiveExtractCallbackMessage

30 IArchiveOpenVolumeCallback
40 IInArchiveGetStream
50 IArchiveOpenSetSubArchiveName
60 IInArchive
61 IArchiveOpenSeq
70 IArchiveGetRawProps
71 IArchiveGetRootProps

80 IArchiveUpdateCallback
82 IArchiveUpdateCallback2
83 IArchiveUpdateCallbackFile

A0 IOutArchive

08 IFolder.h

00 IFolderFolder
01 IEnumProperties
02 IFolderGetTypeID
03 IFolderGetPath
04 IFolderWasChanged
05 // IFolderReload
06 // IFolderOperations old
07 IFolderGetSystemIconIndex
08 IFolderGetItemFullSize
09 IFolderClone
0A IFolderSetFlatMode
0B IFolderOperationsExtractCallback
0C //
0D //
0E IFolderProperties
0F

10 IFolderArcProps
11 IGetFolderArcProps
12 // IFolderOperations
13 IFolderOperations
14 IFolderCalcItemFullSize
15 IFolderCompare
16 IFolderGetItemName
17 IFolderAltStreams

09 IFolder.h :: FOLDER_MANAGER_INTERFACE

00 - 04 // old IFolderManager
05 IFolderManager

// 0A PluginInterface.h
00 IInitContextMenu
01 IPluginOptionsCallback
02 IPluginOptions

Handler GUIDs:

{23170F69-40C1-278A-1000-000110xx0000}

01 Zip
02 BZip2
03 Rar
04 Arj
05 Z
06 Lzh
07 7z
08 Cab
09 Nsis
0A lzma
0B lzma86
0C xz
0D ppmd

C6 COFF
C7 Ext
C8 VMDK
C9 VDI
CA Qcow
CB GPT
CC Rar5
CD IHex

CE Hxs
CF TE
D0 UEFIc
D1 UEFIs
D2 SquashFS
D3 CramFS
D4 APM
D5 Mslz
D6 Flv
D7 Swf
D8 Swfc
D9 Ntfs
DA Fat
DB Mbr
DC Vhd
DD Pe
DE Elf
DF Mach-O
E0 Udf
E1 Xar
E2 Mub
E3 Hfs
E4 Dmg
E5 Compound
E6 Wim
E7 Iso
E8
E9 Chm
EA Split
EB Rpm
EC Deb
ED Cpio
EE Tar
EF GZip

{23170F69-40C1-278A-1000-000100020000} ContextMenu.h::CZipContextMenu

// {23170F69-40C1-278A-1000-000100030000} // CAgentArchiveHandler
// {23170F69-40C1-278B- old codecs clsids
// {23170F69-40C1-278D-1000-000100020000} OptionsDialog.h::CLSID_CSevenZipOptions

{23170F69-40C1-2790-id} Codec Decoders
{23170F69-40C1-2791-id}
Codec Encoders
{23170F69-40C1-2792-id} Hashers
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->

```
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-024-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
  {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-024.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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- -

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- -

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 patterns for Galician, written in the utf8 encoding.
%
% Generated with the mkpattern utility (v. 1.2), on 2010/04/23
% The original source file were glpatter-utf8.tex
% This is a generated file
%
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%
% For bug reports and comments:
%
% Javier Mgica, javier at digi21.eu
%
% Note that there is no 'j' nor 'y' in Galician
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*
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*
* An almost complete rewrite  of MySpell for use by
* the Mozilla project has been developed by David Einstein
* (Deinst@world.std.com).  David and I are now
* working on parallel development tracks to help
* our respective projects (Mozilla and OpenOffice.org
* and we will maintain full affix file and dictionary
* file compatibility and work on merging our versions
* of MySpell back into a single tree. David has been
* a significant help in improving MySpell.
*
* Special thanks also go to La'szlo' Ne'meth
* <nemethl@gyorsposta.hu> who is the author of the
* Hungarian dictionary and who developed and contributed
* the code to support compound words in MySpell
* and fixed numerous problems with the encoding
* case conversion tables.
*
*/
```

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wOF2OTTOK @w
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
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+4qAZ5pL
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wOF2OTTOT @
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
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.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
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+4qAZ5pL
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s$,lfxC[4JFITwgL#*7<[61C#ja|#RS~3

n;"372dec~F)B\
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid license Element With dir Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-025-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a div element with INVALID as the value for the dir attribute."

```
/>
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-025.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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a7b132b7c549620321e5da488d8262ae
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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in license Element text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element with an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
```

```
  }
  pre {
   font-size:
 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family:
 "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
```

<body>
If the UA does not display WOFF metadata, the test passes if the word PASS appears below.
The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.
P
</body>
</html>

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```
PROG = 7zDec
CXX = gcc
LIB =
RM = rm -f
CFLAGS = -c -O2 -Wall

OBJS = 7zMain.o 7zAlloc.o 7zArcIn.o 7zBuf.o 7zBuf2.o 7zCrc.o 7zCrcOpt.o 7zDec.o CpuArch.o Delta.o
LzmaDec.o Lzma2Dec.o Bra.o Bra86.o BraIA64.o Bcj2.o Ppmd7.o Ppmd7Dec.o 7zFile.o 7zStream.o

all: $(PROG)

$(PROG): $(OBJS)
 $(CXX) -o $(PROG) $(LDFLAGS) $(OBJS) $(LIB)

7zMain.o: 7zMain.c
 $(CXX) $(CFLAGS) 7zMain.c

7zAlloc.o: ../../7zAlloc.c
 $(CXX) $(CFLAGS) ../../7zAlloc.c

7zArcIn.o: ../../7zArcIn.c
 $(CXX) $(CFLAGS) ../../7zArcIn.c

7zBuf.o: ../../7zBuf.c
 $(CXX) $(CFLAGS) ../../7zBuf.c
```

```
7zBuf2.o: ../../7zBuf2.c
 $(CXX) $(CFLAGS) ../../7zBuf2.c

7zCrc.o: ../../7zCrc.c
 $(CXX) $(CFLAGS) ../../7zCrc.c

7zCrcOpt.o: ../../7zCrc.c
 $(CXX) $(CFLAGS) ../../7zCrcOpt.c

7zDec.o: ../../7zDec.c
 $(CXX) $(CFLAGS) -D_7ZIP_PPMD_SUPPPORT ../../7zDec.c

CpuArch.o: ../../CpuArch.c
 $(CXX) $(CFLAGS) ../../CpuArch.c

Delta.o: ../../Delta.c
 $(CXX) $(CFLAGS) ../../Delta.c

LzmaDec.o: ../../LzmaDec.c
 $(CXX) $(CFLAGS) ../../LzmaDec.c

Lzma2Dec.o:
 ../../Lzma2Dec.c
 $(CXX) $(CFLAGS) ../../Lzma2Dec.c

Bra.o: ../../Bra.c
 $(CXX) $(CFLAGS) ../../Bra.c

Bra86.o: ../../Bra86.c
 $(CXX) $(CFLAGS) ../../Bra86.c

BraIA64.o: ../../BraIA64.c
 $(CXX) $(CFLAGS) ../../BraIA64.c

Bcj2.o: ../../Bcj2.c
 $(CXX) $(CFLAGS) ../../Bcj2.c

Ppmd7.o: ../../Ppmd7.c
 $(CXX) $(CFLAGS) ../../Ppmd7.c

Ppmd7Dec.o: ../../Ppmd7Dec.c
 $(CXX) $(CFLAGS) ../../Ppmd7Dec.c

7zFile.o: ../../7zFile.c
 $(CXX) $(CFLAGS) ../../7zFile.c

7zStream.o: ../../7zStream.c
 $(CXX) $(CFLAGS) ../../7zStream.c
```

```
clean:
 -$(RM) $(PROG) $(OBJS)
wOF2OTTO6 @b
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,L~| 26}~%N4-((edB[E
1}cSpN,lq81
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in copyright Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family:
 "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
```

</body>
</html>
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wOF2OTTO @BZ
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$YnJV$Y)@V[&6
KI|M@
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HISTORY of the LZMA SDK

18.05 2018-04-30

- The speed for LZMA/LZMA2 compressing was increased
  by 8% for fastest/fast compression levels and
  by 3% for normal/maximum compression levels.
- Previous versions of 7-Zip could work incorrectly in "Large memory pages" mode in
 Windows 10 because of some BUG with "Large Pages" in Windows 10.
 Now 7-Zip doesn't use "Large Pages" on Windows 10 up to revision 1709 (16299).
- The BUG was fixed in Lzma2Enc.c
  Lzma2Enc_Encode2() function worked incorretly,
    if (inStream == NULL) and the number of block threads is more than 1.

18.03 beta 2018-03-04

- Asm\x86\LzmaDecOpt.asm: new optimized LZMA decoder written in asm
 for x64 with about 30% higher speed than main version of LZMA decoder written in C.
- The speed for single-thread LZMA/LZMA2 decoder written in C was increased by 3%.
- 7-Zip now can use multi-threading for 7z/LZMA2 decoding,
  if there are multiple independent data chunks in LZMA2 stream.
- 7-Zip now can use multi-threading for xz decoding,
 if there are multiple blocks in xz stream.

18.01 2019-01-28

- The BUG in 17.01 - 18.00 beta was fixed:
 XzDec.c : random block unpacking and XzUnpacker_IsBlockFinished()
 didn't work correctly for xz archives without checksum (CRC).

18.00 beta 2019-01-10

- The BUG in xz encoder was fixed:
 There was memory leak of 16 KB for each file compressed with
 xz compression method, if additional filter was used.

17.01 beta 2017-08-28

- Minor speed optimization for LZMA2 (xz and 7z) multi-threading compression.
 7-Zip now uses additional memory buffers for multi-block LZMA2 compression.
 CPU utilization was slightly improved.
- 7-zip now creates multi-block xz archives by default. Block size can be
 specified with -ms[Size]{m|g} switch.
- xz decoder now can unpack random
 block from multi-block xz archives.
- 7-Zip command line: @listfile now doesn't work after -- switch.
 Use -i@listfile before -- switch instead.
- The BUGs were fixed:
 7-Zip 17.00 beta crashed for commands that write anti-item to 7z archive.

17.00 beta 2017-04-29

- NewHandler.h / NewHandler.cpp:
  now it redefines operator new() only for old MSVC compilers (_MSC_VER < 1900).
- C/7zTypes.h : the names of variables in interface structures were changed (vt).
- Some bugs were fixed. 7-Zip could crash in some cases.
- Some internal changes in code.

16.04 2016-10-04

- The bug was fixed in DllSecur.c.

16.03 2016-09-28

- SFX modules now use some protection against DLL preloading attack.
- Some bugs in 7z code were fixed.

16.02 2016-05-21

- The BUG in 16.00 - 16.01 was fixed:
  Split Handler (SplitHandler.cpp) returned incorrect
  total size value
  (kpidSize) for split archives.

16.01 2016-05-19

- Some internal changes to reduce the number of compiler warnings.

16.00 2016-05-10

- Some bugs were fixed.

15.12 2015-11-19

- The BUG in C version of 7z decoder was fixed:
  7zDec.c : SzDecodeLzma2()
  7z decoder could mistakenly report about decoding error for some 7z archives
  that use LZMA2 compression method.
  The probability to get that mistaken decoding error report was about
  one error per 16384 solid blocks for solid blocks larger than 16 KB (compressed size).
- The BUG (in 9.26-15.11) in C version of 7z decoder was fixed:
  7zArcIn.c : SzReadHeader2()
  7z decoder worked incorrectly for 7z archives that contain
  empty solid blocks, that can be placed to 7z archive, if some file is
  unavailable for reading during archive creation.

15.09 beta 2015-10-16

- The BUG in LZMA / LZMA2
  encoding code was fixed.
  The BUG in LzFind.c::MatchFinder_ReadBlock() function.
  If input data size is larger than (4 GiB - dictionary_size),
  the following code worked incorrectly:
  - LZMA : LzmaEnc_MemEncode(), LzmaEncode() : LZMA encoding functions
    for compressing from memory to memory.
    That BUG is not related to LZMA encoder version that works via streams.
  - LZMA2 : multi-threaded version of LZMA2 encoder worked incorrectly, if
    default value of chunk size (CLzma2EncProps::blockSize) is changed
    to value larger than (4 GiB - dictionary_size).

9.38 beta 2015-01-03

- The BUG in 9.31-9.37 was fixed:
 IArchiveGetRawProps interface was disabled for 7z archives.
- The BUG in 9.26-9.36 was fixed:
 Some code in CPP\7zip\Archive\7z\ worked correctly only under Windows.

9.36 beta 2014-12-26

- The BUG in command line version was fixed:
 7-Zip created temporary archive in current folder during
 update archive
 operation, if -w{Path} switch was not specified.
 The fixed 7-Zip creates temporary archive in folder that contains updated archive.
- The BUG in 9.33-9.35 was fixed:
 7-Zip silently ignored file reading errors during 7z or gz archive creation,
 and the created archive contained only part of file that was read before error.
 The fixed 7-Zip stops archive creation and it reports about error.

9.35 beta 2014-12-07

- 7zr.exe now support AES encryption.
- SFX mudules were added to LZMA SDK
- Some bugs were fixed.

9.21 beta 2011-04-11

- New class FString for file names at file systems.
- Speed optimization in CRC code for big-endian CPUs.
- The BUG in Lzma2Dec.c was fixed:
  Lzma2Decode function didn't work.

9.18 beta 2010-11-02

- New small SFX module for installers (SfxSetup).

9.12 beta 2010-03-24

- The BUG in LZMA SDK 9.* was fixed:
 LZMA2 codec didn't work,
 if more than 10 threads were used (or more than 20 threads in some modes).

9.11 beta 2010-03-15

- PPMd compression method support

9.09 2009-12-12

- The bug was fixed:
  Utf16_To_Utf8 funstions in UTFConvert.cpp and 7zMain.c
  incorrectly converted surrogate characters (the code >= 0x10000) to UTF-8.
- Some bugs were fixed

9.06 2009-08-17

- Some changes in ANSI-C 7z Decoder interfaces.

9.04 2009-05-30

- LZMA2 compression method support
- xz format support

4.65 2009-02-03

- Some minor fixes

4.63 2008-12-31

- Some minor fixes

4.61 beta 2008-11-23

- The bug in ANSI-C LZMA Decoder was fixed:
  If encoded stream was corrupted, decoder could access memory
  outside of allocated range.
- Some changes
  in ANSI-C 7z Decoder interfaces.
- LZMA SDK is placed in the public domain.

4.60 beta 2008-08-19

- Some minor fixes.

4.59 beta 2008-08-13

- The bug was fixed:
  LZMA Encoder in fast compression mode could access memory outside of
  allocated range in some rare cases.

4.58 beta 2008-05-05

- ANSI-C LZMA Decoder was rewritten for speed optimizations.
- ANSI-C LZMA Encoder was included to LZMA SDK.
- C++ LZMA code now is just wrapper over ANSI-C code.

4.57 2007-12-12

- Speed optimizations in ++ LZMA Decoder.
- Small changes for more compatibility with some C/C++ compilers.

4.49 beta 2007-07-05

- .7z ANSI-C Decoder:
  - now it supports BCJ and BCJ2 filters
  - now it supports files larger than 4 GB.
  - now it supports "Last Write Time" field for files.
- C++ code for .7z archives compressing/decompressing from
 7-zip
 was included to LZMA SDK.

4.43 2006-06-04

- Small changes for more compatibility with some C/C++ compilers.

4.42 2006-05-15

- Small changes in .h files in ANSI-C version.

4.39 beta 2006-04-14

- The bug in versions 4.33b:4.38b was fixed:
 C++ version of LZMA encoder could not correctly compress
 files larger than 2 GB with HC4 match finder (-mfhc4).

4.37 beta 2005-04-06

- Fixes in C++ code: code could no be compiled if _NO_EXCEPTIONS was defined.

4.35 beta 2005-03-02

- The bug was fixed in C++ version of LZMA Decoder:
  If encoded stream was corrupted, decoder could access memory
  outside of allocated range.

4.34 beta 2006-02-27

- Compressing speed and memory requirements for compressing were increased
- LZMA now can use only these match finders: HC4, BT2,
 BT3, BT4

4.32 2005-12-09

- Java version of LZMA SDK was included

4.30 2005-11-20

- Compression ratio was improved in -a2 mode
- Speed optimizations for compressing in -a2 mode
- -fb switch now supports values up to 273
- The bug in 7z_C (7zIn.c) was fixed:
 It used Alloc/Free functions from different memory pools.
 So if program used two memory pools, it worked incorrectly.
- 7z_C: .7z format supporting was improved
- LZMA# SDK (C#.NET version) was included

4.27 (Updated) 2005-09-21

- Some GUIDs/interfaces in C++ were changed.

IStream.h:

  ISequentialInStream::Read now works as old ReadPart
  ISequentialOutStream::Write now works as old WritePart

4.27 2005-08-07

- The bug in LzmaDecodeSize.c was fixed:

if _LZMA_IN_CB and _LZMA_OUT_READ were defined,
decompressing worked incorrectly.

4.26 2005-08-05

-
Fixes in 7z_C code and LzmaTest.c:
previous versions could work incorrectly,
if malloc(0) returns 0

4.23 2005-06-29

- Small fixes in C++ code

4.22 2005-06-10

- Small fixes

4.21 2005-06-08

- Interfaces for ANSI-C LZMA Decoder (LzmaDecode.c) were changed
- New additional version of ANSI-C LZMA Decoder with zlib-like interface:
  - LzmaStateDecode.h
  - LzmaStateDecode.c
  - LzmaStateTest.c
- ANSI-C LZMA Decoder now can decompress files larger than 4 GB

4.17 2005-04-18

- New example for RAM->RAM compressing/decompressing:
LZMA + BCJ (filter for x86 code):
  - LzmaRam.h
  - LzmaRam.cpp
  - LzmaRamDecode.h
  - LzmaRamDecode.c
  - -f86 switch for lzma.exe

4.16 2005-03-29

- The bug was fixed in LzmaDecode.c (ANSI-C LZMA Decoder):
If _LZMA_OUT_READ was defined, and if encoded

stream was corrupted,
decoder could access memory outside of allocated range.
- Speed optimization of ANSI-C LZMA Decoder (now it's about 20% faster).
Old version of LZMA Decoder now is in file LzmaDecodeSize.c.
LzmaDecodeSize.c can provide slightly smaller code than LzmaDecode.c
- Small speed optimization in LZMA C++ code
- filter for SPARC's code was added
- Simplified version of .7z ANSI-C Decoder was included

4.06 2004-09-05

- The bug in v4.05 was fixed:
LZMA-Encoder didn't release output stream in some cases.

4.05 2004-08-25

- Source code of filters for x86, IA-64, ARM, ARM-Thumb
and PowerPC code was included to SDK
- Some internal minor changes

4.04 2004-07-28

- More compatibility with some C++ compilers

4.03 2004-06-18

- "Benchmark" command was added. It measures compressing
and decompressing speed and shows
rating values.
Also it checks hardware errors.

4.02 2004-06-10

- C++ LZMA Encoder/Decoder code now is more portable
and it can be compiled by GCC on Linux.

4.01 2004-02-15

- Some detection of data corruption was enabled.
LzmaDecode.c / RangeDecoderReadByte
.....
{

```
    rd->ExtraBytes = 1;
    return 0xFF;
  }
```

4.00   2004-02-13

- Original version of LZMA SDK

HISTORY of the LZMA

 2001-2008: Improvements to LZMA compressing/decompressing code,
           keeping compatibility with original LZMA format
 1996-2001: Development of LZMA compression format

 Some milestones:

 2001-08-30: LZMA compression was added to 7-Zip
 1999-01-02: First version of 7-Zip was released

End of document
wOF2OTTO1 @]
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
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Kurmanji hyphenation patterns (v. 1.0 2009/06/29 JKn and MSh)

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```
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%
%
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%
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%
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%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
% The patterns were generated by Jrg Knappen with patgen using
% a list of 2.5k hyphenated Kurmanji words provided by Medeni Shemd.
%
% For 8-bit engines the T1 (Cork) encoding is needed
% for the special letters occurring in the Kurmanji alphabet.
%
% \message{Kurmanji hyphenation patterns kmrhyph v. 1.0 2009/06/29 JKn and MSh}
%
```

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<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Nested div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
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  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains nested div elements." />
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   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }

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  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
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   &lt;div&gt;Paragraph 2&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
C_OBJS = $(C_OBJS) \
 $O\Aes.obj

!IF "$(CPU)" != "IA64" && "$(CPU)" != "MIPS" && "$(CPU)" != "ARM" && "$(CPU)" != "ARM64"
ASM_OBJS = $(ASM_OBJS) \
 $O\AesOpt.obj
!ENDIF
```

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AppLicense__imp_AddDemoAppLicensedgbabt.o/   1516160780 0   0   100666 638   `

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100666 744 `
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ibwinapi_oemlicense_a__C__Users_Peter_Code_winapi_rs_x86_64_lib_libwinapi_oemlicense_a_inamedgbabs000
01.o/ 1516160780 0 0 100666 694 `
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<link rel="author" title="Tal Leming" href="http://typesupply.com" />
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<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
<meta name="assert" content="The copyright element matches the schema. It contains one text element that does
not have a language tag." />
<style type="text/css"><![CDATA[
@import url("support/test-fonts.css");
body {
font-size:
20px;
}
pre {
font-size: 12px;
}
.test {
font-family: "WOFF Test CFF Reference";
font-size: 200px;
margin-top: 50px;
}
]]></style>
</head>
<body>

Test passes if the word PASS appears below.
The Extended Metadata Block is valid and may be displayed to the user upon request.
P
The XML contained in the Extended Metadata Block is below.
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<metadata version="1.0">
 <copyright>
  <text>
   Copyright without language.
  </text>
 </copyright>
</metadata>
</pre>
</body>
</html>
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*  --------------------------                                          *
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- -

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http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

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Portuguese hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and
 resulted from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: pthyph.tex (1994-10-13 - date on CTAN) or (1996-07-21 - date in file) - no idea
% Author: Pedro J. de Rezende <rezende at dcc.unicamp.br>, J.Joao Dias Almeida <jj at di.uminho.pt>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%
        and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated patterns into TeX Live
%
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%
% The copyright statement of this file is thus:
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%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if
 possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-----------------------------------------------------------------------
%
%%%
```

%
% (C) 1987 by Pedro J. de Rezende
% Version: 1.0 Release date: 02/13/87
%
% ---
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% of the LaTeX Project Public License Distributed from CTAN
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% ---
% Remember! If you *must* change it, then call the resulting file
% something else and attach your name to your *documented* changes.
% ==
%
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wOF2OTTO8 @d
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$v,V8s+%OB,lhO,5!uMPMdtcM&W6+|f-be^B}B*f`Rn>t

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SOFTWARE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in span Element in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a span element with rtl as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }

```
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span dir="rtl"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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wOF2OTTO; @g
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$lGh@FNr+y:MK\
-
]&h//<)9_
~l<_oL:d2iXS
wOF2OTTO4 @`
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf s$-
lF^%OU+m01;VA[?r<4B6_
~>l)$*
8E{
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COPYING-MPL-1.1

```
Please see each file in the implementation for Copyright and licensing
information.
PROG = 7z.exe
MY_CONSOLE = 1
CFLAGS = $(CFLAGS)  \
 -DEXTERNAL_CODECS \

!IFNDEF UNDER_CE
CFLAGS = $(CFLAGS) -DWIN_LONG_PATH -D_7ZIP_LARGE_PAGES -DSUPPORT_DEVICE_FILE
!ENDIF

COMMON_OBJS = \
 $O\CommandLineParser.obj \
 $O\CRC.obj \
 $O\IntToString.obj \
 $O\ListFileUtils.obj \
 $O\NewHandler.obj \
 $O\StdInStream.obj \
 $O\StdOutStream.obj \
 $O\MyString.obj \
 $O\StringConvert.obj \
 $O\StringToInt.obj \
 $O\UTFConvert.obj \
 $O\MyVector.obj \
 $O\Wildcard.obj \

WIN_OBJS = \
 $O\DLL.obj \
 $O\ErrorMsg.obj \
 $O\FileDir.obj \
 $O\FileFind.obj \
 $O\FileIO.obj \
 $O\FileLink.obj \
 $O\FileName.obj \
 $O\FileSystem.obj \
 $O\MemoryLock.obj \
 $O\PropVariant.obj \
 $O\PropVariantConv.obj \
 $O\Registry.obj \
 $O\System.obj \
 $O\TimeUtils.obj \

7ZIP_COMMON_OBJS = \
 $O\CreateCoder.obj \
 $O\FilePathAutoRename.obj \
 $O\FileStreams.obj \
 $O\FilterCoder.obj \
```

```
 $O\LimitedStreams.obj \
 $O\MethodProps.obj \
 $O\ProgressUtils.obj \
 $O\PropId.obj \
 $O\StreamObjects.obj
 \
 $O\StreamUtils.obj \
 $O\UniqBlocks.obj \

AR_COMMON_OBJS = \
 $O\OutStreamWithCRC.obj \

COMPRESS_OBJS = \
 $O\CopyCoder.obj \

C_OBJS = \
 $O\Alloc.obj \
 $O\CpuArch.obj \
 $O\Sort.obj \
 $O\Threads.obj \

!include "../../Crc.mak"
!include "Console.mak"

!include "../../7zip.mak"
wOF2OTTO: @f
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,sD 8m~@
.mrgO=YBl6AM["BGBKW"GM U\/3M
wF,d/C
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Mixed text Element Language Tags 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
```

```
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-007-ref.xht" />
  <meta name="assert" content="The license element matches the schema. One text element does not have a language
tag. One text element has a language tag." />
  <style
 type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-007.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
  &lt;text xml:lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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*
PROG = 7zS2.sfx
MY_FIXED = 1

C_OBJS = \
$O\7zAlloc.obj \
$O\7zArcIn.obj \
$O\7zBuf.obj \

```
 $O\7zBuf2.obj \
 $O\7zCrc.obj \
 $O\7zCrcOpt.obj \
 $O\7zFile.obj \
 $O\7zDec.obj \
 $O\7zStream.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\DllSecur.obj \
 $O\Lzma2Dec.obj \
 $O\LzmaDec.obj \

7Z_OBJS = \
 $O\SfxSetup.obj \

OBJS = \
 $(7Z_OBJS) \
 $(C_OBJS) \
 $O\resource.res

!include "../../../CPP/Build.mak"

$(7Z_OBJS): $(*B).c
 $(COMPL_O1)
$(C_OBJS): ../../$(*B).c
 $(COMPL_O1)
```

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%
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%
% Changes:
% - The first version was realised in late 1994.
% - Second, much more improved version was realised in the beginning of 1996.
%   (Date of the last change: 19.03.1996).
% - In summer 2008 the patterns have been incorporated into hyph-utf8
%   and renamed from hrhyph.tex to hyph-hr.tex.
% - LPPL licence added on 06.06.2011.
%
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element Without url Attribute</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-002-ref.xht" />
  <meta name="assert" content="The license element does not have a url attribute but it still matches the schema." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-002.woff2") format("woff2");
   }
   body {
    font-size: 20px;
```

```
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license id="License ID"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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$I>[
_5E
_5E
_5EPhE_5E
_4E_5E*XE_5E*IE_5E*ME_5ERich
_5EPELu[DG @P d@ T.text `.rdata @@.data00
@.rsrc@@@=$0@uh0@ @$0@D$-8#$0@UE-VHlEHtHHt3Vu@ @V< @I3f}D 0@y 0@(= 0@r
0@(0@3;tP8 @ 0@Vh@u40@5,0@4 @;(0@EPhu0 @P, @EPuL @jVVuuV5(0@$ @j5(0@ @S]W=(@h @S50
@h @hSPht @jSPhd @jSPhT @jSPjjS= @PjhSPjjhSH @jS_[3^VW @h @ @5 @3WWh1@WjiW,0@PD @W
@_^"!!! "."""P"`"n"""""""!>"CancelNext >< BackNSIS versionNSIS User Interface - TestingRichEd32.dllll!! \!! |!" d!"
"!!!
"."""P"`"n"""""""!>"ExitProcessGetModuleHandleWULoadLibraryWKERNEL32.dllCOMCTL32.dll@SendMessage
WSetWindowTextWShowWindowSetWindowPos1ScreenToClienttGetWindowRectGetDlgItemVCreateDialogPara
mWDestroyWindowPostQuitMessageEndDialogDialogBoxParamWUSER32.dll)CreateBrushIndirectGDI32.dllfhgj
ki0 HX@DHMS Shell Dlg 2@,@
@@@@@H
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  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid license Element With dir Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-016-ref.xht" />
  <meta name="assert" content="The license element contains a text element with INVALID as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-016.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
```

```
  <div class="test">P</div>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Two div Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains two div elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div&gt;
    div 1 start
    &lt;div&gt;
     div 2 start
     &lt;div&gt;
      div 3
```

</div>
div 2 end
</div>
div 1 end
</div>
</text>
</copyright>
</metadata>
</pre>
</body>
</html>
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.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$.vP,
2v
{=.^[(&1)%u.}lN ;ML'|-#u|#]6,fXXZq nDN
t
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With class Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />

```
  <link rel="match" href="metadatadisplay-schema-license-033-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a span element with
&amp;quot;class1 class2 class3&amp;quot; as the value
 for the class attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-033.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&amp;lt;?xml version="1.0" encoding="UTF-8"?&amp;gt;
&amp;lt;metadata version="1.0"&amp;gt;
 &amp;lt;license url="http://w3c.org/Fonts"&amp;gt;
  &amp;lt;text&amp;gt;
   Text with &amp;lt;span class="class1 class2 class3"&amp;gt;span&amp;lt;/span&amp;gt;.
  &amp;lt;/text&amp;gt;
 &amp;lt;/license&amp;gt;
&amp;lt;/metadata&amp;gt;
  </pre>
 </body>
</html>
```

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wOF2OTTO? @k
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,x3U-y:MRp\X*1<C||7|$JWE
=7Qv(J@UR/3MYx'Z~>c
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in copyright Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-008-ref.xht" />
  <meta name="assert" content="The copyright element contains an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";

```
   src: url("support/metadatadisplay-schema-copyright-008.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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MY_STATIC_LINK=1
SLIB = sLZMA.lib
PROG = LZMA.dll
SLIBPATH = $O\$(SLIB)

DEF_FILE = LzmaLib.def
CFLAGS = $(CFLAGS) \

LIB_OBJS = \
$O\LzmaLibExports.obj \

C_OBJS = \
$O\Alloc.obj \
$O\LzFind.obj \
$O\LzFindMt.obj \
$O\LzmaDec.obj \
$O\LzmaEnc.obj \
$O\LzmaLib.obj \
$O\Threads.obj \

OBJS = \

```
 $(LIB_OBJS) \
 $(C_OBJS) \
 $O\resource.res

!include "../../../CPP/Build.mak"

$(SLIBPATH): $O $(OBJS)
 lib -out:$(SLIBPATH) $(OBJS) $(LIBS)

$(LIB_OBJS): $(*B).c
 $(COMPL_O2)
$(C_OBJS): ../../$(*B).c
 $(COMPL_O2)
```

A. HISTORY OF THE SOFTWARE
==========

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)
0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no

1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3				
2.1.2	2002	PSF	yes	
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1	2010	PSF	yes
3.2	3.1	2010	PSF	yes

Footnotes:

(1) GPL-compatible doesn't mean that we're distributing Python under the GPL. All Python licenses, unlike the GPL, let you distribute a
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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible,
because its license has a choice of law clause. According to
CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1
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Thanks to the many outside volunteers who have worked under Guido's
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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One Language Tagged (using lang) text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element matches the schema. It contains one text element that has a language tag using the lang tag instead of xml:lang." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
```

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text lang="en"&gt;
   Copyright with "en" language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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```
OBJS = \
 $O\StdAfx.obj \
 $(CURRENT_OBJS) \
 $(COMMON_OBJS) \
 $(WIN_OBJS) \
 $(WIN_CTRL_OBJS) \
 $(7ZIP_COMMON_OBJS) \
 $(AR_OBJS) \
 $(AR_COMMON_OBJS) \
 $(UI_COMMON_OBJS) \
 $(AGENT_OBJS) \
 $(CONSOLE_OBJS) \
 $(EXPLORER_OBJS) \
 $(FM_OBJS) \
 $(GUI_OBJS) \
 $(7Z_OBJS) \
 $(CAB_OBJS) \
 $(CHM_OBJS) \
 $(COM_OBJS) \
 $(ISO_OBJS) \
 $(NSIS_OBJS) \
 $(RAR_OBJS) \
 $(TAR_OBJS) \
 $(UDF_OBJS) \
 $(WIM_OBJS) \
 $(ZIP_OBJS) \
 $(COMPRESS_OBJS) \
 $(CRYPTO_OBJS) \
```

```
 $(C_OBJS) \
 $(ASM_OBJS) \
 $O\resource.res \

!include "../../../Build.mak"

# MAK_SINGLE_FILE = 1

!IFDEF MAK_SINGLE_FILE

!IFDEF CURRENT_OBJS
$(CURRENT_OBJS): ./$(*B).cpp
 $(COMPL)
!ENDIF


!IFDEF COMMON_OBJS
$(COMMON_OBJS): ../../../Common/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF WIN_OBJS
$(WIN_OBJS): ../../../Windows/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF WIN_CTRL_OBJS
$(WIN_CTRL_OBJS): ../../../Windows/Control/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF 7ZIP_COMMON_OBJS
$(7ZIP_COMMON_OBJS): ../../Common/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF
 AR_OBJS
$(AR_OBJS): ../../Archive/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF AR_COMMON_OBJS
$(AR_COMMON_OBJS): ../../Archive/Common/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF 7Z_OBJS
```

```
$(7Z_OBJS): ../../Archive/7z/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF CAB_OBJS
$(CAB_OBJS): ../../Archive/Cab/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF CHM_OBJS
$(CHM_OBJS): ../../Archive/Chm/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF COM_OBJS
$(COM_OBJS): ../../Archive/Com/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF ISO_OBJS
$(ISO_OBJS): ../../Archive/Iso/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF NSIS_OBJS
$(NSIS_OBJS): ../../Archive/Nsis/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF RAR_OBJS
$(RAR_OBJS): ../../Archive/Rar/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF TAR_OBJS
$(TAR_OBJS): ../../Archive/Tar/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF UDF_OBJS
$(UDF_OBJS): ../../Archive/Udf/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF WIM_OBJS
$(WIM_OBJS): ../../Archive/Wim/$(*B).cpp
 $(COMPL)
!ENDIF
```

```
!IFDEF ZIP_OBJS
$(ZIP_OBJS): ../../Archive/Zip/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF
 COMPRESS_OBJS
$(COMPRESS_OBJS): ../../Compress/$(*B).cpp
 $(COMPL_O2)
!ENDIF

!IFDEF CRYPTO_OBJS
$(CRYPTO_OBJS): ../../Crypto/$(*B).cpp
 $(COMPL_O2)
!ENDIF

!IFDEF UI_COMMON_OBJS
$(UI_COMMON_OBJS): ../../UI/Common/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF AGENT_OBJS
$(AGENT_OBJS): ../../UI/Agent/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF CONSOLE_OBJS
$(CONSOLE_OBJS): ../../UI/Console/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF EXPLORER_OBJS
$(EXPLORER_OBJS): ../../UI/Explorer/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF FM_OBJS
$(FM_OBJS): ../../UI/FileManager/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF GUI_OBJS
$(GUI_OBJS): ../../UI/GUI/$(*B).cpp
 $(COMPL)
!ENDIF

!IFDEF C_OBJS
```

```
$(C_OBJS): ../../../../C/$(*B).c
 $(COMPL_O2)
!ENDIF


!ELSE

{.}.cpp{$O}.obj::
 $(COMPLB)
{../../../Common}.cpp{$O}.obj::
 $(COMPLB)
{../../../Windows}.cpp{$O}.obj::
 $(COMPLB)
{../../../Windows/Control}.cpp{$O}.obj::
 $(COMPLB)
{../../Common}.cpp{$O}.obj::
 $(COMPLB)

{../../UI/Common}.cpp{$O}.obj::
 $(COMPLB)
{../../UI/Agent}.cpp{$O}.obj::
 $(COMPLB)
{../../UI/Console}.cpp{$O}.obj::
 $(COMPLB)
{../../UI/Explorer}.cpp{$O}.obj::
 $(COMPLB)
{../../UI/FileManager}.cpp{$O}.obj::
 $(COMPLB)
{../../UI/GUI}.cpp{$O}.obj::
 $(COMPLB)


{../../Archive}.cpp{$O}.obj::
 $(COMPLB)
{../../Archive/Common}.cpp{$O}.obj::
 $(COMPLB)

{../../Archive/7z}.cpp{$O}.obj::
 $(COMPLB)
{../../Archive/Cab}.cpp{$O}.obj::
 $(COMPLB)
{../../Archive/Chm}.cpp{$O}.obj::
 $(COMPLB)
{../../Archive/Com}.cpp{$O}.obj::
 $(COMPLB)
{../../Archive/Iso}.cpp{$O}.obj::
 $(COMPLB)
{../../Archive/Nsis}.cpp{$O}.obj::
```

$(COMPLB)
{../../Archive/Rar}.cpp{$O}.obj::
$(COMPLB)
{../../Archive/Tar}.cpp{$O}.obj::
$(COMPLB)
{../../Archive/Udf}.cpp{$O}.obj::
$(COMPLB)
{../../Archive/Wim}.cpp{$O}.obj::
$(COMPLB)
{../../Archive/Zip}.cpp{$O}.obj::
$(COMPLB)

{../../Compress}.cpp{$O}.obj::
$(COMPLB_O2)
{../../Crypto}.cpp{$O}.obj::
$(COMPLB_O2)
{../../../../C}.c{$O}.obj::
$(CCOMPLB)

!ENDIF

!include
"Asm.mak"
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>

```
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in span Element in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-028-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with rtl
as the value for the dir attribute."
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-028.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Text with &lt;span dir="rtl"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

wOF2OTTOK @w
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$;!r$/Fw3V
P
6Gftn!YLfhv"8|1;/*v6@">IrF>6<QU9+8h[Pn$
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With class Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-014-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element with "class1 class2 class3" as the value for the class attribute." />
  <style
 type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-014.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>

Test passes if the word PASS appears below.
The Extended Metadata Block is valid and may be displayed to the user upon request.
P
The XML contained in the Extended Metadata Block is below.
<pre>
<?xml version="1.0" encoding="UTF-8"?>
<metadata version="1.0">
<copyright>
<text class="class 1 class2 class3">
Copyright without language.
</text>
</copyright>
</metadata>
</pre>
</body>
</html>
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Child Element in license Element text Element</title>

```
 <link rel="author" title="Tal Leming" href="http://typesupply.com" />
 <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <link rel="match" href="metadatadisplay-schema-license-019-ref.xht" />
 <meta name="assert" content="The license element contains a text element with an unknown child element." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  @font-face
{
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-019.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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```
; AesOpt.asm -- Intel's AES.
; 2009-12-12 : Igor Pavlov : Public domain

include 7zAsm.asm

MY_ASM_START

ifndef x64
  .xmm
endif

ifdef x64
  num   equ r8
else
  num   equ [r4 + REG_SIZE * 4]
endif

rD equ r2
rN equ r0

MY_PROLOG macro reg:req
  ifdef x64
  movdqa  [r4 + 8], xmm6
  movdqa  [r4 + 8 + 16], xmm7
  endif

  push  r3
  push  r5
  push  r6

  mov   rN, num
  mov   x6, [r1 + 16]
  shl   x6, 5

  movdqa  reg, [r1]
  add   r1, 32
endm
```

```
MY_EPILOG macro
  pop     r6
  pop     r5
  pop     r3

  ifdef x64
  movdqa  xmm6, [r4 + 8]
  movdqa  xmm7, [r4 + 8 + 16]
  endif

  MY_ENDP
endm

ways equ 4
ways16 equ (ways * 16)

OP_W macro op, op2
  i = 0
  rept ways
  op @CatStr(xmm,%i), op2
  i = i + 1
  endm
endm

LOAD_OP macro op:req, offs:req
  op      xmm0, [r1 + r3 offs]
endm

LOAD_OP_W macro op:req, offs:req
  movdqa  xmm7, [r1 + r3 offs]
  OP_W    op, xmm7
endm


; ---------- AES-CBC Decode ----------

CBC_DEC_UPDATE
 macro reg, offs
  pxor    reg, xmm6
  movdqa  xmm6, [rD + offs]
  movdqa  [rD + offs], reg
endm

DECODE macro op:req
  op      aesdec, +16
 @@:
  op      aesdec, +0
  op      aesdec, -16
```

```
  sub     x3, 32
  jnz     @B
  op      aesdeclast, +0
endm

MY_PROC AesCbc_Decode_Intel, 3
  MY_PROLOG xmm6

  sub     x6, 32

  jmp     check2

 align 16
 nextBlocks2:
  mov     x3, x6
  OP_W    movdqa, [rD + i * 16]
  LOAD_OP_W  pxor, +32
  DECODE  LOAD_OP_W
  OP_W    CBC_DEC_UPDATE, i * 16
  add     rD, ways16
 check2:
  sub     rN, ways
  jnc     nextBlocks2

  add     rN, ways
  jmp     check

 nextBlock:
  mov     x3, x6
  movdqa  xmm1, [rD]
  LOAD_OP movdqa, +32
  pxor    xmm0, xmm1
  DECODE  LOAD_OP
  pxor    xmm0, xmm6
  movdqa  [rD], xmm0
  movdqa  xmm6, xmm1
  add     rD, 16
 check:
  sub     rN, 1
  jnc     nextBlock

  movdqa  [r1 - 32], xmm6
  MY_EPILOG


; ---------- AES-CBC Encode ----------

ENCODE
```

```
 macro op:req
   op      aesenc, -16
 @@:
   op      aesenc, +0
   op      aesenc, +16
   add     r3, 32
   jnz     @B
   op      aesenclast, +0
endm

MY_PROC AesCbc_Encode_Intel, 3
   MY_PROLOG xmm0

   add     r1, r6
   neg     r6
   add     r6, 32

   jmp     check_e

 align 16
 nextBlock_e:
   mov     r3, r6
   pxor    xmm0, [rD]
   pxor    xmm0, [r1 + r3 - 32]
   ENCODE  LOAD_OP
   movdqa  [rD], xmm0
   add     rD, 16
 check_e:
   sub     rN, 1
   jnc     nextBlock_e

   movdqa  [r1 + r6 - 64], xmm0
   MY_EPILOG


; ---------- AES-CTR ----------

XOR_UPD_1 macro reg, offs
   pxor    reg, [rD + offs]
endm

XOR_UPD_2 macro reg, offs
   movdqa  [rD + offs], reg
endm

MY_PROC AesCtr_Code_Intel, 3
   MY_PROLOG xmm6
```

```
  mov     r5, r4
  shr     r5, 4
  dec     r5
  shl     r5, 4

  mov     DWORD PTR [r5], 1
  mov     DWORD PTR [r5 + 4], 0
  mov     DWORD PTR [r5 + 8], 0
  mov     DWORD PTR [r5 + 12], 0

  add     r1, r6
  neg     r6
   add     r6, 32

  jmp     check2_c

align 16
nextBlocks2_c:
  movdqa  xmm7, [r5]

  i = 0
  rept ways
  paddq   xmm6, xmm7
  movdqa  @CatStr(xmm,%i), xmm6
  i = i + 1
  endm

  mov     r3, r6
  LOAD_OP_W  pxor, -32
  ENCODE  LOAD_OP_W
  OP_W    XOR_UPD_1, i * 16
  OP_W    XOR_UPD_2, i * 16
  add     rD, ways16
check2_c:
  sub     rN, ways
  jnc     nextBlocks2_c

  add     rN, ways
  jmp     check_c

nextBlock_c:
  paddq   xmm6, [r5]
  mov     r3, r6
  movdqa  xmm0, [r1 + r3 - 32]
  pxor    xmm0, xmm6
  ENCODE  LOAD_OP
  XOR_UPD_1 xmm0, 0
  XOR_UPD_2 xmm0, 0
```

```
  add     rD, 16
 check_c:
  sub     rN, 1
  jnc     nextBlock_c

  movdqa  [r1 + r6 - 64], xmm6
  MY_EPILOG

end
```

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 B. Gaulle <cesure-l at gutenberg.eu.org>
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%
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% - Hans Hagen, who did the unicodifisation of patterns already long before
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%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and
```

```
 \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-----------------------------------------------------------------------
%
% French hyphenation patterns
%%%
```

% Norman Buckle, UQAH (nb; many additions)
% Michael Ferguson, INRS-Telecommunications (mjf) June 1988
% Justin Bur, Universite de Montreal (jbb; checked against original list)
% all patterns including apostrophe missing from nb list
% after that, GUTenberg and specially Daniel Flipo and Bernard Gaulle
% did their best effort to improve the list of patterns.
%
% ---
%
% Adaption of these patterns for
% - TeX Version 3.x and MLTeX 3.x (2.x)
% and
% - all fonts in T1/`Cork' and/or CM/OT1 encoding
% by Bernd Raichle 1996/08/28 (using ideas from `ghyph31.tex'
% as of 1994-02-13 maintained by Bernd Raichle).
% (An adaption for the old MLTeX 2.x exists but can not be
% tested in lack of an executable.)
%
% ---
%
wOF2OTTOP @|
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,wp7_/hl3FTc*FAODBu
)7Md[$JS?{60_$xGSDODGp{0txydD\5
License information for hyph_nn.dic:

This file is based on the TeX hyphenation patterns distributed as part of the
hyph-utf8 package, and retains the same copyright and licensing conditions (below).

- -

At the time this file was first modified, a complete, unmodified copy of
the original work was available from:
http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

Portions of this file were originally made available under the following license
(copied verbatim from hyph-nn.lic.txt in the original work):

Norwegian Nynorsk hyphenation patterns

(more info about the licence to be added later)

% Adapted to the new pattern-loading scheme.
% Original file name was nnhyph.tex

% TeX hyphenation patterns for Norwegian Nynorsk
%
% Version 2007-02-10
%
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%
% This file contains hyphenation patterns for Norwegian Nynorsk.
% It uses the Norwegian hyphenation patterns from nohyphbx.tex,
% created by Rune Kleveland and Ole Michael Selberg. Please see
% that file for copyright information on those patterns.
%
% The patterns in nohyphbx are based on both Norwegian Bokmal
% and Norwegian Nynorsk, and works about equally well for both
% languages. This file, nnhyph.tex, contains only a few hyphenation
% exceptions, for words that needs to be hyphenated differently for
% the two languages.
%
% Please send bugs or suggestions to karl@huftis.org.
%

\input hyph-no.tex

\hyphenation{
att-en-de
bet-re
}

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wOF2OTTOS @
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$x^(R%BB%Ix17Bnnd%1izm3HK49SU8"5P={O4DJy!m\4ez
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With One div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains a div element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  Test passes if the word PASS appears below.
  The Extended Metadata Block is valid and may be displayed to the user upon request.
  P
  The XML contained in the Extended Metadata Block is below.
  <pre>
<?xml version="1.0" encoding="UTF-8"?>

<metadata version="1.0">
 <license url="http://w3c.org/Fonts">
  <text>
   <div>Paragraph 1</div>
  </text>
 </license>
</metadata>
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
 ]]></style>
 </head>
 <body>
  Test passes if the word PASS appears below.
  The Extended Metadata Block is valid and may be displayed to the user upon request.
  P
  The XML contained in the Extended Metadata Block is below.
  <pre>
<?xml version="1.0" encoding="UTF-8"?>
<metadata version="1.0">

```
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text dir="ltr"&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Mixed text Element Language Tags 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-005-ref.xht" />
  <meta name="assert" content="The copyright element matches the schema. Two text elements have a language tags." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-005.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
```

The XML contained in the Extended Metadata Block is below.
<pre>
<?xml version="1.0" encoding="UTF-8"?>
<metadata version="1.0">
<copyright>
<text xml:lang="en">
Copyright with "en" language.
</text>
<text xml:lang="fr">
Copyright with "fr" language.
</text>
</copyright>
</metadata>
</pre>
</body>
</html>
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cd $(@D)
$(MAKE) -nologo $(TARGETS)
cd ..
wOF2OTTOP @|
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf s$
xPcSi0vbd GQY2U3i
CB1X,MA9A:T7p%u>#>nBa;q7)l,4w+
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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid copyright Element With dir Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element with INVALID as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
 ]]></style>
 </head>
```

```
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
```

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```
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strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in copyright Element text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-015-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element with an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face
 {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-015.woff2") format("woff2");
   }
```

```
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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wOF2OTTOP @|
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$n.TFFITw%Etn!n93@Tyy\-+ 3RU"7!GOs972d 6hlujE-^2i,
Q`b{
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- -

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- -

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```
% DIVISI'ON DE PALABRAS
% ~~~~~~~~~~~~~~~~~~~~~
%
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element Without id Attribute</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element does not have an id attribute but it still matches the schema." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
```

```
  body {
   font-size: 20px;
  }
  pre {
   font-size:
 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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7-Zip for installers 9.38

7-Zip is a file archiver for Windows NT/2000/2003/2008/XP/Vista/7/8/10.

7-Zip for installers is part of LZMA SDK.
LZMA SDK is written and placed in the public domain by Igor Pavlov.

It's allowed to join 7-Zip SFX module with another software.
It's allowed to change resources of 7-Zip's SFX modules.

HOW to use

7zr.exe is reduced version of 7za.exe of 7-Zip.
7zr.exe supports only format with these codecs: LZMA, LZMA2, BCJ, BCJ2, ARM, Copy.

Example of compressing command for installation packages:

7zr a archive.7z files

7zSD.sfx is SFX module for installers. 7zSD.sfx uses msvcrt.dll.

SFX modules for installers allow to create installation program.
Such module extracts archive to temp folder and then runs specified program and removes
temp files after program finishing. Self-extract archive for installers must be created
as joining 3 files: SFX_Module, Installer_Config, 7z_Archive.
Installer_Config is optional file.
You can use the following command to create installer
self-extract archive:

copy /b 7zSD.sfx + config.txt + archive.7z archive.exe

The smallest installation package size can be achieved, if installation files was
uncompressed before including to 7z archive.

-y switch for installer module (at runtime) specifies quiet mode for extracting.

Installer Config file format
~~~~~~~~~~~~~~~~~~~~~~~~~~~~
Config file contains commands for Installer. File begins from string
;!@Install@!UTF-8! and ends with ;!@InstallEnd@!. File must be written
in UTF-8 encoding. File contains string pairs:

ID_String="Value"
~~~~~~~~~~~~~~~~~~~~~~~~~~~~

ID_String Description

Title Title for messages
BeginPrompt Begin Prompt message
Progress Value can be "yes" or "no". Default value is "yes".
RunProgram Command for executing. Default value is "setup.exe".
Substring %%T will be replaced with path to temporary
folder, where files were extracted

Directory Directory prefix for "RunProgram". Default value is ".\\"
ExecuteFile Name of file for executing
ExecuteParameters Parameters for "ExecuteFile"

You can omit any string pair.

There are two ways to run program: RunProgram and ExecuteFile.
Use RunProgram, if you want to run some program from .7z archive.
Use ExecuteFile, if you want to open some document from .7z archive or
if you want to execute some command from Windows.

If you use RunProgram and if you specify empty directory prefix: Directory="",
the system searches for the executable file in the following sequence:

1. The directory from which the application (installer) loaded.
2. The temporary folder, where files were extracted.
3. The Windows system directory.

Config file Examples
~~~~~~~~~~~~~~~~~~~~

```
;!@Install@!UTF-8!
Title="7-Zip 4.00"
BeginPrompt="Do you want to install the 7-Zip 4.00?"
RunProgram="setup.exe"
;!@InstallEnd@!
```

```
;!@Install@!UTF-8!
Title="7-Zip 4.00"
BeginPrompt="Do
 you want to install the 7-Zip 4.00?"
ExecuteFile="7zip.msi"
;!@InstallEnd@!
```
~~~~~~~~~~~~~~~~~~~~

```
;!@Install@!UTF-8!
Title="7-Zip 4.01 Update"
BeginPrompt="Do you want to install the 7-Zip 4.01 Update?"
ExecuteFile="msiexec.exe"
ExecuteParameters="/i 7zip.msi REINSTALL=ALL REINSTALLMODE=vomus"
;!@InstallEnd@!
```

Small SFX modules for installers

7zS2.sfx - small SFX module (GUI version)
7zS2con.sfx - small SFX module (Console version)

Small SFX modules support this codecs: LZMA, LZMA2, BCJ, BCJ2, ARM, COPY

Small SFX module is similar to common SFX module for installers.
The difference (what's new in small version):
- Smaller size (30 KB vs 100 KB)
- C source code instead of ++
- No installer Configuration file
- No extracting progress window
- It decompresses solid 7z blocks (it can be whole 7z archive) to RAM.
  So user that calls SFX installer must have free RAM of size of largest
  solid 7z block (size of 7z archive at simplest case).

How to use
---

copy
 /b 7zS2.sfx + archive.7z sfx.exe

When you run installer sfx module (sfx.exe)
1) It creates "7zNNNNNNNN" temp folder in system temp folder.
2) It extracts .7z archive to that folder
3) It executes one file from "7zNNNNNNNN" temp folder.
4) It removes "7zNNNNNNNN" temp folder

You can send parameters to installer, and installer will transfer them to extracted .exe file.

Small SFX uses 3 levels of priorities to select file to execute:

1) Files in root folder have higher priority than files in subfolders.
2) File extension priorities (from high to low priority order):
   bat, cmd, exe, inf, msi, cab (under Windows CE), html, htm
3) File name priorities (from high to low priority order):

setup, install, run, start

Windows CE (ARM) version of 7zS2.sfx is included to 7-Zip for Windows Mobile package.

Examples
----

1) To create compressed console 7-Zip:

```
7zr a c.7z 7z.exe 7z.dll -mx
copy /b 7zS2con.sfx + c.7z 7zCompr.exe
7zCompr.exe b -md22
```

2) To create
compressed GUI 7-Zip:

```
7zr a g.7z 7zg.exe 7z.dll -mx
copy /b 7zS2.sfx + g.7z 7zgCompr.exe
7zgCompr.exe b -md22
```

3) To open some file:

```
7zr a h.7z readme.txt -mx
copy /b 7zS2.sfx + h.7z 7zTxt.exe
7zTxt.exe
```

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wOF2OTTO< @h
.`f6$ Q

NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$xs5pT*"NB]=t
iVfu^j+h|"&]3~c1PXE
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```
CFLAGS = $(CFLAGS) -D_7ZIP_PPMD_SUPPPORT

PROG = 7zDec.exe

C_OBJS = \
 $O\7zAlloc.obj \
 $O\7zBuf.obj \
 $O\7zCrc.obj \
 $O\7zCrcOpt.obj \
 $O\7zFile.obj \
 $O\7zDec.obj \
 $O\7zArcIn.obj \
 $O\7zStream.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\Lzma2Dec.obj \
 $O\LzmaDec.obj \
 $O\Ppmd7.obj \
 $O\Ppmd7Dec.obj \

7Z_OBJS = \
 $O\7zMain.obj \

OBJS = \
 $O\Precomp.obj \
 $(7Z_OBJS) \
 $(C_OBJS) \

!include "../../../CPP/Build.mak"

$(7Z_OBJS): $(*B).c
 $(CCOMPL_USE)
$(C_OBJS): ../../$(*B).c
 $(CCOMPL_USE)
$O\Precomp.obj: Precomp.c
 $(CCOMPL_PCH)
;!@Install@!UTF-8!
Title="Software 7.00"
BeginPrompt="Do you want to install the Software 7.00?"
RunProgram="7zr.exe b"
;!@InstallEnd@!
Copyright (c) 2015 David Cuddeback

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```

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; LzmaDecOpt.asm -- ASM version of LzmaDec_DecodeReal_3() function
; 2018-02-06: Igor Pavlov : Public domain
;
; 3 - is the code compatibility version of LzmaDec_DecodeReal_*()
; function for check at link time.
; That code is tightly coupled with LzmaDec_TryDummy()
; and with another functions in LzmaDec.c file.
; CLzmaDec structure, (probs) array layout, input and output of
; LzmaDec_DecodeReal_*() must be equal in both versions (C / ASM).

```
ifndef x64
; x64=1
; .err <x64_IS_REQUIRED>
endif

include 7zAsm.asm

MY_ASM_START

_TEXT$LZMADECOPT SEGMENT ALIGN(64) 'CODE'

MY_ALIGN macro num:req
      align  num
endm

MY_ALIGN_16 macro
      MY_ALIGN 16
endm

MY_ALIGN_32 macro
      MY_ALIGN 32
endm

MY_ALIGN_64 macro
      MY_ALIGN 64
endm


; _LZMA_SIZE_OPT  equ 1

; _LZMA_PROB32 equ 1

ifdef _LZMA_PROB32
      PSHIFT  equ 2
      PLOAD macro dest, mem
            mov     dest, dword ptr [mem]
      endm
      PSTORE  macro src, mem
            mov     dword ptr [mem],
 src
      endm
else
      PSHIFT  equ 1
      PLOAD macro dest, mem
            movzx   dest, word ptr [mem]
      endm
      PSTORE macro src, mem
            mov     word ptr [mem], @CatStr(src, _W)
```

```
      endm
endif

PMULT          equ (1 SHL PSHIFT)
PMULT_HALF     equ (1 SHL (PSHIFT - 1))
PMULT_2        equ (1 SHL (PSHIFT + 1))



;      x0      range
;      x1      pbPos / (prob) TREE
;      x2      probBranch / prm (MATCHED) / pbPos / cnt
;      x3      sym
;====== r4 ===  RSP
;      x5      cod
;      x6      t1 NORM_CALC / probs_state / dist
;      x7      t0 NORM_CALC / prob2 IF_BIT_1
;      x8      state
;      x9      match (MATCHED) / sym2 / dist2 / lpMask_reg
;      x10     kBitModelTotal_reg
;      r11     probs
;      x12     offs (MATCHED) / dic / len_temp
;      x13     processedPos
;      x14     bit (MATCHED) / dicPos
;      r15     buf



cod     equ x5
cod_L   equ x5_L
range   equ x0
state   equ x8
state_R equ r8
buf     equ r15
processedPos
 equ x13
kBitModelTotal_reg equ x10

probBranch   equ x2
probBranch_R equ r2
probBranch_W equ x2_W

pbPos   equ x1
pbPos_R equ r1

cnt     equ x2
cnt_R   equ r2

lpMask_reg equ x9
dicPos  equ r14
```

```
sym     equ x3
sym_R   equ r3
sym_L   equ x3_L

probs   equ r11
dic     equ r12

t0      equ x7
t0_W    equ x7_W
t0_R    equ r7

prob2   equ t0
prob2_W equ t0_W

t1      equ x6
t1_R    equ r6

probs_state     equ t1
probs_state_R   equ t1_R

prm     equ r2
match   equ x9
match_R equ r9
offs    equ x12
offs_R  equ r12
bit     equ x14
bit_R   equ r14

sym2    equ x9
sym2_R  equ r9

len_temp equ x12

dist    equ sym
dist2   equ x9



kNumBitModelTotalBits   equ 11
kBitModelTotal          equ (1 SHL kNumBitModelTotalBits)
kNumMoveBits            equ 5
kBitModelOffset         equ ((1 SHL kNumMoveBits) - 1)
kTopValue               equ (1 SHL 24)

NORM_2 macro
      ; movzx   t0, BYTE PTR [buf]
      shl     cod, 8
```

```
    mov     cod_L, BYTE PTR [buf]
    shl     range, 8

   ; or      cod, t0
    inc     buf
endm


NORM macro
    cmp     range, kTopValue
    jae     SHORT @F
    NORM_2
@@:
endm


; ---------- Branch MACROS ----------

UPDATE_0 macro probsArray:req, probOffset:req, probDisp:req
    mov     prob2, kBitModelTotal_reg
    sub     prob2, probBranch
    shr     prob2, kNumMoveBits
    add     probBranch, prob2
    PSTORE  probBranch, probOffset * 1 + probsArray + probDisp * PMULT
endm


UPDATE_1 macro probsArray:req, probOffset:req, probDisp:req
    sub     prob2, range
    sub     cod, range
    mov     range, prob2
    mov     prob2, probBranch
    shr     probBranch, kNumMoveBits
    sub     prob2, probBranch
    PSTORE  prob2, probOffset * 1 + probsArray + probDisp * PMULT
endm


CMP_COD macro probsArray:req, probOffset:req, probDisp:req
    PLOAD   probBranch, probOffset * 1 + probsArray + probDisp * PMULT
    NORM
    mov     prob2, range
    shr     range,
 kNumBitModelTotalBits
    imul    range, probBranch
    cmp     cod, range
endm
```

```
IF_BIT_1_NOUP macro probsArray:req, probOffset:req, probDisp:req, toLabel:req
      CMP_COD probsArray, probOffset, probDisp
      jae     toLabel
endm


IF_BIT_1 macro probsArray:req, probOffset:req, probDisp:req, toLabel:req
      IF_BIT_1_NOUP probsArray, probOffset, probDisp, toLabel
      UPDATE_0 probsArray, probOffset, probDisp
endm


IF_BIT_0_NOUP macro probsArray:req, probOffset:req, probDisp:req, toLabel:req
      CMP_COD probsArray, probOffset, probDisp
      jb      toLabel
endm


; ---------- CMOV MACROS ----------

NORM_CALC macro prob:req
      NORM
      mov     t0, range
      shr     range, kNumBitModelTotalBits
      imul    range, prob
      sub     t0, range
      mov     t1, cod
      sub     cod, range
endm


PUP macro prob:req, probPtr:req
      sub     t0, prob
     ; only sar works for both 16/32 bit prob modes
      sar     t0, kNumMoveBits
       add     t0, prob
      PSTORE  t0, probPtr
endm


PUP_SUB macro prob:req, probPtr:req, symSub:req
      sbb     sym, symSub
      PUP prob, probPtr
endm


PUP_COD macro prob:req, probPtr:req, symSub:req
```

```
    mov     t0, kBitModelOffset
    cmovb   cod, t1
    mov     t1, sym
    cmovb   t0, kBitModelTotal_reg
    PUP_SUB prob, probPtr, symSub
endm


BIT_0 macro prob:req, probNext:req
    PLOAD   prob, probs + 1 * PMULT
    PLOAD   probNext, probs + 1 * PMULT_2

    NORM_CALC prob

    cmovae  range, t0
    PLOAD   t0, probs + 1 * PMULT_2 + PMULT
    cmovae  probNext, t0
    mov     t0, kBitModelOffset
    cmovb   cod, t1
    cmovb   t0, kBitModelTotal_reg
    mov     sym, 2
    PUP_SUB prob, probs + 1 * PMULT, 0 - 1
endm


BIT_1 macro prob:req, probNext:req
    PLOAD   probNext, probs + sym_R * PMULT_2
    add     sym, sym

    NORM_CALC prob

    cmovae  range, t0

    PLOAD   t0, probs + sym_R * PMULT + PMULT
    cmovae  probNext, t0
    PUP_COD prob, probs + t1_R * PMULT_HALF, 0 - 1
endm


BIT_2 macro prob:req, symSub:req
    add     sym, sym

    NORM_CALC prob

    cmovae  range, t0
    PUP_COD prob, probs + t1_R * PMULT_HALF, symSub
endm
```

```
; ---------- MATCHED LITERAL ----------

LITM_0 macro
      mov     offs, 256 * PMULT
      shl     match, (PSHIFT + 1)
      mov     bit, offs
      and     bit, match
      PLOAD   x1, probs + 256 * PMULT + bit_R * 1 + 1 * PMULT
      lea     prm, [probs + 256 * PMULT + bit_R * 1 + 1 * PMULT]
      ; lea     prm, [probs + 256 * PMULT + 1 * PMULT]
      ; add     prm, bit_R
      xor     offs, bit
      add     match, match

      NORM_CALC x1

      cmovae  offs, bit
      mov     bit, match
      cmovae  range, t0
      mov     t0, kBitModelOffset
      cmovb   cod, t1
      cmovb   t0, kBitModelTotal_reg
      mov     sym, 0

 PUP_SUB x1, prm, -2-1
endm


LITM macro
      and     bit, offs
      lea     prm, [probs + offs_R * 1]
      add     prm, bit_R
      PLOAD   x1, prm + sym_R * PMULT
      xor     offs, bit
      add     sym, sym
      add     match, match

      NORM_CALC x1

      cmovae  offs, bit
      mov     bit, match
      cmovae  range, t0
      PUP_COD x1, prm + t1_R * PMULT_HALF, - 1
endm


LITM_2 macro
```

```
      and     bit, offs
      lea     prm, [probs + offs_R * 1]
      add     prm, bit_R
      PLOAD   x1, prm + sym_R * PMULT
      add     sym, sym

      NORM_CALC x1

      cmovae  range, t0
      PUP_COD x1, prm + t1_R * PMULT_HALF, 256 - 1
endm


; ---------- REVERSE BITS ----------

REV_0 macro prob:req, probNext:req
      ; PLOAD   prob, probs + 1 * PMULT
      ; lea     sym2_R, [probs + 2 * PMULT]
      ; PLOAD   probNext, probs + 2 * PMULT
      PLOAD   probNext, sym2_R

      NORM_CALC prob

      cmovae  range, t0
      PLOAD
  t0, probs + 3 * PMULT
      cmovae  probNext, t0
      cmovb   cod, t1
      mov     t0, kBitModelOffset
      cmovb   t0, kBitModelTotal_reg
      lea     t1_R, [probs + 3 * PMULT]
      cmovae  sym2_R, t1_R
      PUP prob, probs + 1 * PMULT
endm


REV_1 macro prob:req, probNext:req, step:req
      add     sym2_R, step * PMULT
      PLOAD   probNext, sym2_R

      NORM_CALC prob

      cmovae  range, t0
      PLOAD   t0, sym2_R + step * PMULT
      cmovae  probNext, t0
      cmovb   cod, t1
      mov     t0, kBitModelOffset
      cmovb   t0, kBitModelTotal_reg
```

```
    lea     t1_R, [sym2_R + step * PMULT]
    cmovae  sym2_R, t1_R
    PUP prob, t1_R - step * PMULT_2
endm


REV_2 macro prob:req, step:req
    sub     sym2_R, probs
    shr     sym2, PSHIFT
    or      sym, sym2

    NORM_CALC prob

    cmovae  range, t0
    lea     t0, [sym - step]
    cmovb   sym, t0
    cmovb   cod, t1
    mov     t0, kBitModelOffset
     cmovb   t0, kBitModelTotal_reg
    PUP prob, probs + sym2_R * PMULT
endm


REV_1_VAR macro prob:req
    PLOAD   prob, sym_R
    mov     probs, sym_R
    add     sym_R, sym2_R

    NORM_CALC prob

    cmovae  range, t0
    lea     t0_R, [sym_R + sym2_R]
    cmovae  sym_R, t0_R
    mov     t0, kBitModelOffset
    cmovb   cod, t1
    ; mov     t1, kBitModelTotal
    ; cmovb   t0, t1
    cmovb   t0, kBitModelTotal_reg
    add     sym2, sym2
    PUP prob, probs
endm




LIT_PROBS macro lpMaskParam:req
    ; prob += (UInt32)3 * ((((processedPos << 8) + dic[(dicPos == 0 ? dicBufSize : dicPos) - 1]) & lpMask) << lc);
    mov     t0, processedPos
```

```
      shl     t0, 8
      add     sym, t0
      and     sym, lpMaskParam
      add     probs_state_R, pbPos_R
      mov     x1, LOC lc2
      lea     sym, dword ptr[sym_R + 2 * sym_R]
      add     probs, Literal * PMULT
      shl     sym, x1_L
      add
    probs, sym_R
      UPDATE_0 probs_state_R, 0, IsMatch
      inc     processedPos
endm



kNumPosBitsMax          equ 4
kNumPosStatesMax        equ (1 SHL kNumPosBitsMax)

kLenNumLowBits          equ 3
kLenNumLowSymbols       equ (1 SHL kLenNumLowBits)
kLenNumHighBits         equ 8
kLenNumHighSymbols      equ (1 SHL kLenNumHighBits)
kNumLenProbs            equ (2 * kLenNumLowSymbols * kNumPosStatesMax + kLenNumHighSymbols)

LenLow                  equ 0
LenChoice               equ LenLow
LenChoice2              equ (LenLow + kLenNumLowSymbols)
LenHigh                 equ (LenLow + 2 * kLenNumLowSymbols * kNumPosStatesMax)

kNumStates              equ 12
kNumStates2             equ 16
kNumLitStates           equ 7

kStartPosModelIndex     equ 4
kEndPosModelIndex       equ 14
kNumFullDistances       equ (1 SHL (kEndPosModelIndex SHR 1))

kNumPosSlotBits         equ 6
kNumLenToPosStates      equ 4

kNumAlignBits           equ 4
kAlignTableSize         equ (1 SHL kNumAlignBits)

kMatchMinLen
          equ 2
kMatchSpecLenStart      equ (kMatchMinLen + kLenNumLowSymbols * 2 + kLenNumHighSymbols)
```

```
kStartOffset    equ 1664
SpecPos         equ (-kStartOffset)
IsRep0Long      equ (SpecPos + kNumFullDistances)
RepLenCoder     equ (IsRep0Long + (kNumStates2 SHL kNumPosBitsMax))
LenCoder        equ (RepLenCoder + kNumLenProbs)
IsMatch         equ (LenCoder + kNumLenProbs)
kAlign          equ (IsMatch + (kNumStates2 SHL kNumPosBitsMax))
IsRep           equ (kAlign + kAlignTableSize)
IsRepG0         equ (IsRep + kNumStates)
IsRepG1         equ (IsRepG0 + kNumStates)
IsRepG2         equ (IsRepG1 + kNumStates)
PosSlot         equ (IsRepG2 + kNumStates)
Literal         equ (PosSlot + (kNumLenToPosStates SHL kNumPosSlotBits))
NUM_BASE_PROBS  equ (Literal + kStartOffset)

if kAlign ne 0
 .err <Stop_Compiling_Bad_LZMA_kAlign>
endif

if NUM_BASE_PROBS ne 1984
 .err <Stop_Compiling_Bad_LZMA_PROBS>
endif


PTR_FIELD equ dq ?

CLzmaDec_Asm struct
      lc      db ?
      lp
     db ?
      pb      db ?
      _pad_   db ?
      dicSize dd ?

      probs_Spec      PTR_FIELD
      probs_1664      PTR_FIELD
      dic_Spec        PTR_FIELD
      dicBufSize      PTR_FIELD
      dicPos_Spec     PTR_FIELD
      buf_Spec        PTR_FIELD

      range_Spec      dd ?
      code_Spec       dd ?
      processedPos_Spec  dd ?
      checkDicSize    dd ?
      rep0    dd ?
      rep1    dd ?
      rep2    dd ?
```

```
    rep3    dd ?
    state_Spec      dd ?
    remainLen dd ?
CLzmaDec_Asm ends


CLzmaDec_Asm_Loc struct
    OLD_RSP    PTR_FIELD
    lzmaPtr    PTR_FIELD
    _pad0_     PTR_FIELD
    _pad1_     PTR_FIELD
    _pad2_     PTR_FIELD
    dicBufSize PTR_FIELD
    probs_Spec PTR_FIELD
    dic_Spec   PTR_FIELD

    limit      PTR_FIELD
    bufLimit   PTR_FIELD
    lc2       dd ?
    lpMask    dd ?
    pbMask    dd ?
    checkDicSize   dd ?

    _pad_     dd ?
     remainLen dd ?
    dicPos_Spec     PTR_FIELD
    rep0      dd ?
    rep1      dd ?
    rep2      dd ?
    rep3      dd ?
CLzmaDec_Asm_Loc ends


GLOB_2  equ [sym_R].CLzmaDec_Asm.
GLOB    equ [r1].CLzmaDec_Asm.
LOC_0   equ [r0].CLzmaDec_Asm_Loc.
LOC     equ [RSP].CLzmaDec_Asm_Loc.


COPY_VAR macro name
    mov     t0, GLOB_2 name
    mov     LOC_0 name, t0
endm


RESTORE_VAR macro name
    mov     t0, LOC name
    mov     GLOB name, t0
```

```
endm


IsMatchBranch_Pre macro reg
      ; prob = probs + IsMatch + (state << kNumPosBitsMax) + posState;
      mov     pbPos, LOC pbMask
      and     pbPos, processedPos
      shl     pbPos, (kLenNumLowBits + 1 + PSHIFT)
      lea     probs_state_R, [probs + state_R]
endm


IsMatchBranch macro reg
      IsMatchBranch_Pre
      IF_BIT_1 probs_state_R, pbPos_R, IsMatch, IsMatch_label
endm


CheckLimits macro reg
      cmp     buf, LOC bufLimit
      jae     fin_OK
      cmp     dicPos,
 LOC limit
      jae     fin_OK
endm



; RSP is (16x + 8) bytes aligned in WIN64-x64
; LocalSize equ ((((SIZEOF CLzmaDec_Asm_Loc) + 7) / 16 * 16) + 8)

PARAM_lzma      equ REG_PARAM_0
PARAM_limit     equ REG_PARAM_1
PARAM_bufLimit  equ REG_PARAM_2

; MY_ALIGN_64
MY_PROC LzmaDec_DecodeReal_3, 3
MY_PUSH_PRESERVED_REGS

      lea     r0, [RSP - (SIZEOF CLzmaDec_Asm_Loc)]
      and     r0, -128
      mov     r5, RSP
      mov     RSP, r0
      mov     LOC_0 Old_RSP, r5
      mov     LOC_0 lzmaPtr, PARAM_lzma

      mov     LOC_0 remainLen, 0  ; remainLen must be ZERO
```

```
mov     LOC_0 bufLimit, PARAM_bufLimit
mov     sym_R, PARAM_lzma  ;  CLzmaDec_Asm_Loc pointer for GLOB_2
mov     dic, GLOB_2 dic_Spec
add     PARAM_limit, dic
mov     LOC_0 limit, PARAM_limit

COPY_VAR(rep0)
COPY_VAR(rep1)
COPY_VAR(rep2)
COPY_VAR(rep3)

mov     dicPos, GLOB_2 dicPos_Spec
add     dicPos, dic
 mov     LOC_0 dicPos_Spec, dicPos
mov     LOC_0 dic_Spec, dic

mov     x1_L, GLOB_2 pb
mov     t0, 1
shl     t0, x1_L
dec     t0
mov     LOC_0 pbMask, t0

; unsigned pbMask = ((unsigned)1 << (p->prop.pb)) - 1;
; unsigned lc = p->prop.lc;
; unsigned lpMask = ((unsigned)0x100 << p->prop.lp) - ((unsigned)0x100 >> lc);

mov     x1_L, GLOB_2 lc
mov     x2, 100h
mov     t0, x2
shr     x2, x1_L
; inc     x1
add     x1_L, PSHIFT
mov     LOC_0 lc2, x1
mov     x1_L, GLOB_2 lp
shl     t0, x1_L
sub     t0, x2
mov     LOC_0 lpMask, t0
mov     lpMask_reg, t0

; mov     probs, GLOB_2 probs_Spec
; add     probs, kStartOffset SHL PSHIFT
mov     probs, GLOB_2 probs_1664
mov     LOC_0 probs_Spec, probs

mov     t0_R, GLOB_2 dicBufSize
mov     LOC_0 dicBufSize, t0_R
```

```
      mov     x1, GLOB_2 checkDicSize
      mov     LOC_0 checkDicSize, x1

      mov     processedPos, GLOB_2 processedPos_Spec

      mov     state, GLOB_2 state_Spec
      shl     state, PSHIFT

      mov     buf,   GLOB_2 buf_Spec
      mov     range, GLOB_2 range_Spec
      mov     cod,   GLOB_2 code_Spec
      mov     kBitModelTotal_reg, kBitModelTotal
      xor     sym, sym

      ; if (processedPos != 0 || checkDicSize != 0)
      or      x1, processedPos
      jz      @f

      add     t0_R, dic
      cmp     dicPos, dic
      cmovnz  t0_R, dicPos
      movzx   sym, byte ptr[t0_R - 1]

@@:
      IsMatchBranch_Pre
      cmp     state, 4 * PMULT
      jb      lit_end
      cmp     state, kNumLitStates * PMULT
      jb      lit_matched_end
      jmp     lz_end




; ---------- LITERAL ----------
MY_ALIGN_64
lit_start:
      xor     state, state
lit_start_2:
      LIT_PROBS lpMask_reg

  ifdef _LZMA_SIZE_OPT

       PLOAD   x1, probs + 1 * PMULT
      mov     sym, 1
MY_ALIGN_16
lit_loop:
```

```
      BIT_1   x1, x2
      mov     x1, x2
      cmp     sym, 127
      jbe     lit_loop

    else

      BIT_0   x1, x2
      BIT_1   x2, x1
      BIT_1   x1, x2
      BIT_1   x2, x1
      BIT_1   x1, x2
      BIT_1   x2, x1
      BIT_1   x1, x2

    endif

      BIT_2   x2, 256 - 1

      ; mov     dic, LOC dic_Spec
      mov     probs, LOC probs_Spec
      IsMatchBranch_Pre
      mov     byte ptr[dicPos], sym_L
      inc     dicPos

      CheckLimits
lit_end:
      IF_BIT_0_NOUP probs_state_R, pbPos_R, IsMatch, lit_start

      ; jmp     IsMatch_label

; ---------- MATCHES ----------
; MY_ALIGN_32
IsMatch_label:
      UPDATE_1 probs_state_R, pbPos_R, IsMatch
      IF_BIT_1 probs_state_R, 0, IsRep, IsRep_label

      add     probs, LenCoder * PMULT
      add     state, kNumStates * PMULT

;
 ---------- LEN DECODE ----------
len_decode:
      mov     len_temp, 8 - 1 - kMatchMinLen
      IF_BIT_0_NOUP probs, 0, 0, len_mid_0
      UPDATE_1 probs, 0, 0
      add     probs, (1 SHL (kLenNumLowBits + PSHIFT))
      mov     len_temp, -1 - kMatchMinLen
```

```
      IF_BIT_0_NOUP probs, 0, 0, len_mid_0
      UPDATE_1 probs, 0, 0
      add     probs, LenHigh * PMULT - (1 SHL (kLenNumLowBits + PSHIFT))
      mov     sym, 1
      PLOAD   x1, probs + 1 * PMULT

MY_ALIGN_32
len8_loop:
      BIT_1   x1, x2
      mov     x1, x2
      cmp     sym, 64
      jb      len8_loop

      mov     len_temp, (kLenNumHighSymbols - kLenNumLowSymbols * 2) - 1 - kMatchMinLen
      jmp     len_mid_2

MY_ALIGN_32
len_mid_0:
      UPDATE_0 probs, 0, 0
      add     probs, pbPos_R
      BIT_0   x2, x1
len_mid_2:
      BIT_1   x1, x2
      BIT_2   x2, len_temp
      mov     probs, LOC probs_Spec
      cmp     state, kNumStates * PMULT
      jb      copy_match


; ---------- DECODE DISTANCE ----------
      ; probs + PosSlot + ((len < kNumLenToPosStates ? len : kNumLenToPosStates - 1) << kNumPosSlotBits);

      mov     t0, 3 + kMatchMinLen
      cmp     sym, 3 + kMatchMinLen
      cmovb   t0, sym
      add     probs, PosSlot * PMULT - (kMatchMinLen SHL (kNumPosSlotBits + PSHIFT))
      shl     t0, (kNumPosSlotBits + PSHIFT)
      add     probs, t0_R

      ; sym = Len
      ; mov     LOC remainLen, sym
      mov     len_temp, sym

  ifdef _LZMA_SIZE_OPT

      PLOAD   x1, probs + 1 * PMULT
      mov     sym, 1
MY_ALIGN_16
```

```
slot_loop:
      BIT_1   x1, x2
      mov     x1, x2
      cmp     sym, 32
      jb      slot_loop

  else

      BIT_0   x1, x2
      BIT_1   x2, x1
      BIT_1   x1, x2
      BIT_1   x2, x1
      BIT_1   x1, x2

  endif

      mov     x1, sym
      BIT_2   x2, 64-1

      and     sym, 3
      mov     probs, LOC
 probs_Spec
      cmp     x1, 32 + kEndPosModelIndex / 2
      jb      short_dist

      ;  unsigned numDirectBits = (unsigned)(((distance >> 1) - 1));
      sub     x1, (32 + 1 + kNumAlignBits)
      ;  distance = (2 | (distance & 1));
      or      sym, 2
      PLOAD   x2, probs + 1 * PMULT
      shl     sym, kNumAlignBits + 1
      lea     sym2_R, [probs + 2 * PMULT]

      jmp     direct_norm
      ; lea     t1, [sym_R + (1 SHL kNumAlignBits)]
      ; cmp     range, kTopValue
      ; jb      direct_norm

; ---------- DIRECT DISTANCE ----------
MY_ALIGN_32
direct_loop:
      shr     range, 1
      mov     t0, cod
      sub     cod, range
      cmovs   cod, t0
      cmovns  sym, t1

      comment ~
```

```
      sub     cod, range
      mov     x2, cod
      sar     x2, 31
      lea     sym, dword ptr [r2 + sym_R * 2 + 1]
      and     x2, range
      add     cod, x2
      ~
      dec     x1
      je      direct_end

       add     sym, sym
direct_norm:
      lea     t1, [sym_R + (1 SHL kNumAlignBits)]
      cmp     range, kTopValue
      jae     near ptr direct_loop
      ; we align for 32 here with "near ptr" command above
      NORM_2
      jmp     direct_loop

MY_ALIGN_32
direct_end:
      ;  prob =  + kAlign;
      ;  distance <<= kNumAlignBits;
      REV_0   x2, x1
      REV_1   x1, x2, 2
      REV_1   x2, x1, 4
      REV_2   x1, 8

decode_dist_end:

      ; if (distance >= (checkDicSize == 0 ? processedPos: checkDicSize))

      mov     t0, LOC checkDicSize
      test    t0, t0
      cmove   t0, processedPos
      cmp     sym, t0
      jae     end_of_payload

      ; rep3 = rep2;
      ; rep2 = rep1;
      ; rep1 = rep0;
      ; rep0 = distance + 1;

      inc     sym
      mov     t0, LOC rep0
      mov     t1, LOC rep1
      mov     x1, LOC rep2
      mov     LOC rep0, sym
```

```
      ; mov     sym, LOC remainLen
      mov
    sym, len_temp
      mov     LOC rep1, t0
      mov     LOC rep2, t1
      mov     LOC rep3, x1

      ; state = (state < kNumStates + kNumLitStates) ? kNumLitStates : kNumLitStates + 3;
      cmp     state, (kNumStates + kNumLitStates) * PMULT
      mov     state, kNumLitStates * PMULT
      mov     t0, (kNumLitStates + 3) * PMULT
      cmovae  state, t0


; ---------- COPY MATCH ----------
copy_match:

      ; len += kMatchMinLen;
      ; add     sym, kMatchMinLen

      ; if ((rem = limit - dicPos) == 0)
      ; {
      ;   p->dicPos = dicPos;
      ;   return SZ_ERROR_DATA;
      ; }
      mov     cnt_R, LOC limit
      sub     cnt_R, dicPos
      jz      fin_ERROR

      ; curLen = ((rem < len) ? (unsigned)rem : len);
      cmp     cnt_R, sym_R
      ; cmovae  cnt_R, sym_R ; 64-bit
      cmovae  cnt, sym ; 32-bit

      mov     dic, LOC dic_Spec
      mov     x1, LOC rep0

      mov     t0_R, dicPos
      add
    dicPos, cnt_R
      ; processedPos += curLen;
      add     processedPos, cnt
      ; len -= curLen;
      sub     sym, cnt
      mov     LOC remainLen, sym

      sub     t0_R, dic
```

```
      ; pos = dicPos - rep0 + (dicPos < rep0 ? dicBufSize : 0);
      sub     t0_R, r1
      jae     @f

      mov     r1, LOC dicBufSize
      add     t0_R, r1
      sub     r1, t0_R
      cmp     cnt_R, r1
      ja      copy_match_cross
@@:
      ; if (curLen <= dicBufSize - pos)

; ---------- COPY MATCH FAST ----------
      ; Byte *dest = dic + dicPos;
      ; mov     r1, dic
      ; ptrdiff_t src = (ptrdiff_t)pos - (ptrdiff_t)dicPos;
      ; sub   t0_R, dicPos
      ; dicPos += curLen;

      ; const Byte *lim = dest + curLen;
      add     t0_R, dic
      movzx   sym, byte ptr[t0_R]
      add     t0_R, cnt_R
      neg     cnt_R
      ; lea     r1, [dicPos - 1]
copy_common:
      dec     dicPos
      ; cmp   LOC rep0, 1
      ; je
 rep0Label

      ; t0_R - src_lim
      ; r1 - dest_lim - 1
      ; cnt_R - (-cnt)

      IsMatchBranch_Pre
      inc     cnt_R
      jz      copy_end
MY_ALIGN_16
@@:
      mov     byte ptr[cnt_R * 1 + dicPos], sym_L
      movzx   sym, byte ptr[cnt_R * 1 + t0_R]
      inc     cnt_R
      jnz     @b

copy_end:
lz_end_match:
      mov     byte ptr[dicPos], sym_L
```

```
      inc     dicPos

      ; IsMatchBranch_Pre
      CheckLimits
lz_end:
      IF_BIT_1_NOUP probs_state_R, pbPos_R, IsMatch, IsMatch_label



; ---------- LITERAL MATCHED ----------

      LIT_PROBS LOC lpMask

      ; matchByte = dic[dicPos - rep0 + (dicPos < rep0 ? dicBufSize : 0)];
      mov     x1, LOC rep0
      ; mov     dic, LOC dic_Spec
      mov     LOC dicPos_Spec, dicPos

      ; state -= (state < 10) ? 3 : 6;
      lea     t0, [state_R - 6 * PMULT]
      sub     state, 3 * PMULT
      cmp     state, 7 * PMULT
      cmovae  state, t0

      sub     dicPos, dic
      sub     dicPos, r1
      jae     @f
      add     dicPos, LOC dicBufSize
@@:
      comment ~
      xor     t0, t0
      sub     dicPos, r1
      cmovb   t0_R, LOC dicBufSize
      ~

      movzx   match, byte ptr[dic + dicPos * 1]

  ifdef _LZMA_SIZE_OPT

      mov     offs, 256 * PMULT
      shl     match, (PSHIFT + 1)
      mov     bit, match
      mov     sym, 1
MY_ALIGN_16
litm_loop:
      LITM
      cmp     sym, 256
      jb      litm_loop
```

```
    sub     sym, 256

  else

    LITM_0
    LITM
    LITM
    LITM
    LITM
    LITM
    LITM
    LITM_2

  endif

    mov     probs, LOC probs_Spec
    IsMatchBranch_Pre
    ; mov     dic, LOC dic_Spec
    mov     dicPos, LOC dicPos_Spec
    mov     byte ptr[dicPos], sym_L
    inc     dicPos

    CheckLimits
lit_matched_end:
    IF_BIT_1_NOUP probs_state_R,
 pbPos_R, IsMatch, IsMatch_label
    ; IsMatchBranch
    mov     lpMask_reg, LOC lpMask
    sub     state, 3 * PMULT
    jmp     lit_start_2



; ---------- REP 0 LITERAL ----------
MY_ALIGN_32
IsRep0Short_label:
    UPDATE_0 probs_state_R, pbPos_R, IsRep0Long

    ; dic[dicPos] = dic[dicPos - rep0 + (dicPos < rep0 ? dicBufSize : 0)];
    mov     dic, LOC dic_Spec
    mov     t0_R, dicPos
    mov     probBranch, LOC rep0
    sub     t0_R, dic

    sub     probs, RepLenCoder * PMULT
    inc     processedPos
    ; state = state < kNumLitStates ? 9 : 11;
    or      state, 1 * PMULT
```

```
      IsMatchBranch_Pre

      sub     t0_R, probBranch_R
      jae     @f
      add     t0_R, LOC dicBufSize
@@:
      movzx   sym, byte ptr[dic + t0_R * 1]
      jmp     lz_end_match


MY_ALIGN_32
IsRep_label:
      UPDATE_1 probs_state_R, 0, IsRep

      ; The (checkDicSize == 0 && processedPos ==
 0) case was checked before in LzmaDec.c with kBadRepCode.
      ; So we don't check it here.

      ; mov     t0, processedPos
      ; or      t0, LOC checkDicSize
      ; jz      fin_ERROR_2

      ; state = state < kNumLitStates ? 8 : 11;
      cmp     state, kNumLitStates * PMULT
      mov     state, 8 * PMULT
      mov     probBranch, 11 * PMULT
      cmovae  state, probBranch

      ; prob = probs + RepLenCoder;
      add     probs, RepLenCoder * PMULT

      IF_BIT_1 probs_state_R, 0, IsRepG0, IsRepG0_label
      IF_BIT_0_NOUP probs_state_R, pbPos_R, IsRep0Long, IsRep0Short_label
      UPDATE_1 probs_state_R, pbPos_R, IsRep0Long
      jmp     len_decode

MY_ALIGN_32
IsRepG0_label:
      UPDATE_1 probs_state_R, 0, IsRepG0
      mov     dist2, LOC rep0
      mov     dist, LOC rep1
      mov     LOC rep1, dist2

      IF_BIT_1 probs_state_R, 0, IsRepG1, IsRepG1_label
      mov     LOC rep0, dist
      jmp     len_decode

; MY_ALIGN_32
```

```
IsRepG1_label:
      UPDATE_1 probs_state_R, 0, IsRepG1
      mov     dist2, LOC rep2
      mov     LOC rep2, dist

      IF_BIT_1 probs_state_R, 0, IsRepG2, IsRepG2_label
      mov     LOC rep0, dist2
      jmp     len_decode

; MY_ALIGN_32
IsRepG2_label:
      UPDATE_1 probs_state_R, 0, IsRepG2
      mov     dist, LOC rep3
      mov     LOC rep3, dist2
      mov     LOC rep0, dist
      jmp     len_decode



; ---------- SPEC SHORT DISTANCE ----------

MY_ALIGN_32
short_dist:
      sub     x1, 32 + 1
      jbe     decode_dist_end
      or      sym, 2
      shl     sym, x1_L
      lea     sym_R, [probs + sym_R * PMULT + SpecPos * PMULT + 1 * PMULT]
      mov     sym2, PMULT ; step
MY_ALIGN_32
spec_loop:
      REV_1_VAR x2
      dec     x1
      jnz     spec_loop

      mov     probs, LOC probs_Spec
      sub     sym, sym2
      sub     sym, SpecPos * PMULT
      sub     sym_R, probs
      shr
    sym, PSHIFT

      jmp     decode_dist_end


; ---------- COPY MATCH CROSS ----------
copy_match_cross:
      ; t0_R - src pos
```

```
      ; r1 - len to dicBufSize
      ; cnt_R - total copy len

      mov     t1_R, t0_R        ; srcPos
      mov     t0_R, dic
      mov     r1, LOC dicBufSize   ;
      neg     cnt_R
@@:
      movzx   sym, byte ptr[t1_R * 1 + t0_R]
      inc     t1_R
      mov     byte ptr[cnt_R * 1 + dicPos], sym_L
      inc     cnt_R
      cmp     t1_R, r1
      jne     @b

      movzx   sym, byte ptr[t0_R]
      sub     t0_R, cnt_R
      jmp     copy_common




fin_ERROR:
      mov     LOC remainLen, len_temp
; fin_ERROR_2:
      mov     sym, 1
      jmp     fin

end_of_payload:
      cmp     sym, 0FFFFFFFFh ; -1
      jne     fin_ERROR

      mov     LOC remainLen, kMatchSpecLenStart
      sub     state, kNumStates * PMULT

fin_OK:
      xor     sym, sym

fin:
      NORM

      mov     r1, LOC lzmaPtr

       sub     dicPos, LOC dic_Spec
      mov     GLOB dicPos_Spec, dicPos
      mov     GLOB buf_Spec, buf
      mov     GLOB range_Spec, range
      mov     GLOB code_Spec, cod
```

```
    shr     state, PSHIFT
    mov     GLOB state_Spec, state
    mov     GLOB processedPos_Spec, processedPos

    RESTORE_VAR(remainLen)
    RESTORE_VAR(rep0)
    RESTORE_VAR(rep1)
    RESTORE_VAR(rep2)
    RESTORE_VAR(rep3)

    mov     x0, sym

    mov     RSP, LOC Old_RSP

MY_POP_PRESERVED_REGS
MY_ENDP

_TEXT$LZMADECOPT ENDS

end
```

```
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TORT OR OTHERWISE, ARISING
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SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
```

```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
```

```
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Mixed text Element Language Tags 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element matches the schema. Two text elements have a language
tags." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
  {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text xml:lang="en"&gt;
   Copyright with "en" language.
  &lt;/text&gt;
  &lt;text xml:lang="fr"&gt;
   Copyright with "fr" language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
## -*-makefile-*-
## Copyright (C) 2016 and later: Unicode, Inc. and others.
## License & terms of use: http://www.unicode.org/copyright.html
```

```
## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
## Copyright (c) 1999-2013, International Business Machines Corporation and
## others. All Rights Reserved.

## Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation
 rules
%.$(STATIC_O): $(srcdir)/%.c
 $(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
 $(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
 $(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
 $(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<



## Dependency rules
%.d: $(srcdir)/%.c
 @echo "generating dependency information for $<"
```

```
 @$(SHELL) -ec '$(GEN_DEPS.c) $< \
  | sed '\"s/\($*\)\.o[ :]*/\1.o $@ : /g'\" > $@; \
  [ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
 @echo "generating dependency information for $<"
 @$(SHELL) -ec '$(GEN_DEPS.cc) $< \
  | sed '\"s/\($*\)\.o[ :]*/\1.o $@ : /g'\" > $@; \
  [ -s $@ ] || rm -f $@'

## Versioned libraries rules

%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
 $(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
 $(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

##  Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup
```

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.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
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+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$.ldL9:Zh|4Q!pW3@d9~ax0)bT/~

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element with rtl as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text dir="rtl"&gt;
   Copyright without language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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Files that don't get a copyright, or which are taken from elsewhere.
#
All lines in this file are patterns, including the comment lines; this
means that e.g. `FTL.TXT' matches all files that have this string in
the file name (including the path relative to the current directory,
always starting with `./').
#
Don't put empty lines into this file!
#
.gitignore
#
builds/unix/pkg.m4
#
docs/FTL.TXT
docs/GPLv2.TXT
#
include/freetype/internal/fthash.h
#
src/base/fthash.c
src/base/md5.c

src/base/md5.h
#
src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
src/bdf/rules.mk
#
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
src/pcf/pcfutil.c
src/pcf/pcfutil.h
src/pcf/README
src/pcf/rules.mk
#
src/gzip/adler32.c
src/gzip/infblock.c
src/gzip/infblock.h
src/gzip/infcodes.c
src/gzip/infcodes.h
src/gzip/inffixed.h
src/gzip/inflate.c
src/gzip/inftrees.c
src/gzip/inftrees.h
src/gzip/infutil.c
src/gzip/infutil.h
src/gzip/zconf.h
src/gzip/zlib.h
src/gzip/zutil.c
src/gzip/zutil.h
#
src/tools/apinames.c
src/tools/ftrandom/ftrandom.c
#
#
EOF
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">

```
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid copyright Element With dir Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-029-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a span element with
INVALID as the value for the
 dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-029.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
```

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- -

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http://tug.org/svn/texhyphen/trunk/hyph-utf8/tex/generic/hyph-utf8/patterns/?pathrev=580

- -

Portions of this file were originally made available under the following license
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Latin hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted
from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%

```
% Source: lahyph.tex (2007-09-03)
% Author: Claudio Beccari <claudio.beccari at polito.it>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%             and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated
 patterns into TeX Live
%
% However, the "copyright/copyleft" owner of patterns remains the original author.
%
% The copyright statement of this file is thus:
%
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%    If you're the original author of patterns or taking over a new revolution,
%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
%
 - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
```

```
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-------------------------------------------------------------------------------
%
%                ********** lahyph.tex **************
%
% Copyright 1999- 2001 Claudio Beccari
%               [latin hyphenation patterns]
%
% ----------------------------------------------------------------
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%
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% archives in directory macros/latex/base/lppl.txt; either
% version 1 of the License, or any later version.
% ----------------------------------------------------------------
%
% Patterns for the latin language mainly in modern spelling
% (u when u is needed and v when v is needed); medieval spelling
% with the ligatures \ae and \oe  and the (uncial) lowercase `v'
% written as a `u' is also supported; apparently there is no conflict
% between the patterns of modern  Latin and those of medieval Latin.
%
% Support for font encoding T1 with a 256-character set.
%
% Prepared by  Claudio Beccari
%              Politecnico di Torino
%              Torino, Italy
%              e-mail beccari@polito.it
%
% 1999/03/10 Integration of `lahyph7.tex' and `lahyph8.tex' into
% one file `lahyph.tex' supporting fonts in OT1 and T1 encoding by
% Bernd Raichle using the macro code from `dehypht.tex' (this code
% is Copyright 1993,1994,1998,1999
 Bernd Raichle/DANTE e.V.).
%
% 2010/05/31 Removal of OT1 support
% 2010/06/01 Removal of pattern 2'2 (probably a leftover from Italian)
%
```

```
% \versionnumber{3.2}   \versiondate{2010/06/01}
%
% Information after \endinput.
%
%%%
```

```
  <div class="test">P</div>
 </body>
</html>
```

```
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; XzCrc64Opt.asm -- CRC64 calculation : optimized version
; 2011-06-28 : Igor Pavlov : Public domain

include 7zAsm.asm
```

```
MY_ASM_START

ifdef x64

  rD   equ  r9
  rN   equ  r10

  num_VAR     equ  r8
  table_VAR   equ  r9

  SRCDAT  equ  rN + rD

CRC_XOR macro dest:req, src:req, t:req
  xor     dest, QWORD PTR [r5 + src * 8 + 0800h * t]
endm

CRC1b macro
  movzx   x6, BYTE PTR [rD]
  inc     rD
  movzx   x3, x0_L
  xor     x6, x3
  shr     r0, 8
  CRC_XOR r0, r6, 0
  dec     rN
endm

MY_PROLOG macro crc_end:req
  MY_PUSH_4_REGS

  mov     r0, r1
  mov     rN, num_VAR
  mov     r5, table_VAR
  mov     rD, r2
  test    rN, rN
  jz      crc_end
 @@:
  test    rD, 3
  jz      @F
  CRC1b
  jnz     @B
 @@:
  cmp     rN, 8
  jb      crc_end
  add     rN, rD
  mov     num_VAR, rN
  sub     rN, 4
  and     rN, NOT 3
```

```
  sub     rD, rN
  mov     x1, [SRCDAT]
  xor     r0, r1
  add     rN, 4
endm

MY_EPILOG macro crc_end:req
   sub     rN, 4
  mov     x1, [SRCDAT]
  xor     r0, r1
  mov     rD, rN
  mov     rN, num_VAR
  sub     rN, rD
 crc_end:
  test    rN, rN
  jz      @F
  CRC1b
  jmp     crc_end
 @@:
  MY_POP_4_REGS
endm

MY_PROC XzCrc64UpdateT4, 4
  MY_PROLOG crc_end_4
  align 16
 main_loop_4:
  mov     x1, [SRCDAT]
  movzx   x2, x0_L
  movzx   x3, x0_H
  shr     r0, 16
  movzx   x6, x0_L
  movzx   x7, x0_H
  shr     r0, 16
  CRC_XOR r1, r2, 3
  CRC_XOR r0, r3, 2
  CRC_XOR r1, r6, 1
  CRC_XOR r0, r7, 0
  xor     r0, r1

  add     rD, 4
  jnz     main_loop_4

  MY_EPILOG crc_end_4
MY_ENDP

else

  rD   equ  r1
```

```
  rN   equ  r7

  crc_val     equ (REG_SIZE * 5)
  crc_table   equ (8 + crc_val)
  table_VAR   equ [r4 + crc_table]
  num_VAR     equ table_VAR


  SRCDAT  equ  rN + rD

CRC macro op0:req, op1:req, dest0:req, dest1:req, src:req, t:req
  op0     dest0, DWORD PTR [r5 + src * 8 + 0800h * t]
  op1     dest1, DWORD PTR [r5 + src
 * 8 + 0800h * t + 4]
endm

CRC_XOR macro dest0:req, dest1:req, src:req, t:req
  CRC xor, xor, dest0, dest1, src, t
endm


CRC1b macro
  movzx   x6, BYTE PTR [rD]
  inc     rD
  movzx   x3, x0_L
  xor     x6, x3
  shrd    r0, r2, 8
  shr     r2, 8
  CRC_XOR r0, r2, r6, 0
  dec     rN
endm

MY_PROLOG macro crc_end:req
  MY_PUSH_4_REGS

  mov     rN, r2

  mov     x0, [r4 + crc_val]
  mov     x2, [r4 + crc_val + 4]
  mov     r5, table_VAR
  test    rN, rN
  jz      crc_end
 @@:
  test    rD, 3
  jz      @F
  CRC1b
  jnz     @B
 @@:
```

```
  cmp     rN, 8
  jb      crc_end
  add     rN, rD

  mov     num_VAR, rN

  sub     rN, 4
  and     rN, NOT 3
  sub     rD, rN
  xor     r0, [SRCDAT]
  add     rN, 4
endm

MY_EPILOG macro crc_end:req
  sub     rN, 4
  xor     r0, [SRCDAT]

  mov     rD, rN
  mov     rN, num_VAR
  sub     rN, rD
 crc_end:
  test    rN, rN
  jz      @F
  CRC1b
  jmp     crc_end
 @@:
  MY_POP_4_REGS
endm

MY_PROC
 XzCrc64UpdateT4, 5
  MY_PROLOG crc_end_4
  movzx   x6, x0_L
  align 16
 main_loop_4:
  mov     r3, [SRCDAT]
  xor     r3, r2

  CRC xor, mov, r3, r2, r6, 3
  movzx   x6, x0_H
  shr     r0, 16
  CRC_XOR r3, r2, r6, 2

  movzx   x6, x0_L
  movzx   x0, x0_H
  CRC_XOR r3, r2, r6, 1
  CRC_XOR r3, r2, r0, 0
  movzx   x6, x3_L
```

```
  mov     r0, r3

  add     rD, 4
  jnz     main_loop_4

  MY_EPILOG crc_end_4
MY_ENDP

endif

end
```

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limitations under the License.
wOF2OTTOE @q
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$wdV*'2Ff&}~@|Sy(mtP7|LqV~t0f>a($ a&3Z9:
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OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
wOF2OTTOE @q
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$wpNJbK1qacsTc#H%k
FJ)"N_w6C8IY:'j:/Z>a['Ek$
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid license Element With dir Attribute in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />

```
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element with INVALID as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
  <div class="test">P</div>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-012-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element with rtl as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-012.woff2") format("woff2");
```

```
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text dir="rtl"&gt;
   Copyright without language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Unknown Attribute in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-011-ref.xht" />
  <meta name="assert" content="The license element contains an unknown attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
```

```
   font-family:
 "WOFF Test";
   src: url("support/metadatadisplay-schema-license-011.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
  <div class="test">P</div>
 </body>
</html>
wOF2OTTO5 @a
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$vLg*cc>
?r s0jN3o!yEtV[F
-+:'2MK,
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: More Than One license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
```

```
 <meta name="assert" content="The license element occurs more than once." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test
 CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in div Element in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-024-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a div element with rtl as the value for the dir attribute." />
  <style
 type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-024.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
```

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;div dir="rtl"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
wOF2OTTOM @y
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$lG%C&NK}+x|~J%AD`b|d7@]dX\ M/+Bb
P/q;=-R8Qzl_
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Two span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element that contains two span elements." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size:
 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
    Text with &lt;span&gt;span 1&lt;/span&gt; and &lt;span&gt;span 2&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
```

</metadata>
</pre>
</body>
</html>
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wOF2OTTOF @r
.`f6$ Q

NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,lG#gLPI&s{W|gb7-q?5SuvqdgjCs{N;4[,kH^aj%
License information for hyph_fi.dic:

This file is based on the Finnish hyphenation patterns for OpenOffice.org,
which in turn are based on patterns for TeX.

- -

At the time this file was first modified, a complete, unmodified copy of
the original work was available from:
http://openoffice.org/bugzilla/attachment.cgi?id=42837

- -

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%
% -----> Finnish hyphenation patterns for MLPCTeX <------
%
% + January 1986: first release by Kauko Saarinen,
% Computing Centre, University of Jyvaskyla, Finland
%
% + January 1988: completely rewritten by Kauko Saarinen.
% The new patterns make much less mistakes with foreign
% and compound words. The article "Automatic Hyphenation
% of Finnish" by Professor Fred Karlsson is also referred
%
% + 8th March 1989 (vers. 2.2): some vowel
triples added by
% Kauko Saarinen based on Fred Karlsson's ideas
%
% + 9th January 1995: added \uccode and \lccode by Thomas Esser
%
% + 24th May 2004: changelog reformatted for disambiguation and
% Thomas Esser's \[u/l]ccode changes complete rewritten to make
% certain Saarinen is the sole copyright owner of the file again,
% by Jarno Elonen (who donated all his modification works into
% the Public Domain.)
%
% + 24th May 2004: rewrote donated into the Public Domain
% by Kauko Saarinen

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2006-Jan-27

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David Turner, Robert Wilhelm, and Werner Lemberg

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==========

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```
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"""
```

Please replace <year> with the value from the FreeType version you actually use.

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- freetype@nongnu.org

  Discusses general use and applications of FreeType, as well as future and wanted additions to the library and distribution. If you are looking for support, start in this list if you haven't found anything to help you in the documentation.

- freetype-devel@nongnu.org

  Discusses bugs, as well as engine internals, design issues, specific licenses, porting, etc.

Our home page can be found at

https://www.freetype.org

--- end of FTL.TXT ---

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid license Element With dir Attribute in span Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-032-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a span element with INVALID as the value for the dir attribute."
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-032.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
```

```
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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wOF2OTTO0 @\
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,mc,yr-}~]|@
:%` nHyg%
o*rO7r`qd.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in div Element in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-021-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element with rtl as the value for the dir attribute."
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-021.woff2") format("woff2");
   }

```
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   &lt;div dir="rtl"&gt;Paragraph 1&lt;/div&gt;
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
wOF2OTTOE @q
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$x.)7TzU%OU)x)S<Q+JLTb fwDl{gTFb34Xey/&#
```

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```
PROG = lzma
CXX = g++ -O2
# -Wall -Werror -Wno-delete-non-virtual-dtor
CXX_C = gcc -O2 -Wall -Werror

ifdef SystemDrive
IS_MINGW = 1
endif

ifdef IS_MINGW

RM = del
CFLAGS = -c
LIB2 = -loleaut32 -luuid
LDFLAGS = -s

FILE_IO =FileIO
FILE_IO_2 =Windows/$(FILE_IO)

MT_FILES = \
 LzFindMt.o \
 Threads.o \

else

RM = rm -f
CFLAGS = -c -D_7ZIP_ST

FILE_IO =C_FileIO
FILE_IO_2 =Common/$(FILE_IO)


endif


OBJS = \
```

```
 $(MT_FILES) \
 $(FILE_IO).o \
 LzmaAlone.o \
 Bench.o \
 BenchCon.o \
 ConsoleClose.o \
 LzmaDecoder.o \
 LzmaEncoder.o \
 LzmaRegister.o \
 CreateCoder.o \
 CWrappers.o \
 FileStreams.o \
 FilterCoder.o \
 MethodProps.o \
 StreamUtils.o \
 CommandLineParser.o \
 CRC.o \
 CrcReg.o \
 IntToString.o \
 MyString.o \
 MyVector.o \
 MyWindows.o \
 StringConvert.o \
 StringToInt.o \
 PropVariant.o \
 System.o \
 7zCrc.o \
 7zCrcOpt.o \
 Alloc.o \
 Bra86.o \
 CpuArch.o \
 LzFind.o \
 LzmaDec.o \
 LzmaEnc.o \
 Lzma86Dec.o \
 Lzma86Enc.o \


all: $(PROG)

$(PROG):
 $(OBJS)
 $(CXX) -o $(PROG) $(LDFLAGS) $(OBJS) $(LIB2)

LzmaAlone.o: LzmaAlone.cpp
 $(CXX) $(CFLAGS) LzmaAlone.cpp

Bench.o: ../../UI/Common/Bench.cpp
```

```
	$(CXX) $(CFLAGS) ../../UI/Common/Bench.cpp

BenchCon.o: ../../UI/Console/BenchCon.cpp
	$(CXX) $(CFLAGS) ../../UI/Console/BenchCon.cpp

ConsoleClose.o: ../../UI/Console/ConsoleClose.cpp
	$(CXX) $(CFLAGS) ../../UI/Console/ConsoleClose.cpp

LzmaDecoder.o: ../../Compress/LzmaDecoder.cpp
	$(CXX) $(CFLAGS) ../../Compress/LzmaDecoder.cpp

LzmaEncoder.o: ../../Compress/LzmaEncoder.cpp
	$(CXX) $(CFLAGS) ../../Compress/LzmaEncoder.cpp

LzmaRegister.o: ../../Compress/LzmaRegister.cpp
	$(CXX) $(CFLAGS) ../../Compress/LzmaRegister.cpp

CreateCoder.o: ../../Common/CreateCoder.cpp
	$(CXX) $(CFLAGS) ../../Common/CreateCoder.cpp

CWrappers.o: ../../Common/CWrappers.cpp
	$(CXX) $(CFLAGS) ../../Common/CWrappers.cpp

FileStreams.o: ../../Common/FileStreams.cpp
	$(CXX) $(CFLAGS) ../../Common/FileStreams.cpp

FilterCoder.o: ../../Common/FilterCoder.cpp
	$(CXX)
	$(CFLAGS) ../../Common/FilterCoder.cpp

MethodProps.o: ../../Common/MethodProps.cpp
	$(CXX) $(CFLAGS) ../../Common/MethodProps.cpp

StreamUtils.o: ../../Common/StreamUtils.cpp
	$(CXX) $(CFLAGS) ../../Common/StreamUtils.cpp

$(FILE_IO).o: ../../../$(FILE_IO_2).cpp
	$(CXX) $(CFLAGS) ../../../$(FILE_IO_2).cpp


CommandLineParser.o: ../../../Common/CommandLineParser.cpp
	$(CXX) $(CFLAGS) ../../../Common/CommandLineParser.cpp

CRC.o: ../../../Common/CRC.cpp
	$(CXX) $(CFLAGS) ../../../Common/CRC.cpp

CrcReg.o: ../../../Common/CrcReg.cpp
	$(CXX) $(CFLAGS) ../../../Common/CrcReg.cpp
```

```
IntToString.o: ../../../Common/IntToString.cpp
 $(CXX) $(CFLAGS) ../../../Common/IntToString.cpp

MyString.o: ../../../Common/MyString.cpp
 $(CXX) $(CFLAGS) ../../../Common/MyString.cpp

MyVector.o: ../../../Common/MyVector.cpp
 $(CXX) $(CFLAGS) ../../../Common/MyVector.cpp

MyWindows.o: ../../../Common/MyWindows.cpp
 $(CXX) $(CFLAGS) ../../../Common/MyWindows.cpp

StringConvert.o: ../../../Common/StringConvert.cpp
 $(CXX)
 $(CFLAGS) ../../../Common/StringConvert.cpp

StringToInt.o: ../../../Common/StringToInt.cpp
 $(CXX) $(CFLAGS) ../../../Common/StringToInt.cpp

PropVariant.o: ../../../Windows/PropVariant.cpp
 $(CXX) $(CFLAGS) ../../../Windows/PropVariant.cpp

System.o: ../../../Windows/System.cpp
 $(CXX) $(CFLAGS) ../../../Windows/System.cpp

7zCrc.o: ../../../../C/7zCrc.c
 $(CXX_C) $(CFLAGS) ../../../../C/7zCrc.c

7zCrcOpt.o: ../../../../C/7zCrcOpt.c
 $(CXX_C) $(CFLAGS) ../../../../C/7zCrcOpt.c

Alloc.o: ../../../../C/Alloc.c
 $(CXX_C) $(CFLAGS) ../../../../C/Alloc.c

Bra86.o: ../../../../C/Bra86.c
 $(CXX_C) $(CFLAGS) ../../../../C/Bra86.c

CpuArch.o: ../../../../C/CpuArch.c
 $(CXX_C) $(CFLAGS) ../../../../C/CpuArch.c

LzFind.o: ../../../../C/LzFind.c
 $(CXX_C) $(CFLAGS) ../../../../C/LzFind.c

ifdef MT_FILES
LzFindMt.o: ../../../../C/LzFindMt.c
 $(CXX_C) $(CFLAGS) ../../../../C/LzFindMt.c
```

```
Threads.o: ../../../../C/Threads.c
 $(CXX_C) $(CFLAGS) ../../../../C/Threads.c
endif

LzmaDec.o:
 ../../../../C/LzmaDec.c
 $(CXX_C) $(CFLAGS) ../../../../C/LzmaDec.c

LzmaEnc.o: ../../../../C/LzmaEnc.c
 $(CXX_C) $(CFLAGS) ../../../../C/LzmaEnc.c

Lzma86Dec.o: ../../../../C/Lzma86Dec.c
 $(CXX_C) $(CFLAGS) ../../../../C/Lzma86Dec.c

Lzma86Enc.o: ../../../../C/Lzma86Enc.c
 $(CXX_C) $(CFLAGS) ../../../../C/Lzma86Enc.c

clean:
 -$(RM) $(PROG) $(OBJS)
```

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wOF2OTTOJ @v
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf

s$x.)7TyU%BB%IxmLo- mx<ISWe
?zG}]MZ(z ,JfM
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*
* ---
* Author: Kostya Serebryany
*/
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid copyright Element With dir Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-022-ref.xht" />
  <meta name="assert" content="The copyright element contains a text element that contains a div element with
INVALID as the value for the dir
 attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-022.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
```

```
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

```
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limitations under the Licenses.
PROG = 7zra.dll
DEF_FILE = ../../Archive/Archive2.def
CFLAGS = $(CFLAGS) \
 -D_NO_CRYPTO

COMMON_OBJS = \
 $O\CRC.obj \
 $O\CrcReg.obj \
 $O\IntToString.obj \
 $O\NewHandler.obj \
 $O\MyString.obj \
 $O\StringConvert.obj \
 $O\StringToInt.obj \
 $O\MyVector.obj \
 $O\Wildcard.obj \

WIN_OBJS = \
 $O\FileDir.obj \
```

```
 $O\FileFind.obj \
 $O\FileIO.obj \
 $O\FileName.obj \
 $O\PropVariant.obj \
 $O\Synchronization.obj \
 $O\System.obj \

7ZIP_COMMON_OBJS = \
 $O\CreateCoder.obj \
 $O\CWrappers.obj \
 $O\InBuffer.obj \
 $O\InOutTempBuffer.obj \
 $O\FilterCoder.obj \
 $O\LimitedStreams.obj \
 $O\MethodId.obj \
 $O\MethodProps.obj \
 $O\OutBuffer.obj \
 $O\ProgressUtils.obj \
 $O\PropId.obj \
 $O\StreamBinder.obj \
 $O\StreamObjects.obj \
 $O\StreamUtils.obj \
 $O\UniqBlocks.obj \
 $O\VirtThread.obj \

AR_OBJS = \
 $O\ArchiveExports.obj \
 $O\DllExports2.obj \

AR_COMMON_OBJS = \
 $O\CoderMixer2.obj \
 $O\HandlerOut.obj \
 $O\InStreamWithCRC.obj \
 $O\ItemNameUtils.obj
 \
 $O\OutStreamWithCRC.obj \
 $O\ParseProperties.obj \


7Z_OBJS = \
 $O\7zCompressionMode.obj \
 $O\7zDecode.obj \
 $O\7zEncode.obj \
 $O\7zExtract.obj \
 $O\7zFolderInStream.obj \
 $O\7zHandler.obj \
 $O\7zHandlerOut.obj \
 $O\7zHeader.obj \
```

```
  $O\7zIn.obj \
  $O\7zOut.obj \
  $O\7zProperties.obj \
  $O\7zSpecStream.obj \
  $O\7zUpdate.obj \
  $O\7zRegister.obj \


COMPRESS_OBJS = \
  $O\CodecExports.obj \
  $O\Bcj2Coder.obj \
  $O\Bcj2Register.obj \
  $O\BcjCoder.obj \
  $O\BcjRegister.obj \
  $O\BranchMisc.obj \
  $O\BranchRegister.obj \
  $O\ByteSwap.obj \
  $O\CopyCoder.obj \
  $O\CopyRegister.obj \
  $O\DeltaFilter.obj \
  $O\Lzma2Decoder.obj \
  $O\Lzma2Encoder.obj \
  $O\Lzma2Register.obj \
  $O\LzmaDecoder.obj \
  $O\LzmaEncoder.obj \
  $O\LzmaRegister.obj \

C_OBJS = \
  $O\Alloc.obj \
  $O\Bcj2.obj \
  $O\Bcj2Enc.obj \
  $O\Bra.obj \
  $O\Bra86.obj \
  $O\BraIA64.obj \
  $O\CpuArch.obj \
  $O\Delta.obj \
  $O\LzFind.obj \
  $O\LzFindMt.obj \
  $O\Lzma2Dec.obj
  \
  $O\Lzma2DecMt.obj \
  $O\Lzma2Enc.obj \
  $O\LzmaDec.obj \
  $O\LzmaEnc.obj \
  $O\MtCoder.obj \
  $O\MtDec.obj \
  $O\Threads.obj \
```

!include "../../Crc.mak"
!include "../../LzmaDec.mak"

!include "../../7zip.mak"
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wOF2OTTOB @n
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,wnV_)+y'nQBjYt`0t0GjKBkxEg;.K63Op 00T~dLy!U\
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- -

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- -

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(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted
 from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: cahyph.tex (2003-09-08)
% Author: Gonal Badenes <g.badenes at ieee.org>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines

```
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%               and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated
 patterns into TeX Live
%
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%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only
 "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
```

```
%-----------------------------------------------------------------------------
%
% Hyphenation patterns for Catalan.
% This is version 1.11
% Compiled by Gonal Badenes and Francina Turon,
%       December 1991-January 1995.
%
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%
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% version 1 of the License, or any later version.
% ---------------------------------------------------------------
%
%%% ====================================================================
%%%  @TeX-hyphen-file{
%%%     author          = "Gonal Badenes",
%%%     version         = "1.11",
%%%     date            = "15 July 2003",
%%%     time            = "15:08:12 CET",
%%%     filename        = "cahyph.tex",
%%%     email           = "g.badenes@ieee.org",
%%%     codetable       = "UTF-8",
%%%     keywords        = "TeX, hyphen, catalan",
%%%     supported       = "yes",
%%%     abstract        = "Catalan hyphenation patterns",
%%%     docstring       = "This file contains the hyphenation patterns
%%%                       for the catalan language",
%%%  }
%%% ====================================================================
%
%
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%        1995) except for the updated copyright notice above.
%
% The macros used were created for ghyph31.tex by Bernd Raichle (see the
% German hyphenation pattern files for further details)
%
% This patterns have been created using standard, conservative
% hyphenation rules for catalan. The results have refined running them
% through patgen. In that way, the number of hits has been increased.
%
% These rules produce no wrong patterns (Results checked against the
```

% ``Diccionari Ortogr\`afic i de Pron\'uncia'', Enciclop\`edia
% Catalana. The percentage of valid hyphen misses is lower than 1%
%
% Some of the patterns below represent combinations that never
% happen in Catalan. We have tried to keep them to a minimum.
%
% *** IMPORTANT ***
% \lefthyphenmin and \righthyphenmin should be set to 2 and 2
% respectively. If you set them below these values incorrect breaks
% will happen (specially at the beginning of
 foreign words and words
% which begin with some prefixes).
% *** IMPORTANT ***
%
% Please report any problem you might have to the authors!!!
%
%%%

%
% Turkish hyphenation patterns
%
%
 This file is auto-generated from source/generic/hyph-utf8/languages/tr/generate_patterns_tr.rb that is part of hyph-utf8.
% Please don't modify this file; modify the generating script instead.
%
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%   2008, 2011 TUG
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% version 1 of the License, or (at your option) any later version.
%
% Credits:
% - algorithm developed by P. A. MacKay for the Ottoman Texts Project in 1987
% - rules adapted for modern Turkish by H. Turgut Uyar <uyar at itu.edu.tr>
% - initiative to improve Turkish patterns by S. Ekin Kocabas <kocabas at stanford.edu>
% - script written by Mojca Miklavec <mojca.miklavec.lists at gmail.com> in June 2008
%
% See also:
% - http://www.ctan.org/tex-archive/language/turkish/hyphen/turk_hyf.c
% - http://www.tug.org/TUGboat/Articles/tb09-1/tb20mackay.pdf
%
%
 Differences with Ottoman patterns:
% - adapted for the use on modern TeX engines, using UTF-8 charactes
% - only letters for Modern Turkish + (the first one often needed, the other two don't hurt)
% - (if needed, support for Ottoman Turkish might be provided separately under language code 'ota')
%
% Changes:
% - 2008-06-25/27/28 - create this file by adapting Ottoman rules for modern Turkish
% - 2011-08-10 - add LPPL licence with permission of Pierre A. MacKay
%
This file contains a list of people who've made non-trivial
contribution to the Google C++ Mocking Framework project. People
who commit code to the project are encouraged to add their names
here. Please keep the list sorted by first names.

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Zhanyong Wan <wan@google.com>
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  <UseDebugLibraries>true</UseDebugLibraries>
  <PlatformToolset>v141</PlatformToolset>
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  <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
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</Project>

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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use
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In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must

be combined with the library in order to run.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables
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whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a) Accompany the work with the complete corresponding machine-readable
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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!
wOF2OTTOU @
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
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<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
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<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Unknown Attribute in copyright Element text Element</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />

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 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <meta name="assert" content="The copyright element contains a text element with an unknown attribute." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body {
   font-size: 20px;
  }
  pre {
   font-size:
12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```

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```
<Project DefaultTargets="Build" xmlns="http://schemas.microsoft.com/developer/msbuild/2003">
 <PropertyGroup>
  <Configuration Condition=" '$(Configuration)' == " ">Debug</Configuration>
  <Platform Condition=" '$(Platform)' == " ">AnyCPU</Platform>
  <ProductVersion>8.0.50727</ProductVersion>
  <SchemaVersion>2.0</SchemaVersion>
  <ProjectGuid>{CE33DF18-F9C8-4D6F-9057-DBB4DB96E973}</ProjectGuid>
  <OutputType>Exe</OutputType>
  <RootNamespace>LzmaAlone</RootNamespace>
  <AssemblyName>Lzma#</AssemblyName>
  <WarningLevel>4</WarningLevel>
 </PropertyGroup>
 <PropertyGroup Condition=" '$(Configuration)|$(Platform)' == 'Debug|AnyCPU' ">
  <DebugSymbols>true</DebugSymbols>
  <DebugType>full</DebugType>
  <Optimize>false</Optimize>
  <OutputPath>.\bin\Debug\</OutputPath>
  <DefineConstants>DEBUG;TRACE</DefineConstants>
 </PropertyGroup>
 <PropertyGroup Condition=" '$(Configuration)|$(Platform)' == 'Release|AnyCPU' ">
  <DebugSymbols>false</DebugSymbols>
   <Optimize>true</Optimize>
  <OutputPath>.\bin\Release\</OutputPath>
  <DefineConstants>TRACE</DefineConstants>
  <PlatformTarget>AnyCPU</PlatformTarget>
 </PropertyGroup>
 <ItemGroup>
  <Reference Include="System" />
  <Reference Include="System.Data" />
  <Reference Include="System.Xml" />
 </ItemGroup>
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  </Compile>
  <Compile Include="..\..\Common\CRC.cs">
   <Link>Common\CRC.cs</Link>
  </Compile>
  <Compile Include="..\..\ICoder.cs">
   <Link>ICoder.cs</Link>
  </Compile>
  <Compile Include="..\LZ\IMatchFinder.cs">
   <Link>LZ\IMatchFinder.cs</Link>
  </Compile>
```

```
  <Compile Include="..\LZ\LzBinTree.cs">
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   </Compile>
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  </Compile>
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  </Compile>
  <Compile Include="..\RangeCoder\RangeCoderBit.cs">
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  </Compile>
  <Compile Include="..\RangeCoder\RangeCoderBitTree.cs">
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  <Compile Include="LzmaBench.cs">
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  </Compile>
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    <AutoGen>True</AutoGen>
    <DependentUpon>Settings.settings</DependentUpon>
   </Compile>
  <None Include="Properties\Settings.settings">
    <Generator>SettingsSingleFileGenerator</Generator>
    <LastGenOutput>Settings.cs</LastGenOutput>
  </None>
  <AppDesigner Include="Properties\" />
 </ItemGroup>
 <Import Project="$(MSBuildBinPath)\Microsoft.CSHARP.Targets" />
</Project>
```

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STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING
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POSSIBILITY OF SUCH DAMAGE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With dir Attribute in text Element 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element contains a text element with ltr as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text dir="ltr"&gt;
   Copyright without language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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SOFTWARE.

```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in span Element in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-031-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains a span element with rtl as the value for the dir attribute."
 />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-031.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
```

```
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   Text with &lt;span dir="rtl"&gt;span&lt;/span&gt;.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
# -*- Mode: python; indent-tabs-mode: nil; tab-width: 40 -*-
# vim: set filetype=python:
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# file, You can obtain one at http://mozilla.org/MPL/2.0/.

EXPORTS.snappy += [
  'snappy-stubs-public.h',
  'src/snappy-c.h',
  'src/snappy.h',
]

UNIFIED_SOURCES += [
  'src/snappy-c.cc',
  'src/snappy-sinksource.cc',
  'src/snappy-stubs-internal.cc',
  'src/snappy.cc',
]

# We allow warnings for third-party code that can be updated from upstream.
AllowCompilerWarnings()

FINAL_LIBRARY = 'xul'

# Suppress warnings in third-party code.
if CONFIG['CC_TYPE'] in ('clang', 'gcc'):
  CXXFLAGS += [
```

```
    '-Wno-sign-compare',
    '-Wno-unused-function'
  ]

if CONFIG['TARGET_ENDIANNESS'] == 'big':
  DEFINES['IS_BIG_ENDIAN'] = 1
```

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-001-ref.xht" />
  <meta name="assert" content="The license element matches the schema." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src:
 url("support/metadatadisplay-schema-license-001.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
```

The XML contained in the Extended Metadata Block is below.
<pre>
<?xml version="1.0" encoding="UTF-8"?>
<metadata version="1.0">
<license url="http://w3c.org/Fonts" id="License ID">
<text>
License without language.
</text>
</license>
</metadata>
</pre>
</body>
</html>
The MIT License (MIT)

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SOFTWARE.
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
<head>
<meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
<title>WOFF Test: Valid license Element With One Language Tagged (using lang) text Element</title>
<link rel="author" title="Tal Leming" href="http://typesupply.com" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
<link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
<link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
<link rel="match" href="metadatadisplay-schema-license-006-ref.xht" />

```
  <meta name="assert" content="The license element matches the schema. It contains one text element that has a
language tag using the lang tag instead of
 xml:lang." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-license-006.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
PROG = lzma.exe
MY_CONSOLE = 1
CFLAGS = $(CFLAGS)

CURRENT_OBJS = \
 $O\LzmaAlone.obj \

COMPRESS_OBJS = \
 $O\LzmaDecoder.obj \
```

```
 $O\LzmaEncoder.obj \
 $O\LzmaRegister.obj \

COMMON_OBJS = \
 $O\CommandLineParser.obj \
 $O\CRC.obj \
 $O\CrcReg.obj \
 $O\IntToString.obj \
 $O\MyString.obj \
 $O\NewHandler.obj \
 $O\StringConvert.obj \
 $O\StringToInt.obj \
 $O\MyVector.obj

WIN_OBJS = \
 $O\FileIO.obj \
 $O\PropVariant.obj \
 $O\System.obj

7ZIP_COMMON_OBJS = \
 $O\CWrappers.obj \
 $O\CreateCoder.obj \
 $O\FileStreams.obj \
 $O\FilterCoder.obj \
 $O\MethodProps.obj \
 $O\OutBuffer.obj \
 $O\StreamUtils.obj \

UI_COMMON_OBJS = \
 $O\Bench.obj \

CONSOLE_OBJS = \
 $O\ConsoleClose.obj \
 $O\BenchCon.obj \

C_OBJS = \
 $O\Alloc.obj \
 $O\Bra86.obj \
 $O\CpuArch.obj \
 $O\LzFind.obj \
 $O\LzFindMt.obj \
 $O\Lzma86Dec.obj \
 $O\Lzma86Enc.obj \
 $O\LzmaDec.obj \
 $O\LzmaEnc.obj \
 $O\Threads.obj \

!include "../../Crc.mak"
```

```
!include "../../7zip.mak"
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With Mixed text Element Language Tags 1</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-004-ref.xht" />
  <meta name="assert" content="The copyright element matches the schema. One text element does not have a language tag. One text element has a language tag." />
  <style
 type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-004.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text&gt;
   Copyright without language.
```

</text>
<text xml:lang="en">
Copyright with "en" language.
</text>
</copyright>
</metadata>
</pre>
</body>
</html>
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---

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```
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: No text Element in copyright Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The copyright element does not contain a text child element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family:
 "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test fails.</p>
  <div class="test">P</div>
 </body>
</html>
```

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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>

```
  <title>WOFF Test: Valid license Element With One Language Tagged (using lang) text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element matches the schema. It contains one text element that has a
language tag using the lang tag instead of xml:lang." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body
 {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID"&gt;
  &lt;text lang="en"&gt;
   License with "en" language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
; 7zCrcOpt.asm -- CRC32 calculation : optimized version
; 2009-12-12 : Igor Pavlov : Public domain

include 7zAsm.asm

MY_ASM_START
```

```
rD   equ  r2
rN   equ  r7

ifdef x64
  num_VAR     equ r8
  table_VAR   equ r9
else
  data_size   equ (REG_SIZE * 5)
  crc_table   equ (REG_SIZE + data_size)
  num_VAR     equ [r4 + data_size]
  table_VAR   equ [r4 + crc_table]
endif

SRCDAT  equ  rN + rD + 4 *

CRC macro op:req, dest:req, src:req, t:req
  op      dest, DWORD PTR [r5 + src * 4 + 0400h * t]
endm

CRC_XOR macro dest:req, src:req, t:req
  CRC xor, dest, src, t
endm

CRC_MOV macro dest:req, src:req, t:req
  CRC mov, dest, src, t
endm

CRC1b macro
  movzx   x6, BYTE PTR [rD]
  inc     rD
  movzx   x3, x0_L
  xor     x6, x3
  shr     x0, 8
  CRC     xor, x0, r6, 0
  dec     rN
endm

MY_PROLOG macro crc_end:req
  MY_PUSH_4_REGS

  mov     x0, x1
  mov     rN, num_VAR
  mov     r5, table_VAR
  test    rN, rN
  jz      crc_end
 @@:
  test    rD, 7
```

```
    jz      @F
    CRC1b
    jnz     @B
  @@:
    cmp     rN, 16
    jb      crc_end
    add     rN, rD
    mov     num_VAR, rN
    sub     rN, 8
    and     rN, NOT 7
    sub     rD, rN
    xor     x0, [SRCDAT 0]
endm

MY_EPILOG macro crc_end:req
    xor     x0, [SRCDAT 0]
    mov     rD, rN
    mov     rN, num_VAR
    sub     rN, rD
  crc_end:
    test    rN, rN
    jz      @F
    CRC1b
    jmp     crc_end
  @@:
    MY_POP_4_REGS
endm

MY_PROC CrcUpdateT8, 4
    MY_PROLOG crc_end_8
    mov     x1, [SRCDAT 1]
    align 16
  main_loop_8:
    mov     x6, [SRCDAT 2]
    movzx   x3, x1_L
    CRC_XOR x6, r3, 3
    movzx   x3, x1_H
    CRC_XOR x6, r3, 2
    shr     x1, 16
    movzx   x3, x1_L
    movzx   x1, x1_H
    CRC_XOR x6, r3, 1
    movzx   x3, x0_L
    CRC_XOR x6, r1, 0

    mov     x1, [SRCDAT 3]
    CRC_XOR x6, r3, 7
    movzx   x3, x0_H
```

```
  shr     x0, 16
  CRC_XOR x6, r3, 6
  movzx   x3, x0_L
  CRC_XOR x6, r3, 5
  movzx   x3, x0_H
  CRC_MOV x0, r3, 4
  xor     x0, x6
   add     rD, 8
  jnz     main_loop_8

  MY_EPILOG crc_end_8
MY_ENDP

MY_PROC CrcUpdateT4, 4
  MY_PROLOG crc_end_4
  align 16
 main_loop_4:
  movzx   x1, x0_L
  movzx   x3, x0_H
  shr     x0, 16
  movzx   x6, x0_H
  and     x0, 0FFh
  CRC_MOV x1, r1, 3
  xor     x1, [SRCDAT 1]
  CRC_XOR x1, r3, 2
  CRC_XOR x1, r6, 0
  CRC_XOR x1, r0, 1

  movzx   x0, x1_L
  movzx   x3, x1_H
  shr     x1, 16
  movzx   x6, x1_H
  and     x1, 0FFh
  CRC_MOV x0, r0, 3
  xor     x0, [SRCDAT 2]
  CRC_XOR x0, r3, 2
  CRC_XOR x0, r6, 0
  CRC_XOR x0, r1, 1
  add     rD, 8
  jnz     main_loop_4

  MY_EPILOG crc_end_4
MY_ENDP

end
```

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7z ANSI-C Decoder 9.35

7z ANSI-C provides 7z/LZMA decoding.
7z ANSI-C version is simplified version ported from C++ code.

LZMA is default and general compression method of 7z format
in 7-Zip compression program (www.7-zip.org). LZMA provides high
compression ratio and very fast decompression.

LICENSE

7z ANSI-C Decoder is part of the LZMA SDK.
LZMA SDK is written and placed in the public domain by Igor Pavlov.

Files

7zDecode.* - Low level 7z decoding
7zExtract.* - High level 7z decoding
7zHeader.* - .7z format constants
7zIn.* - .7z archive opening
7zItem.* - .7z structures
7zMain.c - Test application

How To Use

You can create .7z archive with 7z.exe, 7za.exe or 7zr.exe:

```
7z.exe a archive.7z *.htm -r -mx -m0fb=255
```

If you have big number of files in archive, and you need fast extracting,
you can use partly-solid archives:

```
7za.exe a archive.7z *.htm -ms=512K -r -mx -m0fb=255 -m0d=512K
```

In
that example 7-Zip will use 512KB solid blocks. So it needs to decompress only
512KB for extracting one file from such archive.

Limitations of current version of 7z ANSI-C Decoder

- It reads only "FileName", "Size", "LastWriteTime" and "CRC" information for each file in archive.
- It supports only LZMA and Copy (no compression) methods with BCJ or BCJ2 filters.
- It converts original UTF-16 Unicode file names to UTF-8 Unicode file names.

These limitations will be fixed in future versions.

Using 7z ANSI-C Decoder Test application:

Usage: 7zDec <command> <archive_name>

<Command>:
e: Extract files from archive
l: List contents of archive
t: Test integrity of archive

Example:

```
7zDec l archive.7z
```

lists contents of archive.7z

```
7zDec e archive.7z
```

extracts files from archive.7z to current folder.

How to use .7z Decoder

Memory allocation
~~~~~~~~~~~~~~~~~~

7z
Decoder uses two memory pools:
1) Temporary pool
2) Main pool
Such scheme can allow you to avoid fragmentation of allocated blocks.

Steps for using 7z decoder

Use code at 7zMain.c as example.

1) Declare variables:
```
inStream              /* implements ILookInStream interface */
CSzArEx db;           /* 7z archive database structure */
ISzAlloc allocImp;    /* memory functions for main pool */
ISzAlloc allocTempImp;  /* memory functions for temporary pool */
```

2) call CrcGenerateTable(); function to initialize CRC structures.

3) call SzArEx_Init(&db); function to initialize db structures.

4) call SzArEx_Open(&db, inStream, &allocMain, &allocTemp) to open archive

This function opens archive "inStream" and reads headers to "db".
All items in "db" will be allocated with "allocMain" functions.
SzArEx_Open function allocates and frees temporary structures by "allocTemp" functions.

5) List items or Extract items

Listing code:
~~~~~~~~~~~~~~

Use SzArEx_GetFileNameUtf16 function. Look example code in C\Util\7z\7zMain.c file.

Extracting code:
~~~~~~~~~~~~~~~~~~

```
SZ_RESULT SzAr_Extract(
  CArchiveDatabaseEx *db,
  ILookInStream *inStream,
  UInt32 fileIndex,         /* index of file */
  UInt32 *blockIndex,       /* index of solid block */
  Byte **outBuffer,         /* pointer to pointer to output buffer (allocated with allocMain) */
```
~~~~~~~~~~~~~~~~~~

```
  size_t *outBufferSize,    /* buffer size for output buffer */
  size_t *offset,           /* offset of stream for required file in *outBuffer */
  size_t *outSizeProcessed, /* size of file in *outBuffer */
  ISzAlloc *allocMain,
  ISzAlloc *allocTemp);
```

If you need to decompress more than one file, you can send these values from previous call:

```
  blockIndex,
  outBuffer,
  outBufferSize,
```

You can consider "outBuffer" as cache of solid block. If your archive is solid,
it will increase decompression speed.

After decompressing you
must free "outBuffer":

```
allocImp.Free(outBuffer);
```

6) call SzArEx_Free(&db, allocImp.Free) to free allocated items in "db".

Memory requirements for .7z decoding

Memory usage for Archive opening:

- Temporary pool:
  - Memory for uncompressed .7z headers
  - some other temporary blocks
- Main pool:
  - Memory for database:

    Estimated size of one file structures in solid archive:
    - Size (4 or 8 Bytes)
    - CRC32 (4 bytes)
    - LastWriteTime (8 bytes)
    - Some file information (4 bytes)
    - File Name (variable length) + pointer + allocation structures

Memory usage for archive Decompressing:

- Temporary pool:
  - Memory for LZMA decompressing structures
- Main pool:
  - Memory for decompressed solid block
  - Memory for temprorary buffers, if BCJ2 fileter is used. Usually these temprorary buffers can be about 15% of solid block size.

7z Decoder doesn't allocate memory

for compressed blocks.
Instead of this, you must allocate buffer with desired
size before calling 7z Decoder. Use 7zMain.c as example.

Defines

_SZ_ALLOC_DEBUG - define it if you want to debug alloc/free operations to stderr.

---

http://www.7-zip.org
http://www.7-zip.org/sdk.html
http://www.7-zip.org/support.html
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- -

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- -

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Danish hyphenation patterns

(more info about the licence to be added later)

% This file is part of hyph-utf8 package and resulted
 from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% Source: dkcommon.tex (yyyy-mm-dd), dkspecial.tex (yyyy-mm-dd)
% Author: Frank Jensen <frank.jensen at hugin.com>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%   and helped with testing, suggestions and bug reports
% - Norbert
 Preining, who tested & integrated patterns into TeX Live
%
% However, the "copyright/copyleft" owner of patterns remains the original author.
%
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%
%    Do with this file whatever needs to be done in future for the sake of
%    "a better world" as long as you respect the copyright of original file.
%    If you're the original author of patterns or taking over a new revolution,
%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns
 should be in UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-------------------------------------------------------------------------------
%
% This file contains the hyphenation patterns for Danish.
%
%% Copyright 1994 Frank Jensen (Frank.Jensen@hugin.com)
%
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%   http://www.latex-project.org/lppl.txt
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% version 2003/12/01 or later.
```

%
% Changes:
% - 2011-01-11 - remove support for OT1 encoding
%
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LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM,
OUT OF OR IN CONNECTION
 WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN THE
SOFTWARE.
MZ@ !L!This program cannot be run in DOS mode.

$j!OEOEOEDEOEEEOEDAEOEKEOENEOE-EEOERichOEPEL@Z.*@@<\x.text `.rdata~@@.dataX
@.reloc@BTSUV3Wh@llHR$h;u@8_^]3[T}@_^][T
|@$lx@L$
VLXLtDDTth@c_^][TL$lm3$JL$$3T$;$uDl$$$$>$;qL$TT$QR$$&;|$F~l$<l$(l$0d@:utP:Vuu3;\$<$3;D$
Ll$4l$P$3T$DT$8(L$<"l$;D$$u;t
9T$0<j$HD$H;v%T$3
63t$HU
DtVHD<$($`@:utP:Vuu3;u \$(\@:utP:Vuu3;X@:utP:Vuu3;u \$0]@D3RT($
$Dt,+llUV/$ tL$t$$T$x2
LxL$|$$f$$L$x{T@q$eT@[$DOP@EL$D$@D$$L@D$(@@u4@GD$D$$tL@CTTDRL$<PT$LQD$\RL$
DPT$`QRW$$PDD$D$(WD$D$f8t7f8/u$D$0tL$f|f\FD$f>uD$$tL$RT$L$,`L$8D$4L$@LDT@RL$0T$@D$8;
$ 33t(t$$tdTdDDh(t(t$,L$\TT$`L$\utD$,VjQP$@L$,G
uZ$t'(t"$t%T$PR(@$G;i*D$ @@@DT4LT |$3T$3T$$^LT$ L$t
;u@_^]3[Tu@_^][Tu@_^][Tu@_^][T$DRP3t$D_^][Tl@ PQp@jQ@t3% @VLLj$u
L$x@L$^QSUV3Wf9utFf8u\vdfu
_^][Yt0LTQL$RSPVUjQD$3_@t;s_^]3[Y_^][YVW9~r_^x@ahx@_^$SUVt$4W|$<T$3jj
WV~0jj
WD VCuT$+~   LKMEu_]^][$SUT$Q
VDD!D$$D$&3W3VhRP\$0D$1\$2\$4\$6\$7\$9\$;Sjj<WVjj<WVD$,5jj<WVjj<WVD$(jjWVxjjWVD$$k93:A
Y+uiTqsmuR+0<Bag+uiu!3du

uD$!3;r
+E3\,
;sL$jkU-FF-WT$F T$:^T$^F_^][SVW33
0T4FuT$ +~U0000]T4NCu_^[V03
^0Q@vV@c\^u .tDD$@$.P.P %.P.Hu3Rh@Rd@YAVWFu_^L$t
~_^_3^VPF^h@@P@@VW3@@
3IuF@@r@@@@3@@Qr@@H@@@5D@2Iu5@@_^SVt$W|$v!t333OAwrIU333D$\$333Mu]v333AOu_^[QS
Vt$W|$v!t333OAwUT$Q3T$3D$3\$33T$33\$3%3%3T$33JT$t]v333AOu_^[Yj D$VjhQ%jjPR@^t3
@D$VjhQ%jjPR@^t3 @jVtP@u @^3^SUVW|$7tY@@wUL$jQPSRD$(@L$+t
tv @_^][_^]3[SUVW|$7tY@@wUL$jQPSRD$(@L$+t
tv @_^][_^]3[SVWV \$ADDt"HtHt_^W[3PD$PSQ@L$u
@uLD33F3_^[0"@AP"@DPCDP#SUVWF^(\$L+TLL$,K(D$V L$83:;$3;;T
3R$D$$D$(D$,UQP$QNRPFVQN$L$LR\$
DDUDHDuD$uBFt;t#$$N;tDD_^][_^][pSUV3WT$\$$l$(l$,l$ l$
;l$;$$DmL<\8
L$$3|$9D$$D$4D$8<L4l$<l44sD3;|$@;8$u!$D|,=(3;r
;$$$+|$|$ t$$|Ll$+$Q$P5nCu$;|$;$UWb=u,$L$$PQVt$ R3S UW/!uE$L$$PQVt$ R3S UW$$=$L$$PQVt$ R3S
UWC=$XphxD$+3;r;j$$AuI$$$USj4$$WVS:T$(L$,|$
$;D$D$@D$PDDDTCDHDXCDLD\$D$`L$@D$d5L$@5QL$@q;q@rD$x\D$`L$d;LD$h@$$6u5{
'$?D$3WVB$?K =wdtS=t0=t=jj<djj9UL$jQjD$;9jj8*=t=uxjj:
jjY8\$$lEl;q3_^][p$_^][p$_^][p$_^][p$_^][p$K|$KD$*@SU33VW\$D$4l$ll
l$,l$0l$$l$(=y3+@"
LTie8KPL$X6fu_^][KL$TefL$<,uL$LPL<t9l$,tD$0;t|}$4K3;v*$0KT$<L$Tu9l$,u;|F;r\$;tD$0;t56D$L$
|$$t$(+$$K;u;$(Ku9l$Lt$8KL$T4e_^][K_^][KQVWNF;t
AN_^YFu\V~+NT$NFN FQN FD$PD$?FD$~Nt
G~_^YF_2^YlS$3RPL$\$$\$b;:$$UVL$(WL$D$(B$$;$$wrs$$$R$;$\$0L$D$$QTjRPL$(AT$$++uZDt9$;L$
0uut9u;\$0t*$Qu3#$D$0;ut$L$`_^][lW3|$4|$,t_$PL$;;$$SUL$<VL$
T$D;$$;|Dwrs\$$DPT;L$|$HTLLRD$jPQL$0;TL++L$u@|$t!u;|$Ht*$Qu3XDH;ut$L$0_^][_DDS
UVWwDL D$w
r=sDDT$PuOL$tQtT|t +|t
Suwrw3_^][_^][wtt!t3t3SV;rxwsN84tg;u9^u\FuUFuN^3[ui8^Lu?9^u:Fu39^u.9^ u)F$u"vHwt5t0t(t
^[ttu^3[uNDtNPt~`u~du~u~u~u9^uFuN;u~
{N$p~(f9^,]9F0T~4J^3[yuIrt3VWVWVG_^V83F8F@F<FDFHFlFpFtFxFLFPFTFXF\F`FdFh^3AAAAAAAA
A$A(VWWDVWHVWlVWpVWtVWxVOL OTO\Od _^ VWWVOWVWVWVW VW$VW(V_^(SUVW3T$
l$|$D$4}}}};T$;yp;|$]8\$0n>MnGL$>:D$(&;339T$(vPL$(L$(r3L$(GMI>nL$(ur \$0L$CCC
tET$$D$$T$$C}|$$mL$|$3S|$Q tPT$(D$(J6D$(N;=|$4+C N+ND$L$ @;D$\$0l$|$Z;s
_^][(]+L$$Mv"3|$(l$L$$|$v"3|$0l$L$$|DT$j ?+EL$;L(D(} ?T$;spL<0D<0udD$@;D$rL$$l$|$3v\0t@;rE;u
_^][(u43v
L(t@;r;u
_^][(_EU^]3[(3vFD$\$$hT$4Nu>D$4L$;sL(uEG;D(rT$
l$_U^]3[(_^][(AVu
^DuTABHA3^T$Eu8D$wL$r
^wr
^3^SUVWFu
^][.PVMEL$.uD$%W^]3[u
_^][J3V]E@u?3G_^][u
^][J@VP3D$ G\$L$tZFHF@L$D$%0nOT$B@OT$r^\$]3[TL\$D$%H#3mGG_^]3[_^\$][ASVq
I$3^[TD$S\$VWPSt

^[SUVWL$3jT$0t$|$0|$4V;jj T$L#;LDDJt^][D$`L$,T$0t$X\$P|Tl\D$D$
t$<H0P4L$Pk;D$Lt_^][3;u;t_^][u_^]3[;8r;. ;r;LT4T$8jT$8QL$,D$0T$4D$8;w; ;w;L$0
;w;l$WSV!$\$<WL$(u_^][T$$S!L$$;DL(D$$L$
TLD$|$DL4JLdaDT$dWRH4P0QRD$DjPT$4L$dtL$4Q0L$$FL$8D$4L$(L$
TLD$$D$Qu/|$u#DuWQLUT(L$$_^][_^][_^][3S@@@:u
@r[3[D$(SUVt$HWPLDQL$HPjjQT$D|DHT$|$FT$u_^][(3v|$<FC;rF3v4|$<3;u~;uzL$PQthFC;rF3vH|$<T$
D$RPu1L$PWQLRTPQRUu
FC;r3_^][(_^][(S\$
UVW|$03L$oooooo+T$k7;D$uJ9l$uD;D$4\$P;T$;D$\$u:9l$u4L$4T$(D$$QL$$RPQ0;ugT$;uXD$u-
9l$u'Wd;u;T$u;t_^][SDDu_^]3[_^][SUVWt$ TL$ Fu_^][333D$ v@L$Q|T$LD;rVw;rPD$
F;rDTH|nu_DLtO
u&u"D$u7_^][*uT$t_^][3_^][T$Vu.T$F3;L$rw;v
^+F3^SVWT$uE3D$L$;u;t%t*|u$T$mt_^[_^3[_^[SUVWNu
_^][Y^Au)};v
_^][9V+VN;v
_^][+_N3^][QS33VD$t)WuFL$|$KJu_^[YSUVWt$8l$D3;T$;L$$;v_^][MF;u_^][HF@T$'u
VDTNT$
I;D$ $;r_^][$TDLD$$;Eu_^][L$;Eu_^][T$;Eu_^][T$;E
u_^][TD3;D$4\$<\$,L$0D$T$4L$D+3QL$|$T$(;gl$(;g@^;|$
L$]T$IL$B,;2+L$TDHtITHLTTLD$H@9|$TLl(T$ T$t9T$XL$<D$XL$;T$l$(+L$TD @;D$ D$;u
9|HL];O@*v3|$\v3$3v{|T@LD@;L\D\T$@L$LD$@l$(;F;r|$+tG3vA\$TLL>D$L;ZL\D\JF;rl$(3vt@;rD
$;t$0LDl<T$,\$ A,AA 0D$(;T$,
+;DFl<;t$0l$Dt8UD$,\$D$4U+D$<Uu3@E(u_^][t4t$8D$'t'D$t_^][VL$TLmD$,umE$\$,3vELF;rT$LL
DLL$Ptp
u@u<T$EWPL$vud1E$u_^][L$su1TLLbt_^][D$8L$T$P3_^][S\$Ul$VWGSUuSU
^][D$SUVWE^u_^][xE}@Eu\;v_^][LLu_^][uME+_ME^]3[LD(u^][t+D+_^]3[SUVW\$uTL$
QC;ID$3vrtF\$;r`\$+\$-L$$L$ Au_^][@;r\$T$;_^S]3[_^][SUVqWL$3T$$l$$l$(l$tl;D$$T$(
;D$03l$HvqT$ t$;tT$D2D$u#L$Atut$D$E;rD$0t$P+3H'
u;tJ u;tAt;;T$$;_^][_^][l$0Mu$LtItT$(+D$$tt]uu9u5UT$uxU+T$$E|u_T$(D$$tO
u"uUu6M+E<uT$$-uD$$T$(u3_^][SUVW}ut8tANGt(@ut/NG
t@r;w+Ju}u_^]3[_^][HUVWTDtOuD\S\$dLSPDdT$ QRT$ PAt$
Ou[t_^]HL$_^]AHSUV3WT$t$`t$dthtltXt\t$Tt$P;
D$ug9t$uaT$<; D$<L$@t+; T$<;t_^][T$i;
D$9t$$$U4E0WQRPjQ$n$$$.t_^][T$D$$ueL$u]$L$P}8VQ$WPQh@U0E4WWT$_D$L$u_^]3[c3;YT$|$|$$|$0
|$D|;;D$T$E@;!L$DTHT$LC|$H3;r;LSLC7$V@HC@L$u O|$
8T$<y$D$<;a$T<T$ D$ DD$EtNT$ u3,Ex3L$ EtLPQMxD$]D$ S+SFT$0RT$
DTL0D$$D$DT$$D$DPm}TUT$VW(GC7HC@u@T$(W$D$(;$T$4DD$8L$4T$VW_$$VM\RT$$PS
5$$VMdRT$$PS+CT$3A_^][L$T$$D$P+;:T$,TDt+T$t_^][E33|$(|$
|$,|$4|$|$|$<\$\$\$D$Elu_^][E@u.EpM@EDu5_^][U@Epu_^][U@uBEHU@MLV]D$ltUD$hK@\$lHLDht*6U
@EHu_^][LTD<YD$3>T$\$$u*UHLFDULLDTEDLL$LEPD$0tetMD$Dt&L$(L$(uT$D$(T$D$,MpID
$,uy;}Ul4L$XD$4tT$4LXD4D$,u8L$LL;L$w
9D$sG|$ Ep<D$0tD$,HD$,|$ tMl\$p}DD|$+|$H|$|$L|H|$L;w$;L$;D$w;|$4uMEtFt.MD$ UPDT
DD @D$ D$u
D$<t&t!D$$MPL$D$$D$
LD @D$ {TxL`LxTD$|T$T$;T$w
9LDu
D$<t&t!D$$MPL$D$$D$
LTD$F;T$tMHT$FMLT$EDLTTD$,uTEl4E;s/D$Xt?T$4L$Xpu3D$4u&MlG4E;rD$XD$_^][O@O@P@N

@P@P@P@P@SVWu
W_^3[t$Su_^[Gu_^[BQ;vVt$q+q3^SVt$3W|$u_^[r;\u3\u+t'f8t f8u+^Ot;u_^[+_^[D$
S\$UVWPWt$`Cu_^][HC@L$$uKL$=T$yDL,;r_^][L$(T,T$uNwO?3v|9tIT$;rUUVT$+T$0L$0Gu_^][FL$@;rD
$$u D$+C3_^][S\$
UlVL,WFpTD<u/t$$L$4QT$ D$(:_^]3[L$$9tT$ 9:3;u;t_^][D$ L$$8L$4QT$$D$(t(L$4L$$u_DD^][
D$$T$8RV<F8SQL$RPQ.D$\$,FDTT$Vl+<;FDT+L$0;L$(;v_^][FLt5tL$$ |VP;tDD_^][AtVW|$4D+tIxqv+f<
f9Nu_^TDTjTD$QS\$VWt"D$P\$VuD$t+u3_^[D$_^[D$jP[@u@\@A\@A\@A]@S\$VW3NV+u%9t!NT$FF
F RVDLN9vNV_^[SV3WNV|$+u#9tFFF ;vNVWN9vNV_^[AA3SUWkC+uD$KP_][T$
;vVsK^_K]3[3AAD$IPQ3WA(A,A3A4A8y<_QA4SUVWwiA(
t7q4uA8u2AQ;t3Q83@AFQ8A4uQ8;u_^][Y_^]A(3[YA44A(r,w'A Q$;Q(T,Q(B@Q(A
vA(<3A4=s#Qq;tA4A83BA8QQ+kq y$+;v
d<Q3ut+ttF@;tF@;tu+;!uY3Xy \$Y,@FQ3Y,y ut$
3y43fDq<i8;sQ4+fTq<++y4+i8fDq<A(Y33@4D;33&VF3V4q Q,Q,Q$++reA3q F_^]A(3[Y_^]A(3[YPyi,Y +_i,^Y
A(]Q33[Yy(7A0vA1vFA2vFA32A
Q(A4=s#Qq;tA4A83BA8Q_^]3[YSVt$W|$t,;sT8Xux+x;s(8Xux++x_^+[SUVWl$L:T$tr;33PX#r3XP+3XPXXP;wj
33PX#r3XP+3X+PXXP+_^][SUVW|L;sqHuPu33pXP3Xt+++HPXXX+_^][SUVW|$;@uHt
u8Hr33pXP3X\$t+++@PXXXm+_^][UWL$3T$9s
_3]LSVDL$4*;stF;r+++;vE33NYuT3F3F3FL$)L$$t-
+twtL1AuEb+t0BuL$$J3t+T3^FDN^^[vT$3:_]T$:_]UWl$s
_3]D$SV3+|$tD
K3Cl(E$<uk3eEpu[EL$$L$ t+p#uC{tl|$;<^[_]USVM333EE]MUEMUEMUE^[]VStC
Et5FNPVQR3|FNPVQRVc^3^UQMX3EPX3R!EE]SVqW3@9ruzY;u :Y;t@@r_^[
L$VW2L$%L$qt3HtHuUrPuFrI_3^ r<u2r5
t0_3^ r#ut&t't5t
6t_3^ _^ W@3_Vt$v+@Nu^SUVWWL$33$v)$3v;sT4T4@F;r;r;u3T4+PxT$+Vh_^][T$V-
uDTPjN%^<(vu%BJBJ33AAAzSUlVWL,EE |D|D^(;uL$(tBt=M;D$ AM@D$ L$;WYIM++N;D$$D$r
D$Nunu3jNRD$$;vD$$|$ PN]D$$|$]~+~uBRNBD$$N;vL$$D$,L$PD$$T$(QRPN}T$$L$ L$
}N+NN(+^+^u;D$,uuuuuL$, AD$,_
^][T$,_^]3[D$,_^]3[L$,_^]3[T$,_^]3[t_^]3[QST$w=$l@D$2:QAu[Yu"<uA[Y<wyu
[Y:ArQA[YQ3d$[QYAA$BQ$[Y3t$[QYYA$@[BQY|$oVW
98_gfffTtQ^YQ[Yk@l@1l@Ql@bl@l@S\$VWxPPH,PuHx(+;wH,H(_^H([3AD;ALt
A(A,AD9D$tADjASUVW|$ L$(T$7ED=t$\$v"ELsT(PULBCULBNut$EL3;tUPs_^]3[33eQMSER3UTE
}LE$ED=v?ME}tf3E<E8E4E0}@}D\$T$`ED=XT$}3;Lr/uMRL$$\D$L$}Lugr
uD%V6L$t aT$P]HL$ E+9+9t$3s;t$sD/PGFr}Lrt$WUP2L$t T$EPPEuTE+P;
+;D$ +D$+ELD$ED=E$_^][L$(_^]3[T$(_^]3[}PD$L$ EL_^]D$[3L$(_^][D$
T$(_^]3[D$(_^][_^][L$(_^]3[QWyDtlsdASUiY0Vq+t$;sQ,uQ+Q(;wQQ,Q(+yDQ(t|$;#+@N*T(u^]A[_YSUVW|$F
,u'~(FN++;vu ~$sB|$WLu@F,u
FN(;rF,9^s9~s~Ds_^][3_^][XT$HSUVJ0B@L$,J8LDJDB43D$@B<
D$HJKJWzL$<J,+jD$\BD$8B(D$,Bt$`rDB
T$P|$@\$dl$(L$TD$4L$,T$#L$ 3=\$$f_sT$3BT$T$;+L$$fOL$,uL$Tt-
ul$<L$83T)L$,L$\#L$`R<Wl$,LEl,r+L$3fOs\$3C\$\$;s+fW+++fO,
3=f/s3C\$;s+f/+++f/E3=f/s3C\$;s+f/+++f/E3=f/s3C\$;s+f/+++f/E3=f/s3C\$;s+f/+++f/E3=f/s3C\$;s+f/+++f/E3=f/s3
C\$;s+f/+++f/E3=f/s3C\$;s\$+f/++\$+f/MhL$
+T$0L$L$(;#l$<+T$83*3L$fOs\$3C\$\$;s'+fOT$+++fOT$lL l$#l$ o3=T$$f*s3C\$;s(+l$$fUTl
T$3+++T$$f*TTTlL l$#l$ o3=T$$f*s3C\$;s(+l$$fUTl T$3+++T$$f*TTTlL l$#l$
o3=T$$f*s3C\$;s(+l$$fUTl T$3+++T$$f*TTTlL l$#l$ o3=T$$f*s3C\$;s(+l$$fUTl
T$3+++T$$f*TTTlL l$#l$ o3=T$$f*s3C\$;s(+l$$fUTl T$3+++T$$f*TTTlL l$#l$
o3=T$$f*s3C\$;s$+l$$fUTl 3+++T$$f*TTl#<o3=f/s3C\$;s+f+++Lf/|$8T$(:BT$(,+++\$$f_3fLW

=sT$3BT$T$;s%+T$f\W +++fLW 3fLW8=sT$3BT$;+L$f\O8\$$3f_sT$3BT$;s]+\$L$$fOL$0|$<;#+L$8El$(
T)l$,El$,L$
++l$+T$$fWl$+++fLo83fLoP=sT$3BT$;s+L$DfToP|+++fLoP3fLoh=sT$3BT$;s+L$HfToh+++T$HfLohLLTL
TDTHT$0L$0TDL3=fsl$3UEl$;/+L$
f<O3fOs\$3C\$\$;s+fW+++fO, 3=f/s3C\$;s+f/+++f/E3=f/s3C\$;s+f/++M+f/+++f3fO=s\$3C\$\$;!+L$
fo|O3fOs3C\$;s+fW+++fO, 3=f/s3C\$;s+f/+++f/E3=f/s3C\$;s+f/+++f/M+++T$fO, 3=f/s
3B;s+f/+++f/MrTTL$4r|$@A3=fys\$3C\$\$;s+fQ+++fy,3=f<)s3C\$;s+f)+++f<)E3=f<)s3C\$;s+f)+++f<)E3=f<)s
3C\$;s+f)+++f<)E3=f<)s3C\$;s+f)+++f<)E3=f<)s3C\$;s+f)++]+f<)@oIL$$Cl$@3=f]sl$3MAL$L$$;s
+|$@f_+++l$@f]IL$$u+=s|$3BT$+_#Iul$@3f}=sL$3AL$;s+fM+++f}3=fL}L$$sL$3AL$L$$;s+fT}+++fL}3=fL
}L$$sL$3AL$L$$;s+fT}+++fL}3=fL}L$$sL$3AL$L$$;s+fT}+++fL}LHTDLLL0LDLTHST0
LLTuL$,;\$4l$X|$(+;rT$0;+#L$<\$4+T$,T$,T$<+;T$8w!+*|$(\$$|$$B;u\$<GA;u3Mu|$(\$L$(T$X;sY;\$lsSl$(\$
d|$@L$L$4D$PT$(_^P][XL$Py_^][X|$4=sT$3
BTLPQT$(A D$,QT$0A(D$DQ0T$HyDA4D$Lq$Q8T$_^A<]Q@3[XTLSHT$ PT$X(Uh@Vp
Wx$#\$L$)3T$$fSs)T$\$,;r_^]3[3BT$;wL$H,T$uH(tFhuhP33L**HH(J#T$Lm
T$x@sc3fJs(D$l$,;Vl$3E@D$;s++L Xh0;sH3@+3D$ #)l$$3f,Bs%DT,;3T$@D$;sD$$3++L sD$ D$ XT$++3fLj
s)D$\$,;r_^]3[3@D$;sD$$D$ f\$++3fDj8D$
s#;\$,r_^]3[3C\$;szD$$l$3fBs&;l$,r_^]3[3MEl$;=s;l$,r_^]3[_^][++3fDjPs#;\$,r_^]3[3C\$;sJ++3fLjhs#;\$,r_^]3[3C\
$;s++D$$l$3fs$;l$,r_^]3[3MEl$;sTD,S3T$v++3fCs$;l$,r_^]3[3MEl$;sTlSTT$++T$D$3fD]s%L$T$,;3T$AL$
;s++\D$;r+D$$MrDTk3fD]s;T$,3
B;s++\@r@TILsDXLIl$,s;3B+H#+IuL$TLD$\$3fKs;T$,s63B;s++D$HD$t_^]T$3[TsT,D$;r_^]3[D$
_^][VVPF^D$Vs ^33fVF3V=sA<D$r ^t$W 98_gfffT$t$Q3^QD$VPL$<u#L$T$Q(uT$D$F3^VWJFt;~Ht2S\$?F[u
_^
~HF_3^V3WB03rFD
nANuLH@&rAA=3@r=sNuAq@=r30_^VV0P3F(F0^VWF0t9~(t/S\$FD8F0[u_^~(_^VV%f@^3SUVW&3FDN0
N(F<n,<~8+@+F@F4FFrn+3F$F GF8FhffAF4F3F4FBN3ACr
lLT$\$@j33f@+fJu@rDT@=DTr3L
XfHNuBr_^][fQfQU)VWfU%+ftz3;S3f},X3;<S3Rf+;q[x_fH3+T$f0^
]L$_^]VNAAfPA:AvWy$~G<|_vB^zSVWfvffyfr_^[SUVWHF;L$:|$ fRfT$$tP/;+ff{uT$
3fT$$fQHfzHn+33HLlfK\$l$3H+l$HP:l$vM\$ fRfT$$Q*+fRfST$;tT$!Y:wT$ \$fT$$fQK\$zH@f
C8t+f
ff=uPTD D$!fY*wED$!3D.mPhL$
F_^fX][M%@;tT$PQPB+D$fy_BF^][D$V2^QUl$VWL$33DmT/m;D$thtRSil$F+h)hihll$MluTRi[_^]YRP4_^]
YV0^D$SV3WLH|$3D>H+I3T0m3L7H;tO3L7H+IHPW_^[Q3V3fBfrQ^u;A<vA$SUV33FWfXfHH\$sWztNf?uG
<
sAG:O8t Q:uAQ:riA<ss
AfGFu6-
Fu=_^][$N_^][$NV<N<F@AN<;T$$r_^][$tI;w"3u_^][$D$FHFu*N>F<3;\$$+F<ND$.~33fMW~3fE++D$A;L$0D
$3f\$
33L.mT.n;L$(tlQL$,9GlDl$+h)hihll$MluD(TPL,D$OfOWL$,,;,A;E#,;D$,Hf[3W)+fIfKGHsH@xl$f@;\$
fNf
f
n3Eil$0,I;s%,;E;D)D$ff4I,I;C;E;@Lf\$ L$fW[,JT$$FT$CUMf;\$^_^][$Vt^3WDHy84@A4+;_w0^Q4^SUVWF,ut
_^][d}&t8u=G&uN@^<3D.Hn,+@M;n,v+_N@^][_^]3[
UW_^][T$SUVW3C,ft+HtPBffquE&rD$fDTBC4K8;tfL$;tY33fJIftf8u3s+px~p@FI3fJftf8tRL$;u|$T$;tuG3fwD
$v nj%,Mu33LmT+H;tM3D+H+@NVUL$|$;u_^][SYUV3\$fs3fC3W9L$u \$GtXT\$f?tO8tY:uO33fAfY;uG
Eu
VLLffu WTVGL8t
X:u3H3fOH++;w;UUUUB3D$\L$A8Q4;tpq8"t3U

3t/fD$VMfL$~fJt_^]3[_^][SV33NQ3fH;V NV fHFPPVz|v^[VqF<VA @AA ^VfHFPPFx|vwN$N
^VNFFt3^WNN%OFu_^0@AP@A@AV33Aq^AVD$QA+Q^V~sENN%NF=NsNV%FFF^VqWQ;s3A_^++Qqi_^
SUVWl$f8'x3fPU3W;s,R3U~D$D$_^]%[3Ff3_I;wIt?3_;v3GP+U~LD_^]%[3fY;F331F3fA+PUD$ PPPPPPP
Iu3D3fH3HDuV33330NP23P@3f32X&F ^ <V3fUfu? +fFTSD_^]%[3 +f%
@D$ VPPPPPPP Iu3HFD3f\$NANI3f;tA3f33+33XL#+;uTRTT$\$DT;rRL$;+QSD$fL$fP3O
Du7_^][3Q;wU3J;vm3EP+S|$G<s!OOuf'GWnED$D$_^]%[_^][SVD$uLD3D$A\$TDud$D$r;T$wr;D$vN3
^[SDuLD$3D$3P\$TDud$d$r;T$wr;D$v+DT+DT[@s
s333DLL$u D$SDdD$[Q=L$r-=s+@P@s s333Ujh@h@dPd% SVWeej0@Y
L@
P@8@
,@@@
(@D@T@=@uh@H@Yh@h@$@EEP5 @EPEPEPP@h@h@MT@Muuui0EPX@E
MPQYYeu`@%\@%L@hhYY3%4@%<@^RF8*|l\H<,@@CRC errorcan not allocate memorydecoder doesn't support this archive
Everything is Ok
can not write output filecan not close output filecan not open output fileExtracting Testing / xetlcan not open input fileincorrect commandUsage: 7zDec <command> <archive_name>

<Commands>
e: Extract files from archive (without using directory names)
l: List contents of archive
t: Test integrity of archive
x: eXtract files with full paths

7z Decoder 18.05 (x86) : Igor Pavlov : Public domain : 2018-04-30

ERROR: 7z'GenuineIntelAuthenticAMDCentaurHauls
<?YHdZ2fQ`@@0Xp^RF8*|l\H<,Zfputs_iobmalloc^freeMSVCRT.dll_exitH_XcptFilterIexitd__p___initenvX__getmainargs_initterm__setusermatherr_adjust_fdivj__p__commodeo__p__fmode__set_app_type_except_handler3_controlfpSetFileAttributesWSetFileTimeiGetLastErrorHCreateDirectoryWWideCharToMultiByteMCreateFileAPCreateFileW.CloseHandleReadFileWriteFileSetFilePointerKERNEL32.dllt
0/0K0c0o00a1223@334"4M4`4i445;6a6h6o6667#7q7|7777b888;<<<3=?=U=|==========??
$010]0g000>1`111"2)2590u;|;;@>P$66666666;;;;;`66;<<====S11?? ?D325555555555556
666'646F6K6P6[6{666666x0|033
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```
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PROG = 7zr.exe
MY_CONSOLE = 1
CFLAGS = $(CFLAGS) -DPROG_VARIANT_R

!IFNDEF UNDER_CE
CFLAGS = $(CFLAGS) -DWIN_LONG_PATH -D_7ZIP_LARGE_PAGES -DSUPPORT_DEVICE_FILE
!ENDIF

COMMON_OBJS = \
 $O\CommandLineParser.obj \
 $O\CRC.obj \
 $O\CrcReg.obj \
 $O\IntToString.obj \
 $O\ListFileUtils.obj \
```

```
  $O\NewHandler.obj \
  $O\StdInStream.obj \
  $O\StdOutStream.obj \
  $O\MyString.obj \
  $O\Sha256Reg.obj \
  $O\StringConvert.obj \
  $O\StringToInt.obj \
  $O\UTFConvert.obj \
  $O\MyVector.obj \
  $O\Wildcard.obj \
  $O\XzCrc64Init.obj \
  $O\XzCrc64Reg.obj \

WIN_OBJS = \
  $O\DLL.obj \
  $O\ErrorMsg.obj \
  $O\FileDir.obj \
  $O\FileFind.obj \
  $O\FileIO.obj \
  $O\FileLink.obj \
  $O\FileName.obj \
  $O\FileSystem.obj \
  $O\MemoryLock.obj \
  $O\PropVariant.obj \
  $O\PropVariantConv.obj \
  $O\Synchronization.obj \
  $O\System.obj \
  $O\TimeUtils.obj \

7ZIP_COMMON_OBJS = \
  $O\CreateCoder.obj \
  $O\CWrappers.obj \
  $O\FilePathAutoRename.obj \
  $O\FileStreams.obj \
  $O\InBuffer.obj
  \
  $O\InOutTempBuffer.obj \
  $O\FilterCoder.obj \
  $O\LimitedStreams.obj \
  $O\MethodId.obj \
  $O\MethodProps.obj \
  $O\OffsetStream.obj \
  $O\OutBuffer.obj \
  $O\ProgressUtils.obj \
  $O\PropId.obj \
  $O\StreamBinder.obj \
  $O\StreamObjects.obj \
  $O\StreamUtils.obj \
```

```
 $O\UniqBlocks.obj \
 $O\VirtThread.obj \

AR_OBJS = \
 $O\LzmaHandler.obj \
 $O\SplitHandler.obj \
 $O\XzHandler.obj \

AR_COMMON_OBJS = \
 $O\CoderMixer2.obj \
 $O\DummyOutStream.obj \
 $O\HandlerOut.obj \
 $O\InStreamWithCRC.obj \
 $O\ItemNameUtils.obj \
 $O\MultiStream.obj \
 $O\OutStreamWithCRC.obj \
 $O\ParseProperties.obj \


7Z_OBJS = \
 $O\7zCompressionMode.obj \
 $O\7zDecode.obj \
 $O\7zEncode.obj \
 $O\7zExtract.obj \
 $O\7zFolderInStream.obj \
 $O\7zHandler.obj \
 $O\7zHandlerOut.obj \
 $O\7zHeader.obj \
 $O\7zIn.obj \
 $O\7zOut.obj \
 $O\7zProperties.obj \
 $O\7zRegister.obj \
 $O\7zSpecStream.obj \
 $O\7zUpdate.obj \

COMPRESS_OBJS = \
 $O\Bcj2Coder.obj \
 $O\Bcj2Register.obj
 \
 $O\BcjCoder.obj \
 $O\BcjRegister.obj \
 $O\BranchMisc.obj \
 $O\BranchRegister.obj \
 $O\ByteSwap.obj \
 $O\CopyCoder.obj \
 $O\CopyRegister.obj \
 $O\DeltaFilter.obj \
 $O\Lzma2Decoder.obj \
```

```
 $O\Lzma2Encoder.obj \
 $O\Lzma2Register.obj \
 $O\LzmaDecoder.obj \
 $O\LzmaEncoder.obj \
 $O\LzmaRegister.obj \
 $O\XzDecoder.obj \
 $O\XzEncoder.obj \

CRYPTO_OBJS = \
 $O\7zAes.obj \
 $O\7zAesRegister.obj \
 $O\MyAes.obj \
 $O\MyAesReg.obj \
 $O\RandGen.obj \

C_OBJS = \
 $O\7zStream.obj \
 $O\Alloc.obj \
 $O\Bcj2.obj \
 $O\Bcj2Enc.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\LzFind.obj \
 $O\LzFindMt.obj \
 $O\Lzma2Dec.obj \
 $O\Lzma2DecMt.obj \
 $O\Lzma2Enc.obj \
 $O\LzmaDec.obj \
 $O\LzmaEnc.obj \
 $O\MtCoder.obj \
 $O\MtDec.obj \
 $O\Sha256.obj \
 $O\Sort.obj \
 $O\Threads.obj \
 $O\Xz.obj \
 $O\XzDec.obj \
 $O\XzEnc.obj \
 $O\XzIn.obj \

!include "../../UI/Console/Console.mak"

!include
 "../../Aes.mak"
!include "../../Crc.mak"
!include "../../Crc64.mak"
```

!include "../../LzmaDec.mak"

!include "../../7zip.mak"
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Preamble

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MZ@ !L!This program cannot be run in DOS mode.

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QPPFfLfF^DS3Vf9t;C@@f8ut.W|$SWeL$xvf9f8@@AAJu^_^[SVW|$_t+St$&T$Pvf9f8@@AAJu^_^[fD$Sf;D$VWt.39~v%T$xV|xfD$f{G;~r_^[UQS]VW{tRuMuBCEF3E9wv/VP|uVquVu;wr_^[A;Bu
RQFYYujX3T$VWJi~HP|?WQPFN_^SVt$WL$_1;v+v)1iTR1PHRPF)w_^[VW|$t#iNL
QxQPF)~_^DHzDtTHDTH j XVh3;YtFN
F"^FWUQSVW3GfV;t}FPPRQPuEuFtZEhGPEtN=;GvPDEV6PQRVjuuEhGPEtNGf$A_^[UEPj_u]USVW3G M
V;PPFPPRQPuEu#FEhGPEtNx
e}t}teE;GvP*VEEu#PEu#PuQRuju}u}U
uEhGPEtNG$_^[JF eVWMCeEPj_uMEP}uYM_^d
t
VW3f1f0r+f9w%=w0+;wAA3t
_^USVWUte3f3f0rUf9wOwAr }w6jj
RuKE0j3+^;wr9}w MCC33EtE_^[t
V310r
9wAr
Fw
arfwuA3^t
^UQQSVW3FUfOEQP3<_EE;FvP+UPuMN_f$J^[UeUUM;UESVWjGYEBs}ffsj@X5s)sjXsjXjX;Ut
rsBHtuV;s}tf7EMQ+s0}tf
-($fEf0ME}tfEMe;U_^[MEEUQQSVWf GA;G;^EEvSKMUHPa^_^$[+;tHVW1AAr.s@#r;s;t1;r
sAA@@;u_^W;|$SVGGs@s?@H@@rksc;|$t]7rRsJ(?$
GG?X??XH" ??XH;|$+^[_=XGts=XGtRQFYYKFSVWUs3;tnM3;}v7fxf\tf/u
H+MRPMEP3OG;r+VHMPMEPucYM_^[d
IVW4H;tf~Nf\t
f/t+QP]L$V_^IVW4H;t,f~Nf\tf/u;tf~Nf\tf/u+QPL$V?_^Qd$VW~DAP_^Y
QSUVf3Wf}ftiCD$f*uL$uWftV8f?uftI$f;t=XGu9fxf;u#CCD$f3EEff}u3f2_^][YA3Vv f1f*tf?tBAA;r2^^y
t.yt(yt"yuh<FPFYYujX3UQ}SVWu
~EN@+3!}}t"~u~t~
ut}~t}Et
~
uHE;}EZ3vAE~tet

u G;~rN;tE@;EE~2_^[39Au9Au9A,uy u
A2VW39~vD$FHatG;~r_^|$t$t(oKF;4SVuWFvk~]t}tuiSm}'WMukePO MMj GM~<w~t~tu FVu6M_^[d
DVtN3FFFFF F$F(F,F0^EKFCQVuN(E
NE
eNw vY^Md
Q V3vIxuxwF;r2^^VW39~ w9~vFuG;~r2_^|$VWquq(39~vt$t$u
G;~r2_^XKF_SVuWuVjEt huVj~Evb0|NVMlejMGuuEP|tMM9MM)EM_^[d
UVuuuu>tMFPj62^]lKFk8SVuM(F(P39^v`WFM<GP}1uGMPePEH2MMEEMW C;^r_M^[d
VW39~vD$tG;~r_^fft*fy:u#fyuf=arf=zvf=Ar
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9]t}uAE9Xu7M7hDFMPMEuYEE2]3EEEEMPEEuEEEEME]QuH_MEuMYMM_^[d
VW3N>~~~~~~ ~$~(~,~0_^SVW39~ttj9yt3NtP9zuj[;tm@=FhHFPYYtDh@F@PYYu7~t)@tjdF@ujX
j[FC;v_^[KF|QVueN6[Y^Md
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MF
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wjX3^[SVWuWRL$xxP;~s
~f$x_^[HNFMSVWMweEPuY_M^[d
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h FM{MEt6tu,fx\u%F(N(3yf8uEPPw3ff M>~EMPEME5EMPEPENt4}t&UMu#uEYMjF26MvA;s
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=t^ F uN(uiYM]T0OFT8SVMMyeVMytEt2uYM^[d
DOF8SVMM eVM tEu2uYM^[d
XOF8SVM<MeVMumYM^[d
Vqt$3j*d^9tt$b
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t2_^tDt F@tOF SVWM=Fjj];wP]YueVS3;]tj;]weME}v:j3[f|u"MPMGEEP]GCC;}r]uV9]YY VY2M_^[d
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E YUQQSVW}M;_]vS73v]+fftfA@@;Mrf$NO_^[VWNM=Ft/Nqu
39F'FvPYYuFvPVYY@_^(FOF!eQMMet"Ef@\EHt
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:uA;r:w5TFNpt$jY;vF"ff0rf9wA@@;r2_^fj\Zf;tf=/uhfAf;tf=/uYfy?uRfAf;tf=/uCfAf=Utf=uu3fA
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fu2^^VW$t~u|$uW2_^DPF@HSV3WK]ff>f;}sf.ufFf;`f;u
f9NQef~.7N'VMDMeu
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Mt
cuSBt
8FjE_;Mff=\tf=/ufAf=\tf=/uj[t<FEM'f}\Et$f}/tMAMP?MV6MV+Mu2G}tuMWESPMMPE8EuaYEMPuJYuAYM
VMEEM<6PMEuuuY2YG5<F;usEuf$EPEP}uuYYM_^[d
UQMeHMPuEUSVfff Ph$FPv
wjX3^[UQSVW}3fff.tfLpf\t
f/fLpf.uf\pNf\tf/t
fpf:f\tf/uNJJpNHH|ff\tf/u|+ftA3QMV}/f\tf/tfujXtNfujXPV~Ffp2_^[UQQVW}UMtDuMt3Gu9Fu8Eu"u
u=~u
u-2_^hPF$SVWSMtQ<6t=SMeM<tM;qs
qf$EPu)YYM_EtWMMt"MM(Mt)u^2uYM_^[d
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F^f3^VFt f3^f
F^Vt$f&G^Vt$5^UVEfffHQPFFuFhGEEP"^]UVMfffFuFhGEEP^]Vf>t
ufD$fF^Vf>t

PfD$F^Vf>t
0fDFDF^Vf>@t
f@D$N@F^3%}/;t+~~! ~~QF|~@uQffQfQfQQ3f9u3VVFL$%}/|~%
~~QVF^|~@ujQVF3Vt$Wf>t,|jWVf'3_^Vf>u3^S}f
F^UVuE}%=uFhGEEPf
F^]U
VEPQ&FtEPEPFu2SW}'}0r3'3F}j
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G@GGs
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\$;XtG;r2_^[pQd$=GSUVWv(hG7VuD$D$;Gr2_^][YFEFEVW=GWz3~9Gv$ShG>Z@RT;Gr[_^VW|$gg G;
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F(F0N,N4ENPuuUMt
3N(V,N0V4FNLtPvPu@~
%
_^[t3Fu@~
%
U9EsEVMjQPujFPFEtMuFmu3
^]UEVsu~ta~utVt!HtHt~F N$F(N,EMU|Ms
SMEN(V,tP3:MWQPuNuxU}V(V0~,~4ut~_^]DtHU4EPEpFtmVutWjjjuM3V_E^tMMHEtMMHEtMMHEtM3F
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EF$EF(EF,EF0EF4EF8EF<_^3[F[[UEVuPuNuUVVut(^]U}r1EPEuHuu{UtM
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I03A(A$A4A,UQ}Vu]~4uFPh@FtF4ENPuuwu2%uUN0F0EF(V,3;E^US]VuOF$W+t6;vN
WuQbUN0W]~$~(F0V,+}Su-_^[]0QFSVWNAt\F$M33t$V

MP)uBv$V ~$E~4MveMuM@M_^[d
;^,wor;~(shEMPhv t8}tJuV M
)u'uV M_}E;^,rw@M^{MOE9F0b;~(F(V;XM3KDLH(3DLH,3VWFL~H;rf;s~@t9~Dt
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F@$F@(xF@,F@0hF@4XF@8HFH<H@PHPLT$HXH`HDPPHQH\HdHhHlH|8F@(F@F@F@F@F@F@F@
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)l$,l$.lllllllllllllll$l$ulxl
zl$ Wl$ Zl$$\l$$9l$$<l$(>l$(l$(l$,
l$,l$,l$0l$0l$0l$4l$4l$4l$8l$8l$8UQSVu3]WNF+NUF3;rw;v3t#FURSuPQUVVuF M_^[tUEt VuWVF$~N ;r
w;r3j+S3;r
w9]v]F(N,[;Fu;NtFNu-FURuuPQUMt
3NVNV_^]UMEt!ItItBH P$HPMUU|Ms
MHPEtP3]QFQQSVW3Mj0;o;Ytxx(F3;utVPuN}%EMF(FEN,F EN~~F$;EtM;tVPEWWWWVP33M_^[d
Vt$NFut
V'oY3^RFQMAetPQMd
UQeS]VWt#uU3~N;r+w;v%u~Fu@;t31VUNt}WRuQPUE)V^tE_^[UQEt SVuWVF$~N ;r
w;r3+3;w
r;MsMN(F,V83+!];rAw;N8r:^4]3;w.Ur;w%+9UwEtRPF0PurE;;Nu;FtNF>u3FURuuPQEEFVMtFEV_^[AjjqqPRU
MEt!ItItBH P$HPMUU|Ms
MHPEtP3]UVuUWFRuuPQUMt
N~VNV;wr~;sVN_^]UME3V+Wt!ItItYHpHpMu};|u;s
.uMp;xt1yx$HRRP 1PxWRQV_^]D$TSVtX HPp@$W9PSQW_^[VWfftCf=+uf~t6dFqu&f>-
uf~t`Fqt'2_^t3ttWfyI|%W3UQV1WUE+_^UQfVf=uyu"MB'fuqtU;tW
EM3^UQyu/f8tU7u28EtEjX@M3f8tW
ubG
LtH@S\$Vt$W~h(@7jVtWajY_MtVIjY^[5UVF;FrEhGPEMW9<QNNoF_^]0RF(S]VW33;FQM9~}vM4vG;~r3

;t>
EfEfEEsMEE3EMP8eMauEuuPQMMM_^[d
FRF2SVW}3P~^hPhYEY;]th(@h@XWjS8M^_^[d
3ffHHV6hYN^tjAHx ;T$tH}Vj}jfxu@ vj
X^`RFC4SMMVW&3;4@F]uf]f]]]EtMU+;f]f]]fEf=uutf=uEUt{uMXu~;tNf=uU;u1uM-f=uU|;uuMu
VMEMPEf^uM MW!M]MEPFMM_^[d
UQVW9UE+V_^UQVWUM+vSY;[;u-@sx s
jZR`jX3RP3UfJfAr fZw bt+ttIIu'jj
jZj Yj+^;sPDW_^SVW3DFluCFr_^[UQff;t?fuf=u.jqM-fuUuuMfu
QM2vv
tu2RF[(S3VWM]]]MU]39]v]EM<`sMETsEUPEMEPEPu;u7du]dFY;uYrMM-S3M_^[d
dudMYYMRRFSVWU~Mr~3;]tAv3fXf=:uMEP\Eef PMPXC;rMEP\udY_M^[d
SVWf=h|W6#tDx0N3vff0rf9vG@@;rW6sxL$P%s_^[RF4SVu3WMF;t`}f9uMMqM]qEUPEJMEPEPu7cu/cYY};}
Wk4@F]f]f]]EEztUH;t%fEPuWMEPMM_^[d
RFVWff=u0N@s^j XsP3RP3;f=u0MpveMqMfubYWM_^d
RFSVWCc{f: EfuMp3;M|;us
Eufp9MvEf8s'A@@;Mru4kuMaY}3,uvaYWE{DpPpWM_^[d
RF>VuW~thF.gu
uVo?uf>tW/M5oveMepEPu`YM_^d
SFQVWj~`YuetEHNPN3MV+M_^d
VWj,`Y3;tHfHfH
H3V_^VW|$jX;s>t9~t~f43_^Vl4&^Q3AAQAAA$AQSVq3Wy+;_^[vIUQQSVWF~;r~+F 3;]tWNQP
~ F;tU]RWVRPQ}E~NF;u^9FuF$^N;Nv~FE_^^[VF;Ft
^t^3^UQtEEh(GP3HHHHH
H(HH$H,@0@1lFUVujhGu&5tjhFuuE@3@^]Vt$NFut
V;^Y3^7SF1QVuFEtPQvetVPM^d
SVWF~t PQ't^GWhFSD_F^[UVuF
EF$EF(EF,EEt@MEEt@ME~0tFtURURPQu2~1t*FNEMEEM~uEvPVQ3^jjQPtDtHttDtHt*Vu3Nuu
3Ng^V=N5N-3F
FFFFF^LSF|~QSV3Wj^^F
^^^,\;YtXpF3;tWPEj]]8[;YtXpF3;tVPEM_0^[d
UVujhGu&ytjhFucuE@3@^]Vt$NFut
V[Y3^FIVt$NFut
VI[Y3^AF``HD$VWt |$t^~
tN+uN F
F;vt<Wvt$~~D$ ~Vt8)~uNF
R+3_^UQQS]VWt#}tI~uKENF~jEFEEjPjFr@+~t
t;3 F _^[US]3V;t9MtuVF;r
wN;r32NW+;}v}FWPu}~Vt;3_^[]UMEt%ItItFHMUHPMUU|Ms
MHPEtP3]UVujhGu&|t,jhFu|tjhFu|uE@3@^]Vt$NFut
VXY3^lSFzQMAetPQMd
VvFf&Y^VW|$;v:@v
jX;;vWvFYYu2F>_^VL$F;r
QNu3FF^S\$VqWyVtVW3{_^[S\$VWt#|$t.t$WuWt$Pa{~t;3_^[UVW}Gw+;uvutOVuQ"{wEt0_^u
}t@3]UUVuRuFuPQMUNV^t
]UQSVuWU>&t+;sURPuSQMM+uu3_^[UVuEuPu;u^]USVW}t-
;sURPSVQ]+}u9Eu@3_^[]UQQeSVWwutMuEUGH;suwtbRPuytO|FuE9EuwOVuu/u{}tuu7yy_^[VWjU3Y;t0pVh
^SVW~^uu>~~utu* f~Nt3V@^[Vt$Ne~uN3^aV~NFtxNt^a|$Vt$tA38D$30T @|Fy F As3ys-Q#twrs

tjX3^t%3jZ;tjZ;tAv3SF>vQSVW39^~u5hSYM;]t"3MF@4vn8]9hSYM;Et
S3M;~}tWPjEqS;YtXXXtFX3NPF;t3PM;tWPvuNFPQM_^[d
UVujhGu&vtjhFuvuE@3@^]SFtQVuE.FetPQ6MtVPM^d
yt
AHPQSF{tQSVW}w
eFeURhFPEu
@W jRPQtEMPQEMtPQE}t0FuPQFjPQt]#GtjX3uN#PS}t$FPQuFPQut(#tvH;uF;tjX3M_^[d
D$3D$rW%FLLf|FfD$ 3D$3D$rW%FLLf|FfD$
3UE3fMfMM,toGt2JtNu|E8H1ttpDp@MdE8H2t\pLpHE38H,ujZ8H/t
8H.t@8H3t8H4tRMAE8H0tp<p8MEP(uMFM3D$3U@W$Et%
FEMtFM+L@u
FSVtF+D1FuOVuY3j w_j ^j[j333^t
WDFt
V@FtSHFoMEP^[_V3jZ;tAvSbu^Ifu
k{I38t@<0uf[^V:qt
+D2FuL$<I38t@<0u^UE3fMfMMt&Htu?MEPO1E8H0t)p<p8E9H$tH@#t
PQM7uM:M3|$
VsjXZL$9r3MAQ #t3rw9t$st|$ry
u#tAuA^T$sjXD$t t3 J@RPkUSVuWVP]jU2uvF NPEPtA38F |u2~8WjjjSPu?9Gw?rFFu~u~ujXSN$F,"SN(F-
3_^[Vt$NFPQN$F(t3^SVt3^8^,^0^1^2^.^/^3^4^<^-F$;t PQ^$F(;t PQ^(^3[SFndSV3W9]e]t}t}uE9t
Wu}~0tv<v8WP]39]MEEuQSWP;EtEe;tPQEr9]u9]u3auWPjAK;YtXXF3;}}tWPuOE__E;t PQ]j8JYM;Et
/3;E}tWPjuE~-F$;u;;EtWP9]Et EPQEe;tPQ@xSSSSPQ;Et=;EtWP9]Et EPQEe;tPQE.F-]]v(F
MEP;Et9ME8;EtWPEE;tPQEe;3E]]]]]]E_(G EG$EG,EtGMEEEtPQEEtPQEetPQEO3M8F
EEPEuP|EtGMEUEEtPQEEtPQEetPQEE;EttxF MPEPuE\uEEMuUeP3ME}@A@IDEEMX@Eu
F3E)}E}t
f,ER}t}uF}tF.
Ex0uF/E^@F8EF<EFDEFHEFLF0F1F2~,uj^/~/tj~3tj~.tj}uj39uub}t
EPQeEVPQMEEEtPQEEtPQEetPQ[MEEEtPQEEtPQEetPQE}TM@M_^d
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2TFhQjP}FYM3;EtPMd
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UVujhGu&iuE<jhFuitjhFuiuEP#@3@^]Vt$NFut
VEY3^[TFgQVuF(EtPQv$etVPM^d
FJYjP?EYt
j3@F"YllMD$rW%FL$L$f|FfD$ 3D$rW%FL$L$f|FfD$
3UE3HfMfMMt<&t/t#u6Ep4p0M#E9Ht@p0EpQMuMMI3TFGfSVWVPE3;]URhFP]9]M@Rf|f~]E|RjPEQ;t,|uCE
M;YtPQbf|t|uCY+uMR|E`j.MVxEWPMEMExPQEMPE@RMEHMYQMEMQEj_E;]r9MDFLAHt!EE;vaMLAf9au
TGII;rKE}-M@FLA]HEvMLAf90uGII;r;}+MPpP5PUME>PPME /RuABpE2BEYY+MAMPQEEEsh8FN$R
HN$Pu0R}MQjSSWP;EtLuAuAuAuAuAuAEM;tPQELSSSSWP;EtLuuAumAueAu]AuUAuMAEM;tPQEEUF0R
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h
EE;tPQF}MESQW]P;EtLu@u@u@u@u@u@EM;tPQEAMNEMPEU]EUREuPQE;%E;URjSSPQ;EfESSSSPQ;E
EMF0QPN4NlENPDFttSQWxPE;EE;tPQuE?YMMEE;tPQun?~Yu
8]uV?uN?uF?u>?u6?u.?EM;tPQ3EE;tPQu>u>u>u>u>u>u>EM;tPQElEE;tPQu>u>u>u}>uu>um>ue>EM;tPQEEE;
tPQu0>u(>u >u>u>u>u>EM;tPQE;tPQu=u=u=u=u=u=u=EM;tPQEGu=uy=uq=ui=ua=uY=EM;tPQjXM_^[d
UQeQuMJETF_SVWVFJ~f<t fzufafZu0fAtf9uf0t2<^j1=PF^Gf<PSM~JMePoLu<YM_^[d
TFj^QSVuWeeuutVP7@M_^d

[Vt$W3~0~4F$~(Nf8K~_3^L$39AL$3U3}fEfEEt}v(}w"EMp4p0EM$PuMM{3,UFy]SV3W9]e]}t}uE9t
W"Eu]]p4p0VQ;]39]MEWQSVP;EtE];tPQE9]u9]u3WVP;tE];tPQj(:YM;Et
E];EEtPQj8EF:YM;Et 3;E}tWPSVEE]EM;HEMG(G
O,O$;EMSS@SS0VP;Et:EE;tPQEE;tPQE];tPQEuESSuVPQ;u3EuH @$MEECE;t
PQ]SVPEE;tPQEE;tPQE];tPQH@M_^d
[3@F@F@F@FHHHH H$F@xF@hF@XF@HFUSVuW}j&[ShGWD\u
EShFW"\tShHFW\u
EPUShFW[u
EP8ShFW[u
EPShFW[uEP#@3@_^[]Vt$NFutV7Y3^RUFY(SV3W9]e]t
WEj(k7YM;Et
(E];]EtPQ}E3;ws\]]]]]]]GME0GMDEEPEHEE;tPQFM(ME3@M_^d
[UQSVW3339F EvGEFxXV
QIPjjjQRuEE;F r3F~FF^3_^[3HHH H$FUVujhGu&YtjhFuYuE@3@^]Vt$NFut
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FHUF0VQVj 3YM3;EtuMPM^d
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VWjd3YtD$t
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13YVW|$tPQGFGFGFGFGFGF_^VF9FuGSW\;wPp2FYtP6WV6w2Y>^_[^lllellQlxl$=l$dl$).VFTQVW3
$FFFFFFFFFu~N}kE2:MFFFFFF|FFhF_^d
NVFaSQVu03F$F(F,N0EzEEM^d
UQQVeF@FfFfMEt%EUjFYVyUjjRPVFV^kVFRQV3uFFNE
9NE>M^d
Vvr0vj0YY^USVuW}j&[ShGWSu
EShFWStShFWSu
EPUShFWSu
EP8ShXFWSu
EPShhFWcSuEP#@3@_^[]D$@@Vt$NFutCVe/Y3^VF[QQVueN0MN$M^d
VF%QQQVuFFFFFF|FFhFE9tPQEtPQ.FY#uN0EeN$M^d
D$rW%XFL$L$f|FfD$
3D$3D$rW%\FL$L$f|FfD$ 3VFO SVW3e]f]f]]EE:(&,tl/u8M6E@t
hFM7t
hFM79]tuM"uL-Y-E8p|px{E8Xr
aE9M|@M8Yr9w 8MGt6JtNuE8XptwE8XqtaE38ujZu 8Xst@uuuu;t RMMuMpM3M_^d
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Z9^/WFL8SVWuTTMO03;tETPEP0;uR9}uMj&Y@t'Ed4] Mu3EsxVjWWPQ;)E;tVWPQ;ulj|Z}}
;Et_ulMuE}}MT5Eh4GPEP|ME6
u 9}@o}gj;EEM5EMCpCq5UCr|AE9E3; ;oM};}}MIEM}MtB9zEE}E3MeJAEAEEAEAtS;rE;EE:8yxyx}E
px}x}xuEEEIM;wr
;sEEE;B7M39}u;uEU;u8MAxxp8xxP3?;t*@#uu4GM2M^M_^[d
U(eSVWM~hF3;u;tE=FrElFu
WMR!E]P039~vqj:('N!u;u~u

VGu;u~uF@P0)j[&~vT>G;~rj]&_^[VW
s07P&
F0rF7P&_^UQeVu AEkv
EmPI0}t
u5&^SUV3WvR
j^Hu3Ft+`/t
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3X;tPQ;tPQW_^3[V4G&N^VN3^;rWFEQVu,F4EtPQN0v$v#Y^Md
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w9MvM}
;V^r"w;rF
3N;w;0RW0\8<+_G<G3;wr9^(s
@f WGjUjuPQRu{F$0PSwN0w7kuVGFGF^ FN +F+9MvMN$}WQuCEE~Vt83@M_^d
[USVWU~>uu
2fEfFUu}3}]3MMMME9Eu;9Mu6;MMEw
r;s;}tEURu6PQE3UE+U+EUUR3;EjERP3ENPEPu0EMEM+N}u}tu3QtA;u4t0E9Eu(fNV33F N$;Eu
;Mu3jX_^[UMEt!ItItBHPHPMUU|Ms
MHPEtP3]WFAESV39]We]t
W }8_hr;3;;M:u
E@]EUjYC;r;h8YM;Et 3;]utVPN$E+vG4P0GxFG|FE^^^^^ 03 @jXM_^d
[V@Fff$f(N0J4,F^WF@lSV3W9]e]}t}uE9t
Wu}8tv|vxWPMQW]]P;]39]MEEuQSWP;EtE];tPQE9]u9]u3{uWPj8 YM;Et
a3;E}tWPjuE8ti;u(;EtWPE];tPQ@SSSSPQ;Et-;EtWPE];tPQEj]XE@EE]]]WuEPEP8]u;u?@8M;Eu;tEE;t
PQ]EuPQM;t;EtWPE];tPQAM_^d
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@MQMQMQSWP;9]tUf]f]]MQjSWEP;tf9]tf}uf9]tWMTeMC9]f]f]]MQjSWEP;u&f}tWEMPQWMER;tMeMhYM
;Et 3e;utVP}EG
;ujXFEEGP#P;uNPNTw(;Ete;tVPEuP23;O<sCG89ufxu@r=wFlAe9]6EPQ(t]FhE;G0sVG,MeE;Cs:MHQ0EtEetPQ
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UW*;EtWPeE;tPQAM_^d
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WUf:sf9Z]zu2tOttWff{E<KUu*Vjj'%W7EU}t333RuNR)uYM_^[d
DXF7QSVW}3ew]NX);]s!EPE4C39_Lt, s%OHlF4hFtG t]Oxw0P4wBu0~uh0F35C0Fu$FuWAM_^d
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F'VWwtSNtK|SYu[7Y_^XF5QVW}wetSNtSeYu[7XY_M^d
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XF3SV3W;9^,N(9]URhFP]E;tVURPQ;tEM;tPQE#Et
NU;Qwr9Es~4tEEM9^]FURhxFPEF,3;vQF(9tCEURWPQuYE#Et"NU;Twr9Es~4tE3G;~,rME;tPQ3M_^[d
EMtPQUQQESVWV:;F8uyeF?@(v[MuN_u<FeHvP;]t5E9Mr|(@tvtEEE;rM2_^[A3VvI1;t$tB;r3^VFPFp$
tN3v
6<t @;r^2^a,a8XF0SVF3;]tWNP;uM;v<WuN(e<<vSN4jEE;rEC;^r3_FN9Mt2$]]]M]u&ucYM^[d
UESVMWF43;tFIHFF^~SF_^N(S3^,9FvHM3;t*;Ut
URSV(\N(@;Fr_^[]US]VWSw%tPN3vv9t@;r}EhGPEw3D2ODOD<u2P_^[]D$SUVWN,3N<v+PatE;r_^][2UQQE

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H(H,H0H4H8H<VW|$WGNP*GNPG$N(F$G(P4N4W_^D$@h@hVt$NhFhut+VY3^t$AhLtt$YF,QVWu~TF`F
NxENlE4M#M_^d
VvP
vD|
YYN^Vv4e
v(]
vU
vM
6F
^
YF>,QSVuWF {DEOUGF{PEOUGKhFG6UpvOJp3;tv]MQhFV]E;M;O8tPQ]MQhFVEE;MO9_^;[tPQMd
AhL$TYF@+ESVW{`tK<<K0+EoCl0eMeURhFP}<C`t E MIMt}
C00Ee}eMEURhHFP}eEPEuuuEuPQt:EEtPQEetPQEMtPQEEEtPQEetPQ}}eMEURhFP39uuEM;tPQ@l9uvLeEP
EEPuuVEuVPQEEuCtPQF;urEetPQEM3M_^[d
EEtPQEetPQEMtPQuAhYF(QeSVWe~`t}9~,u53"F$}3vN
99tB;r|M4~`WNu|Q}EMtPQWF~`t@MQPutEMtPQ9~|w^x$9~|vFx3<9w_uU97uQj
;YtppFV=l7uN0lfff7MlEM3EM;tPQ@M_^[d
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Q3VvI1;t$t@;r^Q3VvI1;t$t@;r^YF&eSVWe~`u}9~,u53"F$}3vN 99tB;r|M4~`WNumBE}EMtPQWNN`t@Eu
N<EEFl8URhFP}F`tKMEtF0<8}e}eURh8FPE}t~eEPWuEuEuPQt:EEtPQEetPQEMtPQEEEtPQEetPQ}u
}}9~|w^x9~|vFx4~^ue~u_j3Y;t@FxxF@FWi~uO
iggvMhEM3EMtPQ@M_^[d
UVujhGu&%uE<jhFu%tjhXFu%uEP#@3@^]D$@@Vt$NFut
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YFt#SVW~`u
}9~,u+"F$}3vN 99tB;r|3~`WNu)3;}W|NN`t@uN<<Fl]];t@URhXFPEtPQWH&MEtPQM_^[d
;tt uuSU\$VWF`tNlILDtF0339|$vPG;|$r_^][UMSVW^(]Fh;^(Et#~\t38H938H8t
]ExF,~PE.utG3vU9tA;r|=~\t
U38B8U38B9u3}uFD<u]F\NEhGPEw3$}t] }|]^`_^[,ZF!LESV
WNt
~`Nh}EM]TeMTF\jYtMSUtKMt}N,UMu}F,ME39}vKeENPEEPuuu~EMPEEtPQG;}r]39}eENPEEPu.u3EMPVE
EtPQG;}r]39EEEEtPQeMMMc.}}}}}}9}EvEM0'oG;}r39}vEM0
oG;}re~lE9EFhM~\<t5eE(URhFPEtUPQu|EDeEURh(FPEt
~\tO(9WPQu?EEtPQEE;FlOF\SuS(t(K(&EEEEPQKtu81M21M1PWuCu8QuuRuPW u3tuuNk39^xv!Ftt~HC;^xr
u3u9~lvFhuuEG;~lr3uueYYM
MM0
M_^[d
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N|EhGPEw3~`_^[]ZFeSVWF|EFx<FN`t EUtEE_pEvEMu|]v
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QEE;tPQE];tPQEM;tPQEEE;tPQEE;tPQEE;tPQEE;tPQE];tPQEM;tPQE<EE;tPQEE;tPQEE;tPQEE;tPQE];tPQMM
jXS\$Vt'W~hA7hVtWpY_tVpY^[dFQVuE @EtPQDDEtPQEtPQhBjjPERhBjFjPE<FEtPQFetPQMhAjjVM^d
ISDQSUVW38\$jYu;w28\$u:t39SjVWL$(T$ LSDUPQ_^][YQSUVWGv3UjQPW3QjRPA`UjUW
jYx_^][YS\$Vt$W~hA7j VtWnY_tVnY^[j*S\$Vt$W~hAA7j8VytWHnY_tV0nY^[dF%QVuF(etPQ)M^d
dFS3VW]]hM]Xu30@;r=s)ucEPEUE;]3E]<0UDU!;u0E} rE}rMAM_^[d
2V3v#SW<3<G3F;r_[^SVW3vL$2F;r_^[dFeMVM ue;FtPWFWMvNMP(wMh
FvMj6<MWvMjv&_MEPuu?lEYM^d
U VuMugEP*v^DAhDApD$At3VNXPub8D$tZW|ww w4w0
RPwNww7v\ANQNhvtvpQNXRPvx~`_uNXP3^UVWv$v v4v0RPdt$j
tYRPj ZZEuUuzEUt uUuuuuPuuu tE}Su3>u^SWFuuVSWFuuVSWF[V_^USVWj`Ej PMuueEFj__P;Yv+L=Q_^[R
UVW}WPP=Q_^UQESVWjM['SVQUPRPjZ*jZuujZuuw8]tkE E$ujZKWu$u uuSjdRP!uj3XRPSVuuRPjZQjZu$u
uu;_^[DVWL jh@BQPRPd_^UQSVWjjduMuEjUYmuuSWlMRP_^[U@SKXP8EVuWKv4v0v,v(v$v
vvv6{`EUtKX5FSdKXj}Yuuuu^uEUuuuGEC8PChstKXeUspUEPuus|U_^3[IX`VWV38t@<0uT$+~W_^V3W8t@
<0uT$+~.V_^UHVWf g EPFU/hFhFDFPFtMQU_^eFSVWFPjMYePAqMuffYfu ~Jt%f= u ~!thFpv,qhFpF
RP?hFpF"RP%~NjXjEX3;u9Ut#}u~@thF6pjvhFpvp~thFoV,~thFoV
hFoFv3;tjP7hFo3%uWVM_^[d
U$EVM#3u}EUSj
Y3#EKu6j
YEM#u"j
YEG#3uj
YETRMP`MEPn}[tuMaeuMu^Vfu
R=ohFn^
R$o^UeVM;MsuU=_EPl^ eFD,VMf mGt1MleUMHEPwnMucYhFn=GthFmM^d
U8SV3sW
EPEPEPU}utF}swEMuMue3}UEPEPEPE"Fuj_FOuE}|}mOU^j(cEPmj)c_^[UQSVW3]}tudcF|_^[4eFVWf
3WdFt0lLFMWj!ePlMubbYGArM_^d
fFJSVWU>ujXj3X@D}t;tRv@EePEEL}}0}}}EE}@;EM4MVpMcfM8Fg9~vE,EPx9}ar
}REEFuMt:EPuuE3;E;M;t!h4F3uuPKf`fbh9~Et`NE/M,Fet>tynL`QEt`;Y>M$FeM}&nM`QEEu_;YC9})Wh@BWu
;EUhF3WuPh$FMdtyEMPmMDFEduJ9}uf`uhdu*E`PMuu;v^Y'uE`PEP;O`uE^EEYM;H;MfE-
trhFMhtMhFMVZhMhMhF?hMhMV(hMhuPEu]YMIfME =fUME
^9}u9}tBuE;EuuuYYt9}thFuPuh]uE\]YYtetE9xttPtE]Yt|etE9xttPtE\$F;}E?EE@E}t<Wh@Buu9Uw&r9EsEUjY;E
Uwrw}P;owEU5Gjh@BuuvjYME?+u}u}ueE"EUuuRP#uuRPhluXhFmWW@VuW
y[Y`bl3}Th}|WEPjh@BlhjLRP2}}0E9Ew(rE9EsEUjYuEUE3}PPFuhFD@PtPE
Et}ZYP3}}EP9,EuhF(%c9}tQQUuSjuPPuuJ%(TPbTFE_thFTbT(PbTg;uThYYj^
}uEE'}}FvMaMEaM:E];E|%EMDMPaE;Es
ME$TM?(tg9}tEMF^t"hFu}YYEt98tu4^tU4EFE^u*unXuEbXEYFYuDXuE8XYY}uT%XY@cPhFU3@'EjZWP[
U9Uj^us'"EjZWP7UF9UruPPPWYEE;EvE&f~39}}}}E
3MjX;EPp!R38pt@pup:@pp qQ9}}uP;#UM&PWWWWWQM}}#MQjuMQuu;EUjY,{RPjZEMNEE;EiPE}E}
EE;EEEtMPhF}v-uuuv6yjYzRPjZVOuPuUTUYY3

uUTUY (DFk[(eFET[tEE}E}Eu}6 8}Et+} E}EurE
dd}vPP}EPt7v2ujZM;Drw;@vNwjX}Et#;thFjZjVPMUQhFuRPjxF?PP}teEEe3}}}tE}EtE*t7lFu.5F\FuP}TFuLF3j
DF<Fj8Fj0Fj}tj,F$Fj}t;u5FF|P}
FFj
FHj=j2j'}tjj}t}u5FE}|PPEMtEM}e}39}tPjFhtEEu3jXlQMQMuuPuudMEtMEu
tjZ3ELPuuGfdduuj^Vq}u@}EuE}u
}u3dQSuMPuuu|MuVPdPSuud2Fj,EEEEEEt3MQMQMuuPuudEE;EKTFEGV'eFvMXtEXM:ET|"EtDPY;}s
E}$TMatGxtkhFtuYYKt 8tBt43Uu-tOuEOEYFY,8FtelFpFtaOuEUOYY}9}the}E=@}uj^jZ;vvNj_;_I38t@u:j
Pe,FEXTtgffhXFE]EQP 0NY*E}tE=rhUQdQSjuP Puu6P}E t,Fj_;}EE;EXu-P:}t5F-
VP}tSFv|UPuu5FUPuuPF#ULPTX\|`DDPHuu
PE}TLK@TLYF EfTKYMV MCWM6 M#M_^[d
VW|$j'|fxu @2_^Vj|fxu@^jLX^3I^UQQEVuUUM&Fwr= =s
}u =3rw=r}t!}tRPjuojYJoV^\fFlSVWMu _al3PEP,];p}
@j_9lt
W8]}utN}uE9]u@;u }Wj
I ;v;vuEuMu9]E}]VM\#M$`HM \FQpPE;E\9u 9^\EE(jSxNhHHFiFHPnj F(SPnjFXSPn$9]]v}ELPLP2uEuA;E
LM9uE9$LEEEE;ErEE;EMDU;H;] tUeE 9]] NPREFEFE FDP uP;EEE;Er\EJM];tjuVE\/LEM];tjgu%ELM];tjB@M
M];tj]t9]E]E]uuiu3PE(uSplRPF(FV,E EjXF;YtXX8X<XP@FE]EM PAME MtAHu9]uFM$HPMH8HX<@}v
EViP%AF0a;+EE;E}v9]vEEMuu;]]OEPM]]]]G(EG,;EvPU]U]IuM$UjRP;E]E9]]E]?uEi9]uwM(ua}aMuWQM$F(F
V,HPMH8HX<1tP`FM];tj1H(N(@,F,
<;jX}Fvg39]vrEEE9]u ;ujY3UiPD0xPPHAx0;E
G;}rSk;;EE;EE}](9]E}E9]v/EEE0\E0;tE(MuEMu9](tLtP`FM];tj](tP`FM];tjc]
u;lOEP]]]]w,w(SuuhME;UvPU]U]Iuu$MSQP;t"tP`FM];tj5MjQPt;P`FM];tjM
M_^[d
$Vj
|fxu@^^V%uj|
3A^jX^Vj|fxu@^3^D$@@hfFcQQSVueWF0e$ugE@AuEtPFHHMe3d
_^[UVuWF$g>vNe_3^]~fF=cSVW}i3P3@YE;uthAhApWhV8
dM3_^[d
$gFbQVWhAhAjjVuc3}~~hBhAjFjPEchBhAjjPEFtFuc8x8xE YM_^d
tjU}jXwr9EseEuuuucuj3X]VW|$WBGNPHNWO_^Q`F
T$3333333333333333333333333333333333333333333333333333333333333333333333IUQQSVWUj
MZu>^uj3_uuSWvvaRPSWvvaRPSWvvaMRUP_^[UVWhhFW}tEUjY|bRPjZ5h`Fh\F=GthXFhPFujZju_^]j3HHp
H`HdHhHtYpFHxH|3AA
A(A0AA$A,A4A8A@AHAPA<ADALATHgF_HSVW}UMFVOXj4FMGGFG3EfEfEEvMEMJMEQPM^Eu<Y8]
Eu
9^uNUOxNO|OQWu2vuPEPuu=@u`u
ou=bMMMF3M_^[d
MMM]fgF]E S3VEW;UXuj_EUjYA`EUu
E]]]]]P9;YE ;EthAhApWj V8V^3;]uv+3Eu JpupuXp@uh;] v)3EAQ;KEE 9} rM3;vzE;u
B ;rdhEM5G;MEw;v3MP;5GEUu9]wE
0EhP]]n9]]]]]E]u3uVWD_ShRP7_MRPEUVWRP]M8]QtEUtMI33SQPuuUuuSP;tMMM_^[d
UQQSVuW=GF^Ewv,NPFu 5GVUu~_^3[SUW339ovV7C ;_r^g_][V^tjgFZSV3;WuMuEE UHxu
@]]M3]u;t3QMu
E;EEUE;EtuuSh3uuO]WVRP[SuRP[EjU^uu]`d]]}E;k8P7YE;EthAAhAApWj8V8Z3;Eu]uMk8u?u~(MuE(;9]?MQh
FWE9]t#M
EPEu];}UE$MFEVFEFEPNE^ ;tF$3;F
EiP%F0E;tPQuE6GY;}}}0b;]vy3EAQ;u[EE89}rR@u16YlEE;tPQu6Y]MM

M1MY3;hE,;u
B8;rOUMEPj0]ME>uUMEuX;Et"uq5EY;EtPQuE;u$uH5EY;EtPQ@m]URhFPE9]t$M EPuu]
;t EE'u$UMuuuu;E;Et-
;tPQu4EY;EPQ;tPQu4EY;EtPQp0P]]uuSuYRPSWY9]$u}u}E]tQW3VPuaYjYXpRPM$u(u4u0u,RPupnM$P;WVp
M3QM]XM3M_^[d
0UPSMV33WU3\u@r39]]w9]v|uU;t3%uP;u\VPuuVPMQVPVP@w<33;v3L;rE;t;u]E;Erw;}r3_^[jXUVuF0$xXvF(
N$VPF #NPvveF,3^]SUW339ovV7eC8;_r^g_][V^tjVWt$E_^
`(gFTPVWUMFEjHX9M/efefee6MEM@MEQPMsEu1Yu|u$EUMu uuuPFPEvvPuu=@tu=MMMMMF53M_^d
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PRP VG_^sAI@@;vr6VhFFthxFPFu3^VjX^gFlRQQVW};ukSFW}c^3vLj
0Y}et EHOPOb3FM<FC;]r[M_^d
Vt PQ&^ gFQeVuMu1eMBEP>u~/YM^d
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UQeVuR;^,hFxPPSVWM}3M]EGEGEGEGEGEGEGEG EG$EEEEEEEG(P=EN$PO
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VM8Ef$pO?u+pv
f7f2BBGGIuU|6
q xvuf1f7GGAAMuM MEP8;u,EYM_^[d
UQA$UeP
Rp@APuEUQA$UeP RpDAPuEVwNoNgN$_^Vt$N;t$N/t$
$^II%VhFUMQSV3u^^]^^^^^^ ^$^(^,^0^1^8^@^H^P^X^`NhE^<^D^L^T^\^d6XEgM^[d
3HHHHHHHH Vv*v*YY^UESVWEtEU]eSFPQuREtE;veE\FzEukE;vfPhUUURPQuuEt
E;tFE}tu?NhWM3}uEuu_^[hF`K,SVW38_0toEH
taU(PM7EMS]p,]g~tcMFUERPQM0|t'M]t4u(Y3M_^[d
}(t]M]A4F;EuE;t
P(Y9]^tuEP)ul(YhFdJSVW39yUM}MM49~u]MVSSE}uEM9ytQMSSn}}}UWuM}QVPNJM]8;u5EEM;HyMM
3M_^[d
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qItE}uE;E9X8]]]]F];]EMU@4MPE4EEE9Xu48^
u/E9Xuj\M] 6t
j\M]MSjM4UMEt&uME\6E$Et.8^t.>uMjuEP ;8^
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WjPEPMuu#E;l;[8]]]]uMEAE;|79u!+E@EM5EMEMuEE@4uuM'jEWPuUEPuE;MuK$u]@$EEYYM;H
9^]/E;EsM8 EMU@4M~W1MEgM2EE9Xu?9^u%j\M3. u#u!Gt
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8X
u*8Xt%wFuEf8*tf8?uEE;F rE;F]]]ME l9]]EM4M0EME
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MjM]NdM.E\PEPEjtl8]tu9tM#;uuG;uuuMWuuuu?;uWuE
(
EYMcMW.yu;u$uY`Y\_3$u`YY_M_^[d
yuy
uytCtjX3liF}ASVMUWj\MEePuVuU~(EPM-uMEVuPSeu;~(YuuY_M^[d
iF@SMVWUMueM-jM[],EMPEPEb}VM+`uMGEMPE`EMELEP0MESPjAu8EuMESPSpAu
unuEj
pY(PM-EMPE/EMpEtpePj]uu4uuYYEMu]YM+UhFdq+MPE0dKuCu;M-]u$uYYM]3M_^[d

iF>0SVWUMF(PE+EueME)FEMPESEMP
FF M$EPEPj?EMt!
eM
ucY3
F Me$PEPj?}t]tMuUM)MEQPEuYteMQ
uYV_(uuuGF tEE1tIuUM)WMuUEQPuuEEup}Ytu|G$uVSuw9C0t3pF t]39suMEP;|
MI4}utSMDuuEPMuEUWPuuuPeM- uYM_^[d
VW39~ v%Fxuxu9uG;~ r_^2iFj<SVQMi)3VM],Mj\[Mff=\tf=/usuuuYM^[d
VjF<SW039_(]V3G$M49^0vMt|]F0N,URPQM]l@}6EMP^nMEqu
ujZM'u`EPFUE6U`E$YtuuYYEMP(uEEPEE9Es+EMAM5t(uuuuIEM+EPEP]MEEfDAf=\t
f=/tQMsEt>E9Et69Ev1PlPMU-5l+Yt0uuuuuuxMPEMEHxPJ)ee}xMEPitEN4P6uxu{usukucu[MM"EE;G(g^M_[d
jF+9SVWMl3$]EE$PuE{}\j;YEt"
]$MME39LvFH@<u*EVP$yPMEEueYF;Lre$Y9]uSM5|FEhh%GP;Ml39]Ev=MR$EUEQEMPEuFY;ur]]]UMES
Mu/MV8}u/V#}vJEME4qE4dvLE2t:C;]ruAeYMMM3M_^[d
M0hFEhh%GP9jF6QVuEYNh4N$EvvMYY M^d
VW39~vQ G;~r_^jFm6(SVWV}V(e~v1FMXu8u
EE;FrFvxeENUM;MsHFM<KWt%GKP}?(K(Wq?NuEE;FrEEMFI;r39~vNFUMCPY!ej\MEuLuM@GY;~YrM_^[
d
kF&5DVW~tf2u[MVM8!eVM
MEtkuuMWtEP"uuYYM_^d
SUW39ov5V4~u"~u{uu7E;or^_][kFR4QVu ue8$}t|#u#M^d
*kF4QVjYM3;Etu NMF^d
S\$VW4td3VYS_^[VWwtSNt3xSrYYu[7dY_^D$SVW;tpVhtVS74_^[VF;Ft5W3tPFP6W46FY>F_^>kF2QVjPY
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S\$VW&f{tWW3PI3~_^[QSUVW3{T$t(4/D$e,t}n;u_^][YkFR1S],V3s0s(s sss3W\Ms4s,s$ssssuuuuuEux$t E
E@E39udxRMEEdhx$u+Ed0REhMddhEuMeJGY;}l9uut9EwEP
YE9uEvM}3h
YE,;Et5N,EhhGPEF2EhhGPEF233E;XtVPM}u$EAIf\NTxph`XU/&FX|tld\}}/t,uE
uPQ;Et;EtVPue39}}}EM<EMdEsPMEEx$tdh}&Ed0P%EM EMI'QM
PR$;Eyz\MEEEuEH(@$MMMuP;Eu94|E}@EuM PPuR(;E9}toEEx$
hOEhE 0OtuhlEMYEx$9EM}vKMM,;|;EvMM)MMDEEE;rMEt2EM;
|;s33U QPEu MR;E-DMy$u}ujY39}$|M`EPu(dVu h`uuQMP;EEx$u dhMUUMMUTH P$TpH(tH,E(9xT YEM
EEu, YE@;EE2}/u
}uE uPQ;E;EtVPuu;YEtVPuYEM
Eru;YEtVP@NiYEM E*uE;YEtVPu#YEMt Eu;YEtVPuYEM. Eu;YEtVPuYEME[uv;YEtVP@:EhhGPEF4,E
URPQ;Et*;EtVPuuuYYX;C \C$`C(dC,hC0lC4pCtCxC|CEE@{TH K@$CtVPuuxY3YM_^[d
(SV3N"NH^(^,^0^4^8^<^Dy^[3HHHHHH H
HHH
HHH@@HHHHH@@@@@@@
kF'QV3uFFEFFFFN$EFF
F!N0EM^d
lFr'QSV3uN^N^^^^^N$]vM^[d
AlF 'QVuEfrN0E-v$eYNPMJM^d
Vv$vYY^S\$Wt9j |Yt``F3VPj^X_[Vt$NFut
VNY3^`lFD&QMAetPQMd
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U;t1EfLPf\tf/uJUfP9]tEMPEuEYM"8^$E3EURPQ;t@D39]E
8]u/8]t*8^puDE8t,pQ)M@PW\.;8^u8pub8]t=UXt(EXfAf;u
8suf=\t f=/t]8]uM@.Ot WM_sG;};9]u(uP,SP0@EFt@EoF8]tEMP9]uthFMuE}
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;iEWQuuPRE3;t
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$Vt^j&^qHYlF!QVW}wetSNtS[Yu[7NY_M^d
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VW~r FLxt3v,fPPf:tf\t
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j_MEPOu1YM_d
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f.ufxu af V39qvPpfpf=:t6f=*t0f=?t*f= r$f=<tf=>tf=|tf="tf=/tf=\uf_F;qr=Gt#AtftBf.tf u
HfB_u^PmFS3VuWU9YuYVj[;Hyj[;@h@FPFYYuwj[9vo@wUt_j
hUtTj[|v=MP eEPS%MuYjX9Gv
Gf`teF;sL<Tu)}u}uFH;t
S%j_ eC;^r}u>Fu$j_MPE%uzYLyuj_ M_^[d
dmFPeMVWMve39~v!t
j\M:M4G;~rMEP uEYM_^d
mFlSVWMuM,#39]]u$5PFMtPMEAuY]]]ME`O39]EMEME4Q;9]u5PFM-hDFufYYEupUMguMuM;Mt
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uEY;EPQEhhGPETFEE;tPQuEY;EtPQMEu_MYMduCEY;EtPQ@MEuMYMvSV3f@fD9^v'WF<PQwjW{C;^r_^
[S\$VWj^@_~D9~vFSt$PQG;~r_^[D$A@D$ADUX}VWt
GW0}tG
G@ODW$G0O4GG@ODWG(O,3jEVP}tE9wuSGM}4u^SPQ}u
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]u3{
}tl33 PED$eP;tCMShQMPEeEu"9}Yt}@t
MhFuQP;uhdFHd|FSdE;t-{
EE}}}}}}|}txt
WjPRQP;uE;Eu
dQ}}P ;u9}X}ME)E2]E8X
]t'j;YtxhF3PM,XjX}YE;EtDgE}uMEWMQhX<@LEXP]oPME"EX-Yu{uEhPEE
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Pu;EY;EtPQEE;dQP0MehMhtM_^[d
OuGYku<EY;EtPQ]^uEY;EtPQuvuEY;EtPQuTuEY;EtPQMFeh2Mh5L$IAu
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Vv~6YtVP^VWL$iWR0DRPF7D$D$FG_^G@s

G@GVW39~vFD$uG;~r_^nF
0SV3W]]]]]]]MUEMU39]MMEEME4F;us2EM43h<FuFYYEt E]fEPuEu
FY;uYxM]^MMRM_^[d
nFSVWUM~3;]tMv9fXf=
u}t"MEP)Eef PMC;r}tMEPuQY_M^[d
nF>`Me=G&SVWEGEM0veMFMEF EEMEFEt PMFt
PMEMPEPuueFYE~YEFEEtVNEP)~^MqW@tWS6MEPhMMEEE;G_^[M3d
SV3N^^^^^ ^$^(^)^,^0^4^8^[UQSVWMU}v57GM;uw2MV?tVW3)uu_^[2nFL
QVuN0EeNvY^Md
USVW}j.;SMDXP[39}t/MFu9~vFEP} G;~ruY_^[VW39~vD$FHuG;~r_^nFR
ESVW}3pM9wf.p|0}_+PEVPrHe}tcMEPa}h<FuFYYu1MVXuMs;wYrM_^[d
E`uY2oFQSVjJYuetW}WmNWE]_3MVWM^[d
,oF#QVW}ewtNtj|=7Y_M^d
VWwtSNt3SYYu[g_^BoFQVj<sYM3;EtuKMP4WM^d
VWwtSNtSEYu[g_^boF5QVW}uNGFGFGPeGPN:G$0F$GF(GF)GWN0EF,M_^d
oFSV3Wu^^E]x;}t?;wPSY~;]vVj?Y]Et(EM<W[KWEK}3FeFE9}rM_^[d
oFQQSV3WuFFMEy;t?;wPY~3v:jYEEtM 43NeFC;rM_^[d
UQ3EUt]ISVuWMEy9~w,r9sU%9~rw9wYFI~;wr;rBUUE;u_^[oFpS]VW}FN;wLr;sFMFeNE+M^EEPNU~^MM5
;r1w;v+~t%FNDxlt+PHP^~M_^[d
oF?QSVuNk3N]^N0ERN<EFM^H^I^J^K^L^M^l^[d
Vv<v0vv^Vt$Wv6V0|+VPO^NFWF;wrW;sOG_^D$3D$rW%FLLf|FfD$
3@D$f 3oF QeSVuWeVPuN _G3;BM_^d
[Vt$N]F t
PQf 3^DL@3pF@SVW3e]f]f]]EEHE4MtRv9t9^@<8^H(MV|HvvUHEMP9^Etj.MFMP9^
tj.MMVkEMPGuYf9^4ta0VMFS<B[t.Itt*u48^It/vTvPv68^Ktvdv`8^Jtv\vXMwGuMzHM H3M_^d
[PpF<S3}VW]E8]et E@EE9X t 9]333;Ms&}tE}x@A}VRWPj8]]NYM;Et 3e;uutVPSWEJj(
;YtXXDF3;}}tWPEOEp wDj ;YtXX4F3;EEtPQj(EYM;Et3E]tPQe}EE;E{EMF F(N$N,UtIEtSPEtWP}Et EPQe}t
EPQde3E9E}EtE EMMu}IWP4MQRt_EEtPQEtSP}Et EPQ}Et EPQe}t EPQ~^}]}u}u E;EuPQEteEEtPQ}Et EPQ}Et
EPQ}Et EPQe}t EPQE%EuHJBEt
PQeE3QQ`xX@EvM@ 6PR3E;t]EE;tPQEE;tPQEE;tPQEE;tPQEe;tPQEwEu`
x}X]VVSuppWP;EtYEE;tPQ;EtWP;EtSP9uEt EPQe9ut EPQEMAAtEEuPQE;EEtO;tPQ;EtWP;EtSP9uEt EPQe9ut
EPQEl;tPQEu}]yEtSPEtWP}Et EPQe}t EPQ3BBM_^d
[Vt$NFut
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Vt$NFut
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Qu
uM'ef]f]]URjuEPQ;
f}u9],uMf9]E]fM]7@GMU]RURuPQ;}tLM;|>EMDpP}MEPSEiu];uYsEufpuEPS=8]t+Fj:LMFtM\WE9Et}]uEt,Eu
^Y2MJ?uIY@M.?u-Y@M_^[d
pF
SV3W}Fu;to8ugURURURjuPQuL}QuFuN;wvV;v uMf Ef/uj\Yf@@Muf;wuzuf]f]]URju]PQ;Et
MQ>EUf}u9]t<uf9]u*_fMM>9_u
Wu$3
M=@M_^[d
pFSVW}U3_fEPn;u^8]uWPf]f]]6MQju]VP;t@f}u.j.uMY=3M_^[d

f9]t@M3=UQVuuu28t(EUEPu8EtMhTFP3^V3fftf:u}
f\uFAA^pF4SVu3WMF$^0E~2XNfF^Mf^fN4EUW;L}F3VuE;,ENVEM
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;|IE;xs xfxMDxPe;tf|xf\t
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MS93M_^[d
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w;vF3M_^[d
pqFSVu3Wf]f]]}WRQ]P ;tKE+tCt/tHt
Ht@,EMMHEMXEMQM/4M_^[d
qF SVu3Wf]f]]}WRQ]P ;tKE+tCt/tHt
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uVPRQ^3D$
3rFSV3Wu;tPQF;t PQ^F;t PQ^NNVpdX<]j.E;|DA<Ptx|uE]@];tvPYEE39YvMM@ ;Aru8^tF
F$~F;]]8|*WtE9EXE<]]]f]f=ztf=Ztf=rtf=Ru8fQf;t,f0r&f9w
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Et
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L8:uAArhx su/@
t)ptM45t;}|E39xv tt
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@Et]M8Yyt8]uMU 8]e9]\XOMP NN@'868]-M! ;|EM8]tEx}!:tE8]Et

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;v8_u38t/;|;w 8]98M EE;tPQEE;TM}9P8;~8Yu8YAA]EE;tPQEE;tPQuut<@EE;tPQp1EE;tPQj[u#EE;tPQu)u!t<fM
I4<P|JtA8]t<uut<jXj(cYP;E t `3;EEEtPQj
E
*;Yt*FF@FXXF@F3;utVPEENp8}"F`\X^]]9dE]wr9`s
`EVM8SSSSPQ;t2uEY;E
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WM+EE;x9]|uMSB9]]E}EMuITF8;t`RSPQ;pESSSSPQ;p]EEP7Vu;M;u E
$uE}p88_PE0\ EuEM9]SSSum;p9|9pVEptx|8u;|;sxpt|8_EM8]tG:tG8]G

uG
E8^r8_t7QPu&;/8^6E98E}Tt]8]EMptu%8u|;Hrw8x;s.TMpP;MpPkp8EE
;tPQEE;E,}7uuEY;YE
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43;wnr;sh+Pu>w6E<+V+MQPF}E3+u}}E]}E+Pu8s0E}<+WQPFu}]
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Q,xw 9UmI0H;P;]vp+::2EG9Mr}]E8]@tEEtA,3;Eu;UuE8}U8]hlu+A,;w;}+EhlI;MtCl;Er8w;}r1+E;Es'UN<+
RPU;iu 8][EM8]tE:tE8]Et

H]EMPEuu;S
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;Dt;lw~rp;hsnE8XufE9Xt^xGDH0;trEw;pr;P@xp|t;wr;w$E9Xu8]5M8Y)8^u]F:EtG8YtBQPux;8^t]ptt8u38]Ht@&
@!8]uH8|E98}
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tPQEE;tPQupEE;tPQEE;tPQEE;tPQuu~uv k,`E;E
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F sF:$SVWEPU}u3P]NMuEYN<P^HFMPjTZk;&Ef]f]] $FLMQjWEP ;EtMEf}@uEFHF(EF,Mo8^HuTf]f]]MQj
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sF <SVWuE9F^t PQ#F~t
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u~uvun<EPS;}3NUHQPMdPEu.Yu%uYY3M_^[d
sFS3VW]]]}E8_Dt-j;YtXhF3PM9EG49_0jX{YE;Et B
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VWwtNtg
3_^VFt
PQf6VP^VN0F$f(f ~t z^EtFvSVWM]C(@ r
@e\{C(jY8r_<ES(pJB;s+j<tY
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My vU3J;ui|+sHV}sVgS|f;M|P
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^Md
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N _^][SUVW(Fj_FojZMj#E-PZj#J-Z#K-P}t$ SFtSFtTF f
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u&vF-|t4FWu%u^v@FjZ|J4DF-Pu9u4F3F;uB|t5Fr]}hFz~u~uu.wMWDhFIj_7M0G|vMj-y6MOu_^[VW
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%
SVW339~,vF(t6tG;~,r_^[SVW39~,v$F(tt$Hu2G;~,r_^[wF<ESVW3;t89}MuF,V;}M}MEuFM@EP}sj0MF@MPW
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)ME9]r]FSuPQucEF ^$E;G,s4_(CtFt
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j.3;|sGH;uNFfBG$O(fTMDXP{UO$e$/rtEP|;^s^f$X
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}tf<uf<1f\tf/uf|FuSQMvMePxuhYQQd$SUVf1Wf:ftaL$)T$ftRf;t7=XGumfmf;tf\tf/u"f\tf/uDDCCf4f;fuD$_^][Y
YyFS]VW3eUMt u#Bj^;9E}t9s<t
@t&9s<C8jYl8ArMMRM'9v
K&u}|uhXFu
xK(Pk9uEh<FHo@ef\k;}XT-VMEtTEfYMOtEP}vEuEf}Yu8"u-MVhFCZfOfYYM9f.fYYR`} j_}t/uuutuuu
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<thhGPFAt
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@~xgv)]EPK\@K354F!m5M;E t#j cYtjsY3PM3uuuE
x]C(LHEE0E4EPPE8}s}uPH;tmuPu9|j^@;tVuQuP<uu!vRE@054FHlP
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bu%bMYY\@PauaYEYMGc}DEaY}39s}:5<FMpMPE#zCEua9sY}
@E#u8Et
@x6OE
#03Eu3$PEp@ POEA3e}e{<S8d39tFrjY8uArEEE;C<r}P@tX

MxPP7t!@
EhFHhQPDuu~NDmlE0nj.lESl<M0
4$8(<,@0D4lH8LtExPlPD4Ex0El^DE^YYxleeMElEt5t,58FM^ExPnxEMeEuf{Dt`C8pCHPT$}#u t>}u8u/F<E~@t
PVm39s<v_C8}xtxOPlmu2w}u(EPMEuyuEL]YF;s<rM}#ETMEPV!ME`$MEMEPKEbuhxFNPeENP`U6u@~u\Y
ME,MEJEMum\xb\Y 3}}Et!MV3;vM$@;r9{<}.}#tDEeC8}M4-
jEthpFMlE#E@ENPPMEkEu[Yu{Dt}#tEeMuuPL}!E!EMUQWuMQxu#EQPEPFPduVuh
MQPPuEZEE;C<Y}#}uZYEM"ME$HMEIE%uZuZYEYM!ME MEHEuKZuCZYEYM!ME
MEHE!uZuZYEYMY!ME"MEIHE#X*Ms}C8MQE&}#E't]M7uQEPhXFWuqYuiYYEYM
ME(MEGE)UMuvEPh@FEOPQu@~
%
uXuXYEYM9 ME*iME)GE+8uXYEe h(FM E-funhF4u@jjF}~
%
M u?XYEMME.MEFE/hFu FEuehFu@~
%
M
uWYEMME0KMEFE1Mo3E29s<vhC8TQME3eTUE4J;EMPOu3WTE2$WFY;s<Yrh3E69uDj_ET4fTME7$uT`E
8UlE9jjPE:H|` $l(j0$jP|MLP\}E;t4j`jP{El`dDH3$PjQPPUuUlU`UuUTE6UF;uE2:ME-DM
E[`_ME<S3E=9w=FSx4EP=)F<E>uM]`uuTPhFuTTTYE-YMACM uTYEMME5LMECE<uFvuE?tW;uM
;FuBFP4Pu1$VPu
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SYEYM^MEME
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0$xE?=u*RMYL3M_^d
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zFrsVue~t6M_MERtM)t+u#QY6QY^Md

A;Bu RQFYYujX,zFrQMevMM6?Md

{FrdSV3WUu]E~;]tv;0;utVPMQhhFVE;t0FhGEEP2u;]tVPuE;uC0FhGEEPuMEPW;EtEM;tPQEURPQ;EtEM;tPQ
E}E8@<t8t
]]]METE";E]9XDEM]4Mu]8^
EE]9~EMEPFWPMuX8^t@z;E|2PEPMEPEPSE[u&uENYEEM;r3E-
j:MEBMDHMP_EubNY8]|}~}}}]v$}|~ut2EU}PjHu;ufEMPFEj|YM8EMjE<YuEMEEYM;HJuMYEM;upMY}]]]u
M EPEu
0E0UE4EEM#MPEPuEF6EuMYE;ZMjE^yG:8_G"M
I$H<t&8t8XLt78t@8t@xO;|q:uU8Yt8tp0X4H0XA8Yt8tp@XD0pXIP H$8tp8X<pXIPHMM0RP;
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u JEY];]]$8t0jI;YtXXXF3PMENj IYE;E
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3VME
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u_GYj.N@t;E(^^^ FL^^^^$uMW(b;Et^EE ;tPQEE;tPQ;EtVPMES5uFEM;YtPQE8"jXFYE(;EtS3;u(E
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^^F$^^uMeuEVuuPQ;EtJEE
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9^D},h`FO_IFLOPajEE
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089]]]]3E8XtH;|E @$<8Xt0@;|)M <;tEWPF}EGEEE;Er9]u9]t,u;tENPF}$}$;tEPo}$u9]0tF;]]t
EPe!E;tH@MEG O$EME4MMH;t 5;t
EEE
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`H$FSVWU3~H^0^8^@^4^<^D7_0_8_@_4_<_D_0_8_@_4_<_D_^[3HHHH
H(HHHHH$H,a{F^QV3uFFEF(F,F0F4F8F<N@EJMF^d
UVujhGu&_tjhxFu_uE@3@^]Vt$NFut
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KEMEEhEMEEPEPVj;MEPEPVi;f]f]]MQj(VWEP;umf}uE;E|`[MEMEPu]uy:FY;uMhr3M_^[d
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VWwt
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GFG(F(_^{FXQVW}uGFtPQeGPNEGMFGFG F GF_^d
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GxhBZYhG`Fc|FWQSV3uFFFFFFFFtFFdFFFFFF
\F^$]^,^0^4N8^@^D^H^L^P^T^X^\^`NdECNpEtC^|E0FFFFFFFFFFFFFFFF FqMF(^[d
USVuW}j&[ShGWWu
EShFWWtShxFWWu
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EPrShFWkWu
EPUSh(FWNWu
EP8Sh8FW1Wu
EPShFWWuEP#@$3@_^[]D$@$@$VtNF$ut
V3Y3^|FUQVuEtPQvp2vd2vX2NLE+!v@2v,2YY^Md
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38XU;t
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jMuMM3D$ D$3D$L$ t3UUESVuW}38t4}>u.8u&Ht;t(IH;|@p@h
@3_^[]@x;t@;tWVRj>PQUESVW}3}>Et
}YE8M8\t;qt3P|R;t);t
@$u
uWuPRQ]8YuVPtI}>r$u8t<IH;|5Bh8t%9Y8A4u9Y0A,tM@E3_^[]@}FQdESVuW3e]9f]f]]<8UE~6;F8%O;a@$M
MC0[,eMQPSMEEMP?}EuEMPhwEP4MEp<MUEjt%EUPMPMEu-Yu-u-YYuM:u-
YEMS9M3>ME89UE3XZuS;F8uEF<V, @@tEPPMEuU-Y9_t;t6t6?t,tjM(SSM^xuG;|L 4MkTu;|%;u!;tP2u9_|
ut@t?u6Gt.;t$;t6@$u1RPQV
F$tKt8ItIIp<MIH<t33@PQ@<$PEWPPMEQujBAtIt
3u@LPMuM3MqM_^d
[{}FbM4S3VWUM]]]]j]_]}]MUE.HMU#H39}v6}E;Es'EM GuFEE;Er;u,EuEtuM9}wM8FEE;EshFM<EE;uus+9ut
j\MEM4M&<EE;ErMEPZ9}u9*YMEM]ME_^[d

Gh|BOYhG`F}FKtESVuW38Ee }<u
@NxP;SN{EM7CE;| MP69.[;|'N|ESP`PME9uE!)YuvxjuuP;tQ}uIj
(;YtxxF3;utVP~WNE~~~5E03u(YtsN|EPuNxEjuPRuep(Yty}`t@}t
}Gj(Yt`hF3tVPE0jX'YMEt 3e}tWPNGHFE#GLEGPsN|EPuN<E]ENHP7 F|K@$39H0u9H8tSh
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EEEEF,EEPM;N,u MN4F8u>tGVhFMCV lFEu8%Y3yBM_^d
[T$B|I$@<R|@2}FGQEeSVH|WeuP$BM_^d
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}FuOxVuP,u#YBM_^d
[~FyE4eSVW3Me}1}Eu
@}uW]tiut
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[T$J\ujX"D$;rARXL$V41^DA34~FRD,ESVW3UHe]EMP0E}sj0M5uMFdP1j.MEEMP3pMV3j
!;YtXH$F3;utVPES^PNE^cr:uF;EtVPE0uj!ub!YYBM_^d
[@~F=CQESVWHheeuuP0WBM_^d
[L~FBQEeSVHdWeuP4BM_^d
[`~FBQS]V!Wu7GW}hFN$3;EvV
;]t5@;r]WlF~M_^[d
N,j!N\PR tWlF1lFt~FBQQSVGWVuhFW$eevG O ;]t E9UrEhGPEET3Du#uO,JVlFM_^[d
T$VWJi?~WQPFN_^UeSYVuWM]teEM @<Q9Vr8w9>r2]9Vw1r9>w+QI~MN;u;Mt1;Mwr9Vs]@E9]jYSM
^[SVWAD$S<@R+D@R9RPF;t$jYC^[VF9FuMSW\
;w@P%FYt@P6WA6)Y>^_[^llllll8lrlXlzl}l$:l$\l$_l$l$>l$All
l$#l$ll~F6?lSV3WUMuuuuuuuZA(;EE]ut?;wPYE;Ev }3SMM;]t3;v
M@;ruMhBCjX;vOuj[3E+EM
u
NEE;Er]3uuuMEN6Mu}v-MEVP6PME EuFY;urUMvjX9EvEE3MUx448u
FSEM;Mr]3MUUU;Ur 9]3;U}}}usE<E@$EE;sIM;UE4sQEV8uMFI<:tCH}UEBE}U~F]E38F EEMt"MVM
FPE~ Uu
FFP~}]tF(uENQMQ'3t7t)~tE;u@;Fu EEEEEEMEtxLu
tx~trEtD@9Av<f<B:u3PEPM 6uYtEE]{u;UC]]MCEMxMEuWuOuGMd
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VWVO35uG:Ft $@3_^D$V1W:u+;A#_^VW~;~t\S3tDPYtW|HBI IJu3NQ6P<;6FYF[_^VF9FuoSW\;wP3Yt W|HBI
IJu3FtP6W:6Y>^_[^U<SVW3U9{}}3MuFMEFeEEE+It(ItIuNE9~E?;,uv;|9|}QeeEjuY}uFju<EY@3v;|M;tVEEE;C
MR_^[tFhGEEP:tFhGEEP:2HHHHHHHVW~;~tqS3;tYPi;Yt/W;|$HBIII
YYYYJu3FP6Q86<FYF[_^qt$FYYqj
FYYG%@UQQEjRQ3MEFEUQVjhCF\FuEhhGPEF9^|$ujXG=GVD$tVoY^jhCF\Ft
hF(1t@D$VWPpt$pQ
q$P Px$qx4~#gr_^~F5V~toUSWME~e75FFYYSuMYuN~d]NF_[E^~
%
Md
Vt
jNnpN^tU VWuMu4EPW_^UQS]WSuFEeVj
^KEiEBrw }rjG^w }rjM^hFE3j]X7EU6RP+j)^_[D$V#D$thF=tt,^VWNt(u FtFQPhFFNu 8FtFQPvvkFFt&vFvev$v
=_^SVW[F0F4t+lF0N42u `FtPFQPF8F<t=t 5hF/F8N<2u DFt8FQPF@FDt+t hFvD Fv@_^[~F2V~t7M
UeM}v6uFYYcujYM^d

Gh/C]5YhG`F~F+2QVGWVuhFEetMH MH$H_nVlFM_^d
F1QVGWVuhFEetMtPIHHmVlFM_^d
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7=FhFYUYuu0hlFYUYu u_wm$3C@dE$'EEE$ E$tcht@r@SlFM^[d

C
C
C

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CCVt
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uNihGlFM_^[d
e|t9u
jeh\F-QSVGWS]hF3};ueu``xXXxt,75FFYYuySlFM_^[d
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0h FYYPK

uYG WlFM_^[d
U}t%VuuuNFuPQ ^W]t$
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[USV3;W` X!d9;t?8]FuFEF 7uFYYu
9_t
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YM_^[d
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F&SV3W9t)jNb9t^(^,F0^4
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N"Y_^USVWuNU$}$3EC 9GM4}tEvT0NP+PYY}tPEE;Guu2tj}utDuMuuEPXYj
Y+~+u+v" j
MZ;Vu3t]V03FYY_^[VvSW
_[$^SV39t_t$DSPtt$D83t29u05FFYYPF9tFN^W^[UVW39~vRSFjD~u~uFFtj%F
F$tjG;~r[_^]FSV5FW7CPYYMdCejMZ+7uuM7E4FE((sTH 7(PYYxM_^[d
U
VWuME:uE =F6u6EP"_^WOtjWT1SVt$^SjjSv,v(1~u~uFFt,FNtQPhlF vvhdFv,v(hFF N$t
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G;~r39~vj-
@G;~rMu_^[0F<VWMeMEMUEEEPEPMFMV;}tVMCMM3E9uvEfpf=sPMF;ur;uuEMP3uEYEP"MMOM_^d
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PFYYSVWt$3u;Yv'+3+tHtHu3+5PGt$FYY_^[5PGq<FYYtFh|eeSVWF^]M<uEW(<9G(E't5PGGPFYY
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N PG0O4F0_N4^VW|$t
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UAE75FP7hF_^Vtt]^F VW=F6R6hF}tUF4uM^eEPuuYM_^d
FVRMMef
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Vf0@@ftf
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@^f2BB^+QfPFSVWMU3VSMui9utqMMEUEPEPE9u5Ft7uYY
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~tjFvFP~(tj`Fv$_^SVt$W~!t 7hF6~<|)F$PCF<Kj7hhFFYYkdV0-3_^[F^SMM
VWU}8EE<3_TX8](]ujh0FT&4ulp]F;tvPYE39^EvM@;FrE]]]@];]]]]PGV EM80 EM8]]]4uo%6E&F;uj^9]~E%
EPG5GEE65,FFYYuEYu
G-5GWtsPG[5G]65,FFYYu
G5G6hFFYY#E_YEEMEMYtE
Gu00E,HE,PG E|(E4VEEEEE.8]$t8PG(5PG5FFYYutPP:X;Eu90E@9EmPG5G65F5,FYYu
G(G0h F}YYuU
GP`E5FEG0hFUYYuG0hdFY'0
GEY
G_HEYY|w;v;v\M<jEZ9X8tY9Xu
8Xu
8XtY9X|Y9Xu8Xt_9X,t_MujZU]U]8]uNE]MU3;v*M|;|;EvMF;r8]$tiUPPG;PG8](t5PGhFF8](YYu$TuTtE(`d:uMt
DTiE]uT;F;tPTj;DT9EURPQ;\ME29]E]u9GhPun8]Et=W9]8t#UMEP-;E8]8];t(UME;P
;E8];t 8]TUMEP;E28]uh8];t-Pu8;TM
\)0Uh8]EM
PPEPR8]TUMTPj;UMdPj;UMtP.;8]t8];TuTP"8];t8]tTTPuu;_EE;ELP8]u_84uW8luO9tMULu33EM,40Pu$u\8]$ty8

](u'TPGTP90|E5PGhFFYPGYFSE0S@P
PPGkMEEcYEtsE!EEM;HPG8]$8](Exw9]w}vmTxTPPGFuu FuuFuu?u
9_u9]t]uMYTaM_^[d
8PHEAYY|4MEE' HEYY|4@MEBEHEYY|J4uXMEEsHEdYY|4E%u@MYT!EMEE
HEYY|3uME=EHEYY|E3uVMEEMEEoQYEtaEu*MYTFQVhuNFC3EMF^d
FQVuwEef4N8F0NDENPEN\ENhE1EMO^d
FQVW3uNN>~N~~bN$}(M_^d
3HHHHHH(H H$H0H4VW|$WG8N8PGpOtFp_Nt^FSVu3Wf]^^f]]}W]uRQP;t
MFM(=M_^[d
,FSVu3Wf]^^f]]}WjRQ]P;t#f}@uEFEFf9]t@M<M_^[d
VW=F6R6h,Ftt^_^aFeQV3uFFNEN EN,EmN<EM^d
Vv<v,v v^VYNr/^vFQVj0YM3;EtuMPLDM^d
F}QVWj0CYuetNNE3MVCM_^d
VWwtSNtSYu[7Y_^VWwtSNtgSYu[g_^FQVW}uNGFGPeGPNWG MF GFG(F(G,F,_^d
F\SVWPF3]]]]TFMPjU_MEe]u9]YvSM9]u
Gjx^ExPEPE{HGUGMG;t+#GPG;tH#G9]t9MtUt>
GTxEre#MMp3t

G2
xP9}t;tu9]EPtEPjFPXFtEEjbYE;Eta3;}EtWPGEGEG;u4}utt@FhGEEPx|]xPEvIuDFhGEEPlpt39EvZ]]]MQEP}El0
4$Eu;FY;r]]5G;uPG6h<FFYYh,FY;=FE]#E6M@hLFF{(6H#
Pe}vAMjXt,FDFhGEEPS 6PEYEY;Er6j YSYj)6j YYMre{E v]}tMcCMMPEExt&hFMEMPMj)EE;Crj
U6j {,YYt!6h$FFYYs,0j YYe{4}t6hFFYYC0MeExvl}t6j EYYM< v<r3E
s076PEYY
s076PEEYYM;HrEE;C4ZEuEEYM;H36hFFYY9G]EGE(Cu
6j YYP{6H% P;6H$ P6j ss6j 6[SFEEE;Gd6hFFYYe=G
EhGEWSj6j YYss86j 6[SFNEEE;GruTG;uPG0 EtG;t0hFFYYE]M}dW}
t9uhF`GuGEt9tEeE41P H;EEt
EHE`t9u ppdP 5GG,EEME<OHjP`PEP\PPPxE&`9Et
jPq%G;u
GjQMPHEPP#EMEXuYEHE4Yhc$
@9]Et
EH=G5G$EM4q@tPEPEPE
ETPESG;u
G@QMPPPH#E`E"TuYEY@3
<FjZS3}}}}}}FEt#MPMP_hFkFGE#
GM4MI;<tt6hFYY<@DHLPTX\`dhM3Q<QMQMQPE
EEu 94Yt
jL"9}ut<P4E!9}}hxYE;Et3;EutVPPEXu5G5G
}9}pElt
EH8y,8PE
\^]_ P`{M|F9E}tjEPORURuNV#8RQPEMPExPlP9Et
jN 9}tGG;t2EE0hFYYMMu~9}uE@=Getb]w vI7hFYYp0'7hFYY,(04t-
Et%7hxFYY40g8<t1E7h`FYY<8(t}@@Ht$7hDFY@Yp0OPTt::PTt%7h8FYYTPHLtBEt@HLt+7h(FYYLHW}tE
8<lXXHW} $t+7hFYY$ <
(u,u$u04t}7hFYY,(04tJ7hFYY40V7hFYY17hFYYMiE0hFYYM@}tR3<XXHt$7hFYXYp0EjuY`EJDYEVPMQ
MQ QQQ}}}}MPERxJPEPlPEPGt49}w9}v*5PGh8FYYuu9}w9}v:t*5PGhFYYuuLEME
MEt=Gtz

MW
lrYExEEtPQxEeMMEM_^[d
3HHHHHHHFVt$NFut
VY3^FQMHFeMd
V3F$F%F&F'F(F,F0^FQSV3uN^^^^^N]M^[d
q4YV&Nfff^FQQSVW3u~^^^^}O]jN(_}N8E^H^L^PNXEMEEHyNpEF|XXEE XXXXdXXXX
XXERMXX_^[d
FQSVu3]^8^<^@NHEFD?EEEEvEaMXXXXXXXX^[d
V&N{f(f,f0^(FQVueNMM^d
tF\QVuE<Y6%NHeN8MN(PM^d
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FQSVhu03]"EMXXXXXXXXXX^[d
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VvY;^MFQVuNFe$EM1023F^d
V$FLAYY#^UQSV5GUt'W6=FhF6S_EhXgGEEP^[SVWt"6=F50FYYt654FYY_^[UVUyUu^V3W8t@<0uL$;s+S
7j FYKYu[7VFYY_^V| j6hLFFYY6j FYY^VrW2D$;s+7j
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UQtEhgGPMUxEPEPEPEPFPpFSVWM3Ej(SPn]]]hF]F5FEhTFP;uh@FF;t
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G)_^[U@SVWuM}uE38Et@|u;s+ONuG0WFYY_^[UQQSV5GW6W=F6hpFjhuuZj
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VuMuUj^FQQVWuE
YEDEkYEN|,vp~XY}OEE&NHEv8xv(pYYuNEMM^d
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\FcQVW}ewtNtj7/Y_M^d
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FQVW}ewtNtj7OY_M^d
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FGuC
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Fs
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Fu
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II3AAAQAA(I$fUQQSV^H]t_W~PC;GvP_E
Pv UE
bB+VT0vPFYY_}tFDfH^[U$SVW33~^E%#uVjYEMtjjdv6)SWRPRMPEPsY@MjLd_;s^8+j
DOuEN8P_^[U0SVW39~0}t 0FEe9~HtpF0;tM+N4;V|tIV$;t7Fx;Fu/F|;Fu'Fp;FuFt;FuFh;uFl;FEF8^8{ }t SFVtRMPj
@EP9~tj 'FP9~(F<;j F$~\P2WcVPFTF<;F(EtoEujX)EF$PN`E+MQPEIhDFuWVPdFTF<;v}u}u
FPfT
FPP
3NDXt;W03FYYt8SND3VNh.9~0tEF4_^[A;Bu RQXYYujXVW|$NGFGFGFGFGFGP.$N$W_^
GN\h}CYhG`FUVW39NtjE}9xSEM<}_t u 9_t#)}tEpp%ttSFt7t-3GEhFFYYut`u 9G(t#v}tEpprXttSF=(t7t-
3G$EhFFYYutt|+tuUtEEM;H:[}u txuU4ukq\tWtI05`FFYYu3uUXu
3_^VWt65\FFYYhFO _3^VtDDWt~HPHPPNVH HP$PN(W_FNP^4FSV39tj9;t+8]dFu`FUWME
]
7uFYYSuMYuw_M^[d
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t$N,vN_^3[VNtjt)tC3^bVt{SWN\$fuhF{H;uhlFhTF_[3^V03t&9N0u!9N4u9N8u9N<u9N@F@u
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uv^hGlFM_^[d
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^d
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0Ev
@(4~tE4ME4{fS4K,C(E2}Dt.uv;rw;v@
;u
;uEeE8tP4C
Ep`@`t PQ&EhxPt8MYEEtj]
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@g}|LC(MM>39EvMU9t@;ErLT+TEV0EEEE;EEtM;XtC(MuMuI\PuRuuEEEEE;EYY39u
tE}H\RPuuuj0ERYE@;EEEPQEEEtPQ@EEEtmPQeuFEEPQYuFEEtPQEEuuYY@^3;EEE4tPQeC}uEvMM]SQj
MURQVPEVH}O\q`8utEC]}eD}^e7_uE]}u)EjjSVPQuMOExPuJ}CuDj Yt``\FEeuM
EuHMEBj MYt``LFEeuMEuHMEHpXj(3Y;tXXDF3VM$|uNqGO+EOf
F^N^E;tPQEDED;E}E3ut?;vPkYu33;EvEA;r9U U@9U$EthO\q`suYjYEEt7u$I-EEtPQEEPQ3PM@EoE
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Vt$NFut
V}Y3^FxQMAetPQMd
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QQSVqW;rev9+;E9urA;Bev1rAu)E~]0;4urp;7ukEu;qryDev#B@q@+;u@\;Xu7E9}rA;Bu$3vRI+<;:uF;r_^[2Vv@|
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Vt$NFut
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~\~PW@FD3;MtaV@HKe+<UEUA:Mv*UEMUE;rEMu_^[USVW}OvP4NPev9GU+VLELIHNP9MrvDYVD3;v2
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_^[d
4FQeVWEeURhFP}t"WuMEtPQ&N3v6xt#B;r2%WM_^d
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ju_^FxSVW3;9^uuV;NP]]]MEMEME~DjXE9F\}Ev%MM!MuEE;F\rF\jY;Mjp;YtXXXF3;}tWPEMEDMGEP<j_p
;Yt83PM/EE;tPQEE;F\z};]vNSSSuPE9}rj pYE;EtaE]EE;EtPQ]]uMETE]E]]uE ;]ENuP8;ux]MQhFWE
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EM9]|}tgjn;YtXXXXF3;}tWP_EWPEQ;EBuZ;E
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;tPQEE ;tPQEEm;EtNSPNq``j0mYE;EVEJEE
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tWPEE
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Q;Er9^\tNjk;YtXXF3WM}{E;uEPOh__uMj_9~\vEMD0G;~\rE;uE$N}WPuEPR;twukEY;EtPQEE;tPQMEzMElM
E
E];tPQEM;tPQw9^\tMqAM
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P[3EE;F\r}]_9^DM4N@e;}tMAQWmujEY;EtPQEE;tPQMEyMEkMEE];tPQ]rNPRMRP2EE;FDRujYEMMEMEy
ME-kMEM]^MMRM_^[d
Vt$NFut
V{iY3^Vt$NFut
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VFffN&f(f,F^Vt$NFut
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Vv$fvfYY^VW|$WGNP
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YuRH;t"~L
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WNXkG;}r~@T~\tzFd<tlVheWEv_E9tE9Ur|FLDUW3vO]9t@;r| PNX~1F\NDeEMvF^LMEMNXE
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VWwtSNt)LS_Yu[7_Y_^S\$V;uKW{;~v%6_ff&P_YY~~tP36)_^[JF~QSW};{tfV3tL
;wvP!_YEethChCxVjW0>}3Mtj?3{^M_[d
VW|$;~t,S3t;wP^Y6^Y~[_^VW|$;~t,S3t?;wPg^Y6^Y~[_^^FuQVWj8;^YM3;utYMVM_^d
pF-QVW}wetSNt?S]Yu[7]Y_M^d
VW|$W
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DAF~QSVW3N(F,V xX;t;u
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tt

ujX3}FFFGFu-}u'F,N(;s<u

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VFt
PQfF ft
;N(u F
N$F(v0t$VP^38AtQ,Vq(WyRX9|2_^tjXPV~$t/F,N(@XHuj(ut^3^UES3V;WtM;u8^3;Fwr;NrN8^Mt;vMF3;t}WQu
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EG$MjEto`CEEtPQEEtPQEEtPQDE7EEtPQEetPQE|CEEtPQEEtPQEEtPQDEEEtPQEetPQ@@CM_^d
[3HHH@
H08FVt$NFut
VQY3^FsQVuF0EtPQvetVPM^d
#FsQVuF`etPQvDM_QYFM^d
SVWt$N4|$D$3WN8F(~,^0fbWNDWNPN^^
^^^$F;t PQ^_^[@FrQQSV3WN^^ ^^^$F0;F,]N4}W~(]4QR;tuJuNE;uMF4F0SPQ
;E;PEM;tPQEM;tPQQ]URhhFPEE;tN QPRuFE;]tPQEM;tPQ3M_^[d
EM;tPQEVW~8`OT$GvNPvFNDP;_^UES3V;t9]uF;tkMMvEURuuPQ;u9]uUF;t PQ^F0jUF4N^^ ^^^$SPQ
;u=F0;F,s3;u*vuUNllMFEF^;t3^[]LDVW!|$aPP3;t$rw);v#;s@LD4xuP@AjX#pPx@ ;r
w;v1A3_^~F+pQSV3u$FFFFFFF^N]wEyE\MXXXXFFpFF`FFLF^[d
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FmQVueN06MN$
8M^d
V4K0K vK^VW[KPKEK4K7-KK7KK7J~|J7JvpJvdJvXJ8_^VvLJv<Jv8Jv4Jv0Jv,Jv(Jv$JvxJvpJvhJ,^V
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U eVuMuM+QiR^D$VW3
s07T$Nj>Yl
s07Nj>YlT$u_^|Fi8SVW3e]f]f]]EE,h
) t;tHHEEPQMOuE;MM|>Q}!uPuLhFM;QuUAjZM`EMPP1}uuhFMPWEuPCOuMjuFYCM3;s<w@EeuM\E}-
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s
0fj)s1&s23fNjX^fu
k?38t@<0u0d0@[^U$USVW4De+MMEf*EM(MEjEE_W4[}v3EjY0EUiMEEE;ErEEut'E tM)eEMy2_^[D$

D$>3UEVW}3}70E0uIM;t<|;t2uSDi[=sJMEQ_3^]FeXVM!Uu3.eSDeW+M}uE(eE
M:'MEjEE[4]]]v9EjY0}UchMEE;E}r}]EEEtMV(MN(33E tM<(MUE4
e}jr _EFuzVM6]Ltcj 3_j ^j[j333^t
WDFt
V@FstSHFa]}E3tF<u38]tC|u<3tG}tGG;E)}ME@A@!u<E0FuuUjZMXuVEF@UEo;38Et@|uLFtF+D
AuVuE\FUAJuEFuEXFuyEFU?K;t)E5:M@@}Mt
@Au}t
E= }MEMwHueMS}t O= uuu=S+}'F;w2+3}v=U@;ruM?Ypu?Y}}t)r$O= O=.O=.O=.M=w_[M^d
VtWDL$|$OOtQOt"OtOPP;`\r<fht+9;R6FfVfff
RR
ffVVP*P,OOtkOtWt:OOD;A<QDNfFttffF\V4M;xst<tjX%3!zt(RfV4LffF3_^A0T$IVtW<+TT_^D$V;Bs2<trDf@
QA^F^SVuWe3V]P};E}tWP];EtMQh8FWlpEulEu$;Et;lE;tPQEE;tPQ;EtWPElPPPuPC;Et8lE;tPQEE;tPQ;EtWPEi
ulE;tPQEE;tPQ;EtWP]LY3EPQ&C&CM_^d
[F*]QSVuW3e;]tPQ:Xf3CM_^d
[SV3WHIJKLNMO800e:44R:YY8@<D_^[SVWr3;t
P:Y;{9;~\F|~h~tYxx_^[VW3>v9~~~~v$9~$v(9~(v,|9~,v0q9~0v4f9~4v8[9~8F<;t
PH9Y~<~@_^3AAAA AAAAA,llll$l$bl$l$lDD 3Vt$W|$FPFPOF}W
W3_^F~Z$Vu3MehFM;AMhFH@ jjMshjMdhjMUjj
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r ~xs
FxF|9F|w~xvNxF|EE;Eu}t
Nx
fxf|3MM"M_^[d
Vj|'fxu@;s
;v(^|j
|DjXi^Vj|fxu@^ |^jHX^FWSVW}339e]utEMW{E:t 8_zEu]8_}t 8_|Eu]O:t8_~u:]uE:t8u:]uE;t;9^\t6:u9^hE8_}u
9E:u
9E:u
9EMpBE]M;M E;uu4Y]Mm@ V(V$VQPR;M | 9(]]]9$E]EE |EEEf|bM;;A\
qX8]uMQP|N
MMNuM;s8Eu]8];FhsNd8tNpULM]]2;MN|s8tIULM]]2;Ms8tIUDE]]2EuEMPMB8]@8]]]1flfntlQjuEVP;lG8]t8]f\f
^dE\Rj uEPQ;t!\u1u1Y7f9\u]"f\t\5dEE\Ek8]tCuUEPEPj
;Et(uG1u?1Y]YMEu8]t@UEPEPjD;Et%u0u0Y]YMXE8]t@UEPEPj;Et%u0u0Y]YME>fLfNTLQjuEVP;Et0LZuY
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f9DEE<Vf,f.4,QjuEVP;Et0,u/u/Y]YMrEf9,u]"f,t,f94E,E8]t]]]]]]8]*8]!E$EflutxEMt78]t2lMuL.uD.Y]YMWlE|MP6
uE.EYvE gwwPE
;Et:E Eu-
Y]ME(P;G]EURh(FPEfE;t[]URURPEQ;tuF<9]8t9]tuJ<uEFGw8E]u)8tZPo<8t98_xtGy:EEtP.<}s]G$uj^'8_wu
8]u;t8_`t8 t8G(EGdGhGlGpGuGv<E
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8EEE
;tPQE nE_uz*Y]MCM_^d
[V3hNAAA FDFHFPFT^VN(Y82F:F;F<F=F>F?^3HHH P@PP
PP$H(@)H*@+FKS3Vf]f]]uV]uRQP;t?f}@uEU33EPEf9]tW
EXEMGM^[d

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pFJQQSVWu3}~0~4~8F<E8xxHE~~T~X~\~`~d~h~l~p~tFxE8xxHEL~E8xxHE /~E8xxHE~E]}KE
}E]}KE}M8xx_^[d
FIQVWuE"7"7'n'7g'V'7O'~xA'7:',Nlv`"YENTvH'v<
'v0'M6M_^d
3HHHHHHHH
H$H(H,Vv$&v&v&6&^2AkAwAlAmAnAoApAqArAsAtAuQXQ`AvQ\QdAiAhAjVD:^USVWuMB5M+e}EU4E
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FX^\3M_^[d
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WEf8su5WMh99}uuqEf98uEPYUM9MXF)tVwM7%MF)tVkMFt)t-
MUE;E%WMF6)tMVm;Fl3MF)tVnMMF(tVpMF(tVrMF(tVtMF(tVvMF(tVjuu6u"YM_^d
VuF^FD,S]VW3KeuuE;EEM4M0ME.'9uu
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VWj %3Y;tH0ppV_^0F'AQVW}wetSNtSYu[7Y_M^d
FF@QVj@YM3;EtumMP^M^d
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@FD$LN8fD$F^VF;FrNAN^VW|$t%F+F;vyFWPt$2?~_^T$VW|$3F+F;rw;v;~_^VRP^UQSVEWPNV+}uEF_^[
USVWFKEuMt~jFEUK_EMt9tTM0@ E UEFKG}@rEU E_^[uHW#3?EUE
33UQw=vEh|GEEP?VF;FvNN^VF;FvNRN^XF;SVWMj ^PXt3F}tGujX0h_EEej SP=eetW+EUUu;s
E]SQM 3QP}R;M;toE\8;wC;7t%{7t{7t{7t;v
CCC;w+}u(MMj UQPF}Aj^9j FPSP5<3}E~@FD~@vDE3 QQVWPRucYM_^[d
V>7u3~zu-~u'~u!~'u~ujNZ5+F^@2^V>7uU~zuO~uI~uC~'u=~u7jNZ5;Fu^j3X80u@ r8Nu
8Nu3^jX^2^SVWfpft_|$^@SjjjWPuANHQjjjWPu-jjs3WPut$Wqu
W}3_^[Vft PQ&f<^VN8t
N8^U0SVW}3;]uEh|GEEP;MSI;uuuEu0EMGW+;3!]!]Ev;EMjY}U;MEM9M}r}GEGEFtFFE
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UvRUMHv
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wS^?3v#E38>tEH8MEF;r_^[Vttt$^lF4
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F3\SVW3WjBN8-
eeE}uM}VF8}XO0EG@P]uO4EO8@PEO,@P3MMMMMMF89MEMEM]3F8MMMP+]G8xEh@_e]EvE{C+C9
EvMK39MEMMMv:EMjEUY5E}E];Er}]CMCPsuOLu"EEt#@EEE}@E tmaKE+K;vxE}!u }uu=N8Q

8ODs-OD(}u}uuN8Q L
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EA9ErMEI+Et4ev,0;EM8EE;Er3;EsM<t@;ErE;EO,]EEMEG0+9EvEMEG4C;]]F8O,UX+]ESO0UN8Q+UO8O<Fe
tSuw<G1u
u
MYYMz3Sj5uO(9]vN8O(C;]TrN8

GPu\Eh|GEEP1Eh|GEEP1Eh|GEEP1Eh|GEEP1Eh|GEEP1Eh|GEEP1Eh|GEEP1Eh|GEEPk1Eh|GEEPW1Eh|GEEPC
1Eh|GEEP/1N8M_^[d
SVSe^6vW3_^[F-,SVuWv~$]D39Fv@;FrK8f
EUu!3;u9~vK8?N$G;~r
ut ut!t
K8 e~MF$eev?HE]K8MSPEEE]9]wr9EsMuV4]N,F(|;}wr;]s+]M}WS.]EE;FQK8BEUE39~v>F$vCu
V4N,F(:tQMG;~r]~3;EvVu;Fs ^<uM@;r]E}E}
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]YK8K8EUE@9GtnPw339VUvZ^$]u&;Vs!^<tV_
U@Mv$_$@IuB;VUrM_^[d
VW|$W~W~_^UQQSVWO8uEUu*u&O8MVQwO8EU}u}uVuUO8EU~t~^uj
`}u!}uuuVO8^U8v^$39~v!W$MRPG;~rEE_^[t4D$Q,Vq4I(4^TUQSVuW}MWk223~6v u
EH8&3C;r_^[SVN8ut$t$2\$t$S^6vW_^[F(S]VW}MWSeeueMuwSe^6?v'M3389tEH8!EV9]rMMM_^[d
F'SVW`ud3]]]]ME[ME[EPEP`PEuS9FxPhy39dE}4}MEM8EE<;Dl;dW\j;Yt
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Eh|GEEP(;EtVPEuuuM`EZFq%SVWN8}EUu
uPN8EU3]]]}]uk9]ufuEuuuPSEtMM>EZKN8fEU3]]]hlptE`X}EuD9]u?hPEPWESPKN8EU3}g9]^N8 jj
Ejj~}v39ltPj
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T H
]@M2]IH;lsh8tjY3HttHE];`;UsM
2H
;UsMU
2 E``}t EC;]]1uuuEEEuv8F+FtMthuMM3M_^[d
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E9Eu2}t4A;~r30N$9uG4}<uEN$U;rGEEC;^\r9EtG;~s0_^[|FiDSVW}F@3FDNVNW8DFdV\N`EFhUEFlEFX;MJ
@EE49]+URjSSPQ;URjSSPQ;EE+MMMUUu}sUUSS3+EQP6R;|uc;dE8QH
u

tu

t;!IU+EE+E
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T\38\$u~PWhxFP9u@%9^Pu@;uFPVjSSPQ_^[VFTt
PQfTFPt
PQfP^UVjEjP8NTUj^ytAy tAH3UV38FtEFE8F t2S]W39EvNP-
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Er]F?|EU#EuM~"uMEUjYNEUu_^[U(SVW}3MVw]9wuuuE7EFEMUt
EE}@reEEt"EUMj\Yc}Uu~^EI }H
E
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;~ujjV<39~v<8t
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B;r(Mj
~vFMj4C;^r3N3;vbF8w
B;rPMj ~]v;F<3v)@;EtEEt4MiEE;rC;^r3]]]]]]E3]]9XvjFu;~s
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D$r G?|_^SVt$Wt5jt$PV39~v<8tFt4G;~r_^[FSVWMj
3Y;tppF3;}tWP]VuCOwOGwSuxNFS,
PN^C3EEESuM@T$PEPV]]sPMPEPWz;tM;tWPM;tWP3M_^[d
F
PSVuW33}GL9^4]]v$F0E@EEEU;V4ErEjMHFX;jF<PF0PSSxFTVP]]]]]]MEA9^dE]vU]}~`8_t5wM7GMEEMU
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>F XSVWM&u
@Mt"333]]]]2E3;]t#9Xu
8X!u];t8X!uE8tEFTNF,CENFEPuF ^8]
~]W]YE}NF@ENDP^Hu]^^ FH9Ft
@M/}MEG!EG$PuE8u}WL]]]]]]PErPPEPEPEPPEN;tNPE`MEu
YE|M]uY>9]F uEhGPE
Sj]]]ME;EPEPEuuuE}uuYPYPEPSM;vEMPMUMuPElMEuYEM]~uMYN;uA~N
EESEESvEFPU6}PQ;uEPM_^[d
39A4u9Au
9AXu9AdujXAdQ|;tuJ;tu<;tu.;tu
;tu;tujX3VW|$;~rN$FD$9t";~rS^jU;~s[FL$_^VW|$;~rN$FD$9t,;~rS^jj;~s[FL$L$L_^USVuWvF${dKx6PW}vF%
vPWgvF&vPWQvF'vPW;v
F(PWF)PW8uKlyu}`^{u;C_^[S\$W;r#VN$F;s^L$_[VF9FuGSW\;wPFYtP6W6Y>^_[^PF
VW3MPCjMZ}?jMZ4jMjMj MjMjMjMjMjMj
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QSUVW3~T$v2L$3(;uh;t
C;rRQL$1SB_^][YSUVqW3v!3(;uh;t
G;rW_^][UQSVU3WNv$8;}u X3;t
B;rRjuj_^[DL3UMEVW;s)34D(F;u;tFrW_^]UAfAMfE83Ul$V3WFF};t"SSXS~~uP

[_^]T$VWJi~WQPFN_^S\$VW{FP(tPF3P~_^[S\$V;^v<;wWPFYtP6WR6Y>^_^[fFQhcYM3;EtHMd
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]D$@u@t$ttPQFESV
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EF't^F@;t SNr4eeUjRuPQ3;M;VEP]~E;t PQ};vUEP;tj9}Eu^=u E6=t#=t=t=u E}$EtEWup$p ?t3;u}}EM;tPQ}t
}tEEM_^MHM3[d
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3;Fwr;NsMFE8^~]tF4URuuPQE8^
tuUNFME;tE)E)F^FFu;uJ;u?E;u89]t1$~;u#FN ;Ht$;u
M;43_^[@UQEt MSVWu~ 3;Fwr;NsMFE~^t}~49EvEF8URuWPQuNEFE)F^;tME)Et}t~<NNuuIu>Eu>t>$~u#FN
;Htu
M3_^[@FSVW3Meu]]]SSFxERURURURWPSSQSNIuF|6YE@|@FHDu3M4;tPQM_^d
[DpFL%F`SVW}4jGO ^u uEURhxFPuuEURhFPE;t33;w;vP*uJUVPQ4VV%;\t-
Ee;tPQEM;tPQ\2#ueuuhluuuME(GeEEty?uW;vm?9pHwPrwI;w tKtFt>IIt:t2t%ttt
E9Use};:s\M39s\v
M@;C\r39wv tU@;Gr9sdC$30;UUL|UMCXMExt.ME |"Uy8uE|@EEME;LEr}Eh<ShPVEC439l0v
h8uxt/A;lreL9EE}u}ue
EEuhME";ur
M4E4FdUTF}t#K0C9L94+4LLuM;|EMEE9lwr9hshl}tEhEUdF;sd39 PTw vE PtEeEMP;vS0x8t>H
x$M}}tPT9Twr9PsPTE9Url9Tw"rP;hshTPuuuVPt=ME|3uuEeYYtPQEMj8Y`Eto}e}E}tWPjVEpa$EEx+tzqtiE6E
EtPQME2u/u'EeYYtPQEMIPQ>uF+P*~+E tO}tI'xt8ED+EEEPQ|EF3D8<@9~E
uDEF4u83F;~D| E|FEM339y\x8x;x:P P$ty}tQPV8;E\M@!;}r
M0EV<>M3F;qR<E :Y8G}}EpjY`3;FwOE
+F<E Y8hGE?)EEE;tPQME/uuwEeY;YtPQEM;tPQE3WME1XW1Ex!t$OPiE ;EE
;tPQEo(EEE;tPQME,/uuEeY;YtPQEM;tPQ@u U RPEQ;\u FEE ;tPQE'EEE;tPQMEz.uuEeY;YtPQEM;tPQ\9u tu
O8u EF3MVue;uM;EE ;tPQE&E4EE;tPQME-u-u%EeY;YtPQEM;tPQ@ptx}E9wvHx8tx;u%x:uH Ht
KX|uVpW:F;wruphZpDjU39tpM <$9xEPt%1#(W$
P&$P7$MPxPPE$FY;tTpEE3Ypp39uv3UD@;ErMW+9udud6}z'~ Et~+tjX3P)l
8Gt*8u:tPf
P_GMeE0x@MS$`Mu<29x}EtcjQjPRE!E040v=EKX|tEEjVjPQEC
EF9}ErUK0uCu<1X4+4}|U|UWVD4PERPTEk
EpxEHT)WuC0Mev8M0MCLT+TRQMEE;GrC,M4X<;QE$EC(Mt4F;r,3ptxEE;UtjQjPRE.
0|e40v~uuesX~tEE|M
x8uEpmtpEttN|MEE;ErE3u@4EjEX;Yt@(FppF@F3;}}tWPEEx+<FL#LPEPE#gf<LH4)84)PHLETLEPQj@YX;Et
3g<WMEXp)Gee#<#3HPPj<PQM# EQMQMuEQHQPPPuS47E9HqHO8(HEtPQ<E9}Y^uN
(N$u(0MpQPEEM_EOpPOOQ(Ex+tl|M}uEEMO0@TQPPG$POT%PPPuO= EqEx+Et
PQeEt

PQeK}4Et 0t?Ut.0$PE4juRX}
}W
};|;E#X@<
)9EFv@+@#;|
;M
ME;ME;ME;O4sW04Tp(P,A;O4rMH pEP$Y`pEH10Me}4EEKX|EEM
<8PPE29t'G;(WPVMPPPEYEEF9E,E0M;HpuVE2%Ee@)\vOe0M\uM< QPpEEjEY;Er\EEPhqD"uMVL
3Ev3}DA;reM3;MhlEEE3; r;MMM 4F N$hEll;H$wlrh;H w_x(tE~(`j.;`v,p}puMUtEEM;MB}sEM_
j`Y`Et8EeuMXEuEMEPu5kEMu`p4hMlEQ0@TRPPA$PTw PPP`J
EH0;H, ME;q4s0 LP(H,MF;q4rH P$39uu*}EMM <$9xEPtmG($PL P?$P7 EH8<1uG
O$dHDHPLtE$MPxPPA$EEF;uYEuH7,uuut)9Er$w9Mr)MEEuuPQME5;uTuYYEXESdd;E XE@usESEE
tPQE".EXEPSEE tPQEE&EEtPQMEuuEeYYtPQEMtPQE1 E^WEREE
tPQE9EEEtPQMEuyuqEeYYtPQEMtPQEEVEREE tPQEEEEtPQMEPuuEeYYtPQEMtPQEpYEVERQEE
tPQEE(EEtPQMEu!uEeYYtPQEMtPQE3HEtPQ<EYEtPQEEtPQpYEUE^PEE tPQEE4EEtPQMEu-
u%EeYYtPQEMtPQE?<YEEtPQEEtPQpYE2TE~OEE tPQE
ETEEtPQMEuMuEEeYYtPQEMPQEEtPQEEtPQpYE]SENEE
tPQE8EEEtPQMEuxupEeYYtPQEMtPQEEEtPQEEtPQpYEREMEE
tPQEdEEEtPQME!uuEeYYtPQEMtPQXEEtPQEEtPQp@YEQELEE
tPQEEEEtPQMEJuuEeYYtPQEMtPQEEt7jujPQt$EEtPQEEt)PQ!@ME-ME-p/@$YEM-uYEY_PEKME h-
E?EME>-MEuu|eYYM-MM-@ME,uB7YYEOEJME ,EEME,MEJuuYYMStPME J,E!EhME
,MEufYMMtMjjjPQt3ME +E
EME+MEaME|ME|eMn+MMb+M_^[d
VWN0j~<OVNTNN`Nl>~xxO.cONO9O$O_^VNNN$^UeSVW{v*;uF;AuF:At
E9}sE&}{u;CHK_^[UeeeVW}EFj@^T;uuxuj@@uE[wWrwM+t-t(t tttu=EuUM7U_^A3v
9uytB;r2=uA=t"=t=t=uAaAF7QQSV3WSSuUuWP;j(;YtXXDF3;utVPWN]SE^FEj(F^^ YM;Et
s3;]}tWPuESSuVWP;Et ;]tWPM;tVPE8G ;EuG$];EtE@;]tWPM;tVPEM_^[d
D$ SV4
F:Q::u^;:Y;tt
H%BQ(N(~;t~:t^[,FES]VWF38EFFF{(QE@j.F;EC,Fj@F;C,taMFeff=s:ft(<A|<Z EMuCCff=rMFuYM_^[d
SV5GWjXFt/ tGu t t:uF@t
tF_^[SUVW;uG;:F;t
HWtG::F:uV(O(N9D$$8SM+;A#EN(ACO(AEN(ACO(AG?u8F?tjXt8~?t`t.~?t(NFWLOT;u;T$t
T$9T$&N$F W$L$O T$;u;TtT9T$wr;sMV(O(Pu>m3{u+;IA#um3+;IA#_^][USVW~t9xuxu
8xt
@qe39u3XS39^vNAB;Vr}t:3ut;u!uP`@E@H`?uwj=?Ett/Gu3#ujujuj[}|}~jXt
FN@D;Fsb4FN=ut !uGuCFu8t4?u
Sj<3}jX+Pj<Sj<E_^[]Vj>|^j>}j>^yt3USYVWjZ;vQy3vA9t
F;s B;s/eeYUCu@<C3W_^[@F4SVWMxMj3^MhV}E}u;Mhj;MVj
;Mjj;MWj;{{}uKjX;vDeEEHsMEYFu@<CEE;Cr}j^EPEEPwEtSe}uCu@<EC@EECu@<C3MMM_^[d
A4B
A<B(AABA=B$ABABA>B%ABABA?B&A:b'B)VT$WG G$FG;F
F_^y;uy:uA I HtjX3Vt$NFut
VY3^`FQMAetPQMd
FhQV3uF(F,EF0F8hEF<FYF4M^d
FQVuF$etPQv MtVPM^d
Vt$jhGt$&pu
D$@03@^D$@0@0Vt$N0F0ut
VY3^FuQVuFv4ERF8YtPQM#M^d

+F QSV3Wu^^^FuN]8]EFtGj8YM;Et3PE
F|@M_^[d
uFqQVuFE;MEtPQEtPQEtPQeNyPM(M^d
3H(H,H0H4FVt$N0F0ut
VY3^FQVuF4EtPQM#]M^d
Vt$jhGt$&u
D$@3@^Vt$NFut(VY3^SVqXW|$3;QhsAd8tqpD$<8tpD$X2q|X$;Vs><tv<xtp
``2X%;Vs><tv<xtp
``2X&;Vs><:tv<xtp
``2X';Vs><t
v4p ` 2X(;_^[s<tjY3H)FQVW}WueGPNBG$FGFGFGF GWN$EF!M_^d
3@(FHHH4H8H<H@HDHHHLHPHTHXF@FUVujhGu&uE<jhFutjhXFuuEP#@3@^]Vt$NFut
VY3^FQVuvPEvD}v8uF4tPQvetVPM^d
D$VtPQ^VF;Ft5W3tPFP6W6FY>F_^VF;Ft.W3tPv6Wd6FY>F_^D$V&t?;vPhY^VF;Ft5W3tPDFP6W6PFY>F_^F
AQQSV3WuFFMEy;t?;wPY~3v:j
YEEtM 4>3NeFC;rM_^[d
VWj z3Y;tH0ppVt$_^Ul$V3WFF};t#SS+S~~uP[_^]UWy}v_SVWNuE}KMuujZE}w^[_VWj3Y;t0pppppVM
_^$FQVW}ewtNtj7TY_M^d
UWy}vkSVuuW
NuEujY}}jCYMjmYuuuujZ}w^[_VF9FumSW\;wPpYt
W|HBaa!Ju3FtP6W6SY>^_[^USVW}UM;]wMssEMPI~M4MV/}}]}}u}_^[D$28QVu
8Pt"8PujX0;rwI@;w^VW|$tWPtPQ>_^US]VW3;^~v66A>~Y;wPYtO}3^_^[]USVWj4;]Y}UEw^E;]susU~Eu4MU
}EMj];]<YvUEju<Y_^[lll$VWP(3zzNu_^VWlOu_^T$S3UVWtH~(st;~t&tDlFlt~E;r_^]3[_^][VTFFFFFNF
FLDFF4FF F^TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3uD$tBH_
HA^H3[F3u/D$tH_
HA^H3[3_
HA^H3[_^@[Vt$FHFutV*3^DFA4FA Ft3D$TLL03S\$V3Wv:tLs Httu-
f9u%ywB;rD$xH_^3[_^W[jh;FdPd%$SUVW|$<D$6wjt$h
3;!l@;t;tP;wrwL$@l$$T$RhXF$PG T$p$$t$p+X
OuLpl(l$\l$`l$
l$$lllLlPmL$<D$@$$QL$$Dl,l$0l$DlHD|LD$2D$&L$@3$L$4QL$4QL$4$$|$<|$@QPRD$;\$D|$Hl
$4L$$\$8;r w;t$ r;wr;t$ wL$u2|$L$;r;L$,T$0l$4D$4D$8D$U0RQP\$T|XL<T$@VD$V3t@D$;t
PQ|$|$LDt:$$$+D$ +L$$3;u;uLp>o\$<$s +$3+T$(3+D$
D$$$L$|$OTtp+V>;uTLLPTLL$PTTp$L$T!D$<@ D$($t9D$
$+$+;[$$+rDD$+$$c;DtD$$K4lTULXQ3VP|$8R;D$T;u
Dt
T$$[D$(t$
T$$$$tLl\|$\$`+T$Xu=SD$LL$\T$lDhThD$RPV|$lQ%D$$tPQ@D$$;PR$|$d\$pD$T+3+P8u4D$T@D$T|D$
$tPR3D$$tPQ|D$$tPQ_D$$tPR@?D$$;tPQ"D$$tPRW$_^][d
UjhPFdPd%QE$M USVWePEQMRUPEQMRPEMd
_^[] DM_^@d
[] jhvFdPd%QSV3t$TFFFFFFFFFFFFF^N \$hBhDjjPD$(L$FFFFFFFFFFxFFdF^[d
TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3u D$H_
HA^H3[xF3u D$H_
HA^H3[F3u D$H_
HA^H3[F3uD$tpH_
HA^H3[(F3uD$tBH_
HA^H3[F3u/D$tH_

HA^H3[3_
HA^H3[_^@[D$HAHVt$FHFutV3^jhFdPd%Vt$hBjjPD$(ctN 3L$^d
DL3VWXPNuAp3JH
Nu_^4aD$(SUVW |$Tt$<jN 3;rF0l$lllTL<aW|\L 3v@;TTr7D4T$@l$H+UD$LPD
PRST$HtslpLp)YYT$HuNsItu9D4T$@l$H+UD$LPD PRST$HutD$TtuD\e+l$T
wV0+t%\$PDPl\$1D$P^0~Dt!t@T$+3;rw;vD$XN0+\$TL \$+|$$T$4u=@rW++T$ D$,D$XRT$,RP\$4Q=D$
L$$DLL$<r^r_^][(D$TDTuDTV0+tPD$P0D$Tt_^][(tt_^][(
D$DtR3D$@D$<pL$@9t$Ph+3+V/;uO;uT$@CT$@|_^]3[(W_^][(
S\$Vt$W|$tVPtPQ_^3[Vt$W|$tPQ_3^D$S3Vt$;:Wt@SNN_^[SUl$ 3V;Wt]|$$;u_^]3[t$\$8t,+3;r
w;vL$$|$ D$$\$[.|$ l$(+t=T$;T$tUT$$+|$ T$$T|LL3;v@;TTr9Ll+UL$
DQPRS3T$;t9l`L`)iiT$;ustt;tptuDL+TOZ_^][_^][9\$uT$8t?8t7;u';u9uB;tu3D$_^][9\$t_^]3[;u_^][DLVWtT+|hLl
D$3+8H_3^tPQl$llllVlVll$&l$&lflll6lllllllll$jhFdPd%QhD$DtLd
L$3d
jhFdPd%QjPUDDtL$d
L$3d
FD$3HH3Vt$FVNPQLRT"`NN^j3;tHHHtFH3jk3;tHH@tFH3
FID$@3Vt$T$FNPQL$VNN^j~3;tHHH@DF3TDSVWG3tF3uD$_^H[AH3_^@[D$HAHj;~3;tHH@@DF3j~3;t
HHH@ DF3j}3;tHH@@ DF3j}3;tHHH@ DF3j{}3;tHH@@
DF3jK}3;tHHH@DF3j}3;tHH@@DF3j|3;tHHH@0DF3j|3;tHH@@0DF3V@F
Fr^3W|$s3_L$V49AQ;u^_W|$s3_L$SV49YAYAYQ;u^[_j{t@HF3j{t@\F3Vt$FHFutW V{3^VFV
8Fr^jhGFdPd%Vt$FFxFFhFFXFFHFND$OvtVPL$^d
3SUVt$3W9nu O;Fu_^][\$$n nT,;D$t(
~ +z~$|$u;sN +DL |$WPFPQRD$L$;;tH3T$$+RVD$(PPSQV L$$V V$;V$ue;tPD$;rL$V V FF;u=9l$07L$0F
PPQR;u"_^]3[_^]@[_^][Vt$W|$tWPFtPQ~FF _3^Vt$Ft
PQF3^QTVtRTDFRT$RPQVL$VVVT$^t
YDLP@A3jhhFdPd%QSUVWj(]x3;tQ@F@F@F@Fxxxx
x$F@xF@hF@XF@HF3;t$tVPT$$RWWSUV|$4Q;D$tVPL$_^][d
jhFdPd%QSUVWj(w3;tQ@F@F@F@FXXXX X$F@xF@hF@XF@HF3;t$tVPT$,D$$RPSWUV\$4Q;t;D$t3+V
D$$;uFL(;u3@;D$tVQL$_^][d
j(v3;tN@F@F@F@FHHHH H$F@xF@hF@XF@HF3pF9D$Hd[3DVtVPD$PHV[^S\$Ul$V3W}vAt$L$s'f9u8t
tu)yrwB;r}_^]3[_^]W[D$jjHD$LLQPRD$HZ3D$VtVPDPH&[^|tWLT$3@B3htt&@HF@F@F3T$D$SV
WG3uD$_^[A3F3uD$_^[A3F3u&D$tH3
A_^3[_^@[DAVtHutVs3^hxst4@F@hF@F@F@F3TDSVWG3uD$_^[A3F3uD$_^[A3F3u
DtH!F3u&D$tH3
A_^3[_^@[DAVtHutVDr3^F l$l$flvlvl$6l$Flll$3@HF@F@F@F@F@F@
F@$FH(P@PDH0@P@H,H4@8H<PHPLHTHXF@F@F@pF@\F@HF@8F@(F@
F@FTD$SVWG3uD$_^H([AH(3F3uD$_^H([AH(3F3u D$H_
H(A^H(3[F3u D$fH_
H(A^H(3[F3u D$4H_
H(A^H(3[F3u D$H_
H(A^H(3[HF3u D$H_
H(A^H(3[(F3u D$H_
H(A^H(3[F3uD$tpH_
H(A^H(3[F3uD$tBH _
H(A^H(3[hF3u/DtH_
H(A^H(3[3_
H(A^H(3[_^@[D$H(AH(L$A(HA(u
tjP3VD$t V4n^jhFdPd%Vt$FFFFFFpFF\FFHFF8FF(FF FF$FN,D$tvXtVPL$^d

DLH03DLH43|$t@D$<(v@L$A43L$3L$A43`SUVt$p3WF,^0;^4u%<G4G;F,u_^][`L$(HNDF@L$,NHD$(F
L;D$0D$tF8^PnT<(u%k3;uP;uL;rHT$4D$8WL$@PUS3;rw9D$tvDtDtuDtLtL$03T$xL$XR+L$@D$|PL$$
WNHTL#DWLPF<T$`Q$RPT$TQN,D$@RV8P\$4\$8|$0vT$VH|$D$$l$~0;n4ueD$H;u]D$`;ttP;uG9^<tB$;t;
uR;t$;tt$P;u@T$T;t_^][`VW3|wr=sw=v3;w
r;s_%^DLP$@(A3D$L$H,3D$LTH,P03VLt F(N,FDFFLu"<GLGFu^V@W~HRFWL,PT$QNRV
A_tD^3^Vt$W|$tVPGDtPQwD_3^Vt$FDt
PQFD3^W|tDUVtLT$$QNRT$$D$,D$D$
nLTnNN^]tL$_t
i3llFlFlllllllllVlll$&l$VlVll$&l$&l$ l$ l$
l$$l$$l$$jhFdPd%Vt$FFFhFFFFDGLGD$FFFFFFFFFuD$hGPD$L$^d
D$HAHVD$t VTf^FAFAFAFIt t+
tD$PtMf:uIJT$3JHff=uD$JH8@<3f=uD$JH8RP<3WPL$SUVW`l$p3v%\$h|lDPPu"F;rLdTI_^][PS\$v<Ul$Vt
$W|$?uf>uFNPQMKu_^]3[3[DHLjTD
D$<L$VPLLQL,.LtXVRLDTD$I#VjjPjjT$@
^DuD,u
D$u<l$l$6l$lll$
l$jhFdPd%QjxcD$DtLd
L$3d
jh+FdPd%Qj%cDDt L$d
L$3d
(F3@HF@F@F@F@F@FH
H$H0H4H5H6PPHTPXH@F@F@F@F@|F@hF@XF@HFHDH,H(HlHdTDSVWG3uD$_^H [AH 3F3uD$_^H
[AH 3F3u D$4H_
H A^H 3[F3u D$H_
H A^H 3[F3u D$H_
H A^H 3[F3u D$H_
H A^H 3[HF3uD$tpH_
H A^H 3[(F3uD$tBH_
H A^H 3[F3u/D$tH_
H A^H 3[3_
H A^H 3[_^@[D$H AH L$A HA u
tjP3VD$t V$`^jhgFdPd%Vt$FFFFFFFF|FFhFFXFFHFLGN\D$N$3tVPL$^d
DLHH3DLH@3VN$t
FXVT;t(3NXFTh3Fu^NXNT3^DTVthLGPNXuNF1^w"$D3W@@IDDDDDDT$S3;Y8:A6Y<tA8RQ<YHYL
Y0\+[D$3H(H,HHL$QH3DLA,3DLP4@8A3,SUVWF$F5vF@ND^tD$,L$0\$D$~HV(N,F,;u,L$uL@PFTP
F$F(PQRD$FpNPnt+;v+D$
F6T$(t4F8NH^L~H+N<3;rw;rF4T$(tD$ F(N,+^$L$$L$QL$$QL$,QPN\T$^(N@V0T$$D$
^(N@FDNtFD+//o8^6ot^Ln<~H;rw n8;rDD\$
ujutbltYTtuLT$(D$;rFD$Vl+QL$HhNtVp;uFt^t\$DuKD\$D$HJV@l$,ND|$0+~HL$8L$4T$4WQPRuxD$
ufD$u6F4t!F6tV8NHFH8;u?NP;u5D$_^][,L$ttN4tuD$_^][,_^][,Vt$F$u
W^T$NFRPQD$LTPQRu+N4tTtN@2W9;_u
Rq;t^Vt$W|$tVPtPQ_3^Vt$tPQ3^D$3;tSUVtWTFt1Fn,N
^0+l$(;rw;rFtD$l$(\$$T$(~F;u.D$(u&FV8WRVRPQD$(F+T$L$ L$RT$$QRVN@PD$4Pl$8LT
NNDNFF$N(N(L$$N,F0D$,+tD$uBt9_^][D$(_^][D$VPcLTQRP^QSgDtUlVWD$S,C(s,;u4D$C(uSTVRS
$RPQD$t[{(+;vs$|$s(K@K@CDs(tCDD
+ttdD_^][Yl$l$flflvl$6l$6lFllllllll$l$vlvllFlFjhFdPd%Vt$FFFhFFFFLGD$FFFFFFFFFuD$h
GPDzL^d
TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3uD$tpH_

HA^H3[F3uD$tBH_
HA^H3[F3u/D$tH_
HA^H3[3_
HA^H3[_^@[L$AHAu
tjP3VD$t VT^FAFAFAFIthDGLG u.f8L$Q
RQH,%Wv3uf8L$PQ0@A43f8@$DD$P3L$3T$B3L$A$3T$B3L$A3T$B3L$A3T$B3L$A,3W}DDDDDDDDDD
DDDDDDff=arf=zwf=HuGff=arf=zwf=Cuz3f0|mkfyudL$f=BuJff=arf=zwf=Tu-3f0|
fyuT$38L$SUVWl$X3v?\$PtTtDPu7f>u93f9NL$8G;rD$LT$H__^][8_^]W[8S\$v<UlVtW|$?uf>uFNPQM]K
u_^]3[3[LDPTID0uTLRTD<L$VPKL$LQL$,L$tXVThDG#hLGVtPDPT$8NxT$
D$$VFD^uD,u
D$u)<l$l$jhFdPd%QhBPD$DtfLd
L$3d
jhFdPd%Qj ODDtYL$d
L$3d
FXSVW_ GGc5?u#<G4G'u_^[XL$4'OGD$t$vO3;u;WtL$HDD@DhPLP7LlQL
txLVT$#D$VttPSLXT(Q%RPVT$P8D$$OuDD$u<D$Ttt/Gu"ttD$,;uFT$0;t_^[X|#ttt@3jhFdPd%Vt$FFFF
FND$FFFFpFBDG4GFrKFuD$hGPDrL^d
TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3uD$tBH_
HA^H3[F3u/D$tH_
HA^H3[3_
HA^H3[_^@[L$AHAu
tjP3VD$t VtL^FAFApFItJD$ w>3D$D3AxAx3Ax3
Ax3WHDPD]DjDwDSUVL$W
u(D$f8t_^]W[@_^]3[u'D$f9t_^]W[HQ_^][uND$ffuP_^]3[fH_P^]3[u)D$f8H_P^]3[|$ff=u
u_^]W[}h_^]3[f=t_^]W[wTbD;t4\FPt

O}_^]W[uEff=-tf=:t_^]W[TkbL;tf9ut=wEl_]h^]3[T$U_^][VW8FVOupFr_^_^SUVt$WN0>l$ 3v+|ttLVPu
C;r3_^][S\$v<UlVtW|$?uf>uFNPQMGKu_^]3[3[D$<L$VWPL$PQL$0]L$t$\V|$HOW"GuOTD#T$,VPHD$
_^uD$,u
D$u<l$l$
lll$
l$VSUl$VWJE;uUu;uLv+B+:u3@;rJE;u!u _^][r}3_^][_^]2[tSVW;?uiK3vLL K@;rK3v S@2FLK;r |
+3_^[tCKU3DD$tvPFKsK{sL$
sC3
J3 lTDPL$ 300u@rDDuS L$qU4F]_^[tSU3CVWv|$C4VuCE;r_^]2[+G
+@ rt!CQP4PFC0_^][S\$VW3FvFPuFG;rS<_^[tFQP<PFN9_^[VF;r&VW|t/WEF_HFD$NPj
^APDY6j 0GL$3HHHhDiY0GSVst#WCN<tOQYDWSDu_SRBD^[@Gh`D0iYh@G`F33HHHH(H,HHPPP
P$H`HPHTHXH\jhFdPd%QVWh@GD$@GhFwVD$Pu&SV0G@uV[uV0Gh@GlFL$_^d
L$V3jAPVVVApG3^$D$(SUPX%@
L$0
AD$utZu3Bu3K
D$ D$0VWp|$jp|,_^L$4UT$w][$jh3FdPd%SV3Wt$$GFFN^^pFthFFxXFhL\$ GFGFtFFxFFA|$;D$t!j j
FGFGF3;\$tWPFp;tPQL$~p_^[d
TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3uD$tpH_
HA^H3[F3uD$tBHt_
HA^H3[F3u/D$tHx_
HA^H3[3_
HA^H3[_^@[Vt$FHFutV

@3^jhFdPd%SVtFpDtPQt^3S(R?st#WCN<tOQ?W?u_SRz?L$d
^[jhFdPd%SVtFpDtPQt^3S(R?st#WCN<tOQ>W>u_SR>L$d
^[SVC(P>st#WKN<tWR>W>u_CPv>^[TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3uD$tBH_
HA^H3[F3u/D$tH_
HA^H3[3_
HA^H3[_^@[D$HAHL$AHAu
tjP3\GAHGA8GjhFdPd%SV3Wt$$GFFN^^pFtHFhL\$ GFGFtG<|$;D$t j SGGGGtG3;\$tWPFp;tPQL$~p_^[d
TDSVWG3uD$_^H[AH3F3uD$_^H[AH3F3uD$tBH_
HA^H3[F3u/D$tHt_
HA^H3[3_
HA^H3[_^@[D$HAHVt$FHFutVZ;3^jhnFdPd%SVtFpDtPQt^3S(R;st#WCN<tOQ:W:u_SR:L$d
^[VHD$t
V:^S\$33KKC3CUCVCCKWPPPD$;t$TL?{uH_^]%W[w_^]W[VT$|$L*;t_^]W[3SvFD;G;r3vFL@;r[v?t_^]@[_
^]3[UjhFdPd%QSVuW}3e];w4t)G0;tP=9_0;_4t)V9G0w4;t0u3Md
_^[]}DM_^d
[]UjhFdPd%QSVuW3eN]]FpURhFPE;t)E];PQMd
_^[]E;u@Md
_^[]N<j
QPR;t%E];tPRMd
_^[]EV\jRPQ;t%E];tPQMd
_^[]vpVRE;]tPQMd
_^[]DM_^d
[]DTRT$@pRPQjhFdPd%SUVWj@6\$3;D$ txt$,VNSS9:yzyzIJCC~;|$t*W6Cvt$,C { \$,D$,EM;D$
uRt??t$wR36Mtut$EP:6]ut$(}E+DQRPFMT$8EL$@_E^][d
lfl6l$6l$t6ltltl$l$lll$l$tltlxlxVlxjhFdPd%Qjx4DDtYL$d
L$3d
jhFdPd%Qj|%4DDt L$d
L$3d
GOjhFdPd%QV3t$FxFFFFL$T$NVN<3F\GFHGF8GPD$QQQOL$^d
Vt$F<L MF^%@D$VHu3^t$u3^s ^HT$VL P^L$VuBr=
w8t$Ft;u)FP+Fu,FT$QNL,F3^W^|tWDL$V182qPrqrIJ3^Q Hu HAD$u7Fu`7FAu=
HEFt2SUl$V3Wv2\$|$;uf?u(GL$Pt F;r_^]3[_^]W[_^]@[jh;FdPd%QVhL0t$3;D$t j P2GFGFtGL$^d
jh[FdPd%QVhL0t$3;D$t!j jFFFFFL$^d
G9xSUVWL$,FjT$L$$D$4FjT$L$$D$|0FD$P@FtjT$L$$SjT$L$$D$>dL$ L$ j L$$NuMuL$ G
_^][xXGh0D`TYhXG`FVWhXGhFF t$t|S$L$Qj L$dL$ L$0jT$LDr9j L$,T$xL$3tLxC@O
ru[hXGlF_^GS\$VWtDP\$uD$t+u3_^[D$_^[DjPQPDuHYD$T$$ATjTS\$VWt%D$PF\$uD$t+u3_^[D$_
^[D$jPS\$VN3+NWu'9t#NFFF TDRVL$N9vNN_^[SVN3+NW|$u#9tFFF
9vNVWN9vNN_^[Q3VWGw+u
O_^S\$;vOVQROw3[_3^3AAIA@DuDADADA@DIADu3QPQPYu3jhQj<FthjQ0FhpGhTGFPFttHuGu3ROROY
u3jhRj<FVu3^GW=<Ft+=@w$;vH0#;rjh QjujhVj_^thjR0F;s3tP%OtHt
BPOYVWOsG;r_3^S3;rOu[_3^W+N#[_J^tIARPDAD3A,A3A4A8WA(y<_A4SVW;w\A(
t0Q4uy8u/A;At0y8B@y8AQ4;u9A8tA4L_^[_^A(3[A(r.;w%d$A ;A$FQ(T,A(@A
9q(vUA(#A4=s$Q;QRA42A8BA8Qy+;q A$+;v
3y3ut-d$ttBF;uBF;t$<u+1;uA3BFq,q BY3u
3y4ti<Y8;s+Q4fDi<+++y4Y8fti<A(3y3t;txt}4y q$A,+Q,+rdQ3y]_^A(3[]_^A(3[]_^A(3[Bq,q
+Q]_^A3Q(3[y(7Q0tQ1vWQ2vWQ3>Q q(A4=s
Q;QtA42A8BA8Q]_^3[3A,fA0A8A<ADAHALAPA\AhWA(A4A@ATyl_UW~8rV< Jt?~;~tLF8V<
kJF1GGn@F@F1nDFDu~~8F1F@F<FD~8_]~_F(]QVF(tu;LFXV(SUF(W~4s"f4F N$+~,ue~+;v

}~0,ut+
tt@B;u@B;tu
+~ ;uH~HN0F OuN0HG~H~$@B+VHD$r(~Pt+VLT;sVT<;sD$Xu
3~4Lnl|$uV4+fTnlF ^0V8V<++~4fLnlFHVHHF
3N0L@;LtI]~,u:9~hs_uu$^h9~hr~(_][^Y_][F(^YF(VX_][^YSUV~\WD$^`N,F$n V\L$L`D$^ N$;tF,~ F\+`+F\t
9AJu~(LTn N$V,u`;t\9D$sM^`@ED$F\n v+n F\~(u#~ N$+t8T`@;rN\\N
^][SVt$|$Wt6;sY8Xux+xI;s)8Xux++x^+[SUl$VW|$L:tv;Pp#rpP0+XPXPd$;wdPp#rpP0++XPXP+_^][SUl$Vt$
W<;sDHuPuPt+++HP_^]+[SUVt$W;s[@uXt
u8XrP|$t+++@P_^]+[D$UV0W3l$s_^3]D$SZ\$/;sI
tB;r+++;vA3JABBBL$,|$$t-+twtD@uGf+t0CuK3|$$t+\$3lBJJB
l$G[vT$3_2^]T$_2^]UWl$s
_3]SV3+t$
|tD$ K3Cl)E$<upEMpu[EL$ |$$t+p#uCvt$l$|$;8^[_]Q$X3$PX3R!$$YSVL$333D$D$\$LTDL
T$D$$LT(D$^[VstC et5FPNQVR3lFPNQVRVS^3^SVWy3G39zuZ;Yu;Yt@$r_^[
DPLQTRT3DPLQTRTL$V%WL$'t7Ht#HuruJsE_^ ru*r
rut&t't5t6t_3^ 4D$ PD$$LFt
|$$@u
34$(tD$4hjQc@UVW3|tUDWP#>$3t6$SI3t;s8*\4L48F@;r;r|$[;u3+tPL4QW=tVL$+D=QP=_^]UVW3ltWDU
Ps=S$3t6$I3t;sl4L4F@;r;rl$[;u3+tPL4QU=tVT$+/RP<_^]W~D~@|$~t~<_V4t9~<tCF4~<F439F4_A+A)Q)Q)QQ~
u-
~\u'~t#NFVL+H;v)FLNuFY+VF<V+F4$tIN8$PF\u$tFF+F;FDw+VN<V+N4$uYVNF@+NQ+F4RP?=N4N@^yt3
A<A4+9AD@yuA+A9ADr V^V~u6N<V4+9NDAt"N@W~+~+WPR<V@V4_^3VQ4QQ$hlA0 AHfAq`% 3 3% 3
3% 3 3% 3 3Bh^VWGV$~F$uV4GF4_^SVWv-|VG3^$8^u
V4G^4_^3[r
rLTUlV@T$*^D\$ P
VHn 3_nTuX3;lrw;hvh;tI
=vuF,@vFTvFTvFTFT~~PFX^tV$<;t9`t>\$ Cn$`;t3F$;tNXV(]_^[3V$C~n$u
V4Cn4]_^3[Wy$ITt3_ATQ$I,AW<t3_UVF4FFFFF\FtcFn+F+;sV+W~D;wt +_;sF
;vnV^]UVN$FTW<N,At3NT~$t3FV4FFFF\FFn+F+;sV~D+;wt +;sV
;v_nF^]W|$3tVd$4;w3+4@;r^_~SWu:`~$+FP3%t;w3+A;r)F)F)F~uF+F9FDuN9NuF^+N+;sV~D+;wt +;sV
;v_^N[TLSDUlLVt(+|$,W|$4t$,D$(L$0;+;#+D$$D$:+uU:EuND$;t$+}T$ T$
::uFG;u|$4;sI7O|$4;t|$4L$+L$|$0__^][$D$$SLL,UVpW|$,t$3+\$t$
t$9t$@D$0L$8L$@;|$<;#\$<+L$4,L$+l$$;r:uWF;t$t/:u'F;t$t
,+):EuFE;tuD0l$$9tHsLDO1ytHLD;t$te:sL$]tt \$L$\$Ult |$,+|$@#LDD_^][UD$_UD$^][D$
SUVt$0HWD$(33+L$|$\$l$9\$<IL$<;D$8;#L$8+t$0<L$,+;rt$,:0uE@;D$$t4+I.:u#@F;D$$uDOT_^]
[;D$$tt$,:0sL$\$|lLl|$|$\$D$(+|$<t
t$4)D$_^][SVNsFFF;Fu^3[BV$W~j8F0SPFPFPF(PPFPFFF+;FuA_^[VFTDs!FFF;Fu3^N$SUW>W3T`G3#F,V
T$+l$l$,3;V+ :{L$G;t(:u@;u\$+JS;uMV0FRVPF(RPFRT$,PQ:FFF;Fu
_][^L$\$PF0RVPFRV(PRVPRT$8FFF+;Fu_][^VFT$D$s!FFF;Fu3^A3D`QSUL$IW|`N33F$#~,l$ +T$$+TT
T$$T$33;^s+ :
uL${y;t$;ns+ :
u\$MLL$tz,
\$$;t\$$:u@;u+T$D;uEF0VPFRV(PRVPRT$4QQFFF;Fu7_][^sPD$F0RVPFRV(PRVPRT$<+\$FFF;Fu_][^VFT$D
$s!FFF;Fu3^A3D`QL$IL`SU3W~3F$#N,+\$ l$$+ll
<l$$L$33;Vs+:uD$jh8;t$;^s+:uD$STtjT$$T$;t\$$:uG;u+L$|;u5VF(L$FFF;Fu[_][^TsDF0QNPFQN(PQNPD$4Q
PR+|$FFF;Fu_][^SUVWI~r8A
V$NF0NPFQN(PQNPQW^F^;Fu{+u_^][QSUVWT$InrRAI3D`~$^3#N,%F0NPFQN(PQNPQUFFF;FuL$u_^][YS
UVWTIFDrrA3D`QIL`^33#N,F$V0FNRV(PQNRT$$PQRRFFF;Fu8L$^_^][QSUVWT$I~rXAy3D`IL`3~3#N,F

$<VF(,FFF;FuL$u_^][YEBEBEyuBEBPEIHuB
EBEuB@EBEBEBEW39~t2NFH~~~NxNPNF0PlFFHN
~,N$N0QhFF,_~W~HtX~uR~,FtF0PlFF,SN N;~HtN$GsN ;~HuF[_V~Wt*~FtN|N$N~(tF0P`FF(NNNN
N$_^>uN03tNF(xuNluN`uWN
uWN$3ruD$T$PNFfu3S\$t:UVt$W|$+$D,+,nBKu_^][|$tIS\$UVt$W|$AAi3)3#D$+,A.BL$
u_^][|$tVS\$UVt$W|$$i3i3i3#D$+,A.BL$ u_^][|$tRS\$UVt$W|$A)3i3i3#D$+,A.BL$ u_^][QSUV
$I<D$@x$044
xIt<-hF_\SXP.+.5lF)/)SXPHF=v$+FPXF,NTV$@Pl$^+^D$E]FH;r@+vN,V`RVTSEPF$QVtQKM^LT$Dp^][YS
+UW|$$3Tl\$G;w
/_][3;99,tN@p,40)D<@l$(XQlFpHTL_XRhFTpH
@@T$W;RT<Iu_][+T$;rD$+@;sD$ +;sT$;T$LILt}L$<@+THP$OQ PQUPQL$0SxL$+P|$$ECL$;L$t+L$+;
u3\l;/_][V,WuXPhFT?~W=v
+Q),uXRlFT_^3(PT8<@DHLA,ATAXA<A@ADAHALAPVW(/F,'G_^W|$v3_3D$DDUV$WN@3NDX~4~8u
NL_NPG~t8pt{,urT4tXPlFTHDy;pt LGdH;pu,NH _^]3D$SUxW{ @r_][Vt$ uDt0CLTD$V
QRP#u^_][S(h@EuSs,hPEx@t ^_][^_]3[SVWx33^^aN
^ANW$VGTFO`N$W(G OHWOW G0_$^[Vq,^Vw,WGtOH?GG@GWOO@G^rW+O RWIG
@GWG;GuG_SYUVqW9/GI$3%4;r%+::D$u+N_p^][D$_^][QSYUVqT$Q$W9O3G3%4\$;r?+D=:u3+HD$HL=:O
u_^][YD$;r+9:u+Np_^][YAVWy4I|ytSUhE>^:ZMu][A_^SUVFNW;T8Vu/FHNrRV+V
F(UF+_^][VN+SF(UK+\Xu+F_^][QSU-
hFVWT$$F;F39~0t.ND^t~0~8~4)NH!N@NDN\QlF^tNL~XNPV\R^XFtN+?FF@FVNN@FrV+N RV+F
FFN^)\$NDF&_^][YQSU-
hFVWT$$F;F39~0t.ND^t~0~8~4)NH!N@NDN\QlF^tNL~XNPV\R^XFtN+?FF@FVNN@FrV+N RV+F
FFHNr@N$3NV%F^)\$NDF_^][YQSUV3WT$]F;F9n0t.ND^tn0n8n4NHN@NDN\QlF^tNLnXNPaV\RhF^XFtN+
?FF@FVNN@FrV+N RV+F
FFHNr8H@n$3L~3N%3F^)\$NDF_^][YP!EBp"EB`"EB$EVxFHtCHt&~`EuEtA("EB&E^tEA("EB%E^tPEA(B$E
B#E^V(wUuDDDPL
TjNTD$wG^^V(wUuD$DDPL$
TjNTD$G^^3fAA>w5$*E2QA:ux<u
A<wyu :ArQA QA@AIQA@AI@<sSV AvF^[^YQ[)E)E)E*E(*EC*ESUlEVL(EW> |D(D$|$^(;u|$(t@t;M;|$
AMoG|$;\$cU+EN+D$$D$;r
D$Nmu3jN<D$$;vD$$ZT$ PF F(RPP~(~<uF+N8;wF<~8D$$ED$
)FFuBRBN<D$$N;vL$$D$,T$PRT$(D$,PRN<T$$UN()VT$ ~+)N^D$,uuu{uwusL$,>
8\T$,_^]3[D$,_^]3[L$,_^]3[T$,_^]3[uD$,_ ^][SUVW|$> T$[\$u
9l$tVtQ;@CuN:u T$+Fu-t6N;v;vF()F+FWFu9Ft_^][u;uvN;v)FuN;v)FNF(+u> _ ^]3[_^][_^][_^][_^][N_
^]3[D(SUVT$$T$<*WL$,lIF9F(uF(V(+T$0D$
;r
LDL L$<<T$HL$3
> |$$LDH DL@D$<)D$D$0~(N l$(+WQUL$8D$D8+l$(L$,*.&l$3I|$$LlT$H^(;u|$
t=t8;TALD$;\$$eW+N+3D$;rVR>u3Nj7D$;vD$V
L$V(PQRw~(~<uF+N8;wF<~8D$DD)FF>u@NPB)7D$N;vL$T$HRUl$D$PNUI7T$N()VT$~+)N^l$D$HtfuuL
$H> 133T$H3{3_^][$_^]3[$AAAAASUVW,3;u_^]3[N.F^~^h^l^p@ XX0Iu_^][SVWt< >tN<0KuO_^[aUVW
@tQF0Mu_^]SV38^ptVg<^pVh;tQ^h^l^[3(SUl$0VWEEv_^][
t$${S0T$@L$(|$4K8L$,CH;{4S<CLCDtCDSHIKLT$0t$<CPCXS@CTC\C`K 33;uD$@KT$C$S
D$@5ED$7ED$8ED$9ExxM|UuqugDuH_^][HpCdxK`|CXK\3SXUK\Mu [dt_^][SUVt$(D3WVLE;GtuEW
WGtT+C33CDK(;TDL$t1CH+KL;rw9D$rD$C@;uT$nT$$T$ VT$,F+D$T$VPFL$ G(QRT$
+VL$VuS(O(+O;w$O(t@sL$ T$$D$,3L$;t%t
t;tO(L$$TNT,NL$,;tQFV_^][uL$3;tD$,3OtOL$lT$cO(t`unFMnH"Ftl$;tD$+;vNVF_^][VWB9@99Pu_^t"V|;s7Q
FxF|u_^FxN|F N$FuRW0 _^SUl$BVW|$

X
L$(3_]]Fu:t
^uT$T$$t \$ uD$ D$3PD$$;DT(QRPyT$$;_N(M]uhDL(;u9\$
t<;t_^][;u2;t_^][;u3_^][QSUVWBT$$3D$1\1@1:T$(tu|$9|$u
_^]3[Yx||;u0tu
;;uu:F8;tlvN8WNPNPVT;VTu+t&jjjjku(_^] [YD$$T$_^]3[Y_^][YHSUVW3T$;t=D$
;FXN\VP^tD$@FTL$DT$HS(3D$L|L|$$T$3L$9L$ub9|$ t%T$S;D$$|$u3|$
L$9~`u2FlVhN4D$D$|$PT$(FdL$;t;tF`C$T${(+V;vD$+3D$FDt2FHVP+VLVTT$T3;T$Trw9D$rD$F@tD$T$,+L
$,L$4QL$(URT$$Q1nXL$,D$D$nXF\T$8F\C(+~P~P~T~T~D;t~L;~Twr~H;~Pw
D$(l$(;;u;|$49n@u
9l$(L$l$0;rC{(D$S N8+WK(S$+ ;uk(S(;T$9l$0N<;
VXl$@~\+l$Dl$<u(=@s!FPl$H+nTlLl<u=@FTPFPPWRNXV\FPL$@NTT$DLLLD$H3D$0(D$8;uUD$4u+
9n@t19nDt,VHFP;u
FLNT;t_^][H9n@u9l$(t
_^]3[H$_^][HVWFpuFpV0~tFP|uBVht
NlF;t(QVFlFhu_^VVl3_^(vVt$WxP0T$L$H43HH;HLHDt@D2pHRPLT$HPHXHTH\P@_^SUl$$VW|$0]3EGN
DT$;\$D$t/FHNPVT+NL3;rw;rD$F@tDD0l$,;u$D$0uNVhN4SD$0L$+T$L$
L$T$$RQNhT$$RT$,QRT$(NtT$3VXN\OL$
NPVTMl$+l$lluC;D$0_^][SUl$EVNW~#D$t+N~3;rw;vD$t+NDPL3N_VV^M][_^E]3[L$38WUl$VDD r;s
^]+D$2WD$PhL$QVmT$Su+D$H;s)=w"GGJh3u74[^3]T$rT$+C;OOt.ST.CD.SWl+D$.PQ)\$$L$,_WtD$0V;3L$1
|$[3[^3][^]38Gt8G@
]EML$A]EtGEWL$ fGWtV;t[^
]L$13[^]V.3F8F<F@FDFH^TSUVW|$,L$,.kDsHK,@lt;~|DlDXD$@$|XDu Ht$;~|$u
Hl$C8s0{4K,K<#l$t$|$
tt;rw;r #uC0K4.C8K<S,{4#s0uDTuC8C<uWS3wr=su=v3;w
r;s%C8K<~q#D$ tfk<{8L$ UWQVRVUWT$8;D$u;T$
tT$(39D$(w$r;t$stuDDC,DlTD$_^kD]S@CH[TQSUT$V3;u^]3[YWNu,n@nDNnHnLnPL$3NhnXn\V`^dF
p@Iu3L_|^][YSVLItOdAKu^|[SVW~p@ItFhVdP~XKu9tpLtNdAKuNdV`|ANd^`A_^[8SUVW|L_+DD~
_^][8}M1_^]3[8DTAXQ\8,3VI;v@(r^DS\$LU3Vt$\Wl$$l$(l$,l$0l$;tDdL(9/u!GKdV;u_^][DDhD$8;tD$4
@E;u9k`uKdC`;tu>SD$/;zL$QT$
g;bT$WGoofGC@SDl$lDlHlLD<T$@9l$htzCXK\#t;L$(rw;D$$r+D$$L$(\$XC@#t;r
w;v3UVJ0ShCdRPh T$@d3Lt$p+t$$#t$t3;rw;vD$ChKdT$lT$$PQh
Vd;`T$`#L$\L$D$;tD$,t$+t$C`QL$QP;uct$,l$T$0|$`t$,tT$d2GD$$OTxL(tt$0VQPu3tL$T$`3q
3#dGD$$OL$(;us9l$ht
TDtHT$3;ue;ua9l$huCL$p39L$$
9D$(9l$ttQT$`;tUL$d;D$r6_^][D$9l$L_^][D$_^][D$@3_^][D$L$\jT$dD$dt_^] [D$ S\$(UV|W3TD;u'Nd|;u
^E][L$!fD$@TRT<PL$$L$DQRUD$$PD$,WUVl$DlHlLlPL^][VpWtW+_
^;xv_^tWRQ)xt_3^SUl$Wt|$$t_][Vt$tGt@+u9WH9ui3D$JED$KEptx;uD$ txGhT$0D$(D$G@T$,;u;ODu
l;s^_][9|tO\WLGX|oju;t+TL
^_][D$0L$,PD$$RT$0QRUPVSGpW^_][LT$PJ0B@L$J8D$B4SL$4JD$0B<UjVD$DJW:JNL$4J+DPBD8B(D
$(BL$J,D$B
RlDt$X|$TLLD0\$(#\$$\$]=st$>Ft$;1+|$(f]u|$Lt.LuL8\$4\L$(#\$PL$T[4NL$D$(rl$+L$Ns}El$;s+f~+++fN
3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=s}El$;s+f<3+++f3C3=
s}El$;s+f<3+++f3C,.=s|$G|$;s)+f<.t$|$4>Ft$+++|$4f.t$M>FtL
+\$,LL;#l$8+L$4,)Ns|$?D$;s#+fN+++fN|$ #<~|$?l$$=slmD;s+|$3L$ f/,+++l$f}l|$$#\$
|$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$
f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-|$$#\$ |$$<^|$?=s\$D$;s+|$3L$ f+++\$f;l-
|$$#~4~=s|$G|$;s*+f>t$|$4L->Ft$

+++|$4ft$L->Ft$
+f]L$LM ++=st$>Ft$;s%+L$ftM |$+++fL} t}8=sL$ D$;+|$ft}8]sL$ D$;sR+t$f]L$,l$4;#\$8D$(++.Ft$L$=
+++f]+++ft}8L}P=stFt;s+L$<ft}Py+++fL}PL}h=st$Ft$;s+L$@ft}h+++t$@fL}hL$HtHt<t$@t$,t$<L$,>L$=s
LAL;#+|$$f4~~sLAL;s+fN+++f~
3=s|$?D$;s+f<3+++f3C<1=s\$D$;s+f1+++f<1A+++f>~=sL$AL$;+|$$f^t~~sLAL;s+fN+++f~
3=s|$?D$;s+f<3+++f3C<1=s\D;s+f1+++f<1A+++f~
$I
3=s|$?D$;s+f3+++f3Kr|L0prAt)=s|$G|$;s+fy+++fql$64=s}El$;s+f<+++f4C4=s}El$;s+f<+++f4C4=s}El$;s+f<++
+f4C4=s}El$;s+f<+++f4C4<=s]El$;s+f+++f<F@+IFI|$Dw=s\$D$;s
+|$Dfw+++\$DfsIu+l$=s
}E+tw#Iu|DOl=s]El$;s+f_+++fOO=smD$;s+f,O+++fOO=slmD;s+f,O+++fOO=slmD;s+f,O+++fOL$@|$L$
HL$<L$@L$,L$<NL$,
LLLuL$(;\$0L$d|$+;r;|$,#t$8L$(+t$,+\$0\$8+;w\$4,+)|$.ME;ul$4./FG;t$8u3Iu|$L$;L$dsSL$;L$hsItXlDl$4D$
`T$_^]P[LL$`y_^][Lt$0L$`|$=sGQ$T$A
D$(QT$,A(D$<y_Q0T$<qDA4DD^Q8T]A<Q@3[LQS^DtbsZNFUn0L$+W~;s~,uV+V(;wVV,N(+^Dt;#T$+:8
@Iu_F][YUlVW~,u'V(FN++;v<u ~sBlUWVu,9F,uF9F(rF,.9^s9ns ~Dr3_^]_^]SUVWL$(o@W
wGLOD$K#_(+HL$ D$s&D$(;D$r_^]3[@D$(D$;~D$,D$u(t=OuO_*OH#G(i\$sa$D$<HsD$(;D$K@D$(;s++L
rO_0;sG3+IlD #DEDsD(;D$(@D$(D$;s
3|$ ++L rDD(s
;DbD_^][|$Lo ++L$L$(s;L$-AL$(D$;sD$ D$D++Do8D$s;L$9AL$(D$;sg|$ xD$(s;D$@D$(;s
;D$W_^][|$(++LhPL$s;|$%G|$(L$;s?Dhh++s;|$G|$(;s++|L(D$
s;L$)AL$(D$(;s_3D$^o++s;D$N@D$(;s_l$++D$s;D$@D$({;s++|?D$;sD$(+|$
0rD$(ol$\}sD$(;D$n@D$(;s++|?@r@IL$sL$yD|$s;@+K#+IuLD(DLL$is"D$9D$(D$(8@D$(;s++L$u3ADAL
;t
A(A,AD9D$tAD3AALA(A,ADD$SUD$$VWT$\$9nDt.$NLsD$
@T1PFLD$ D$$Ku\$FLt
~P^NQVRFSVT~DN$F FLvLN~tfF<F8F4F0F@FDT$9nDu)~T,_^]3[I\$L$3D$9Nr/FDu
9F$=|$(HVDD~Lupru
|$ D;,|$ W3|$t
|$ \$P~IFL$$+)D$|$ 3s;sT$ *L7PGEr~Lrt%VPR|$t \$FPFPuyF+P;+;D$$+(l$
)l$FL~D{D$,_^]3[L$,_^]3[T$,_^][SFPWPD$0L$8^L_^]3[D$$(D$,_^]3[L$,_^][_^][(D$,SUVL$@W9|$$3T$08T$(
9N9Nu~F+D$4;v
L$|$,DDHD$,|$L\$@l$$3~D\$lL;v"FLsT0PFLCAMu\$LlFLt~Pt
sFQNRVSNT~DF$F FLvRNVt f|$LF<F8F4F0F@FDT$~Du%~$$\$l$F3L$;D$r.FDu 9N$=9L$,;;D$ L$ FLufr
ul+$S[|$ t ^\$UL$F+D$)DL3sI;l$s+T7PGEr~Lrt%FPP|$ t
\$FPFPF+P;+;+ll)l$FL~D|$L`XMUNPSQ*l$nL.T$L1DL3LDDD$4L$@)L$$~Nl$0+WQUD$H)|$48l$0ut9\
$(tT$(3q_^][(_^]3[(VV@F^VFWWWGFG_^|$sBVrrr=sAr ^ ^AQ3D$VPL$ludS\$WVt;~Ht0Ul$ EFE?F]t
F~HL$_N3[^_[^D$VPLDUVW|$t;nHt*GFT-Ft$
FnHS\$@r?_]^@r,#;sVt;nt=GFFu VG[_]F^L$^[_n3]^3Q$QA0A4A,A AAAAAAQ(V0yx0u*
L63JPP3;H4rFw;P0v?P0W;v;vAvP_x}@x}@x}@x}3Hx}3J@Px^}39HHx }@
x$uP39HP$x,}xtxuH,8VW|LD$_^8SVWfItVWSGr_^[H,(VW$(P8,H<0P@4HD8<<\@`DdHhLlPpTtXx\|`dhlptx|
` <`!#@HPR$QR_^(H,VW$(,P80H<4P@8HD<\<`@dDhHlLpPtTxX|\`dhlptx|`<
`!#@$RPPQR_^8SVW|$L$2t$T$L$D$==`D$$s=vC039D$ D$(utD$,}}D$4T$03|$8_^3[8_^[8D$T$~0u*V
N$W~+W;tF0 ~(N NV,_SVVW~
lF3Fr;uF_F^[NF@~F;FuhVVt1$F@F;Fu=~F~Fu_^[SUl$V7LAAD##+GAW%+#T$fs
r7^][SUV7DL#LTJ3lT#*D$##+GAW%+#T$fs
2i7^][VW$3q$=rd$A=sNu+r_^3S$W3s[SUV3#\$k33#r^][QUl$V3WD$TELL##+CGS%+#LlfDMs
TLD$u_3^]YL$SUVW/|$+wW+fs'AY|$rF+wW+fsLDPQ/p_^][l$+fs

DlCD$##+GEW%#L$+fCs
@ElDCD$##+GEW#L$%+L$fKs
T$DUCl$D$##+GEW%+#L$fKs
w7_^][U3VLC3k%3DsL$,t$5,Dr^]L$S\$UWi@DDPD$PKFT$,
RQVL$,T$lDD;vFK3IN3sTLC;\$r_][SVFF4F(FH L$u^[W@ ;~0t_^[Un(vVH9+D 4)];t:u@;u_^+[WyTu,|$y
5HH_HVyluH^_y+3HH^_SUVD$W{,x00,*+t5 O +u) O Bt O D$)L$y_^][hSUl$tV3uu9u4uD$ PD$M U$LT
](\$$s^][hv
\$$MEHHt$0t$Lxt4W$<T/8+T<XT$|:uCP:Qu;;vp+:1\$(uBF;rt$L$8T<h;Thvt$4|$8D<hFtrT4|hM0|$D;r#MEW
U4W_^][ht$;r1L$$D MFE4EV_^][h+L$X
Ls8L$|ts_^][hfU,$uL],#f(@#EHMPt$(@<ArD$L$|WHT$|Hdt$(MHD3fJPMH]<HD$<D$|t$@8D$uGM
]THH;DsD3PfJL$ D$D;rL$D$sP_^][hT$XL$\4T$`<8L$d@L*$@H s3D$thr^|$(E,\$W4iD$@y|$)0;QsQ3q9fQN
sD$@D$r\$hrC;\$ E,E<H|$<3L$||$D;H v$;H wL$|D$(i4D$(*0T$(L D$ Crs9/,GL$|D$;PsP3X0fP;H u
L$|;L$$tD$(|$DC JD$9D$i|D8PD$P;E0\.,+u40((uG|$G|$*0ft+|$u2"*(s
GG|$(4($sPuHT$XP@L$\T$`D$dJT$\PuHT$XP*T$`TDT$XDdLXHT$\PT$dL$`D$XL$\T$`4D$df(8<@MU$X
D$|+D$XT$,D$@ELL$$A#D$0D$<EHD$,$D$$K#EHL$|@EPP\$
D$(T$DrD$RHt|$@D$4T$DJ3D$4HD$4sL$$L$$;Ds(D3LPfJD$(T$<U}<T$|5HD$,HDTLH8T$usLrPtafJwWD
$<E
}THH;Dw%D33LPfJD$(u(+D$t$4;vD$4}0t$,;v|$,|$(S8T$I@CT$@:B0K:J$G;vt$|;vB+:uF@;rt$|DGA#MLE
E5D$DH|$DHU<UTH|HTD$$;s..tD+|$|$D N@ut$|iNy;*$*$s
rBfBD$|L$|D$@\$ +DXD$<:S:Ps|$,;vT$ H+
:uFA;rLTD$D;s +)$T$ H@uT$0D$\$|RL$0D$HiLLyLDD$$D$|)0T$$;QsQ3yfQO suFD$@M0T$4F;v;s
\$<T$ <+::u@G;r+D$([T$<T$ <T2$#]L#UH|$DHRUPJQLHTGML$LHyL$$C#ELE
E<HHFH^HT$L$$L$(L$;s%/$+T$I O@uT$(iJyD$$)T(;sD$|FQfqAL$|AL$|WT$PL$,;v&3;H v;H wH
D$8D$@;kL$M<|$t$H\$T:\$P;s(+*t I@u3L$,;H v;H wL$,)L L$@T$(.,NL$<$L$$L$0ip4(0L$0
F|$DrsD$<L$|;OsONOB3fOL$@F;)H \$ M0+T$4H;v;s+\$T\$T:uGB;rN+|$@Y$L$
\2TD#]LT$TT1T$$#UHHRUPJQLXTGML$|HC#ELE EHHDH\HL$|L$@LL$9L$s.\$*$+\$
J@uT$@iJyD$|;)$)$sQT$(fAQD$,D$,;D$8tU|DD@(L =D$(T$((,HL$<L$0D$$\$P
FtD$@D$;DDU_^][hT$8E UU$_^][hS39^4uDPIDN VLT$F(D$s
[=vD$FN8UH\$\$LWT+
:uAP:Qu99|$vP+I
:*uGB;|$r;~0sO;|$v\$ |DCrl$;n0rLL$D EF4FU_][FWV4W_][3\$rcT$D L$v0<
HA;u|$;vML$wl$u|$rDl\$|$rHW;sG;rsO;r+r#T$ GF4FW_][|$~$W`F rHD ;r;M;u;;@;rr
;VN8HD$ LT+
:u2P:Qu*]P+d$;sM
:*uGB\$lL DAL
rtFUV4U_][_][SF,UFW~X+N`Vd+fsFX(V,V<V<+fsFXF,GSj$RFXN,~X\~Xd$3!+N`Vd+f3!sFXC@rI~`VdsFXT
Ku
$I3+N`Vd+f3sFXCr~X_][u99t
t
t
SVtGL#D$
wXIiKu!^[u99t
t
t
SUVWd$w\$3l$3HH\$\$\$3H\$XHYH1w3\$l$H3H\$\$\$3H\$XHYHq\$3l$3HH\$\$\$3H\$XHYHwQ3\$l$3HH\$\
$\$3H\$XHYHqW_^][SUVWt$I2,ID$B#|$+\$,{3\$HI\$<xut$\,Ft$rL$0+$!\$DLD$3l$(t$ F
D$$4G3HG3HG3HG_33HHD$$G\$Ht$ 5,Hl$(Dt$;D$;v4A;rHKHKKH$
,4l$l((l$,(hrDlL$\$D$S_^][8SVW3X~|~xfHT$PD$|$0|D<D$@D$8D$,D$(D$$D$

DDD$|$4h,H~P$_^[8V@t I^S\$VWXfVPG$G3^P$VxG^xG_^[Ul$ tME9t
t
t]SVW\$,\$MUuS_^[]D$,PiuX}X+fs
17MU+E4MPPQM4C\$,MUt
D$9M9MuD$,PUD$ *$P@AT$M}L#|$,M,uXM
|$+f:=s
EXTMUuX+E4L$,P#UHMP}XRQM,Gu,sPR$+E8QR+E`Ud+f
sEXM,M<
+E`Ud+f
sEXM,MT
+f=s
EX>U,U
U |$t+M`Ud+f+f
U,GE,+E`Ud+f
sEXM,MlMlu+f
E<}+M`Ud+fsEX\U,UUu+f
E@+M`Ud+fU@EDUDM<M@U8U<E8sEX|uXDWP(VEXSuxuWxHQU,GE,+f:=s
EXgM,D$uXGL$QPD$($VEXU,uD4uT$R4HQ*U@E<M8UDSE@M<U8s(,*,HuXD$ L$@|$L$r(!L$ DL$
<QQT$$D$##+E`AUd%+#T$$fsEX|$ruXT$Hs#+SPU#P]X
+$#M`UdsEXu##+E`GT$$Ud#T$$%+fsEX"GDED##+E`GUd#L$%+L$fMsEXT$WD$ED$##+E`GUd#L$%+L
$fMsEXBT$<W}##+E`CUd%+#f}sEXuXDD,)E4u#rrMEt:D$,+D$|$0tT,;D$4s +MxUhMp
;D$0YD$,+L$P_^[]=%D$,+D$_^[]QU3WD$9nxuFx;t(Ft9t#9uut
_]Y3VPS;t9$t9t\G$GnP$FPVP3$;;D$v;s+D$tdD$N0PD$hQPv>vr3Vv}[_3]YL$F0QL$hPXQ[t2V>k[_3]YG$GnP
$[_]YQS3Z,UVWzDz@z<z8BxBpBXZ\Z`ZdZhZl $(,048T D$
$ff(+ufqf1fqfq0)|$u!#@zP;vf$&(*_HBLZZZ4+^]BH[YSUVWuG0HxL$34t$U4PHE;r3tHDLDSQ(C;r_^][VW
;v4;v;v;v;v rB
LTQD$3R
;u3_^ILLQLTRQwtsVFW|$;s FFWRP)~~_^VF+F4^D$S\$$VW3DPEDL$|$
;tNxDD0TTFX~\~`~d~h~lNpV|;t*Ul$8MQPV+T$L$ U)]9|$ t_^[WjjVuWuNt3Fp+FxFhVlRPPQujjVt
tq_DVLV|T$QRjuS\$E[^VL$9Ws
_^S[=@r=sA%5d$;v;vAvGGOW_3^S\$Ul$VW#3333tl$+qyiYl$\$ #tl$+AQiYIRPWV_^][ESUl$V7t(DPEtL$+u
tu3^][
D$$SUV0N0F03Wl$,D$&39^8\$\$$D$;~\$tx\$4{uN
Cux8NCQ|l3D$,t~<=WD$3;T$FV+;vD$FL$,3;D$$L$N43\$ %tD$;u{D$@;ru]ruL9D$uN;sG9nu
N;L$,t9@u@D$F8L$(%D$ ujWhFWlF~t L$4AD$$TL~RT8RQN(PSt
DT@88XPPhFnHD$;u FHD0LPlF;l$t9|$FDtu(t#F$N(@t~D
E;r3PhFt\.LtD.LnHPlFN4$uNt&Tm8utX@D$N|$uFuBL$ 3l$,#_^][L$K#_^][3_^][SVW|$O"uULF$uMW7t
P;uN@"uOl"t_^[_^3[V3FFNNNNNN N$N(N,N0N43$PpHHHB@rN@X# ^L#Vt$W@>tNF!!m!Ne!VtFH
AFOut$N0:!N42!N@*!_^VWVv=`FP V_^V~WNt~!!u(NF9uVE uN2!u_^~
%
_^SUVF@WD$;v\$LL$=sAL$=sAL$=@sAL$LvD$L3;F,t%d$;t
N A/KuN,Vn<t9n@N@t ;t
_^][9n0N0tb ;u9n4tN4u\$SN4 u@KrjLFLUPn8nHnDT$ O9ot ;u%Oo9)uWE5;uOi;t~
%
;uLN0MN@;~
%
;u~<;u;u~D;tV_^][3AAAAAVFu#tFVPFRVPRtF
F^VW~WhFVDLD$VVNVT$NNFNu#tVFRVPFRPtF

vWlF_^VW~WhFvWlF_^SVW~WhFFu^WlF_^[WWtVG2HQu^_VF\uNu^F\^WW\tG`Od;u
OQG\SV3GX;w;r++9wKF(r^[uO`Gd;u3__SUVW<Et]WNQOG_uEHttNXB;u
<Gt GO_3;rw;vw+wWWEG_^][F`Nd+tV\M3_N`Nd^]D[EV\t
NQF_^]3[V3qqq\q`qdqqqqq 3PH0pppB( r^VV`F^SVW F$"(KuV\t
NQF_^[V^0VWF~tN NTNLN _^BSUVWF0FD3F~x~h
~|~4~8~<~@~p~l~H~L~P~`~dv N FT~X;t2 9{t_(MuV\;t NQ~\N VFX~$~(T@FX,{;u4M
;u&M;uU;t!~Ds;~;t^@9~8u
9~@u9~<u~DFD_^#][VN uN u^9tfVW
,(Ou_^UVuFIVFe^]VW|$S?G(tCuw(G$~_^SUVL$I3Wt$<t$@t$ttH;C$3;K4C0UVWC0WK4WT$8Wtplt
Kp;D$,L$|$D|$p|l|$L|$|$9|$$T$sd|$0BD$(C`+t$49|$(u+K;D$(L$08;tL$AT$(;T$8t!K\S`t$|tLD$ lL$
QKD$$?3TD,D$
TTTT0;uD$LShShSl3Sl;;L$tSpL$,;tKHSpT$|$,L$T$<|$@+T$$urGD$\L$XP3QWWWT$8WI;D$,v9|$$MD$
TD@D$ T$<3T$03;T$$$T$$$CRQK$$$$DpDHtq$;t+|$8$K`t$43K`;u}9|$;D$
u`D$(;D$0D$(3{`t$4{d;D$$tL$ |$83{`{dD$,1uD$L$ D$D;u{`{dug;{`{dt'T$8+4(<L$
$3t$(+T$tT$$+L$0;t$TDL~;t
D$|$b;uDDlVL$
t|$++Kdt$s`|$4*$39|$0uD$L|$9|$,uL$\D$XQT$(PkD$,D$,|$(;u69|$Dt*9|$$u*L$CQKP;D$(t|DD9|$$tDD|$
;|$0ulCXKT;s49|$Ht.Ta;uCXKXuL$|$DD$01KTD$kXHA;#D$
Hi;D$,33;T$<T$@T$xT$|T$PlTT`TdpDDdD$(XL$IL$8tt$LLD3+3;rw;vL$;T$PlTD4u;l$t3T$4{RLdT
TQL$$RPD$HQKVPWD$(D$4T$PDlT;u
;l$|$<t$@+|$xt$`+|$d|$|wrs
wrIl$\T$$Ul$\UWVPQD$,uJD$$uBTPlTD$`L$dT$<l$@DxL|D$83L$8T$\D$XRPQL$I
e3;PC$|$p;lHL429kPtL$$FD$(;t
CLKPtDlT0;tKPS@3;tKPK839$tKPK<39l$Dt]9l$$t3333LPDTt$`T$dD$TL$PT$dt$`l$<+l$@l$x+l$|RVPQA
3t$,L$D$p3;;T$htp;u;t
9l$$u3L$4T$hsQRT|QKRPD$0PV;tD$HCP9l$hu9l$uL$\T$XQR9l$4tm9l$Dt#;u9kPu9l$(u9l$$u9u9l$HtD9ut$
FtDL$u9l$u1T$LFVNt$9luD
H ;uh29l$ht;uQ9ntHC$;u4Nl$<l$@ltl$H ;_^][3_^][VuFNt3^VEu^~
%
^SUVW|$7t*D$PtUL+u
tu3_^][SVWWhFt%D$;rwL$;v3WlF_^[SVWWhFDLDTu6t,RPRPt

t%L$$;rwT$;v3WlF_^[VWWhFLDt;w r;sWlF_^3g jAgArn<A:OARQAhAA[A
AD`SUV3P0Wrt0rt42t8rt<rP0xXHTPTP@Dt(|$ \$3|$ \$4t63|$h3l$T3|$h
3|$ \$033l$3#3l$|$G\$0#|$|$
33#\$\$\$t6|$833|$l\$X3|$l
3l$4|$333l$ #|$3G\$4|$|$|$
33##llt6|$<33|$0\$\3|$0
3l$8|$33l$ 3l$#3l$ |$G\$8
33l$#t$#+T$,t6t$@33t$4\$`3t$4
3l$<33t$3\$#3t$(,G\$ l$<\$
33#l$#l$ t$(<+t:t$D33t$8\$d3t$8
3t$(l$@33l$3#3l$t$G\$@#tt
33#\$\$\$t6t$H33t$<\$h3t$<
3lDt333t$(#t$3Gt$\$Dttt$
33##l$+t6t$L33t$@\$l3t$@
3lHt33t$3t$(#t$3t$(G\HL,
33t$##+L$$t6t$P33t$D\$03t$D

3lLt,33l$3l$#3lt(G\$L#t$(
33#\$\$(t6L$H3
3L$T\$43L$T3l$P33L$3L$,#3LGL\$PLL
33L$$##llt6L$L3
3L$X\$83L$X3l$TL$33l$,3#3l$,L$G\$TLL
3#L$$3l$#L$TLt6L$P3
3L$\\$<3L$\3l$X333L$#3GL$,\$XL$$L$,L$
33T$##l$,+t6L$`33L$T\$@3L$T
3l$\L$$33L$3L$l$#L$$#3L$G\$\
33\$#\$L$
t6|$d33|$X\$D3|$X
3l$`33|$3l$$#3|$G\$`#|$|$
33#\$\$\$t6|$\3
3|$h\$H3|$h3l$d|$33l$$3#3l$$|$G\$d|$|$|$
33##llt6L$`3
3L$l\$L3L$l3l$hL$33l$3#L$$3G\$hL$$L$
33#l$ #l$$+t6|$d3
3|$0\$P3|$03l$l33\$3\$l$#3\$#<G|lt
33\$#\$t$(@D$t8pHL$8HHL$_HHL$pHHHHL$^HHL$]H[`QSUl$VC ?k
W@S$+;vUD(VPh_^][YWL(VQ+hSt$@r,DD@{(S@L$t$utUV(Suh_^][YSUVW~ ?D7(G38tI?uVE\>(G8uF
N$VN`Fd3LUMr_^ ^$g jFgFrn<F:OFRQFhFF[^][Vt"PFuFu^3^jQFVDPjL$QRjji^t3FuP4Ft3FuPFt3Fu3VjPjj
F^t3FuVjjjj F^t3FuD$VjPRjF^t3FujjP(Ft3FuUjhGhVBFdPd%SVWeEQ,FE3Md
_^[]eEMd
_^[]TVtW38@ud8_^3AAfVV@3FF^u3IHtt
uAAAVHWt:tu@D$PN_^NVQRT$f`_FV^D$NP^F_^Htutt3ASYZZZZZZZ[AQSUVt$3WL$.nD$ wT$39l$v-ID$e
VGy;|$r_^]3[Yuu_^][Y@AVAu|$u,@A3^T$uDw1$0Et%^t^t ^Q3^u3^IEEEEE3yAAAu 5VNW|E~|$VWN
Pt$~_^$~_^NQNVRT$Q~_^VNRT$Q~_^VNRT$Q
~_^VNRT$Q~_^VNRT$Qi~_^VNRT$Q~_^|$~_^EEEEE4EQED$S\$U+VTT,L$WI~;t-+;vL$W0 PQa>;|$
+)FNQ0 R SkbD$@+~F;vD$ NWP1 R`D$08|$,)|$~Ft)PFu9D$(tFF\$5_N;^][u39$u9D$tT$
3D$S\$VWw\rwUuAL$
Au_^[FEFEFPEFEFpEx_X^3[_^[VWtOt%F_^|$tytD$PtG7HHV~tLDt7S\$W~(PDQLPQIV(L$$+T$
_[^PDQLPD$QPpML$T$^SVGwxt
FKuW^[tAGL,SW|$@3D$T$ 9^tF_[$Un
l$E9^L$HUD$8D$PPDHQTPT$PRPL$HQNx3T$D3}^T$4VFL$@u
9\$HutFNuD$8]_[$~tH;v9IVPG|$@u|$<tL$9NuFE;nrT$8]_[$~t19^u F;u]_[$\$Ll$33\$D$
\$<E9~hL,l$(uL$DT$$+D$@L$HT$T,\8L
GF+\$VJ;ul$8\$+]T$4l$(\$T0;T<VD$\ht|$<\$<\$,StPQLQMPD$$PE3;tDh9l$<uD$<T$,3;u~l$ u
;uDT;uDDT@T,FH;DuL8D$4D<l0L$;u;tD$G;~|${|$
tlED<]_[$VVFWNfffkU;Ft_^f;_^FN3W_9D$u>9T$u8U9u*NV fff9L$u~
Yu~Zu3Wz|!Lunyuh9Au^y(wXS3VtFJ$qu u9Aur$w r wQtu
;r^[_^[3_3_SU+W|$2T;+t_][CVGG@tBWUK<Op,3G=rGG@tP+G@D$D$$|dT$R+LD$ P+1T$$3+;wu|$
r;wkwgrw`D$WQPxXD$ D$ @D$;D$u|$G<t
^_][;sFu;r^_]3[^_][L$SYUVWC3;_uUG;t
9l$$u
;uD9l$$u>33;v6GHL)d$(;)uh;iu
B ;rL$ 3D$$T$(GW; /l$;D$ WHLTLIIJ!u;t9l$RQPN3dbF;u;x;uEFFpEEFEFEnh hL$$ht;tH L$(@tH$3;DTl$
@DT;sL$?_^][_G|!u9luGxHtt$$;t;t;u;t;tp P$3;l$;v9D$ wxlUFQRufD$@
D$;r33o<o0o$o@o4o(oDo8o,9Gv!_hwxVE;or3GGGG

3_^][3AAHALAPATAXA\A@ADD$3QhAlApAxAt(AAHALAPATAXA\A@ADWyhg_VW3P~0~4~(~,~_^D$L
$ WPQL$9T$4D$0RPL<$WPT$~0_F4F(F, \D$`SUVWL$(L$xT$
$T$$?1D$xT$$+Lpt(+1D$|$D$@L$D#3tTt<+GO3;rw;rDDuH;O uL;W$zHL#t+G O$3;rw;r
\$DDLl PDxQTRPSL$$Q#Rwh$t
t$V5ul$ DLxDtGDpO0w O$;o$8kfOo _$33GGOQPL$4
SUL44VPT$0[w0$G4G(GGG,@D#t;G;OHL#t;G ;ODxl$$+u
?$`EGsDsL$t:GL$t8D$xGAL$tQO3;GHPw0wLw4w(w,w
G3;D$t@GD$tD$xum9W,G(RPm@3G0O<O4PGG8jPOQJG`oG]w;t5+;v\$tV8SQqPw$0\$t!@;$WoX____
$9RPVGhe;_^][\OOt4T$x@D$tG@D$tc^][\fO[G+t?;vl$tV8URUOw$0l$t;_T$,GtYt$,r;cst,:GvQ:V6vA:F%{$_^
]3[\O;Os'Txtt@G8@FGt$t:w<G4W8O0;wPr;sJ+3;rw;vt$tO`U<HUPG`D$x(o8t$tW<3tCD$tD$tG`33GG`D$xW
8w<W0w4O0PT$,G4w4L$,
+I4;0s5sLx@D$tW:G@D$tW`;GG+;v\$tV8SPMw$0\$tGPO4W0GTG@GDGQRPt_^][\L$t9tG@G@T$x@D$t
G@WD$_^][\@;WuOD;WuD8u?_^][\$_^]3[_^][_^]3[\$_^]3[\$_^]3[_^][\3EnEEEkEEE=E9uyu3AAAAA@SUV
W3;u_^]3[^F~n~t~x~|~0FF(F~ F$F,@
xTx8Iu_^][UV
ItOAFMu^o0]S39tW_Vt;tNA^tV|^x;tNA^|[QSU3VW9t4St;tKAktS|kx;tKAk|9tdD$ 9ot>owttWFMut
OA/G3L$u
;t
KA.nOuK_k0A^][SiUl$V3WM:
L$$]]]]E9]h^^^^0^4^,^(^$^^ V@VDVHVLVPVT9^XuN`L$$FXC^`^d$ 09tPffNpF8L$$F`^d$
^89^,T$RPEPET$RPT$(L$$RN`3D$,L$N$L$)L$$F(MNNN;q9`y0@t,(PQSRjH;rw;G,v_^E][fNpTD[L$VtVVL
$$VFFpfF<fF,F+F\$L$$;rw;wD$$EFV;V+MU8EO0F;s(@;w
r9G,s]9_DtbOH+GL;wr;NsF4EF&9]t}_F0^E][9]tcEMVN0U]_^][iVW::
tF;FsDOAFVuOu_^FF;G0vG0F(,VRPQF(t tt3_^i

S\$$UV

T$03W|$,oL$N kVF;s.+T$$l$;rw;vT$)T$$V9F399.9n(V+0L$$D$ PD$$L$0L$,jQT$<T$8RPL$DQN`3T$
L$,NNV$0V F(oN k;uT$,;T$$uD$4(_^]3[L$9i t"~(tVoF_^k]3[F(_^][iSUWL$038
D$4)T$9l$$99+9SHL(C D$4L$$9u>8LfK<fVKSUQURCt$8;t?9o
t+PD$,UPCKL$89uS$Gp;l$9oDtOH+OPGLGT;rw;vL$8vO8T$(VGPoT;u't$()t$8t'UUUUhqD$;ut$8 GpD$9k(t9o
uL$^_][;u'C9C9C{0tT$43^_][HLT$0ptHT$8l$9k4t^_]3[@L$(QL$0PRD$DPQT$,R3\$8HL$(LHLpt;?9uRT$4$;tat
$0+\$,VSP?4^0_]3[|$(9@L$Tt;u^_][33L$0;uJ9@uBO4D$0T$0P3L$08<;t;t
@+L$,L$(QjD$43RPQT$LR3lPxL,t$(HHLLpt;9tg9t=+L$TT
u@r'hl;HLDL9@vL$0t$T$4D$0^04_]3[^_]@[L$^_]3[9}_]3[3AAAAAAA
A$AA(A,A0A4A84U3W9l$@tD$@Wt;tGx;Gt/OAOWoxGt;u
_]4OOxW|;t;Gt(OAOWG|;tOHPTWXOPG\ST$(WTL$033VD$0T$8LllIL;ui9l$Ht#T$3D$;u3l$HL$9ohu;O4
W|DDP3TLO`Glod;t;tGhw+t$9oDt*GH+GPWLWTT$@3;T$@rw;r9W@t+T$3;LD(PUD$$t$,RPQT$8RW
tT$,\L$$TD(DGXTW\OPWTul$$;r(t$O8RWtGPOT;D$L$<trunwXO\+T$,D$0w%r@sGP+D$4WTT$8T$
u=@r3GTWPPRQO<VuMOXW\GPL$,OTT$0D$4L$8|$$u83D$$ET$L^[_B8]4D$L@8
h0^[_]4|$(SAD$ Ul$ AiBHABLABXA
B\A$VfAW33ur@zDur3r@zDD$0t$|$u8|$4u3t$t$uB@tE>|$8t|$t0)u$;u9|$,tu9l$ u
T$$;T$,tA3+l$T$,T$_^i]A(Q[S3UVW|$D_____ _$__(_,_0_4_8FJNBF
JN$BF(JD$<T$@F8D$4N,L$LV4N<^H^L^D;tFDVH@FLL$8N@^p^P^T^X^\^`^d^h9u*T$H]]]H]L]P]T]X]\]@]
D3~$D9uL$LT$@D$ F(VDHD$ ED$$ED$(ED$,0EHOLWHLNXV\@Nl8Vh<N`D$LVd;u&l;tD$LG0G8
9FPGNTO3;|i0
0
h`@$9RLPHQ<R8PF@QRP9\$Lu^Fp;tD$LG8 ;t/H;8u!L;<uD$LG,G8|;tt$LD$Le_^]@[ D$HWQgD$@F`D$

FdD$$L$LNXT$HV_OWEHGELGEXG
E\G$fGE\$\$uE@D$ED;uED$E@\$DEDDDDDDD$H;u)|$Lu}\$\$uE@t92+u&l$;u;T$tl$u9L$
u9T$$tG3+L$G(T$D$@OW;u1Nl;tVX;V`uV\;VduG,G8;tG49_8uG8_^][SVFD$ @tFPQL$XFtVFRPLF@tHUW
NVQRLjPL>WNQTR4 Mu_]tIDCuLD.DST+^ [Ul$WuG_]FtNPQWx3VtC~_n3]L$
SVWD$DPQL$T$<D$8RPL<L$4T$0QRjP4N~3+;wr;w_^3[
33;u;t_^[\$HQ%_^[L$ VWD$8PQL$T$0D$,RPL<NF+;v!D ;s
_^ PuNNWD$PQD2~3_^ ,SVjPL$pVTD,DVT+ SL$UkUT$+TDU+
L$33s\$tfTT$+V+D$3D$D$$D$<T$(L$$D$)n+D$ VfD$.YZ+
]^[,QD$SUVNW8F#3|$t+FN3;rw;v|$t[NtD$P|$3N
VVWRS0WN(DFL_V^][YT$:_^][YVNWtD$P~_V^FN|$+;v_3^WRVR 0~_V^S3Ul$9^ uEF
;u]C[^(^$^,OUQRNuGNUPFWu
NV3][S\$U+VT$u^]3[W~,u2F$;F(~(u'NV F$WuO9uF,N(+N$+F,T$RV
V$jPFL$(L$(QNRT$$SLN;utt_^][3A`AhAlQpQtQxA|APATAXA\1`UVW|$|$dCxDdL1st{xKH|$@~@D$dD
$ClKHKht$;rw;r #uK0C4j1#KlChQPUW.RPD$ClPChPT$(~/;u;tVLTL%|$duH@V@L95H-
L$39D$w,r;s&uDLCH_st^KpCx]`D$L$_st^KpCx]`PSSlUVsh#;u4{HK0S4CpCt8Cx^]CH[PWC4K8#K<S0;u){
HkhklCpCtSx_^]SH[P|$|$XCxDXL/TPDHL$LSpT$TStS8S<ChKluC8K<9K4rw9C0v#;tC0K4_^]C@CD[PK0
;wr;w#;uC4C8K0K<uk8k<,_^][P;rw;v#;ts8S<
_^][PSU$V3P3W\$9]u3$$9u+$$0;u_^][8$2;t;$;t\E$T$$$uE$$9]tM$$4!^F1$$FDFDT
L$$\$(\$,;tTR;$V`L$XD$0FD$@D$D\$H\$L\$P$$VhL$8NlD$4T$@L$D;uT@\$D;t*D0Gw|$Q;$3\$;uV;u
RRL$\3$D$H\$L9$uL$$|$ L$;t;t$D03R#$Q#$VPD$
#RL,#W5L$(;tL$\$,L$(;#T$HDLLL,UL$$lLXEh($fJ`h<$lW+LT+ L$(T$,+MU9$u}$MUD$
$@$D$$L$$TPD$(\$,\$0$$Q;u)D$(EL$,EM9tTP$3_^][8T$H$;$u9\$Lt_^][8DATQSVt$qW|$yIWVRP_^[
V3WN@pFpp0x4Iu
L3_
^SV
LIt
AKu^
[QUV3W;t
GD$@;t F.V;t
GnN;t,n4L$u9tU
Ld$;t A.Ou_^
]YUT$W!L$3;u_3]SVqGhopot_x_|OoXo\o`od(GGLT^[W_]SA[VAW"_3^DT4D$8SUV
3@WT$oo/oo;u*
;u
_^E][4L$H
L$FPQT$ T$4RD$TPD$`L$$QL$`VR
PQURPD$<k4SU0Ql$TlXl\l$`n;u)T$,L$0T$4OL$8WT$<OL$@WO_^][4SUV
RWV+ uFw+wwV+ uOWGQRPQ_^][`SU3V]HslW{hT$#\$ DD,L$0#VWQP"RPVWD$<T$@q$;u;t
D$,T$0D$MQ

PQL$4RT$<VWRQ$3;C`Gf
GT$4fL$8T$:WL$:D$;gL$D$<jT$8 J9CpU9u.K`T$E"L$xT$t3D$,ChKl#D$, FD$00FCh3;Chu;Klt_^][`3@
PL$x;r9
t7L$x
StuC`|$PD$_^][`_^][`t$xCl|$|$|$H|$L|$P|$T|$x{h#D$@Ft$D39K|L$x3;;
LP3L$;w;9
t;
t=-|$M

u_^][`L$

DLDxL@##D$t$$D$,d$|xDht
3URURT$`RT$4RT$4R$SjjRT$@RT$@RQPPxD$`3|$d|$x|$8tJL$\$h
S+ 8LT+W+ |$8D$`L$dT$\]QPD$`RPD$$LXLHT$\\$ TLtP|$T9D$(D$,GS3Wf
GfLSL\$\$\$\$ D$f\$qDjT u
SD$_[VjjT$u9D$;Gu(
G:L$uG:T$ uT$f^^SVW$T$u\D$u!$_^3[$HPTuT_^[SUiV333Wt9yw;rw;rC;r_^][_^][UlVrt>u;.t
^]D$PUNtt$SX3;;D$6;T$$W|L(w3G;u
_[^A]L$$;v`L$(d$|wt(L$
~W+tdD$ V+tMOtDD$$D$(@D$$;D$rtIT$ Cuu+_[^]_[^][^]LUlWu|$v
]3;u;uEVu F^]L$SjWuL$UWVQ'E[^_]D$V0Wx3u U$,WV<jT$|$YSu|ZDuv+WVuDST$,Ku4\$tD$(|uKuu%ur
|$v[]_^uwrrWVbujT$u|$Yu|$ZuD$L$$,ffffv[]_^L$~9D$\$3;w;$0+WV"$8PU$8S$8US#r;rzw;rt+WV5$4L$ 0xT$
L$l$$Gl$,ff;T$[]_^H[]_^_^3AAUVW39>vS3NG;>r[VE3_FF^]SUVW933333LDD$|&qG?^0OuL$;1sI4vL3_^][
Lt_^][S3UVWTT TD;}IL$IQ333t/)uM;rw;r@;rL$TD ;r4w;r.D$@\$|$ DL;DrDT$
_^][_^][$SVCW3jT$\$|$|$;kL$|$4TULWl$@UWSD$(P7WL$,T$0);FuZ\E[^tV@QRUL$@VA\$nl$@V@L$T$
PL$$HT$(PL$,HT$0POttQPuxL$<tD$+T$WjjRPuPL$UWSL$(Q8OT$,L$0]_^[$]_^[$]_^3[$
]_^[$QS\$UkV3l$tDF;r;+DD$W3|u[GGGGGl$3VvQuLGGGGD$3TFD$;%_^][YVW|$WW3toSD
G<GG3<GG3<G%G3<GA|;r[_^(PSUXVpW3w3_3L$H3O<G3<G3<G3<GL$0LGl3G3G3G\$43H$3x
G3G4G34GT$034GT$43Gl$0
34G3G3p3XLLt$$4G3G3G3GG3G3Gl$3G4G34Gl$34G3P34Gl$3p3H|$<Gl$3<G3<G\$$3<G3xG3G3G3G
3\$G3G3G3G3X\$G3GG3G3GL$3G3G|$3P3G3XL$t$$GGGG3Pl$<UGGGG3Pl$<UGGGG3hT$<jGGGG_3H^]J
[((ST$DP3QUh3iVp3qW839G3G3G3GT$4G3G3G3G|$(G3H3P3G|$43G,G3,Gt$(3,Gt$4
3G3,G3X3hLLT$5I4G34G34G34GL$G|$3G3G3G|$G3G3G3G|$3P3H30|$<G\$3<G\$3<G3<G3xG3G3G3G
3X\$G3G3G3G3X\$G3G3G3G3X,GT$3,GL$3,G3,G3hL$GGGG30|$<7GGGG3p|$<wGGGG3xt$<~GGGG3P_V^
][(BABARQS\$VtDUWzG1O1N1VG1FVFOVWFNOKu_]^[S\$(WtwUVrFVnDD LPLGlT `3L$
NW3T$$VG3D$(T$FO3L$,KD$NL$/WGOu^]_[
Ul$Wt>SVruGD$PGL$1NT$DL1V11NMu^[_]3GG@=rSUVW3IG 3%3GGG$GL-3%3< 3G333333GGGGF H7F
H`7F H7Fm_^][t HEF HCF HpEFD$hGP HhGP HVt$;tT$W933A;u_^3% 3 3% 3 3% 3 3% 3
3GBfGVH3GH3GH3GH3GJuGFFFG H5Gu5
H^D$hGPD$PGVW333B%Wl333B3Wl33B3%Wl33B3Wl33B3%Wl33B3Wl33B3%Wl33BWl33G4GGGpH34G3
G0H34G3GpH34G3GpH34G3GpH34G3Gp H$34G3G
$0O_GHF^%FjPdPD$d%llPUjhGhVBFdPd%SVWeuEEeMx)uMUEMMd
_^[}uuuuuUjhGhVBFdPd%QQSVWeeMxM+MMUuYeMMd
_^[D$8csmt3Ujh GhVBFdPd%SVW3EEEE;E}uUuuEEMMd
_^[}uuuuu%F%F%FSVDuLD$3D$A\$TDud$D$r;T$wr;D$vN3^[SDuLD$3D$3P\$TDud$d$r;T$wr;D$v
+DT+DT[@s s333V;D$tV%Y^%F%F%F= Hut$FYh Hh HttYH%F%FDLL$u D$SDdD$[@s
s333%F%FQ=L$r-=s+@P%F%FUjh8GhVBFdPd% SVWeejXFY
H
H\F
G`F
GdF H=0GuhCFhFYhGhGGEEP5GEPEPEPpFhGhG^tFMuuu10EPxFE
MPQ!YYeuF%F%F%F%|F%lFhhYY3%TFSUVD$qfo1
d$fofoJfoR foZ0fo| fffffo|f8f8f8f8fo<f8f8f8f8fo|f8f8f8f8 ufo<f8f8f8f8ffo2ffforfJffor fR ffor0fZ0@(>fo
foD ff8Df8f8D uf8fffosfq^][SUVD$qfo 5IffDf8Df8f8D uf8fsfD^][$SUVD$qfo1 MEEEE
$d$fo}ffoffoffoffofo|fffffo|f8f8f8f8fo<f8f8f8f8fo|f8f8f8f8 ufo<f8f8f8f8ffJfR fZ0ffJfR fZ0@&;fufoDff8Df8f8D
uf8ffsft^][SUVW|lt2B33DOu|$+3:L:t:3333tL:3333u3:|$+t2B33DO_^][SUVW|lt2B33DOu|$+3:%3L:33L33D:
33D3u3:|$+t2B33DO_^][SUVWD$Tlt1A33D3TOuu|$+39I93333333\3Tu39|$+t1A33D3TO_^][MMMGMMGM

MdM
YGUMF0GBu=YXG,u)YGuYGM)MMMMGMPGMMMxGMMG~MnGjMZ@GVMhGBM/MMGM|GMGMTMl
lLlMMxGMGM~uYGpumY(G\uYYP GHuEYx G4M& G"uY Gu Y GuYM
GuYH
GMM M!p
GMuY
GMruuY
GdMVGRMB0G>M.XG*MGMGMMGMMGM0GMMXGMMQxFGsMbG^dMCMM$
MG'M0
GMMX
GMM
GM
GM
GMGM0GM%MrMjMMK
MRMMBXG>MG*MGMGMM0GM`GMGM@$MGMaM$M$G~MnGjMZMRMJMBM:M2M*M"M@GMMMG
MGuYGMwGuY
GM[GPGMMMxGME#cM"GWMCG?M"(G*ME<#bME@#QMhFMlFM|FM|FMnFM`FMRFMDFPGM/FGzMF
GfME#bGFMaMa8G*u%YhGMuaM_GM`MMGMGMMMa
GMXGMzGvMfGbu]YGLME#`MD8G#MDhGME#`GuYuYM{DGMgDM\DGMGD
GM3DM+Du}YMDMuHG\ME#_G>u9YG(ME#_M$CGMCM|]MMMMM^pMMICMM9CHGMvGrGfMCuYYM
BuGYMBPG.u)YMBMBGME#^@GuYhGMz^MbMGME#E^GuYGtMfGbMR@GNM]MhG,M$M0GMdMGM$
LGME#q]ME#SMM;AM$
G{M4[MbhG^M[GJ|+M0G+`GME#\M$M00GGuYMu@HGMc@uYMQ@MGMPZMHZuYM@M@ugYM@M?
uMYM?(G4M&G"GuYPGuYxGMGM{?GMg?M_?GM GME#MEh#[MlH GWM>M> G:M>M>M>M>M> GM>
GME#Z
!GMs>Mk>Mc>H!GME#IZ!GM/>!GzM<MM>M>MJMBM=M=!G.M=M=0"G`"GM"GM=M="GuY#GM8#GMO
=`#GM#GMv#GrMb#G^uYY$GHM:($G6M&MMMP$G
uYGMn_MGMuY$GMx%GM%GM%G~M%GjMZ&GVMFMM$MM&@&G"DD_D&GMMMM$M0M@&G
M44M|'GxMjM
P'G^MN'GJuEYM0'G,u)YM'Gu
YM(GuY8(GM`(GMMMM$M0M@(GM4(GnM(TM
T)GRMUM(9M,9M09M49M89M<MHM`9Mh9MlMMf9MX9MM(<9MD)MMaM-*uVY0)GEM6MM
)GME#TM(8M,8M08M48M8|8M<MHM`[8MhP8MlMM)8M8MM(7MD(MM$M0*GM
*GM+G@+G+G+GMQH,GMp,GMM,GMz,GvM,GbM-
GNMQMQMPM&MMMMMM6MPMMMMMMMhzMMMMOMxMpMhM`MM6P@-
GAM/.G+M.GM.G/Gh/GMM/G/GMH0GuYp0GuY0GuY0Gp0Gf@1GZMO1GFM6M.u1YM41GM0p2GM42GM
4M4M4M{4Ms4Mk42GMW4MO4MG4H3GM34M+4M#4M4M4M4M4M33GFhAjju^M3M3hBjjEP2hBjjEPM3
M}3MGMGMS33GM!IP4GMGx4GvMf4GbMR4GNM>4G:M*5G&MBGM`LtMMMMMtttTMM~MvMntc
 LB@5G> M'v\'L't{v6G`6GuY6GhAjjuMj1M_1hBjjEPhBjjEPM#1M1MEMEM06G6MMJMP7GM0|u
Y7GMEMuYMMu0Mm0MM]07GM(M\JM8GuYP8GpMbx8G^MNMF8GBM8G.M8GMMMM$MMhVM
9GMMQp9GM9GMMMMxlMMXMxMM5$\\M9GMM`:GMMMM<Mpd:GMMMM~Mv:GrMb0;G^MMFM>`3
M+M#MxMX;GMl$M~MQM~M~;GM,M~M~M$<GM~Mz~P<GvM5M^~<GZMJ~<GFuAY<G0u-Y=GM
~M,~(=GM}dM}u,YXw,dMhaX=GMMM$h}=GdMS}>GOMMM$(}M0[(>GME#Gh>GM|MXM|M|M|M|M|M|M|
@MMYE>GMz|?GvMf|8?GbM+M*M*`?G8M*|?G&M|M|?G
M{?GM\M{M{M?MT~M{Mg*M{MW*@Gh"M|{d!M)5M*MY{u\YXD{M<{@G8u5YM)@GM{ AG
uYHAGM\MTMzMypAGMzAGMbMzMzAGMzMYBGpMbz@BG^uYYMDzhBG@M2zBG.u)YBGMzMBGMy
uYMyCGMyuYPCGMCGMyMyM0}yCGyCGnM(BMVy8DGRuMYM'M'M'u+YM'M'DG
ME#C0EGME#CXEGEGMxMpAMxEGMTAFGM@A8FGrM,A`FG^MAFGJMAFG6FG*M@M@M@M@0GG

M@pGGM@GGMw&GGGGw<wtwMwhzwMrwMjwM&M&PZYM%M%M5wM-wM%pG,w
wMvMvMvM%M%PYMx%p@HGME#UA0IGME#5AMJ&XIGoM^vM
?M?M?M?IG:M>IG&ME#@IGME#@MM
JGMuMuMuMuMuMuXJGM7$M/$uYJGp|M!>M$M#M#\M#M$u\JGM#M#M#M#MM@
MptM|tHKGuYMM#MtMtMtKGM&?MdtM (~KGUMFt(LGBME#>MtM
}M\tMhpMtsMsPLGMsMsMsLGMsLGuYMGuY0MG|uyYXMGhM"M"M!M!MlM@aMpsM|
sMG
MrMrM0rMGMrNGMQMr@NGMK!MuYMJ;MxrMprpNGlM!Ma6MNrNGJM:r0OG6M&rXOG"MrOGMqOGM
qMqMqOGMqPGqOTqMqnM~qMvq:{MM[qMHqxU0vvzlqxqM0RMSMpMTkMTMpMDMpMpMpMpMMpM
MpMM~pMMnpMfpMMVpMMFpM
kQMM.pMMpMTpMpQToMo`oloMo`oM*MoMhylo0PGMwoRGsMboRG^MNoMFoRGBMSG.MMMoM|MnM
nMnuYMMM}MnMnuYMnMnMnuYMnu(YM'MuqYM\nM(SGPTGFME#8hTG&M8M(#iM4mTGME#8TGhMg
M6TGuY(UGuYPUGMzmxUGvMUG_M$8M,@mM@5mMLMXmMdmMp
mMUGME$#7M,lM@lML/MXlMdlMpl(VGVGVG~0WGrWGfM
5MMFlM>lM6lM.lM&lMlWGMMxEMkMkxXGMkMkMoMkuYMUMkXGHYGYGMrkMjkMbkYG^MNkhZGJ
M:kM2kMZG&8[G[GM}[GM}\GMjMjMjM4Mj8\GMj\GMj\GMzj\GvMJ}]GbM6}8]GNM"}`]G:M*j]G&M|]GM|
]GM|Mi^GM|0^GMi^GMi^GMi^GMf|Mzi_GvMfi8_GbMRi`_GNM>iMM.i_G*MZ_GMF_GMhM1`GMh@`GMh
MMMt1h`GMh`GMh`G~MnhMfhM^h`GZM10aGFT/M+hqht*egMLXaGMsaGMM8gMDgMPgM\~gMhaGoM[g
@bGWM0hbGBM/bG.MMgM gM,fbGuYbGuYMf
cGMfPcGMMf}xKuYxlUfMMfMEf`eHMMf@MfPsTeMdM\MeuYMr8MeS8gxcGME#10xdGvMcedG_MMM(8e
M8-
eMHMMXMpeM|~MMMdGM)M8zMHMdMdMzdMldHeGhMeGVMM8MHLM"dMdMdMcMceGMM|.McMc(f
GM;hfGM2MpcfGlMMPcMBcM\fG0MMcgG
MbMbhgGMbgGMBM(bM8bMH
MXMpbM|MJMMMQbMMgG=M`hG*MbhGMbhGMahGMaiGMa(iGMaPiGMAMaxiGMM8waMDlaMPaaM\V
aMhM=ajG9M*ajG&MskGMs(kGMsPkGM`xkGM`kGM`kGM`kGMnslGMZs@lGrMFshlG^MN`lGJlG>mG2M"`
hmGMrmG
M344huYuYMlMqMiMaMYMQMM_M9M_Mx_M!MMM*u@[YM4
M
M6_M@
u1YmG
ME#MEh#)MlnGM)nGME#e)oGME#E)0oGME#%)XoGjME#)oGJM:^oG6M&^oG"ME#MMP]M\]MpM|oGMN"
MC"M8"HpGuYuYM"M'MMM^]MMMMpG:MpG&MMM1MgMAMuYMMM\MuMm|{%MZMRuY qGME#-
'qGrME#
'qGRME#&rG2ME#&M
8rGM
M(M@xM[M[M[M[hrGM.M[rGM,M,rGfM
sGOuIYHsG8u5YpsG$u!YsGu
YsGMZsGM tGMw uYMe D$<MR uYM@ M8 MZ8tG{ME#%M
uGOM8uG7MM`uGM$M7AMMQAuGM{]uGME#e$ME#@M=M$xuG}M$x8vGgMAMXKYMdM|MMMY`vG
MXvGM!MXvGMXHwGMkMclXpwGxGM[hxGMwxGnMlMVX|bl
!\ L < , $$MM.8a<cMxGM
yGMaMPMyGMcM0WMwWM<MTBkM`VWMlxMCWMxM0WMMWMMWMMVyGM'cM0VM<oMTjM`Vz
GMVzGMVzGMrV{GnM^VuaYx{GPuMY{G<M.V{G*u%Y{GMV|GM|GM|MUMU|GMU
}GM|H}G`=MUMUM"6Mp}G`MvMJUh?UhM,UM$UMUM%|M|M|}GMTM{MXv0~Gh~GMm~GM_~GMTMT
M5MTMzTMrT GnM^TM>!%MCTMhP}`M(TMpGMTG
uYGuY GMHGMSpGM[MSMSMSM
h`YML8M`AY`M ^pS$S$pRMML}XY<RHZRRMR`YM'$kRGgMuYYMG@M2R(G.M#PGME#xGMM0M8GM

lGME#ME0#=GMjM2M
MMu@Y0G/MM2MMMGME#ME0#mGM#MPGME#-M(GgMVPuYYXGHM:PG6(GG
MM 0G}EEEMn}E EEM`GEPYGoEP`YGO}EEEMGMGM0G}E(EEMNXGM*GoEP`YGOEP@YG/}E
EEMGM
GEPYHGEPYpGM*GoM:`GRM@EP8YG'}EEEM}EEEM"AG}EEEMN}EEEM@HGlM@EPXYxGG}EEEM}E
EEMB@GGMw0GEP~YGEP~YGEPp~YG_MG?EP0~Y0GEP~YXGx`HTb8"vHxZB*nTD*zfH8, *6HVdr0@Vf|
|lXL<,$zpf\RJB80&
.$F--Too long switch:Incorrect switch postfix:Too short switch:Multiple instances for switch:Unknown
switch:2@FCRC329@DC@@@A@@@@,@@F@F@F@F2@
F SHA256L@xAAJ2@Y2@p2@2@*?*...Empty file pathUNCL@FCRC64L@xAA\L@kL@L@L@.\Error
#kernel32.dllCreateHardLinkW.tmpFindNextStreamWFindFirstStreamWkernel32.dll::$DATA:$DATA,F\??\LF@F
\\?\UNC\\\?\PhysicalDriveGetDiskFreeSpaceExWRtlGetVersionntdll.dllFout of
memoryGlobalMemoryStatusEx`@Cj@@L@qCV@@8@SCB@l@s@_B"@@@>@@F@F@F@F@F@F@F@
@F@F@F@F7zt@@@Q@@@@A@@@@-
@@@@@w@@@Q@@Y@c@m@Q@;@E@O@@@@'@1@@@
@@t@@@@@@@@@@@@@h@}@@@@@i@s@}@5@@@@%@@F@F@F@F@F@F@F@F@F@
@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@
F@F@F@F@F@F@F@F@F@F@F@F@F@F@xA$@r@@\FXFTFPFLFHFDF@F<F8F4F,F(F$F
FFFFFFFFmemusefiltercheckbexpectreducexeosmtapassmcmffblplcpbcomemdOFFONm@xA@@@@@CxA@z
@@xA@_@@xAb@4@@JA
lFlF@@dFdF@
@lzma86lzma@@xAAPDpDLZMA:BCJ
zDxAc@.A$@D.@@@A@7@@@$@N@@=@L@@@@F@F@F@F'O0F,F@001Splitfile01aaB@ AL@D.@
A8@D@ A$@PD@
A@D0D@@@D@F@F@F@F@F@F@F@F)@xAu@]4An5AbBD@@R@A@@M@v@@@@@8@@p@@F
@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@FJ&:/dF\FXFTFLFHF@F 8F!0F(FFFFG$FFFAMA*
.tarxz txzxzNoCheckLZMA2SPARCARMTARMIA64PPCBCJDeltaSB A A ASA@ A A AA A A A@ A A A@
@@@{@@@v@R@g@=@@ @/@@F@F@F@F@F@F@F@F@FBlockUnpackSizeBlockPackSizeCheck-
)@xA@c@f@@F@F@F@F@FC/A<AA(AAeACFaA_A"AA@F@F@F@F@F@F@F@xAA
A-AD-A
A!AAAR
A7"AfcrcyxFFFFDFlF(FFFTFFFFFFFtFhFutf-16beutf-16ledoswinutf-8-pqrxyzwIncorrect Map
commandaudtexlbihI won't write data and program's messages to same streamI won't write compressed data to a
terminalIncorrect item in listfile.
Check charset encoding and -scs switch.Cannot find listfilebsobbbtbdba-helph?asut012sea0-
pstlsdelsntsncsnrsnssnisnlsnhspfspespdsasscssesswsltsccscsslpsosiscrcsemlsfxstmrvuanaxaiiwstxtaoadybspbseUnsu
pported switch postfix for -slp-Unsupported switch
postfix -stmSeLockMemoryPrivilegeUnsupported switch postfix -
bbSeCreateSymbolicLinkPrivilegeSeRestorePrivilegeIncorrect Number of benmchmark iterationsOnly one archive
can be created with rename commandstdout mode and email mode cannot be combined-ai switch is not supported
for this commandCannot use absolute pathnames for this commandArchive name cannot by emptyCannot find
archive nameUnsupported -spf:2Unsupported command:The command must be specifiedThere is no second file
name for rename pair:Unsupported rename command:-r0-rIncorrect wildcard type markerToo short
switchUnsupported Map data sizeMap data errorUnsupported Map dataMapViewOfFile errorCan not open
mappingIncorrect volume size:incorrect update switch commandUnsupported charset:$FFFFFFpFLFCan not set
length for output fileCan not open output fileCan not create symbolic linkCan not create hard linkCan not delete
output folderCan not delete output fileCan not rename existing fileCan not create file with auto
nameSeSecurityPrivilege>ACHA=eA*AqC4AAASCAA

AAl_A_B`AcAdAiA6AA@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F :
BxA^A@@[@zAInternal error for symbolic link fileIncorrect pathDangerous link path was ignoredzDxA-
B@AAxAAFhA`@Cj@@L@qCV@@8@SCB@l@s@_B"@@@A(e|FPpFPdF
|(XFx(LF
:(@F
(0F
NEE$F/zzF
/zzF
zzF
~F
w&FFXF
FFF
.F
SF
F
.y-F
$ +%LF|FFFFFTFFHFLFxFpFlF\FTFLF<F0F(F
FFFFFFFFFFFFFFFFFF|FlF\FPFDF(FFFFFFFFRDTSCPARM_V8_CRC32ARM_V8_CRYPTOARM_V8RDRA
NDARM_FMACARM_EXTERNAL_CACHEARM_64BIT_LOADSTORE_ATOMICARM_DIVIDEFASTFAIL
RDWRFSGSBASEVIRT_FIRMWAREL2ATARM_NEONARM_VFP_32XSAVECHANNELSCMP8XCHG16C
MPXCHG16BSSE3NXSSE_DAZSSE2PAERDTSC3DNOWSSEALPHA_BYTEPPC_MOVEMEM_64BITMMX
CMPXCHGFP_EMUFP_ERRATAARM32_ON_WIN64ARM64NEUTRALIA32_ON_WIN64x64MSILALPHA64
SHXALPHAMIPSx86 |
BLAKE2spSHA1CRC32:8CRC32:4CRC32:1AES256CBC:2AES256CBC:1Delta:4PPMD:x5PPMD:x1BZip2:x7B
Zip2:x5:mt2BZip2:x5BZip2:x1Deflate64:x5Deflate:x7Deflate:x5Deflate:x1LZMA:x5:mt2LZMA:x5:mt1LZMA:x1
zDxAAAzDxAAAABA[A / GetNativeSystemInfo gran: page: cpus: act:. K
(LP)Tot:Avr:x5CPUKiB/s%EffecE/URatingR/UUsageSpeedDictMethodDecompressingCompressingusage:Benchm
ark
threads: Dictionary reduced to: hashAvg:Size8crc32CRCLZMACPU hardware threads:size: -CPU Freq: -
Windowsfreq=freqtimefile size =zDxAAA, # LP MB ?RAM bA|AAFCannot find archive:*Duplicate archive
path:can't decompress folderthere is no such archivezDxAB5YACan not create output directory:
peLFHFDF@F<F8FLPTCOMNULAUXPRNCONFCan't create hasherzDxA-B]@-
Be&B&B&B&B@F@F@F@FScanning errorexe$NF0F(F$F
FF$FFFFFFFFFxarvhdrpmwimcabgzipbzip2rar7ztemubmachoelfCxACBt@@xABB
@[DELETED]\Unknown warningUnknown
error@xA|B@@vBDBTSBBA{BJSBOSBSBOSBOSB@F@F@F@F@Fudfiso000zipbBDlBcCBR@A}B];B;B;B;
Bl=BP;B;B;BCH;BDBNBXBLB&B0B:BTMB}MB/BBBJSBMB@F@F@F@F@FBCB`ABqCBBBSCB
AAB_BlBRAA@F@F@F@F@F@F@F@F@FRHS8DAdNTsLCOIEV.X.PU0pc3d5b7-
9lBsDEFFFFFFFFFFFFpF`FPF8F$F FFFFFF F!F"F#F$F%F&F'pF(dF)PF*0F+F,F-
F.F/F0F1tF2`F9HF=8F>FFFFFFFFFFhF)TF)TF;0F<F8\l#mWwndFFFFFFF F
FFFFFFF|FhFdF`FWOFDFMFILE_PLACEHOLDERNFSAPPXSTRMDEDUPDFSRIIS_CACHESYMLINKFILT
ER_MANAGERDFSCSVWIMSISHSM2DRIVE_EXTENDERHSMMOUNT_POINTTrustedInstallerDeniedROD
CPasswordReplicationGroupAllowedRODCPasswordReplicationGroupRASandIASServersGroupPolicyCreatorOw
nersEnterpriseAdminsSchemaAdminsCertPublishersDomainControllersDomainComputersDomainUsersDomainAd
minsKRBTGTGuestAdministratorCertificateServiceDCOMAccessEventLogReadersCryptographicOperatorsDistrib
utedCOMUsersTerminalServerLicenseServersWindowsAuthorizationAccessGroupPerformanceLogUsersPerforman
ceMonitorUsersIncomingForestTrustBuildersNetworkConfigurationOperatorsRemoteDesktopUsersPreWindows200
0CompatibleAccessBackup

OperatorsReplicatorsBackupOperatorsPrintOperatorsServerOperatorsAccountOperatorsPowerUsersGuestsUsersAdministratorsDomainsNetworkServiceLocalServiceLocalSystemIUserIIS16ThisOrganizationRemoteInteractiveLogonTerminalServerRestrictedCodeAuthenticatedUsersSelfEnterpriseDCProxyAnonymousServiceLogonInteractiveBatchNetworkDialup0ERRORd:s:UNSUPPORTED0xS-1-...:REPARSE:Junction: FPFHFFFF7zEUpdating for multivolume
archives is not implementedupdate operations are not supported for this archiveGetFullPathName error7-Zip cannot find MAPISendMail functionMAPISendMailcannot load Mapi32.dllMapi32.dllcannot move the filecannot delete the filestdoutThe file already existsThere is some data block after the end of the archiveThe file is read-onlycan't find archivetype of archive is not specifiedcannot find specified SFX moduleSFX file is not specifiedrsfxThere are unclosed input file:fBxA^Af@@3@Bcannot open SFX modulecannot open fileBBxAKBBBB@F@F@F@F@F@FBKBBB&BMBBBBmBBBB)BBBB.BBBBBBsBBMChBrB|BBBBJBTB^BBiBB(B5BBBB_BBBBB@F@F@F@F@F

a : Add files to archive
b : Benchmark
d : Delete files from archive
e : Extract files from archive (without using directory names)
h : Calculate hash values for files
i : Show information about supported formats
l : List contents of archive
rn : Rename files in archive
t : Test integrity of archive
u : Update files to archive
x : eXtract files with full paths

<Switches>
-- : Stop switches parsing
@listfile : set path to
listfile that contains file names
-ai[r[-|0]]{@listfile|!wildcard} : Include archives
-ax[r[-|0]]{@listfile|!wildcard} : eXclude archives
-ao{a|s|t|u} : set Overwrite mode
-an : disable archive_name field
-bb[0-3] : set output log level
-bd : disable progress indicator
-bs{o|e|p}{0|1|2} : set output stream for output/error/progress line
-bt : show execution time statistics
-i[r[-|0]]{@listfile|!wildcard} : Include filenames
-m{Parameters} : set compression Method
  -mmt[N] : set number of CPU threads
  -mx[N] : set compression level: -mx1 (fastest) ... -mx9 (ultra)
-o{Directory} : set Output directory
-p{Password} : set Password
-r[-|0] : Recurse subdirectories
-sa{a|e|s} : set Archive name mode
-scc{UTF-8|WIN|DOS} : set charset for for console input/output
-scs{UTF-8|UTF-16LE|UTF-16BE|WIN|DOS|{id}} : set charset for list files
-scrc[CRC32|CRC64|SHA1|SHA256|*] : set hash function for x, e, h commands
-sdel : delete files after compression
-seml[.]
: send archive by email
-sfx[{name}] : Create SFX archive
-si[{name}] : read data from stdin
-slp : set Large Pages mode
-slt : show technical information for l (List) command
-snh : store hard links as links
-snl : store symbolic links as links
-sni : store NT security information
-sns[-] : store NTFS alternate streams
-so : write data to stdout
-spd : disable wildcard matching for file names

-spe : eliminate duplication of root folder for extract command
-spf : use fully qualified file paths
-ssc[-] : set sensitive case mode
-sse : stop archive creating, if it can't open some input file
-ssw : compress shared files
-stl : set archive timestamp from the most recently modified file
-stm{HexMask} : set CPU thread affinity mask (hexadecimal number)
-stx{Type} : exclude archive type
-t{Type} : Set type of archive
-u[-][p#][q#][r#][x#][y#][z#][!newArchiveName] : Update options
-v{Size}[b|k|m|g] : Create volumes
-w[{path}] : assign Work directory.
Empty path means a temporary directory
-x[r[-|0]]{@listfile|!wildcard} : eXclude filenames
-y : assume Yes on all queries

7-Zip (r) 18.05 (x86) : Igor Pavlov : Public domain : 2018-04-30

i#@'0i#@'
i#@'i#@'i#@'i#@'i#@'i#@'i#@'@i#@'7i#@'6i#@'5i#@'4i#@'2i#@'1i#@'0i#@'(i#@''i#@'&i#@'%i#@'$i#@'#i#@'"'i#@' i#@'i#@'i#@'i#@'i#@'i#@'i#@'i#@'i#@'i#@'i#@'i#@'ai#@'qi#@'pi#@'`i#@'Pi#@'@i#@'0i#@'!i#@' i#@'i#@' i#@'i#@'i#@'i#@'i#@'i#@'i#@'Errors: Compressed: Size: Alternate Streams Size: Alternate Streams: Files: Folders: Open Errors: Warnings: Archives with Warnings: Archives with Errors: Can't open as archive: OK archives: Archives: ERROR:Scanning the drive for archives:}C,C@F@F7zCon.sfx
Decoding ERROR
Hashers:Codecs: || offset=KSNFMGOPBELHFormats:zDxAq_C=tCGtCQtCJCtC)tC3tCCOdCdCdC;CC C[CGCCCC=CC/CwCwC,xCxCxC@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@FCC;CCCQCC CCa!C"C5C[C.CCoCC}COCC}CCLCCC@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F@F;CCCNCCCC}CCt!Ca!CV#C>%C@F@F@F@F@F@F@F@F@F@F@F@F@F@F
Error:
fileWARNING: Cannot open WARNINGS for files:Scan WARNINGS: Scan WARNINGS for files and folders:----------PhysicalGlobal Virtual ProcessUser MCyclesKernel QueryProcessCycleTimeGetProcessMemoryInfoPsapi.dllK32GetProcessMemoryInfo = Time = Memory = FFFFFF

Internal Error #Unknown ErrorERROR: Can't allocate required memory!Break signaledCommand Line Error:System ERROR:Unsupported Windows version

. FFFFFFtFlFStdOutWARNING: Scanning the drive:Updating archive: Creating archive: Open archive: [Content]Scan Archive size: Files read from diskWrite SFX: Removing: Removing files after including to archiveAdd new data to archiveKeep old data in archiveDelete data from archiveU+ReadingAHeader creationDR= FF(Y)es / (N)o / (A)lways / (S)kip all / A(u)to rename all / (Q)uit? ?
Enter password (will not be echoed):@xACC@xAGC6CC/ACAbA$A%A$AC#(Aj)A@xAC5CzDxAC@FCxAC@CxAbCCCxACm@CxAA!C@xAxCCFCDPC>D+}DACR>D@F@F@F@FCxAqClC-&CCC[CCgCqC{CCICSC]CBcCCMC8C_BCC<C:C7CCCM<Db<DOC^CLZMA2:7zAESBCJ2:memPPMDBT2CopyqsmtftrtmtatchehcfhcCxAA@
@@@

abcGFF=D=D8FF4F,F0Fsysocxdll
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=B=s===>?0p0000+1P1q112?2K2U2u2{2222333t44455M5_5k5566>66727q7777788@8T8j88888
9T99C:f:~:::::::::;;;$;*;;;'<4<:<@<<<<<<<<<===+=2=J=e=x>>>>>?@\2222222833333W6q666K77N888999:;;3;s;;
;;;;</<v<<<h=m===M>V>d>j>x>>>>>>>u?????P0000g11111112-
262Z2e2k222233;4G444g5s556I6X6c6y66788999:6:q:::1;;;O<<<<<Q=7>P>U>>>>l????`V01233333

40474>4`4{44466!6(6Z6a6r677778D8]8b8|88889999?9K9s99999o:::::;;;;!<'<O<v<}<<<<<<<<<==]==========
>>(>p>y>>>>>>y????????p0
000Q0711222*3p3\4m4q4z4~4444444445585B5W5a5h5p55555555566%6.696G6U6\6e6p6{6666666666667
7K7g777789::<<====_>>>?4?:?00a0g0u00$1+1W111122,292n222222O3333333)4M4444!5<5X5_5v556%66]7}
77-
8Y8e88888889;9Z9g9t99::::::::;8;A;P;\;;;F<K<R<Y<b<i<r<y<<<<<<<<<<<==c=p===>>H>Z>f>v>>>>f??????800
0 1145f5566e99:;O;===!>>>>>@1m1111<2222W3i33*6=6`67799t:::;<<r==?DT0#3R3p3334
49444444=555666:{;;<<.=3==:>?<236[99;:l:::;F<<>Q>X>_>f>>>>>>I?d?x???<
00/2q3334@5558:]:n::;E;t;;;;;;??(1!1=1134*4445"5\7*?s??81228334477m8888929H9^9t999;3<<@==4E0,223566
77789:K?_?s????????40u05n6H788:;Z= ?
?????!?%?)?-?1?5?? (4l444|5\678-
8|<??0$046Z:<<.==>D>I>>J?@337A8~8889Pn00223Y8e8l8W>`0?o?p@01577.8K8889G9f999:::;<<=&=5=U=k=
=&?X04455&5-5Q5~555T6[6b677=>3>R>Y>`>g>n>u>|>>>>>>>>>>!?N?u???P090i001s;;A<<<=|====
>&>L>R>>>>>>>??@?F?l?r?????0011#1A1H1O1V1]1333344"4)4047444444444555555555551!6777783888889!
9T999Q::;
;;;;&;-
;4;k;r;y;;;;;;;;;;<G<y<<<=A=o==C>a>h>o>v>}>>>>>>k?p?E2J2C5b5i5p5|5555555"6)6067689q9999999999999:
::A:n::::+;];;;S<q<x<<<<<<<<^========|555666!6(6/6K6666737777777y89999
9$9(9,9094989<9@9D9H9L9g;n;;C<<<<C>b>i>>>>>>>>?E?u?0 00=0D000000
22267777A8Q8a8g8888889&9,9r9C:e:l:::::::::A;n;;;;s<=>>>e>>??
?3?U?\?q???????p0>0e00#12x3334#5Z678q888888999t:}:::::3;p;w;~;;;;;!<D<]<v<<=!=1=7=K=Q===>0Q0Z0c0j0
q0000111#1911111220e14556
66$6+686?6G6N66667-:M:<:<(>>? D1144557578
8882898F8M8Y8`8g8s8889::::::04444@7<<77;8D8;#>w16v12364>,r133]44455L77b9J::::::=
90:p::X;};;<=1>>q?`00R112/33r455V6l8v888889939C9m9}99999::0:<0=4=8=<=@==|>>>>>>>
)00070>0E08888/>H>+2`4d4h4l4p4t4x4|4d8l8t8|8558==y>>|?
1122%2;;;;;<!<8<H<<<<<<<<<O=V=b=q=========>>$>8>E>q>>>>>>>>>>
??I?T?]?n?????????0<0!030=0M0]0j0~000000
1!151S1_1v111111C2J2V2e22222222233$313i3p3|333333334484C4Q4^4x44444444
55 5/5B5V5d5q55555556
6>6J6a6u66666U8[8s88888A9i99999999999999::::3:?:;;8;V;q;;;;;;;;;;==(=I=P=w=~======>>0>:>W>[>f>>>????
@000N1T1Z1`1m1t1y11122R2X2b2g22222222222222223333(3H3h3n3t3z333399::/:C:W:::::::;;F;Y;m;;;;;<'<9<O
<c<<<<<<
==1=E=Y=u======4>I>e>>>>>>?1?E?Y?u??????PE0i000001111111212G2a2222222383M3m33333434\4444-
5S5q555555
6/6O6l6666707=7s7}777'898E8[8o888889!9U9i99999-:I:U:i:::::::::%;Q;g;;;;;;;<9<f<<<<=/=K=g={====
>???`C0U0i0u00000000
1202E2Y2e2q2222222233C3U3i3334!4I4_44444
5!5556]6i66666%7I7g777778-8A8888S9m9999W:::::;+;?;_;;;<B<a<<<<#=5=Q=e==#>?>Q>g>>>>>>?-
?C?c??????p 0Q0q00000001%111]1q1111222!353U3333F44455~555555R6~66667%797M7a777999:-
:;;5;b;;;;;;;d<<<<<<A=|=====>5>A>M>a>u>>>>>>
?!?5?I?]?y???????
010E0Y0u000001111122-
2b2w222333445u5555+6N6i6}6717H7]7q77777"858I8]8q8888888899)9=9Q9;:x:::::;%;9;;;!<5<<<
=)=T====>#>7>K>_>q>>>?$?A?????N0a0}000000111q222223313G3Y3m33333[4}44444?5Q5g5{55567-
7A7a7777,8v88889%9<9Q9w999:H:i:::::,;h;;;;; <4<<<=o======><>\>
22222222222222222233 3$3(3,3034383h3x33333333(484<444444444444444455555555
5$5(5,5054585<5H5L5P5T5X5\5`5d5h5l5p5t5x5|55555555555555555555555555555555566666666

6$6(6,6064686<6@6D6H6L6P6T6X6\6`6d6h6l6p6t6x6|66666666666666666666666666666666677777777
7$7(7,7074787D7L7T7\7d7l7t7|777777777777777l8p8t8x88888888889999$9(90989L9P9X9`9t9x9|999999999999
9999999999999999:: :H:L:P:T:X:\:`:d:h:l:p:t:x:|::::::::::::::::::::::::::::::::;;;;;;;;
;$;(;,;0;4;8;<;@;D;H;L;P;T;l;t;|;;;;;;;;;;;;;<<<<h<l<p<t<x<|<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<=,=0=4=8=
<=@=D=H=L=P=T=X=\=`=d=h=l=p=t=x=|=========================>>>>>>>>>><>@>H>P>X>`>,4044
484<4@4D4H4p5t5x5|55555555555555555555555555555555566666666$6(6,6064686<666666666666666666666
6666667747L7d7|7777778$8<8T8l88888888899999
9$9(9,9094989<9@9D9H9L9P9T9X9\9`9d9h9l9p9t9x9|99999999999999999999<<<<<<<<<<<@>D>H>L>p>t
>x>|>>>>>
?$?(?,?0?4?P?h?l?p?t?x?|????????????????????4080<0@0D0H0L0P0000000000000000000000001111
1$1(1,1014181<1@1D1H1L1P1T1X1\1`1d1h1l1p1t1x1|11111111111111111111111111111122282<2@2D2H
2L2P2T2X2\2`2d2h2l2p2t2x2|22222222222222222223333$3,343<3D3L3T3\3d3l3t3|33333333333334444$4,44
4<4D4L4T4\4094989<9@9D9;;;;;;;;;;;;;;;;;;;<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<========
=$=(=,=0=4=8=<=@=D=H=L=P=T=X=\=`=d=h=l=p=t=x=|=============================t>>>>>>>>>>
>>>>>>>>>???????? ?$?(?,?0?4?8?<?@?D?H?333333444444444444444444444444455555555
5$5(5,5054585<5@5D5H5L5P5T5X5\5`5d5h5l5p5t5x5|55555555555555555555555555555555566666666
6$6(6,646L6d6|66<<<<<<<
<$<(<0<4<8<<<@<D<;;;;H<L<P<T<X<\<`<d<h<l<p<t<x<|<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<<
==
=$=(=,=0=4=8=<=@=D=H=L=P=T=X=\=`=d=h=l=p=t=x=|==================================>>>>>>
>>
>$>(>,>0>4>??????L0P0T0X0\0`0d0h011L2P2T2X2\2`2d2h2l2p2t2x2|22222222222222222222222222222233
333333 3$3(3,3034383<3@3D3L3P3T3X3\3`3d3h3l3p3t3x3|333333333333333344 4$4555555
5$5(5,5999999999999999999:::::::: :$:(:,:0:4:8:<:@:D:H:L:P:T:X:\:`:d:h:l:p:t:x:|::::::::::::::::::::::::::::::;;;
;$;0;@;D;P;`;d;p;;;;;;;;;;;;;;;;;<<<(<H<L<P<T<X<\<`<d<h<l<p<t<<<<<<<<<<<<<<<<<<

<L<X<t<|<<<<<<<<<=$=0=L=T=`=|===============>$>0>L>T>\>d>p>>>>>>>>>>>>>???4?@?\?h??????????
@<0 0<0D0L0T0\0d0l0t0|000000000000011(1D1P1l1x11111111122242<2H2d2p2222222233
3<3D3L3X3t3|333333333
444@4H4d4l4444444444445,585T5`5|555555566646@6\6h666666667,787T7\7d7l7x77777777(8<8H8d8l8t8|888
88888888888889999$9,989T9`9|9999999999::(:D:L:T:`:|::::::::::;;;;$;,;4;<;D;P;l;t;|;;;;;;;;;;;;;<<$<0<L<X<t<|<<<<
<<<<<<<==,=8=T=`=|============>>>
><>H>d>l>x>>>>>>>>>>??$?,?8?T?`?|????????P@00,080@0T0\0d0l0t0|0000000000000000001111$1,141<1D1L
1T1\1d1l1t1|1111111111111112222$2,242<2D2L2T2h2|222222223$303L3T3\3d3l3t3|33333333333333333444
4P4d4p44444444455$505L5X5t5555555556666$606L6T6\6d6l6t6|66666667,787@7p77777777888$8,848<8D8`8t
88888888888899909D9P9X9999999::$:,:4:P:d:p:x::::::::;
;4;@;H;x;;;;;;;<<4<@<\<d<l<t<|<<<<<<<<<<==4=@=\=h========>$>,>8>@>\>x>>>>>>>??,?4?@?\?h????????
?`X0040<0H0d0p00000000011$1,181T1`1|111111111111122$2,242<2H2d2p22222222233(3D3L3X3t333333333
3333334444$4,444<4D4L4T4\4d4l4t4444444445555$5,545<5D5P5l5t5|555555555556666$606L6T6\6d6p666666
66667$7,747T7d7p7777777778888$8,848<8D8L8T8\8h888888889$909L9X9t999999999999::::$:,:4:<:D:L:T:\:d:l:
|::::::::::;$;0;L;X;t;;;;;;;;< <<<H<d<p<<<<<<<==
=P=d=p==========>>>>$>,>4><>D>L>T>\>d>l>t>|>>>>>>>>>>>??,?8?T?`?|???????pD00$0,040<0D0P0l0t0|
000000000000011(1D1L1T1\1d1l1t1|1111111111112242@2\2d2p22222222222233(3D3P3l3x33333334444@4H4
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8P8d8p88888888889999$9,949<9D9L9T9p99999999::::$:,:4:<:D:L:T:\:d:l:t:|::::::::::;
;(;D;`;t;;;;;;;;<<$<D<T<\<|<<<<<<<<<===(=D=P=l=x==========>>>>$>,>8>T>\>d>p>x>>>>>>>>>??(?D?L?
T?\?d?l?x???????????\00(0D0P0l0x000000001111$1,141<1D1L1T1\1d1l1t1|1111111111111111122$202L2X2t222
2222223$3,383T3\3d3l3t3|333333333344$404L4T4`4|444444445,585T5\5h5555555566,686T6`6|666666677(7D7P
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---
license:
  description: License Check
  include:
    - .
  exclude:
    # These paths need to be triaged.
    - build/pgo/js-input
    # License not super clear
    - browser/branding/
    # Trademark
    - browser/components/pocket/content/panels/
    - browser/extensions/pdfjs/content/web/images/
    # We probably want a specific license
    - browser/extensions/webcompat/injections/
    # Different license
    - build/pgo/blueprint/print.css
    # Different license
    - build/pgo/blueprint/screen.css
    # Empty files
    - config/external/nspr/_pl_bld.h
    - config/external/nspr/_pr_bld.h
    # Unknown origin
    - gfx/2d/MMIHelpers.h
    # might not work with license
    - gradle.properties
    # might not work with license
    - gradle/wrapper/gradle-wrapper.properties
    - mobile/android/components/extensions

- mobile/android/geckoview/src/main/AndroidManifest.xml
- mobile/android/geckoview/src/main/res/drawable/ic_generic_file.xml
- mobile/android/geckoview_example/src/main
might not work with license
- mobile/android/gradle/dotgradle-offline/gradle.properties
might not work with license
- mobile/android/gradle/dotgradle-online/gradle.properties
Almost empty file
- modules/libpref/greprefs.js
- parser/html/java/named-character-references.html
- python/mozlint/test/files/
By design
- python/mozrelease/mozrelease
- security/mac/hardenedruntime/browser.developer.entitlements.xml
- security/mac/hardenedruntime/browser.production.entitlements.xml
- security/mac/hardenedruntime/developer.entitlements.xml
- security/mac/hardenedruntime/plugin-container.developer.entitlements.xml
- security/mac/hardenedruntime/plugin-container.production.entitlements.xml
- security/mac/hardenedruntime/production.entitlements.xml
- servo/components/hashglobe/src/alloc.rs
- servo/components/hashglobe/src/shim.rs
- toolkit/components/reputationservice/chromium/chrome/common/safe_browsing/csd.pb.cc
- toolkit/components/reputationservice/chromium/chrome/common/safe_browsing/csd.pb.h
- toolkit/mozapps/update/updater/crctable.h
- tools/lint/eslint/eslint-plugin-mozilla/lib/configs
By design
- tools/lint/test/

extensions:
- .c
- .cc
- .cpp
- .css
- .dtd
- .ftl
- .h
- .html
- .js
- .jsm
- .jsx
- .m
- .mm
- .properties
- .py
- .rs
- .svg
- .xhtml
- .xml
- .xul

```
  support-files:
    - 'tools/lint/license/**'
  type: external
  payload: license:lint
  find-dotfiles: true
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: No text Element in license Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-010-ref.xht" />
  <meta name="assert" content="The license element does not contain a text child element." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   @font-face {
    font-family:
 "WOFF Test";
    src: url("support/metadatadisplay-schema-license-010.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts" id="License ID" /&gt;
```

</metadata>
</pre>
</body>
</html>
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wOF2OTTO4 @`
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf

s$,x.JT <9V>?at%@6!u*|0tp"`ObT|/Z?l@lh]r=PPA
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wOF2OTTOM @y
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$,=a(< Tm4&w|~\jEB&z8:6l&K49/gL"k+/Z?lC}}t*Hct{Gf &
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With Nested span Elements in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-license-029-ref.xht" />
  <meta name="assert" content="The license element contains a text element that contains nested span elements." />

```
 <style type="text/css"><![CDATA[
  @import
 url("support/test-fonts.css");
  @font-face {
   font-family: "WOFF Test";
   src: url("support/metadatadisplay-schema-license-029.woff2") format("woff2");
  }
  body {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test", "WOFF Test CFF Fallback";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>Test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
 <div class="test">P</div>
 <p>The XML contained in the Extended Metadata Block is below.</p>
 <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text&gt;
   &lt;span&gt;
    span 1 start
    &lt;span&gt;
     span 2 start
     &lt;span&gt;
      span 3
     &lt;/span&gt;
     span 2 end
    &lt;/span&gt;
    span 1 end
   &lt;/span&gt;
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
 </pre>
</body>
</html>
```

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- -

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- -

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Esperanto hyphenation patterns

(more info about the licence to be added later)

```
% This file is part of hyph-utf8 package and resulted
 from
% semi-manual conversions of hyphenation patterns into UTF-8 in June 2008.
%
% See http://tug.org/tex-hyphen and other files in the package
%
% However, this file does not comply with rules of other patterns
% as it still uses TeX macros, and a dedicated converter would be needed
% to convert patterns into OponOffice-friendly form for example.
%
% But it should work OK with both 8-bit engines (TeX, pdfTeX, ...)
% as well as with native UTF-8 ones (XeTeX, LuaTeX, ...)
%
%-------------------------------------------------------------------------
%
% \message{Esperanto Hyphenation Patterns `eohyph', 1999-08-10}
%
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%
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%
% More documentation can be found near the end of this file.
%
\begingroup
%
 Makrooj por la finaoj:
\def\adj#1{#1a. #1aj. #1ajn. #1an. #1e.}
\edef\nom#1{\adj{#1} #1o. #1oj. #1ojn. #1on.}
\def\ver#1{#1as. #1i. #1is. #1os. #1u. #1us.}
%
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-
strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Invalid license Element With dir Attribute in div Element in text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
```

```
 <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-invalid-mustignore" />
 <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
 <meta name="assert" content="The license element contains a text element that contains a div element with
INVALID as the value for the dir attribute." />
 <style type="text/css"><![CDATA[
  @import url("support/test-fonts.css");
  body
 {
   font-size: 20px;
  }
  pre {
   font-size: 12px;
  }
  .test {
   font-family: "WOFF Test CFF Reference";
   font-size: 200px;
   margin-top: 50px;
  }
 ]]></style>
</head>
<body>
 <p>If the UA does not display WOFF metadata, the test passes if the word PASS appears below.</p>
 <p>The Extended Metadata Block is not valid and must not be displayed. If the UA does display it, the test
fails.</p>
 <div class="test">P</div>
</body>
</html>
```


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```
PROG = 7z.sfx
MY_FIXED = 1

CFLAGS = $(CFLAGS) \
 -DNO_REGISTRY \
 -DEXTRACT_ONLY \
 -DNO_READ_FROM_CODER \
 -D_SFX \

!IFDEF UNDER_CE
LIBS = $(LIBS) ceshell.lib Commctrl.lib
!ELSE
LIBS = $(LIBS) comctl32.lib comdlg32.lib
!ENDIF

CURRENT_OBJS = \
 $O\SfxWin.obj \

GUI_OBJS = \
 $O\ExtractDialog.obj \
 $O\ExtractGUI.obj \

COMMON_OBJS = \
 $O\CRC.obj \
 $O\CommandLineParser.obj \
 $O\IntToString.obj \
 $O\NewHandler.obj \
 $O\MyString.obj \
 $O\StringConvert.obj \
```

```
 $O\MyVector.obj \
 $O\Wildcard.obj \

WIN_OBJS = \
 $O\CommonDialog.obj \
 $O\DLL.obj \
 $O\ErrorMsg.obj \
 $O\FileDir.obj \
 $O\FileFind.obj \
 $O\FileIO.obj \
 $O\FileName.obj \
 $O\PropVariant.obj \
 $O\PropVariantConv.obj \
 $O\ResourceString.obj \
 $O\Shell.obj \
 $O\Synchronization.obj \
 $O\System.obj \
 $O\Window.obj \

WIN_CTRL_OBJS = \
 $O\ComboBox.obj \
 $O\Dialog.obj \
 $O\ListView.obj \

7ZIP_COMMON_OBJS = \
 $O\CreateCoder.obj \
 $O\CWrappers.obj \
 $O\FilePathAutoRename.obj \
  $O\FileStreams.obj \
 $O\InBuffer.obj \
 $O\FilterCoder.obj \
 $O\LimitedStreams.obj \
 $O\OutBuffer.obj \
 $O\ProgressUtils.obj \
 $O\PropId.obj \
 $O\StreamBinder.obj \
 $O\StreamObjects.obj \
 $O\StreamUtils.obj \
 $O\VirtThread.obj \

UI_COMMON_OBJS = \
 $O\ArchiveExtractCallback.obj \
 $O\ArchiveOpenCallback.obj \
 $O\DefaultName.obj \
 $O\Extract.obj \
 $O\ExtractingFilePath.obj \
 $O\LoadCodecs.obj \
 $O\OpenArchive.obj \
```

```
EXPLORER_OBJS = \
 $O\MyMessages.obj \

FM_OBJS = \
 $O\BrowseDialog.obj \
 $O\ComboDialog.obj \
 $O\ExtractCallback.obj \
 $O\FormatUtils.obj \
 $O\OverwriteDialog.obj \
 $O\PasswordDialog.obj \
 $O\ProgressDialog2.obj \
 $O\PropertyName.obj \
 $O\SysIconUtils.obj \

AR_OBJS = \
 $O\SplitHandler.obj \

AR_COMMON_OBJS = \
 $O\CoderMixer2.obj \
 $O\ItemNameUtils.obj \
 $O\MultiStream.obj \
 $O\OutStreamWithCRC.obj \

7Z_OBJS = \
 $O\7zDecode.obj \
 $O\7zExtract.obj \
 $O\7zHandler.obj \
 $O\7zIn.obj \
 $O\7zRegister.obj \

COMPRESS_OBJS
 = \
 $O\Bcj2Coder.obj \
 $O\Bcj2Register.obj \
 $O\BcjCoder.obj \
 $O\BcjRegister.obj \
 $O\BranchMisc.obj \
 $O\BranchRegister.obj \
 $O\CopyCoder.obj \
 $O\CopyRegister.obj \
 $O\DeltaFilter.obj \
 $O\Lzma2Decoder.obj \
 $O\Lzma2Register.obj \
 $O\LzmaDecoder.obj \
 $O\LzmaRegister.obj \
 $O\PpmdDecoder.obj \
 $O\PpmdRegister.obj \
```

```
CRYPTO_OBJS = \
 $O\7zAes.obj \
 $O\7zAesRegister.obj \
 $O\MyAes.obj \

C_OBJS = \
 $O\Alloc.obj \
 $O\Bcj2.obj \
 $O\Bra.obj \
 $O\Bra86.obj \
 $O\BraIA64.obj \
 $O\CpuArch.obj \
 $O\Delta.obj \
 $O\DllSecur.obj \
 $O\Lzma2Dec.obj \
 $O\Lzma2DecMt.obj \
 $O\LzmaDec.obj \
 $O\MtDec.obj \
 $O\Ppmd7.obj \
 $O\Ppmd7Dec.obj \
 $O\Sha256.obj \
 $O\Threads.obj \

!include "../../Aes.mak"
!include "../../Crc.mak"
!include "../../LzmaDec.mak"

!include "../../7zip.mak"
```

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basecvt.pod
gcd.pod
invmod.pod
isprime.pod
lap.pod
mpi-test.pod
prime.txt
prng.pod

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86561eb02813e70558e8bcb669c57d27
!IFDEF ASM_OBJS
!IF "$(CPU)" == "ARM"
$(ASM_OBJS): ../../../../Asm/Arm/$(*B).asm
$(COMPL_ASM)

!ELSEIF "$(CPU)" != "IA64" && "$(CPU)" != "MIPS" && "$(CPU)" != "ARM64"
$(ASM_OBJS): ../../../../Asm/x86/$(*B).asm
 $(COMPL_ASM)
!ENDIF
!ENDIF
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Coverage.py was originally written by Gareth Rees, and since 2004 has been
extended and maintained by Ned Batchelder.

Other contributions, including writing code, updating docs, and submitting
useful bug reports, have been made by:

Abdeali Kothari
Adi Roiban
Agbonze O. Jeremiah
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Aleksi Torhamo
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```
<?xml version="1.0" encoding="utf-8"?>
<Project DefaultTargets="Build" ToolsVersion="15.0"
xmlns="http://schemas.microsoft.com/developer/msbuild/2003">
 <ItemGroup Label="ProjectConfigurations">
  <ProjectConfiguration Include="Debug|Win32">
   <Configuration>Debug</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
  <ProjectConfiguration Include="Release|Win32">
   <Configuration>Release</Configuration>
   <Platform>Win32</Platform>
  </ProjectConfiguration>
  <ProjectConfiguration Include="Debug|x64">
   <Configuration>Debug</Configuration>
   <Platform>x64</Platform>
  </ProjectConfiguration>
  <ProjectConfiguration Include="Release|x64">
   <Configuration>Release</Configuration>
   <Platform>x64</Platform>
  </ProjectConfiguration>
 </ItemGroup>
 <PropertyGroup Label="Globals">
  <VCProjectVersion>15.0</VCProjectVersion>
  <ProjectGuid>{83D56BCB-B9D1-44AB-BC96-9EA8A645CB0D}</ProjectGuid>
  <Keyword>Win32Proj</Keyword>
   <RootNamespace>CertCheck</RootNamespace>
  <WindowsTargetPlatformVersion>10.0.15063.0</WindowsTargetPlatformVersion>
 </PropertyGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.Default.props" />
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <UseDebugLibraries>true</UseDebugLibraries>
  <PlatformToolset>v141</PlatformToolset>
  <CharacterSet>Unicode</CharacterSet>
 </PropertyGroup>
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
  <UseDebugLibraries>false</UseDebugLibraries>
  <PlatformToolset>v141</PlatformToolset>
  <WholeProgramOptimization>true</WholeProgramOptimization>
  <CharacterSet>Unicode</CharacterSet>
 </PropertyGroup>
 <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|x64'" Label="Configuration">
  <ConfigurationType>DynamicLibrary</ConfigurationType>
```

```
    <UseDebugLibraries>true</UseDebugLibraries>
    <PlatformToolset>v141</PlatformToolset>
    <CharacterSet>Unicode</CharacterSet>
  </PropertyGroup>
  <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|x64'" Label="Configuration">
    <ConfigurationType>DynamicLibrary</ConfigurationType>
    <UseDebugLibraries>false</UseDebugLibraries>
    <PlatformToolset>v141</PlatformToolset>
    <WholeProgramOptimization>true</WholeProgramOptimization>
    <CharacterSet>Unicode</CharacterSet>
  </PropertyGroup>
  <Import Project="$(VCTargetsPath)\Microsoft.Cpp.props" />
  <ImportGroup Label="ExtensionSettings">
  </ImportGroup>
  <ImportGroup Label="Shared">
  </ImportGroup>
  <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
    <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform"
  />
  </ImportGroup>
  <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
    <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  </ImportGroup>
  <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Debug|x64'">
    <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  </ImportGroup>
  <ImportGroup Label="PropertySheets" Condition="'$(Configuration)|$(Platform)'=='Release|x64'">
    <Import Project="$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props"
Condition="exists('$(UserRootDir)\Microsoft.Cpp.$(Platform).user.props')" Label="LocalAppDataPlatform" />
  </ImportGroup>
  <PropertyGroup Label="UserMacros" />
  <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'"
  />
  <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Debug|x64'" />
  <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
    <LinkIncremental>false</LinkIncremental>
  </PropertyGroup>
  <PropertyGroup Condition="'$(Configuration)|$(Platform)'=='Release|x64'">
    <LinkIncremental>false</LinkIncremental>
  </PropertyGroup>
  <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Debug|Win32'">
    <ClCompile>
      <PrecompiledHeader>NotUsing</PrecompiledHeader>
      <WarningLevel>Level3</WarningLevel>
      <Optimization>Disabled</Optimization>
```

```
    <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>WIN32;_DEBUG;CERTCHECK_EXPORTS;_WINDOWS;_USRDLL;%(Preprocessor
Definitions)</PreprocessorDefinitions>
    <RuntimeLibrary>MultiThreadedDebug</RuntimeLibrary>
    <BufferSecurityCheck>false</BufferSecurityCheck>
    <BasicRuntimeChecks>Default</BasicRuntimeChecks>
  </ClCompile>
  <Link>
    <SubSystem>Windows</SubSystem>
     <GenerateDebugInformation>true</GenerateDebugInformation>
    <EntryPointSymbol>DllMain</EntryPointSymbol>
    <LinkTimeCodeGeneration>Default</LinkTimeCodeGeneration>
  </Link>
 </ItemDefinitionGroup>
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Debug|x64'">
  <ClCompile>
    <PrecompiledHeader>NotUsing</PrecompiledHeader>
    <WarningLevel>Level3</WarningLevel>
    <Optimization>Disabled</Optimization>
    <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>_DEBUG;CERTCHECK_EXPORTS;_WINDOWS;_USRDLL;%(PreprocessorDefinitio
ns)</PreprocessorDefinitions>
    <RuntimeLibrary>MultiThreadedDebug</RuntimeLibrary>
    <BufferSecurityCheck>false</BufferSecurityCheck>
    <BasicRuntimeChecks>Default</BasicRuntimeChecks>
  </ClCompile>
  <Link>
    <SubSystem>Windows</SubSystem>
    <GenerateDebugInformation>true</GenerateDebugInformation>
    <EntryPointSymbol>DllMain</EntryPointSymbol>
    <LinkTimeCodeGeneration>Default</LinkTimeCodeGeneration>
   </Link>
 </ItemDefinitionGroup>
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Release|Win32'">
  <ClCompile>
    <PrecompiledHeader>NotUsing</PrecompiledHeader>
    <WarningLevel>Level3</WarningLevel>
    <Optimization>MaxSpeed</Optimization>
    <FunctionLevelLinking>true</FunctionLevelLinking>
    <IntrinsicFunctions>true</IntrinsicFunctions>
    <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>WIN32;NDEBUG;CERTCHECK_EXPORTS;_WINDOWS;_USRDLL;%(Preprocessor
Definitions)</PreprocessorDefinitions>
    <RuntimeLibrary>MultiThreaded</RuntimeLibrary>
    <OmitDefaultLibName>true</OmitDefaultLibName>
    <BufferSecurityCheck>false</BufferSecurityCheck>
    <WholeProgramOptimization>false</WholeProgramOptimization>
  </ClCompile>
```

```
  <Link>
    <SubSystem>Windows</SubSystem>
    <EnableCOMDATFolding>true</EnableCOMDATFolding>
    <OptimizeReferences>true</OptimizeReferences>

 <EntryPointSymbol>DllMain</EntryPointSymbol>
    <LinkTimeCodeGeneration>Default</LinkTimeCodeGeneration>
    <GenerateDebugInformation>false</GenerateDebugInformation>
  </Link>
 </ItemDefinitionGroup>
 <ItemDefinitionGroup Condition="'$(Configuration)|$(Platform)'=='Release|x64'">
  <ClCompile>
    <PrecompiledHeader>NotUsing</PrecompiledHeader>
    <WarningLevel>Level3</WarningLevel>
    <Optimization>MaxSpeed</Optimization>
    <FunctionLevelLinking>true</FunctionLevelLinking>
    <IntrinsicFunctions>true</IntrinsicFunctions>
    <SDLCheck>false</SDLCheck>
<PreprocessorDefinitions>NDEBUG;CERTCHECK_EXPORTS;_WINDOWS;_USRDLL;%(PreprocessorDefinitio
ns)</PreprocessorDefinitions>
    <RuntimeLibrary>MultiThreaded</RuntimeLibrary>
    <OmitDefaultLibName>true</OmitDefaultLibName>
    <BufferSecurityCheck>false</BufferSecurityCheck>
    <WholeProgramOptimization>false</WholeProgramOptimization>
  </ClCompile>
  <Link>
    <SubSystem>Windows</SubSystem>
     <EnableCOMDATFolding>true</EnableCOMDATFolding>
    <OptimizeReferences>true</OptimizeReferences>
    <EntryPointSymbol>DllMain</EntryPointSymbol>
    <LinkTimeCodeGeneration>Default</LinkTimeCodeGeneration>
    <GenerateDebugInformation>false</GenerateDebugInformation>
  </Link>
 </ItemDefinitionGroup>
 <ItemGroup>
  <ClCompile Include="CertCheck.cpp" />
 </ItemGroup>
 <Import Project="$(VCTargetsPath)\Microsoft.Cpp.targets" />
 <ImportGroup Label="ExtensionTargets">
 </ImportGroup>
</Project>
```

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wOF2OTTO> @j
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$vLl
z7%OBS^ZR0O4[mqx
OV\REc9@.M RWNL_qfK@<
wOF2OTTO: @f
.`f6$ Q
NB3T}1"a5tC::BU1QEX%->+g~Fm*vYP8_*y5{h'5IGVl
t;g"XBD4N7.lGGwSO({crQ5*X!"cIH1$l1\#8j~!0-
.T/bBP&.*?SBPZpOAD6x7ogG~I~.9.tY1Cdy-
vtXK\8S34Slt-q6($oPsKHu3p mZ8^|E_)=+wQhjy5J.ZM/gP2mm
<D6\9%XK52S4@e$K}%lN$ >W#cw)~_;ddtm nI@r ~y!rG[(/}X!=_9M?x{O8ys{
+4qAZ5pL
*u@uT(]8E$^0ZP~q{`u%4$Q4sB]p?Jak(3i![}fsgK@ft9!G,KdikhMm4iI!#zVetl-wnuL@x5x)MiP^:620k;{i6tDf
s$v,*#G!]|~KB%DBu$gRyy#S+"(,bZ+~&}xfU@`Ij&[d;_%Y
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 package and resulted from
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%
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% Author: E. Werner
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%               and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested & integrated patterns
 into TeX Live
%
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%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
```

% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in UTF-8
% - the only "allowed"
 TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%   http://tug.org/tex-hyphen
%
%--
%
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% The latest version of this license is in
%   http://www.latex-project.org/lppl.txt
% and version 1.3 or
 later is part of all distributions of LaTeX
% version 2005/12/01 or later.
% \message{Hyphenation patterns for Upper Sorbian, E. Werner}
% \message{Completely new revision 1997, March 22}
%
% - ccaron
% - cacute
% - ecaron
% - lslash
% - nacute
% - oacute
% - rcaron
% - scaron
% - zcaron
% - zacute
%
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 from
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%
% Source: TODO:WRITEME (2003-03-10)
% Author: Alexander I. Lebedev <swan at scon155.phys.msu.su>
%
% The above mentioned file should become obsolete,
% and the author of the original file should preferaby modify this file instead.
%
% Modificatios were needed in order to support native UTF-8 engines,
% but functionality (hopefully) didn't change in any way, at least not intentionally.
% This file is no longer stand-alone; at least for 8-bit engines
% you probably want to use loadhyph-foo.tex (which will load this file) instead.
%
% Modifications were done by Jonathan Kew, Mojca Miklavec & Arthur Reutenauer
% with help & support from:
% - Karl Berry, who gave us free hands and all resources
% - Taco Hoekwater, with useful macros
% - Hans Hagen, who did the unicodifisation of patterns already long before
%   and helped with testing, suggestions and bug reports
% - Norbert Preining, who tested
 & integrated patterns into TeX Live
%
% However, the "copyright/copyleft" owner of patterns remains the original author.

```
%
% The copyright statement of this file is thus:
%
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%    "a better world" as long as you respect the copyright of original file.
%    If you're the original author of patterns or taking over a new revolution,
%    plese remove all of the TUG comments & credits that we added here -
%    you are the Queen / the King, we are only the servants.
%
% If you want to change this file, rather than uploading directly to CTAN,
% we would be grateful if you could send it to us (http://tug.org/tex-hyphen)
% or ask for credentials for SVN repository and commit it yourself;
% we will then upload the whole "package" to CTAN.
%
% Before a new "pattern-revolution" starts,
% please try to follow some guidelines if possible:
%
% - \lccode is *forbidden*, and I really mean it
% - all the patterns should be in
 UTF-8
% - the only "allowed" TeX commands in this file are: \patterns, \hyphenation,
%   and if you really cannot do without, also \input and \message
% - in particular, please no \catcode or \lccode changes,
%   they belong to loadhyph-foo.tex,
%   and no \lefthyphenmin and \righthyphenmin,
%   they have no influence here and belong elsewhere
% - \begingroup and/or \endinput is not needed
% - feel free to do whatever you want inside comments
%
% We know that TeX is extremely powerful, but give a stupid parser
% at least a chance to read your patterns.
%
% For more unformation see
%
%    http://tug.org/tex-hyphen
%
%-----------------------------------------------------------------------------
%
% Russian hyphenation patterns, version 2003/03/10
% Copyright 1999-2003 Alexander I. Lebedev <swan@scon155.phys.msu.su>
%
% This program may be distributed and/or modified under the conditions
% of the LaTeX Project Public License, either version 1.2 or any later
% version.
%
% Patterns were
 generated with patgen from a 990,000-word list and then
% manually corrected.
```

%
% The program consists of the files ruhyphal.tex, cyryoal.tex and two
% document files README.ruhyphal and hyphen.rules. The file cyryoal.tex
% can be regenerated using mkcyryo script (a part of ruhyphen package)
% and the latest release of rus-ispell dictionaries
% <ftp://scon155.phys.msu.su/pub/russian/ispell/>.
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<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid license Element With dir Attribute in text Element 2</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <meta name="assert" content="The license element contains a text element with rtl as the value for the dir attribute." />
  <style type="text/css"><![CDATA[
   @import url("support/test-fonts.css");
   body {
    font-size: 20px;
   }
   pre
 {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test CFF Reference";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>

```
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;license url="http://w3c.org/Fonts"&gt;
  &lt;text dir="rtl"&gt;
   License without language.
  &lt;/text&gt;
 &lt;/license&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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```
<!DOCTYPE html PUBLIC "-//W3C//DTD XHTML 1.0 Strict//EN" "http://www.w3.org/TR/xhtml1/DTD/xhtml1-strict.dtd">
<!-- THIS FILE WAS AUTOMATICALLY GENERATED, DO NOT EDIT. -->
<html xmlns="http://www.w3.org/1999/xhtml">
 <head>
  <meta http-equiv="content-type" content="text/html;charset=UTF-8"/>
  <title>WOFF Test: Valid copyright Element With One Language Tagged text Element</title>
  <link rel="author" title="Tal Leming" href="http://typesupply.com" />
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="help" href="http://www.w3.org/TR/WOFF/#conform-metadata-noeffect" />
```

```
  <link rel="help" href="http://dev.w3.org/webfonts/WOFF2/spec/#Metadata" />
  <link rel="reviewer" title="Chris Lilley" href="mailto:chris@w3.org" />
  <link rel="match" href="metadatadisplay-schema-copyright-002-ref.xht" />
  <meta name="assert" content="The copyright element matches the schema. It contains one text element that has a
language tag." />
  <style type="text/css"><![CDATA[
   @import
 url("support/test-fonts.css");
   @font-face {
    font-family: "WOFF Test";
    src: url("support/metadatadisplay-schema-copyright-002.woff2") format("woff2");
   }
   body {
    font-size: 20px;
   }
   pre {
    font-size: 12px;
   }
   .test {
    font-family: "WOFF Test", "WOFF Test CFF Fallback";
    font-size: 200px;
    margin-top: 50px;
   }
  ]]></style>
 </head>
 <body>
  <p>Test passes if the word PASS appears below.</p>
  <p>The Extended Metadata Block is valid and may be displayed to the user upon request.</p>
  <div class="test">P</div>
  <p>The XML contained in the Extended Metadata Block is below.</p>
  <pre>
&lt;?xml version="1.0" encoding="UTF-8"?&gt;
&lt;metadata version="1.0"&gt;
 &lt;copyright&gt;
  &lt;text xml:lang="en"&gt;
   Copyright with "en" language.
  &lt;/text&gt;
 &lt;/copyright&gt;
&lt;/metadata&gt;
  </pre>
 </body>
</html>
```

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1.95 jaxen 1.1-beta-6

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$Id: LICENSE.txt,v 1.3 2003/06/29 18:22:02 ssanders Exp $

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*/

1.96 goterm 0.0.0-20190703233501-fc88cf888a3f

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1.97 optparse 0.1.1

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1.98 microsoft.management.infrastructure 6.3.9600.16384

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1.99 pkg-sftp 1.13.4

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1.100 rsync 3.2.5-3.el9

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1.105 vapi-authentication 1.0.0

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1.106 json-smart 2.2.1

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* /json-smart-2-2-1-sources-6-jar/net/minidev/json/parser/JSONParserByteArray.java
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* /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/DefaultMapperCollection.java
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* /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/FakeMapper.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/JsonReader.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/JSONArray.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/parser/JSONParserReader.java
*
 /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/DefaultMapperOrdered.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/parser/JSONParserInputStream.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/CollectionMapper.java
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* /json-smart-2-2-1-sources-6-jar/net/minidev/json/parser/ParseException.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/parser/JSONParserStream.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/JsonReaderI.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/writer/CompressorMapper.java
* /json-smart-2-2-1-sources-6-jar/net/minidev/json/parser/JSONParserString.java
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1.107 tenacity 7.0.0

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1.110 gdbm 1.23-1.el9

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1.112 pam 1.5.1-25.el9_6

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1.113 gson 1.4

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1.114 log4j-api 2.17.1

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1.115 geronimoj2eemanagement 1.0.1

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1.116 spf13-pflag 1.0.5

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1.117 net-pop 0.1.1

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1.118 optparse 0.2.0

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1.119 libtirpc 1.3.3-9.el9

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* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/BigDecimalHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/encoding/DeserializerFactory.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/LongHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/ServiceException.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/QNameHolder.java
*
/axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/handler/soap/SOAPMessageContext.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/DoubleHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/encoding/TypeMappingRegistry.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/CalendarHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/BooleanWrapperHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/encoding/SerializerFactory.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/ShortWrapperHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/DoubleWrapperHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/encoding/Deserializer.java
*
/axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/handler/HandlerInfo.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/IntHolder.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/handler/GenericHandler.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/handler/MessageContext.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/ServiceFactory.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/Stub.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/holders/StringHolder.java

* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/server/ServiceLifecycle.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/encoding/TypeMapping.java
* /axis-jaxrpc-sources-jar-zip/axis-jaxrpc-1-2-sources-jar/javax/xml/rpc/FactoryFinder.java

1.121 libtasn 4.16.0-9.el9

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/VCloudContextBuilder.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/handlers/ParseVCloudErrorFromHttpResponse.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/domain/VCloudLoginAsyncClient.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/compute/functions/ImagesInVCloudExpressVDC.java
*
/jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/functions/OrgNameAndVDCNameToEndpoint.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/domain/VDC.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/domain/ovf/ResourceType.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/domain/InstantiateVAppTemplateParams.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/domain/VCloudSession.java
* /jclouds-vcloud-1-0-beta-7-sources-jar/org/jclouds/vcloud/domain/internal/ReferenceTypeImpl.java
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* /jclouds-vcloud-1-0-beta-7-sources-
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jar/org/jclouds/compute/strategy/impl/EncodeTagIntoNameRunNodesAndAddToSetStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/DestroyNodeStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/NodeState.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/internal/UtilsImpl.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/predicates/OperatingSystemPredicates.java
*
/jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/reference/ComputeServiceConstants.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/OperatingSystem.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/internal/BaseComputeService.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/NodeMetadata.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/RebootNodeStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/ComputeService.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/Hardware.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/AddNodeWithTagStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/callables/RunScriptOnNode.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/internal/NodeMetadataImpl.java
*
/jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/RunScriptOnNodesException.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/Image.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/util/ComputeUtils.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/options/RunScriptOptions.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/stub/config/StubComputeServiceClientModule.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/config/ComputeServiceTimeoutsModule.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/Processor.java

* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/ComputeType.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/options/TemplateOptions.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/internal/TemplateImpl.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/ComputeMetadata.java
*
/jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/Template.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/callables/AuthorizeRSAPublicKey.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/ssh/SshException.java
* /jclouds-compute-1-0-beta-7-sources-
jar/org/jclouds/compute/strategy/PopulateDefaultLoginCredentialsForImageStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/impl/ReturnNullCredentials.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/ListNodesStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/RunNodesAndAddToSetStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/callables/InstallRSAPrivateKey.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/domain/TemplateBuilder.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/ssh/ExecResponse.java
* /jclouds-compute-1-0-beta-7-sources-
jar/org/jclouds/compute/stub/config/StubComputeServiceContextModule.java
*
/jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/strategy/GetNodeMetadataStrategy.java
* /jclouds-compute-1-0-beta-7-sources-jar/org/jclouds/compute/predicates/NodeTerminated.java
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1.137 jcommon 1.0.16

1.137.1 Available under license :

```
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* JCommon : a free general purpose class library for the Java(tm) platform
* ===========================================================
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* Contributor.java
* ----------------
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* Original Author:  David Gilbert (for Object Refinery Limited);
* Contributor(s):   -;
*
* $Id: Contributor.java,v 1.4 2005/11/16 15:58:41 taqua Exp $
*
* Changes
* -------
* 10-Dec-2001 : Version 1 (DG);
* 28-Feb-2002 : Moved into package com.jrefinery.ui.about (DG);
* 08-Oct-2002 : Fixed errors reported by Checkstyle (DG);
*
*/

package org.jfree.ui.about;

/**
* A simple class representing a contributor to a software project.
* <P>
* Used in the AboutFrame class.
*
* @author David Gilbert
*/
public class Contributor {

  /** The name. */
  private
 String name;

  /** The e-mail address. */
  private String email;

  /**
   * Creates a new contributor.
   *
```

```
     * @param name  the name.
     * @param email  the e-mail address.
     */
    public Contributor(final String name, final String email) {
        this.name = name;
        this.email = email;
    }

    /**
     * Returns the contributor's name.
     *
     * @return the contributor's name.
     */
    public String getName() {
        return this.name;
    }

    /**
     * Returns the contributor's e-mail address.
     *
     * @return the contributor's e-mail address.
     */
    public String getEmail() {
        return this.email;
    }

}
/* ===========================================================================
* JCommon : a free general purpose class library for the Java(tm) platform
* ===========================================================================
*
* (C) Copyright 2000-2005, by Object Refinery Limited and Contributors.
*
* Project Info:  http://www.jfree.org/jcommon/index.html
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```
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*
* -------------
* Licences.java
* -------------
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*
* Original Author:  David Gilbert (for Object Refinery Limited);
* Contributor(s):   -;
*
* $Id: Licences.java,v 1.4 2005/11/16 15:58:41 taqua Exp $
*
* Changes
* -------
* 26-Nov-2001 : Version 1 (DG);
* 29-Jan-2002 : Appended the instructions to the end of the GNU LGPL and GPL to comply with
*               GNU requirements (DG);
* 28-Feb-2002 : Moved to package com.jrefinery.ui.about (DG);
* 08-Oct-2002 : Fixed errors reported by Checkstyle (DG);
*
*/

package org.jfree.ui.about;

/**
* Contains the full texts of the GNU General Public Licence and the GNU Lesser General
 Public
* Licence.
* <P>
* These are used in the presentation of a standard 'About' frame.
*
* @author David Gilbert
*/
public class Licences {

  /** The GNU General Public Licence. */
  public static final String GPL = "GNU GENERAL PUBLIC LICENSE\n"
     + "\n"
     + "Version 2, June 1991\n"
     + "\n"
     + "Copyright (C) 1989, 1991 Free Software Foundation, Inc. 675 Mass Ave, Cambridge, "
     + "MA 02139, USA. Everyone is permitted to copy and distribute verbatim copies of this "
     + "license document, but changing it is not allowed.\n"
```

```
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    + "\n"
    + "The precise terms and conditions for copying, distribution and modification follow.\n"
    + "\n"
    + "GNU GENERAL PUBLIC LICENSE\n"
```

```
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"
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```

```
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    + "c) Accompany it with the information you received as to the offer to distribute "
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```

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+ "the Free Software Foundation.\n"
+ "\n"
+ "10. If you wish to incorporate parts of the Program into other free programs whose "
+ "distribution conditions are different, write to the author to ask for permission. For "
+ "software which is copyrighted by the Free Software Foundation, write to the Free "
+ "Software Foundation; we sometimes make exceptions for this. Our decision will be "
+ "guided by the two goals of preserving the free status of all derivatives of our free "
+ "software and of promoting the sharing and reuse of software generally.\n"

+ "\n"
+ "NO WARRANTY\n"
+ "\n"
+ "11. BECAUSE THE PROGRAM IS LICENSED FREE OF CHARGE, THERE IS NO WARRANTY FOR THE "
+ "PROGRAM, TO THE EXTENT PERMITTED BY APPLICABLE LAW. EXCEPT WHEN OTHERWISE STATED IN "
+ "WRITING THE COPYRIGHT HOLDERS AND/OR OTHER PARTIES PROVIDE THE PROGRAM \"AS IS\" "
+ "WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT

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NOT LIMITED "
    + "TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR
PURPOSE. "
    + "THE ENTIRE RISK AS TO THE QUALITY AND PERFORMANCE OF THE PROGRAM IS WITH
YOU. SHOULD "
    + "THE PROGRAM PROVE DEFECTIVE, YOU ASSUME THE COST OF ALL NECESSARY
SERVICING, REPAIR OR "
    + "CORRECTION.\n"
    + "\n"
    + "12. IN NO EVENT UNLESS REQUIRED BY APPLICABLE LAW OR AGREED TO IN WRITING WILL
ANY "
    + "COPYRIGHT HOLDER, OR ANY OTHER PARTY WHO MAY MODIFY AND/OR REDISTRIBUTE
THE PROGRAM AS "
    + "PERMITTED
 ABOVE, BE LIABLE TO YOU FOR DAMAGES, INCLUDING ANY GENERAL, SPECIAL, "
    + "INCIDENTAL OR CONSEQUENTIAL DAMAGES ARISING OUT OF THE USE OR INABILITY TO
USE THE "
    + "PROGRAM (INCLUDING BUT NOT LIMITED TO LOSS OF DATA OR DATA BEING RENDERED
INACCURATE "
    + "OR LOSSES SUSTAINED BY YOU OR THIRD PARTIES OR A FAILURE OF THE PROGRAM TO
OPERATE "
    + "WITH ANY OTHER PROGRAMS), EVEN IF SUCH HOLDER OR OTHER PARTY HAS BEEN
ADVISED OF THE "
    + "POSSIBILITY OF SUCH DAMAGES.\n"
    + "\n"
    + "END OF TERMS AND CONDITIONS\n"
    + "\n"
    + "\n"
    + "How to Apply These Terms to Your New Programs\n"
    + "\n"
    + "If you develop a new program, and you want it to be of the greatest possible use to "
    + "the public, the best way to achieve this is to make it free software which everyone "
    + "can redistribute and change under these terms.\n"
    + "\n"
    + "To do so, attach the following notices
 to the program.  It is safest to attach them to "
    + "the start of each source file to most effectively convey the exclusion of warranty; "
    + "and each file should have at least the \"copyright\" line and a pointer to where the "
    + "full notice is found.\n"
    + "\n"
    + "<one line to give the program's name and a brief idea of what it does.>\n"
    + "Copyright (C) <year>  <name of author>\n"
    + "\n"
    + "This program is free software; you can redistribute it and/or modify it under the "
    + "terms of the GNU General Public License as published by the Free Software Foundation; "
    + "either version 2 of the License, or (at your option) any later version.\n"
    + "\n"
    + "This program is distributed in the hope that it will be useful, but WITHOUT ANY "
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    + "WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A "
    + "PARTICULAR PURPOSE.  See the GNU General Public
License for more details.\n"
    + "\n"
    + "You should have received a copy of the GNU General Public License along with this "
    + "program; if not, write to the Free Software Foundation, Inc., 59 Temple Place, Suite "
    + "330, Boston, MA  02111-1307  USA\n"
    + "\n"
    + "Also add information on how to contact you by electronic and paper mail.\n"
    + "\n"
    + "If the program is interactive, make it output a short notice like this when it starts "
    + "in an interactive mode:\n"
    + "\n"
    + "Gnomovision version 69, Copyright (C) year name of author Gnomovision comes with "
    + "ABSOLUTELY NO WARRANTY; for details type `show w'.\n"
    + "This is free software, and you are welcome to redistribute it under certain "
    + "conditions; type `show c' for details.\n"
    + "\n"
    + "The hypothetical commands `show w' and `show c' should show the appropriate parts of "
    + "the
General Public License.  Of course, the commands you use may be called something "
    + "other than `show w' and `show c'; they could even be mouse-clicks or menu items"
    + "--whatever suits your program.\n"
    + "\n"
    + "You should also get your employer (if you work as a programmer) or your school, if "
    + "any, to sign a \"copyright disclaimer\" for the program, if necessary.  Here is a "
    + "sample; alter the names:\n"
    + "\n"
    + "Yoyodyne, Inc., hereby disclaims all copyright interest in the program `Gnomovision' "
    + "(which makes passes at compilers) written by James Hacker.\n"
    + "\n"
    + "<signature of Ty Coon>, 1 April 1989\n"
    + "Ty Coon, President of Vice\n"
    + "\n"
    + "This General Public License does not permit incorporating your program into "
    + "proprietary programs.  If your program is a subroutine library, you may consider it "
    + "more useful
to permit linking proprietary applications with the library.  If this is "
    + "what you want to do, use the GNU Library General Public License instead of this "
    + "License.\n";

  /** The GNU Lesser General Public Licence. */
  public static final String LGPL = "GNU LESSER GENERAL PUBLIC LICENSE\n"
    + "\n"
    + "Version 2.1, February 1999\n"
    + "\n"
    + "Copyright (C) 1991, 1999 Free Software Foundation, Inc.\n"
    + "59 Temple Place, Suite 330, Boston, MA  02111-1307  USA\n"
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    + "Everyone is permitted to copy and distribute verbatim copies of this license document, "
    + "but changing it is not allowed.\n"
    + "\n"
    + "[This is the first released version of the Lesser GPL.  It also counts as the "
    + "successor of the GNU Library Public License, version 2, hence the version number "
    + "2.1.]\n"
    + "\n"
    + "Preamble\n"
    + "\n"
    + "The licenses for most
software are designed to take away your freedom to share and "
    + "change it. By contrast, the GNU General Public Licenses are intended to guarantee your "
    + "freedom to share and change free software--to make sure the software is free for all "
    + "its users.\n"
    + "\n"
    + "This license, the Lesser General Public License, applies to some specially designated "
    + "software packages--typically libraries--of the Free Software Foundation and other "
    + "authors who decide to use it. You can use it too, but we suggest you first think "
    + "carefully about whether this license or the ordinary General Public License is the "
    + "better strategy to use in any particular case, based on the explanations below.\n"
    + "\n"
    + "When we speak of free software, we are referring to freedom of use, not price. Our "
    + "General Public Licenses are designed to make sure that you have the freedom to "

    + "distribute copies of free software (and charge for this service if you wish); that you "
    + "receive source code or can get it if you want it; that you can change the software and "
    + "use pieces of it in new free programs; and that you are informed that you can do these "
    + "things.\n"
    + "\n"
    + "To protect your rights, we need to make restrictions that forbid distributors to deny "
    + "you these rights or to ask you to surrender these rights. These restrictions translate "
    + "to certain responsibilities for you if you distribute copies of the library or if you "
    + "modify it.\n"
    + "\n"
    + "For example, if you distribute copies of the library, whether gratis or for a fee, you "
    + "must give the recipients all the rights that we gave you. You must make sure that "
    + "they, too, receive or can get the source code. If you link other code with the "
    + "library, you must
provide complete object files to the recipients, so that they can "
    + "relink them with the library after making changes to the library and recompiling it. "
    + "And you must show them these terms so they know their rights.\n"
    + "\n"
    + "We protect your rights with a two-step method: (1) we copyright the library, and (2) "
    + "we offer you this license, which gives you legal permission to copy, distribute and/or "
    + "modify the library.\n"
    + "\n"
    + "To protect each distributor, we want to make it very clear that there is no warranty "
    + "for the free library. Also, if the library is modified by someone else and passed on, "
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    + "the recipients should know that what they have is not the original version, so that "
    + "the original author's reputation will not be affected by problems that might be "
    + "introduced by others.\n"
    + "\n"
    + "Finally, software patents pose
a constant threat to the existence of any free program. "
    + "We wish to make sure that a company cannot effectively restrict the users of a free "
    + "program by obtaining a restrictive license from a patent holder. Therefore, we insist "
    + "that any patent license obtained for a version of the library must be consistent with "
    + "the full freedom of use specified in this license.\n"
    + "\n"
    + "Most GNU software, including some libraries, is covered by the ordinary GNU General "
    + "Public License. This license, the GNU Lesser General Public License, applies to "
    + "certain designated libraries, and is quite different from the ordinary General Public "
    + "License. We use this license for certain libraries in order to permit linking those "
    + "libraries into non-free programs.\n"
    + "\n"
    + "When a program is linked with a library, whether statically or using a shared library, "

  + "the combination of the two is legally speaking a combined work, a derivative of the "
    + "original library. The ordinary General Public License therefore permits such linking "
    + "only if the entire combination fits its criteria of freedom. The Lesser General Public "
    + "License permits more lax criteria for linking other code with the library.\n"
    + "\n"
    + "We call this license the \"Lesser\" General Public License because it does Less to "
    + "protect the user's freedom than the ordinary General Public License. It also provides "
    + "other free software developers Less of an advantage over competing non-free programs. "
    + "These disadvantages are the reason we use the ordinary General Public License for many "
    + "libraries. However, the Lesser license provides advantages in certain special "
    + "circumstances.\n"
    + "\n"
    + "For example, on rare occasions, there may be a special
need to encourage the widest "
    + "possible use of a certain library, so that it becomes a de-facto standard. To achieve "
    + "this, non-free programs must be allowed to use the library. A more frequent case is "
    + "that a free library does the same job as widely used non-free libraries. In this case, "
    + "there is little to gain by limiting the free library to free software only, so we use "
    + "the Lesser General Public License.\n"
    + "\n"
    + "In other cases, permission to use a particular library in non-free programs enables a "
    + "greater number of people to use a large body of free software. For example, permission "
    + "to use the GNU C Library in non-free programs enables many more people to use the "
    + "whole GNU operating system, as well as its variant, the GNU/Linux operating system.\n"
    + "\n"
    + "Although the Lesser General Public License is Less protective of the users' freedom,
"
    + "it does ensure that the user of a program that is linked with the Library has the "
    + "freedom and the wherewithal to run that program using a modified version of the "
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    + "Library.\n"
    + "\n"
    + "The precise terms and conditions for copying, distribution and modification follow. "
    + "Pay close attention to the difference between a \"work based on the library\" and a "
    + "\"work that uses the library\". The former contains code derived from the library, "
    + "whereas the latter must be combined with the library in order to run.\n"
    + "\n"
    + "TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION\n"
    + "\n"
    + "0. This License Agreement applies to any software library or other program which "
    + "contains a notice placed by the copyright holder or other authorized party saying it "
    + "may be distributed under the terms of this Lesser General Public License
(also called "
    + "\"this License\"). Each licensee is addressed as \"you\"."
    + "\n"
    + "A \"library\" means a collection of software functions and/or data prepared so as to "
    + "be conveniently linked with application programs (which use some of those functions "
    + "and data) to form executables.\n"
    + "\n"
    + "The \"Library\", below, refers to any such software library or work which has been "
    + "distributed under these terms. A \"work based on the Library\" means either the "
    + "Library or any derivative work under copyright law: that is to say, a work containing "
    + "the Library or a portion of it, either verbatim or with modifications and/or "
    + "translated straightforwardly into another language. (Hereinafter, translation is "
    + "included without limitation in the term \"modification\".)\n"
    + "\n"
    + "\"Source code\" for a work means the preferred form of
the work for making "
    + "modifications to it. For a library, complete source code means all the source code for "
    + "all modules it contains, plus any associated interface definition files, plus the "
    + "scripts used to control compilation and installation of the library.\n"
    + "\n"
    + "Activities other than copying, distribution and modification are not covered by this "
    + "License; they are outside its scope. The act of running a program using the Library is "
    + "not restricted, and output from such a program is covered only if its contents "
    + "constitute a work based on the Library (independent of the use of the Library in a "
    + "tool for writing it). Whether that is true depends on what the Library does and what "
    + "the program that uses the Library does."
    + "\n"
    + "1. You may copy and distribute verbatim copies of the Library's complete source code "
    + "as you receive
it, in any medium, provided that you conspicuously and appropriately "
    + "publish on each copy an appropriate copyright notice and disclaimer of warranty; keep "
    + "intact all the notices that refer to this License and to the absence of any warranty; "
    + "and distribute a copy of this License along with the Library.\n"
    + "\n"
    + "You may charge a fee for the physical act of transferring a copy, and you may at your "
    + "option offer warranty protection in exchange for a fee.\n"
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+ "\n"
+ "2. You may modify your copy or copies of the Library or any portion of it, thus "
+ "forming a work based on the Library, and copy and distribute such modifications or "
+ "work under the terms of Section 1 above, provided that you also meet all of these "
+ "conditions:\n"
+ "\n"
+ "* a) The modified work must itself be a software library.\n"
+ "* b) You must cause the
files modified to carry prominent notices stating that you "
+ "changed the files and the date of any change.\n"
+ "* c) You must cause the whole of the work to be licensed at no charge to all third "
+ "parties under the terms of this License.\n"
+ "* d) If a facility in the modified Library refers to a function or a table of data "
+ "to be supplied by an application program that uses the facility, other than as an "
+ "argument passed when the facility is invoked, then you must make a good faith effort "
+ "to ensure that, in the event an application does not supply such function or table, "
+ "the facility still operates, and performs whatever part of its purpose remains "
+ "meaningful.\n"
+ "\n"
+ "(For example, a function in a library to compute square roots has a purpose that is "
+ "entirely well-defined independent of the application. Therefore, Subsection 2d "
+ "requires that any application-supplied function or table used by this function must be "
+ "optional: if the application does not supply it, the square root function must still "
+ "compute square roots.)\n"
+ "\n"
+ "These requirements apply to the modified work as a whole. If identifiable sections of "
+ "that work are not derived from the Library, and can be reasonably considered "
+ "independent and separate works in themselves, then this License, and its terms, do not "
+ "apply to those sections when you distribute them as separate works. But when you "
+ "distribute the same sections as part of a whole which is a work based on the Library, "
+ "the distribution of the whole must be on the terms of this License, whose permissions "
+ "for other licensees extend to the entire whole, and thus to each and every part "
+ "regardless of who wrote it.\n"
+ "\n"

+ "Thus, it is not the intent of this section to claim rights or contest your rights to "
+ "work written entirely by you; rather, the intent is to exercise the right to control "
+ "the distribution of derivative or collective works based on the Library.\n"
+ "\n"
+ "In addition, mere aggregation of another work not based on the Library with the "
+ "Library (or with a work based on the Library) on a volume of a storage or distribution "
+ "medium does not bring the other work under the scope of this License.\n"
+ "\n"
+ "3. You may opt to apply the terms of the ordinary GNU General Public License instead "
+ "of this License to a given copy of the Library. To do this, you must alter all the "
+ "notices that refer to this License, so that they refer to the ordinary GNU General "
+ "Public License, version 2, instead of to this License. (If a newer version than "
+ "version 2 of

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the ordinary GNU General Public License has appeared, then you can "
    + "specify that version instead if you wish.) Do not make any other change in these "
    + "notices.\n"
    + "\n"
    + "Once this change is made in a given copy, it is irreversible for that copy, so the "
    + "ordinary GNU General Public License applies to all subsequent copies and derivative "
    + "works made from that copy.\n"
    + "\n"
    + "This option is useful when you wish to copy part of the code of the Library into a "
    + "program that is not a library.\n"
    + "\n"
    + "4. You may copy and distribute the Library (or a portion or derivative of it, under "
    + "Section 2) in object code or executable form under the terms of Sections 1 and 2 above "
    + "provided that you accompany it with the complete corresponding machine-readable source "
    + "code, which must be distributed under the terms of Sections 1 and 2 above
on a medium "
    + "customarily used for software interchange.\n"
    + "\n"
    + "If distribution of object code is made by offering access to copy from a designated "
    + "place, then offering equivalent access to copy the source code from the same place "
    + "satisfies the requirement to distribute the source code, even though third parties are "
    + "not compelled to copy the source along with the object code.\n"
    + "\n"
    + "5. A program that contains no derivative of any portion of the Library, but is "
    + "designed to work with the Library by being compiled or linked with it, is called a "
    + "\"work that uses the Library\". Such a work, in isolation, is not a derivative work of "
    + "the Library, and therefore falls outside the scope of this License.\n"
    + "\n"
    + "However, linking a \"work that uses the Library\" with the Library creates an "
    + "executable that is a derivative
of the Library (because it contains portions of the "
    + "Library), rather than a \"work that uses the library\". The executable is therefore "
    + "covered by this License. Section 6 states terms for distribution of such executables.\n"
    + "\n"
    + "When a \"work that uses the Library\" uses material from a header file that is part of "
    + "the Library, the object code for the work may be a derivative work of the Library even "
    + "though the source code is not. Whether this is true is especially significant if the "
    + "work can be linked without the Library, or if the work is itself a library. The "
    + "threshold for this to be true is not precisely defined by law.\n"
    + "\n"
    + "If such an object file uses only numerical parameters, data structure layouts and "
    + "accessors, and small macros and small inline functions (ten lines or less in length), "
    + "then the use of the object file
is unrestricted, regardless of whether it is legally a "
    + "derivative work. (Executables containing this object code plus portions of the Library "
    + "will still fall under Section 6.)\n"
    + "\n"
    + "Otherwise, if the work is a derivative of the Library, you may distribute the object "
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    + "code for the work under the terms of Section 6. Any executables containing that work "
    + "also fall under Section 6, whether or not they are linked directly with the Library "
    + "itself.\n"
    + "\n"
    + "6. As an exception to the Sections above, you may also combine or link a \"work that "
    + "uses the Library\" with the Library to produce a work containing portions of the "
    + "Library, and distribute that work under terms of your choice, provided that the terms "
    + "permit modification of the work for the customer's own use and reverse engineering for "
    + "debugging such modifications.\n"
     + "\n"
    + "You must give prominent notice with each copy of the work that the Library is used in "
    + "it and that the Library and its use are covered by this License. You must supply a "
    + "copy of this License. If the work during execution displays copyright notices, you "
    + "must include the copyright notice for the Library among them, as well as a reference "
    + "directing the user to the copy of this License. Also, you must do one of these "
    + "things:\n"
    + "\n"
    + "*    a) Accompany the work with the complete corresponding machine-readable source "
    + "code for the Library including whatever changes were used in the work (which must be "
    + "distributed under Sections 1 and 2 above); and, if the work is an executable linked "
    + "with the Library, with the complete machine-readable \"work that uses the Library\", "
    + "as object code and/or source code, so that the user
can modify the Library and then "
    + "relink to produce a modified executable containing the modified Library. (It is "
    + "understood that the user who changes the contents of definitions files in the Library "
    + "will not necessarily be able to recompile the application to use the modified "
    + "definitions.)\n"
    + "*    b) Use a suitable shared library mechanism for linking with the Library. A "
    + "suitable mechanism is one that (1) uses at run time a copy of the library already "
    + "present on the user's computer system, rather than copying library functions into the "
    + "executable, and (2) will operate properly with a modified version of the library, if "
    + "the user installs one, as long as the modified version is interface-compatible with "
    + "the version that the work was made with.\n"
    + "*    c) Accompany the work with a written offer, valid for at least three years, to "
    +
"give the same user the materials specified in Subsection 6a, above, for a charge no "
    + "more than the cost of performing this distribution.\n"
    + "*    d) If distribution of the work is made by offering access to copy from a "
    + "designated place, offer equivalent access to copy the above specified materials from "
    + "the same place.\n"
    + "*    e) Verify that the user has already received a copy of these materials or that "
    + "you have already sent this user a copy.\n"
    + "\n"
    + "For an executable, the required form of the \"work that uses the Library\" must "
    + "include any data and utility programs needed for reproducing the executable from it. "
    + "However, as a special exception, the materials to be distributed need not include "
    + "anything that is normally distributed (in either source or binary form) with the major "
    + "components (compiler, kernel, and so on) of the operating
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system on which the "
+ "executable runs, unless that component itself accompanies the executable.\n"
+ "\n"
+ "It may happen that this requirement contradicts the license restrictions of other "
+ "proprietary libraries that do not normally accompany the operating system. Such a "
+ "contradiction means you cannot use both them and the Library together in an executable "
+ "that you distribute.\n"
+ "\n"
+ "7. You may place library facilities that are a work based on the Library side-by-side "
+ "in a single library together with other library facilities not covered by this "
+ "License, and distribute such a combined library, provided that the separate "
+ "distribution of the work based on the Library and of the other library facilities is "
+ "otherwise permitted, and provided that you do these two things:\n"
+ "\n"
+ "* a) Accompany the combined library
with a copy of the same work based on the "
+ "Library, uncombined with any other library facilities. This must be distributed under "
+ "the terms of the Sections above.\n"
+ "* b) Give prominent notice with the combined library of the fact that part of it is "
+ "a work based on the Library, and explaining where to find the accompanying uncombined "
+ "form of the same work.\n"
+ "\n"
+ "8. You may not copy, modify, sublicense, link with, or distribute the Library except "
+ "as expressly provided under this License. Any attempt otherwise to copy, modify, "
+ "sublicense, link with, or distribute the Library is void, and will automatically "
+ "terminate your rights under this License. However, parties who have received copies, "
+ "or rights, from you under this License will not have their licenses terminated so "
+ "long as such parties remain in full compliance.\n"
+ "\n"
+ "9. You are not required to accept this License, since you have not signed it. However, "
+ "nothing else grants you permission to modify or distribute the Library or its "
+ "derivative works. These actions are prohibited by law if you do not accept this "
+ "License. Therefore, by modifying or distributing the Library (or any work based on the "
+ "Library), you indicate your acceptance of this License to do so, and all its terms and "
+ "conditions for copying, distributing or modifying the Library or works based on it.\n"
+ "\n"
+ "10. Each time you redistribute the Library (or any work based on the Library), the "
+ "recipient automatically receives a license from the original licensor to copy, "
+ "distribute, link with or modify the Library subject to these terms and conditions. You "
+ "may not impose any further restrictions on the recipients' exercise of the rights
"
+ "granted herein. You are not responsible for enforcing compliance by third parties with "
+ "this License.\n"
+ "\n"
+ "11. If, as a consequence of a court judgment or allegation of patent infringement or "
+ "for any other reason (not limited to patent issues), conditions are imposed on you "
+ "(whether by court order, agreement or otherwise) that contradict the conditions of "
+ "this License, they do not excuse you from the conditions of this License. If you "

+ "cannot distribute so as to satisfy simultaneously your obligations under this License "
+ "and any other pertinent obligations, then as a consequence you may not distribute the "
+ "Library at all. For example, if a patent license would not permit royalty-free "
+ "redistribution of the Library by all those who receive copies directly or indirectly "
+ "through you, then the only way you could satisfy both it
and this License would be to "
+ "refrain entirely from distribution of the Library.\n"
+ "\n"
+ "If any portion of this section is held invalid or unenforceable under any particular "
+ "circumstance, the balance of the section is intended to apply, and the section as a "
+ "whole is intended to apply in other circumstances.\n"
+ "\n"
+ "It is not the purpose of this section to induce you to infringe any patents or other "
+ "property right claims or to contest validity of any such claims; this section has the "
+ "sole purpose of protecting the integrity of the free software distribution system "
+ "which is implemented by public license practices. Many people have made generous "
+ "contributions to the wide range of software distributed through that system in "
+ "reliance on consistent application of that system; it is up to the author/donor to "
+ "decide if he or
she is willing to distribute software through any other system and a "
+ "licensee cannot impose that choice.\n"
+ "\n"
+ "This section is intended to make thoroughly clear what is believed to be a consequence "
+ "of the rest of this License.\n"
+ "\n"
+ "12. If the distribution and/or use of the Library is restricted in certain countries "
+ "either by patents or by copyrighted interfaces, the original copyright holder who "
+ "places the Library under this License may add an explicit geographical distribution "
+ "limitation excluding those countries, so that distribution is permitted only in or "
+ "among countries not thus excluded. In such case, this License incorporates the "
+ "limitation as if written in the body of this License.\n"
+ "\n"
+ "13. The Free Software Foundation may publish revised and/or new versions of the Lesser "
+ "General Public License
from time to time. Such new versions will be similar in spirit "
+ "to the present version, but may differ in detail to address new problems or concerns.\n"
+ "\n"
+ "Each version is given a distinguishing version number. If the Library specifies a "
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    + "\n"
    + "<signature of Ty Coon>, 1 April 1990\n"
    + "Ty Coon, President of Vice\n"
    + "\n"
    + "That's all there is to it!\n";

  /** The singleton instance of this class. */
  private static Licences singleton;

  /**
   * Returns a reference to this class.
   *
   * @return the instance reference.
   */
  public static Licences getInstance() {
     if (singleton == null) {
        singleton = new Licences();
```

```
        }
        return singleton;
    }

    /**
     * Returns the GPL text in a non static way to prevent the compiler
     * to copy the contents of the field.
     *
     * @return the GPL licence
    text.
     */
    public String getGPL() {
        return GPL;
    }

    /**
     * Returns the LGPL text in a non static way to prevent the compiler
     * to copy the contents of the field.
     *
     * @return the LGPL licence text.
     */
    public String getLGPL() {
        return LGPL;
    }
}
```

1.138 tomcat-i18n-es 9.0.108

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1.139 pcre2 10.40-2.el9

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1.140 net-protocol 0.1.2

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1.141 maverick 1.7.35

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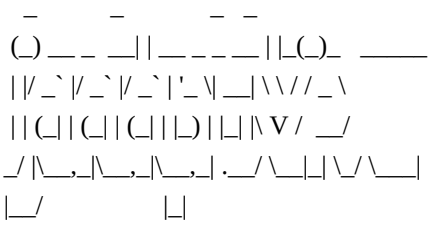
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1.142 nuget.frameworks 6.9.1

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1.143 tomcat-i18n-ru 9.0.108

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Jean-loup Gailly Mark Adler
jloup@gzip.org madler@alumni.caltech.edu

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The following list of people, sorted by last name, have contributed
code or patches to this implementation of sudo since I began
maintaining it in 1993. This list is known to be incomplete--if
you believe you should be listed, please send a note to sudo@sudo.ws.

Ackeret, Matt
Adler, Mark
Allbery, Russ
Anderson, Jamie
Andrew, Nick
Andric, Dimitry
Barron, Danny
Bates, Tom
Behan, Zdenk
Bellis, Ray
Benali, Elias
Beverly, Jamie
Boardman, Spider
Bos, Sander
Bostley, P.J.
Bowes, Keith
Boyce, Keith Garry
Brantley, Michael
Braun, Rob
Bezina, Pavel
Brooks, Piete
Brown, Jerry
Burr, Michael E
Burton, Ross
Bussjaeger, Andreas
Calvin, Gary
Campbell, Aaron
Chazelas, Stephane
Cheloha, Scott
ek, Vtzslav
Coleman, Chris
Corzine, Deven T.
Cusack, Frank
Dai, Wei
Dill, David
Earickson, Jeff
Eckhardt, Drew
Edgington, Ben
Esipovich, Marc
Espie, Marc

Faigon, Ariel
Farrell, Brian
Fobes, Steve
Frysinger, Mike
G., Daniel Richard
Gailly, Jean-loup
Gelman, Stephen
Gerraty, Simon J.
Graber, Stephane
Guillory, B.
Hayman, Randy M.
Henke, Joachim
Hideaki, Yoshifuji
Hieb, Dave
Holloway, Nick
Hoover, Adam
Hunter, Michael T.
Hutchings, Ben
Irrgang, Eric
Jackson, Brian
Jackson, John R.
Jackson, Richard L., Jr.
Janssen, Mark
Jepeway, Chris
Jorge, Joel Pele
Jover, Guillem
Juhani, Timo
Kikuchi, Ayamura
Kadow, Kevin
Kasal, Stepan
Kienenberger, Mike
King, Dale
King, Michael
Klyachkin, Andrey
Knoble, Jim
Knox, Tim
Komarnitsky, Alek O.
Kondrashov, Nikolai
Kopeek, Daniel
Kranenburg, Paul
Krause, David
Lakin, Eric
Larsen, Case
Levin, Dmitry V.
Libby, Kendall
Lobbes, Phillip E.
McIntyre, Jason
MacKenzie, David J.

McLaughlin, Tom
Makey, Jeff
Mallayya, Sangamesh
Manner, Rbert
Marchionna, Michael D.
Markham, Paul
Martinian, Emin
Meskes, Michael
Michael, David
Miller, Todd C.
Minier, Loc
Moffat, Darren
Moldung, Jan Thomas
Morris, Charles
Mueller, Andreas
Mller, Dworkin
Nieusma, Jeff
Nikitser, Peter A.
Nussel, Ludwig
Orbn, Lszl
Ouellet, Jean-Philippe
Paquet, Eric
Paradis, Chantal
Pasteleurs, Frederic
Percival, Ted
Perera, Andres
Peron, Christian S.J.
Peschel, Aaron
Peslyak, Alexander
Peterson, Toby
Petten, Diego Elio
Pickett, Joel
Plotnick, Alex
de Raadt, Theo
Rasch, Gudleik
Reid, Steve
Richards, Matt
Rossum, Guido van
Rouillard, John P.
Rowe, William A., Jr.
Roy, Alain
Ruusame, Elan
Ryabinkin, Eygene
Sato, Yuichi
Snchez, Wilfredo
Sanders, Miguel
Sasaki, Kan
Saucier, Jean-Francois

Schoenfeld, Patrick
Schuring, Arno
Schwarze, Ingo
Scott, Dougal

Sieger, Nick
Simon, Thor Lancelot
Slemko, Marc
Smith, Andy
Sobrado, Igor
Soulen, Steven
Spangler, Aaron
Spradling, Cloyce D.
Spradling, Michael
Stier, Matthew
Stoeckmann, Tobias
Street, Russell
Stritzky, Tilo
Stroucken, Michael
Tarrall, Robert
Thomas, Matthew
Todd, Giles
Toft, Martin
Torek, Chris
Tucker, Darren
Uhl, Robert
Uzel, Petr
Valery, Reznic
Van Dinter, Theo
Venckus, Martynas
de Vries, Maarten
Wagner, Klaus
Walsh, Dan
Warburton, John
Webb, Kirk
Wetzel, Timm
Wieringen, Marco van
Wilk, Jakub
Winiger, Gary
Wood, David
Zacarias, Gustavo
Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see https://translationproject.org for more details.

Albuquerque, Pedro

Blttermann, Mario
Bogusz, Jakub
Buo-ren, Lin
Casagrande, Milo
Castro, Felipe
Cho, Seong-ho
Chornoivan, Yuri
Diguez, Francisco
Fontenelle, Rafael
Garca-Fontes, Walter
Gezer, Volkan
Hamasaki, Takeshi
Hamming, Peter
Hansen, Joe
Hantrais, Frdric
Hein, Jochen
Hufthammer, Karl Ove
Jerovek, Damir
Karvonen, Jorma
Kazik, Duan
Kelemen, Gbor
Keeci, Mehmet
Koir, Klemen
Kozlov, Yuri
Kramer, Jakob
Krznar, Tomislav
Marchal, Frdric
Margeviius, Algimantas
Maryanov, Pavel
Florentina Muat
Nurmi, Lauri
Nikoli, Miroslav
Nylander, Daniel
Pan, Yi-Jyun
Psa, Petr
Puente, Enol
Putanec, Boidar
Qun, Trn Ngc
Rasmussen, Sebastian
Regueiro, Leandro
Sarer, zgr
Sendn, Abel
erbnescu, Daniel
Sikrom, ka
Spingos, Dimitris
Taniguchi, Yasuaki
Tomat, Fbio
r, Balzs

Uranga, Mikel Olasagasti
Vorotnikov, Artem
Wang, Wylmer
Yang, Boyuan

The following people designed the artwork used on the sudo website:

Shield logo: Badger, Trent
Sandwich logo: Stillman, Mark

1.146 e2fsprogs 1.46.5-7.el9

1.146.1 Available under license :

This package was added to the e2fsprogs debian source package by
Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

http://sourceforge.net/projects/e2fsprogs

Upstream Author: Theodore Ts'o <tytso@mit.edu>

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This is the Debian GNU/Linux prepackaged version of the static EXT2
file system consistency checker (e2fsck.static). The EXT2 utilities
were written by Theodore Ts'o <tytso@mit.edu> and Remy Card
<card@masi.ibp.fr>.

Sources were obtained from http://sourceforge.net/projects/e2fsprogs

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```
#
# This is a Makefile stub which handles the creation of BSD shared
# libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#

all:: image

real-subdirs:: Makefile
 @echo " MKDIR pic"
 @mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:  $(BSD_LIB)

$(BSD_LIB): $(OBJS)
 (cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBJS))
 $(MV) pic/$(BSD_LIB) .
 $(RM) -f ../$(BSD_LIB)
 (cd ..; $(LN) $(LINK_BUILD_FLAGS) \
  `echo $(my_dir) | sed -e 's;lib/;;'`/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
 @echo " INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
 @$(INSTALL_PROGRAM) $(BSD_LIB) \
  $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
 @-$(LDCONFIG)

install-strip: install
```

```
install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall::
 $(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
 $(RM)
 -rf pic
 $(RM) -f $(BSD_LIB)
 $(RM) -f ../$(BSD_LIB)
```

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This is the Debian GNU/Linux prepackaged version of the translation
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Theodore Ts'o <tytso@mit.edu> on Fri Dec 14 22:24:35 EST 2007

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*/
Index: tdbsa/tdb.c
===
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
 /*
-   Unix SMB/CIFS implementation.
+   trivial database library - standalone version

-   trivial database library - private includes
-
-   Copyright (C) Andrew Tridgell   2005
+   Copyright (C) Andrew Tridgell   1999-2005
+   Copyright (C) Jeremy Allison   2000-2006
+   Copyright (C) Paul `Rusty' Russell   2000

     ** NOTE! The following LGPL license applies to the tdb
     ** library. This does NOT imply that all of Samba is released
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Gadi Oxman, August 1995

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Theodore Ts'o
23-June-2007

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compelled
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Whether this is true is especially significant if the work can be
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or if the work is itself a library. The
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If such an object file uses only numerical parameters, data
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functions (ten lines or less in length), then the use of the object
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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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relink to produce a modified
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c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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1.149 llvm 19.1.7-2.el9

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```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK:       // %bb.0:
; CHECK-NEXT:    ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT:    mov v0.d[1], v0.d[0]
```

```
; CHECK-NEXT:    ret
 %cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
 %cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
 %r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
 ret <4 x i32> %r
}
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --include-generated-
funcs
; RUN: opt -S -passes=verify,iroutliner -ir-outlining-no-cost < %s | FileCheck %s
; RUN: opt -S -passes=verify,iroutliner -ir-outlining-no-cost < %s --try-experimental-debuginfo-iterators | FileCheck
%s

; This test checks that debug info is recognized as able to be extracted along
; with the other instructions, but is not included in the consolidated function.

define void @function1() !dbg !6 {
entry:
 %a = alloca i32, align 4, !dbg !17
 call void @llvm.dbg.value(metadata ptr %a, metadata !9, metadata !DIExpression()), !dbg !17
 %b = alloca i32, align 4, !dbg !18
 call void @llvm.dbg.value(metadata ptr %b, metadata !11, metadata !DIExpression()), !dbg !18
 %c = alloca i32, align 4, !dbg !19
 call void @llvm.dbg.value(metadata ptr %c, metadata !12, metadata !DIExpression()), !dbg !19
 store i32 2, ptr %a, align 4, !dbg !20
 store i32 3, ptr %b, align 4, !dbg !21
 store
 i32 4, ptr %c, align 4, !dbg !22
 %al = load i32, ptr %a, align 4, !dbg !23
 call void @llvm.dbg.value(metadata i32 %al, metadata !13, metadata !DIExpression()), !dbg !23
 %bl = load i32, ptr %b, align 4, !dbg !24
 call void @llvm.dbg.value(metadata i32 %bl, metadata !15, metadata !DIExpression()), !dbg !24
 %cl = load i32, ptr %c, align 4, !dbg !25
 call void @llvm.dbg.value(metadata i32 %cl, metadata !16, metadata !DIExpression()), !dbg !25
 ret void, !dbg !26
}

define void @function2() !dbg !27 {
entry:
 %a = alloca i32, align 4, !dbg !35
 call void @llvm.dbg.value(metadata ptr %a, metadata !29, metadata !DIExpression()), !dbg !35
 %b = alloca i32, align 4, !dbg !36
 call void @llvm.dbg.value(metadata ptr %b, metadata !30, metadata !DIExpression()), !dbg !36
 %c = alloca i32, align 4, !dbg !37
 call void @llvm.dbg.value(metadata ptr %c, metadata !31, metadata !DIExpression()), !dbg !37
 store i32 2, ptr %a, align 4, !dbg !38
 store i32 3, ptr %b, align 4, !dbg !39
  store i32 4, ptr %c, align 4, !dbg !40
 %al = load i32, ptr %a, align 4, !dbg !41
```

```
 call void @llvm.dbg.value(metadata i32 %al, metadata !32, metadata !DIExpression()), !dbg !41
 %bl = load i32, ptr %b, align 4, !dbg !42
 call void @llvm.dbg.value(metadata i32 %bl, metadata !33, metadata !DIExpression()), !dbg !42
 %cl = load i32, ptr %c, align 4, !dbg !43
 call void @llvm.dbg.value(metadata i32 %cl, metadata !34, metadata !DIExpression()), !dbg !43
 ret void, !dbg !44
}


; Function Attrs: nounwind readnone speculatable willreturn
declare void @llvm.dbg.value(metadata, metadata, metadata) #0

attributes #0 = { nounwind readnone speculatable willreturn }

!llvm.dbg.cu = !{!0}
!llvm.debugify = !{!3, !4}
!llvm.module.flags = !{!5}

!0 = distinct !DICompileUnit(language: DW_LANG_C, file: !1, producer: "debugify", isOptimized: true,
runtimeVersion: 0, emissionKind: FullDebug, enums: !2)
!1 = !DIFile(filename: "legal-debug.ll", directory: "/")
!2 = !{}
!3 = !{i32 20}
!4 = !{i32 12}
!5
 = !{i32 2, !"Debug Info Version", i32 3}
!6 = distinct !DISubprogram(name: "function1", linkageName: "function1", scope: null, file: !1, line: 1, type: !7,
scopeLine: 1, spFlags: DISPFlagDefinition | DISPFlagOptimized, unit: !0, retainedNodes: !8)
!7 = !DISubroutineType(types: !2)
!8 = !{!9, !11, !12, !13, !15, !16}
!9 = !DILocalVariable(name: "1", scope: !6, file: !1, line: 1, type: !10)
!10 = !DIBasicType(name: "ty64", size: 64, encoding: DW_ATE_unsigned)
!11 = !DILocalVariable(name: "2", scope: !6, file: !1, line: 2, type: !10)
!12 = !DILocalVariable(name: "3", scope: !6, file: !1, line: 3, type: !10)
!13 = !DILocalVariable(name: "4", scope: !6, file: !1, line: 7, type: !14)
!14 = !DIBasicType(name: "ty32", size: 32, encoding: DW_ATE_unsigned)
!15 = !DILocalVariable(name: "5", scope: !6, file: !1, line: 8, type: !14)
!16 = !DILocalVariable(name: "6", scope: !6, file: !1, line: 9, type: !14)
!17 = !DILocation(line: 1, column: 1, scope: !6)
!18 = !DILocation(line: 2, column: 1, scope:
 !6)
!19 = !DILocation(line: 3, column: 1, scope: !6)
!20 = !DILocation(line: 4, column: 1, scope: !6)
!21 = !DILocation(line: 5, column: 1, scope: !6)
!22 = !DILocation(line: 6, column: 1, scope: !6)
!23 = !DILocation(line: 7, column: 1, scope: !6)
!24 = !DILocation(line: 8, column: 1, scope: !6)
!25 = !DILocation(line: 9, column: 1, scope: !6)
```

```
!26 = !DILocation(line: 10, column: 1, scope: !6)
!27 = distinct !DISubprogram(name: "function2", linkageName: "function2", scope: null, file: !1, line: 11, type: !7,
scopeLine: 11, spFlags: DISPFlagDefinition | DISPFlagOptimized, unit: !0, retainedNodes: !28)
!28 = !{!29, !30, !31, !32, !33, !34}
!29 = !DILocalVariable(name: "7", scope: !27, file: !1, line: 11, type: !10)
!30 = !DILocalVariable(name: "8", scope: !27, file: !1, line: 12, type: !10)
!31 = !DILocalVariable(name: "9", scope: !27, file: !1, line: 13, type: !10)
!32 = !DILocalVariable(name: "10", scope: !27, file: !1, line: 17, type: !14)
!33 = !DILocalVariable(name: "11", scope:
 !27, file: !1, line: 18, type: !14)
!34 = !DILocalVariable(name: "12", scope: !27, file: !1, line: 19, type: !14)
!35 = !DILocation(line: 11, column: 1, scope: !27)
!36 = !DILocation(line: 12, column: 1, scope: !27)
!37 = !DILocation(line: 13, column: 1, scope: !27)
!38 = !DILocation(line: 14, column: 1, scope: !27)
!39 = !DILocation(line: 15, column: 1, scope: !27)
!40 = !DILocation(line: 16, column: 1, scope: !27)
!41 = !DILocation(line: 17, column: 1, scope: !27)
!42 = !DILocation(line: 18, column: 1, scope: !27)
!43 = !DILocation(line: 19, column: 1, scope: !27)
!44 = !DILocation(line: 20, column: 1, scope: !27)
; CHECK-LABEL: @function1(
; CHECK-NEXT:  entry:
; CHECK-NEXT:    [[A:%.*]] = alloca i32, align 4, !dbg [[DBG17:![0-9]+]]
; CHECK-NEXT:      #dbg_value(ptr [[A]], [[META9:![0-9]+]], !DIExpression(), [[DBG17]])
; CHECK-NEXT:    [[B:%.*]] = alloca i32, align 4, !dbg [[DBG18:![0-9]+]]
; CHECK-NEXT:      #dbg_value(ptr [[B]], [[META11:![0-9]+]], !DIExpression(), [[DBG18]])
; CHECK-NEXT:
   [[C:%.*]] = alloca i32, align 4, !dbg [[DBG19:![0-9]+]]
; CHECK-NEXT:      #dbg_value(ptr [[C]], [[META12:![0-9]+]], !DIExpression(), [[DBG19]])
; CHECK-NEXT:    call void @outlined_ir_func_0(ptr [[A]], ptr [[B]], ptr [[C]]), !dbg [[DBG20:![0-9]+]]
; CHECK-NEXT:    ret void, !dbg [[DBG21:![0-9]+]]
;
;
; CHECK-LABEL: @function2(
; CHECK-NEXT:  entry:
; CHECK-NEXT:    [[A:%.*]] = alloca i32, align 4, !dbg [[DBG30:![0-9]+]]
; CHECK-NEXT:      #dbg_value(ptr [[A]], [[META24:![0-9]+]], !DIExpression(), [[DBG30]])
; CHECK-NEXT:    [[B:%.*]] = alloca i32, align 4, !dbg [[DBG31:![0-9]+]]
; CHECK-NEXT:      #dbg_value(ptr [[B]], [[META25:![0-9]+]], !DIExpression(), [[DBG31]])
; CHECK-NEXT:    [[C:%.*]] = alloca i32, align 4, !dbg [[DBG32:![0-9]+]]
; CHECK-NEXT:      #dbg_value(ptr [[C]], [[META26:![0-9]+]], !DIExpression(), [[DBG32]])
; CHECK-NEXT:    call void @outlined_ir_func_0(ptr [[A]], ptr [[B]], ptr [[C]]), !dbg [[DBG33:![0-9]+]]
; CHECK-NEXT:    ret void, !dbg [[DBG34:![0-9]+]]
;
;
;
```

```
 CHECK-LABEL: define {{.+}} @outlined_ir_func_0(
; CHECK-NEXT:  newFuncRoot:
; CHECK-NEXT:    br label [[ENTRY_TO_OUTLINE:%.*]]
; CHECK:       entry_to_outline:
; CHECK-NEXT:    store i32 2, ptr [[TMP0:%.*]], align 4
; CHECK-NEXT:    store i32 3, ptr [[TMP1:%.*]], align 4
; CHECK-NEXT:    store i32 4, ptr [[TMP2:%.*]], align 4
; CHECK-NEXT:    [[AL:%.*]] = load i32, ptr [[TMP0]], align 4
; CHECK-NEXT:    [[BL:%.*]] = load i32, ptr [[TMP1]], align 4
; CHECK-NEXT:    [[CL:%.*]] = load i32, ptr [[TMP2]], align 4
; CHECK-NEXT:    br label [[ENTRY_AFTER_OUTLINE_EXITSTUB:%.*]]
; CHECK:       entry_after_outline.exitStub:
; CHECK-NEXT:    ret void
;
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=amdgcn -mcpu=fiji < %s | FileCheck -enable-var-scope --check-prefixes=GCN,GCN-
SAFE,VI,VI-SAFE %s
; RUN: llc -enable-no-signed-zeros-fp-math -mtriple=amdgcn -mcpu=fiji < %s | FileCheck -enable-var-scope --
check-prefixes=GCN,GCN-NSZ,VI,VI-NSZ %s

; ------------------------------------------------------------------------------
; rcp tests
; ------------------------------------------------------------------------------

define half @v_fneg_rcp_f16(half %a) #0 {
; GCN-LABEL: v_fneg_rcp_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_rcp_f16_e64 v0, -v0
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %rcp = call half @llvm.amdgcn.rcp.f16(half %a)
 %fneg = fneg half %rcp
 ret half %fneg
}

define half @v_fneg_rcp_fneg_f16(half %a) #0 {
; GCN-LABEL: v_fneg_rcp_fneg_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:
    v_rcp_f16_e32 v0, v0
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %fneg.a = fneg half %a
 %rcp = call half @llvm.amdgcn.rcp.f16(half %fneg.a)
 %fneg = fneg half %rcp
 ret half %fneg
}
```

```
define { half, half } @v_fneg_rcp_store_use_fneg_f16(half %a) #0 {
; GCN-LABEL: v_fneg_rcp_store_use_fneg_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_rcp_f16_e32 v2, v0
; GCN-NEXT:    v_xor_b32_e32 v1, 0x8000, v0
; GCN-NEXT:    v_mov_b32_e32 v0, v2
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %fneg.a = fneg half %a
 %rcp = call half @llvm.amdgcn.rcp.f16(half %fneg.a)
 %fneg = fneg half %rcp
 %insert.0 = insertvalue { half, half } poison, half %fneg, 0
 %insert.1 = insertvalue { half, half } %insert.0, half %fneg.a, 1
 ret { half, half } %insert.1
}

define { half, half } @v_fneg_rcp_multi_use_fneg_f16(half %a, half %c) #0 {
; GCN-LABEL: v_fneg_rcp_multi_use_fneg_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:
    v_rcp_f16_e32 v2, v0
; GCN-NEXT:    v_mul_f16_e64 v1, -v0, v1
; GCN-NEXT:    v_mov_b32_e32 v0, v2
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %fneg.a = fneg half %a
 %rcp = call half @llvm.amdgcn.rcp.f16(half %fneg.a)
 %fneg = fneg half %rcp
 %use1 = fmul half %fneg.a, %c
 %insert.0 = insertvalue { half, half } poison, half %fneg, 0
 %insert.1 = insertvalue { half, half } %insert.0, half %use1, 1
 ret { half, half } %insert.1
}

; ---------------------------------------------------------------------------
; sin tests
; ---------------------------------------------------------------------------

define half @v_fneg_amdgcn_sin_f16(half %a) #0 {
; GCN-LABEL: v_fneg_amdgcn_sin_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_sin_f16_e64 v0, -v0
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %sin = call half @llvm.amdgcn.sin.f16(half %a)
 %fneg = fneg half %sin
 ret half %fneg
}
```

```
; ------------------------------------------------------------------------------
;
 vintrp tests
; ------------------------------------------------------------------------------

define { float, float } @v_fneg_interp_p1_f16(float %a, float %b) #0 {
; GCN-LABEL: v_fneg_interp_p1_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_mul_f32_e64 v1, v0, -v1
; GCN-NEXT:    s_mov_b32 m0, 0
; GCN-NEXT:    s_setreg_imm32_b32 hwreg(HW_REG_MODE, 2, 2), 3
; GCN-NEXT:    v_interp_p1ll_f16 v0, v1, attr0.x
; GCN-NEXT:    v_interp_p1ll_f16 v1, v1, attr0.y
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %mul = fmul float %a, %b
 %fneg = fneg float %mul
 %intrp0 = call float @llvm.amdgcn.interp.p1.f16(float %fneg, i32 0, i32 0, i1 false, i32 0)
 %intrp1 = call float @llvm.amdgcn.interp.p1.f16(float %fneg, i32 1, i32 0, i1 false, i32 0)
 %insert.0 = insertvalue { float, float } poison, float %intrp0, 0
 %insert.1 = insertvalue { float, float } %insert.0, float %intrp1, 1
 ret { float, float } %insert.1
}

define
 { half, half } @v_fneg_interp_p2_f16(float %a, float %b) #0 {
; GCN-LABEL: v_fneg_interp_p2_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_mul_f32_e64 v1, v0, -v1
; GCN-NEXT:    v_mov_b32_e32 v2, 4.0
; GCN-NEXT:    s_mov_b32 m0, 0
; GCN-NEXT:    s_setreg_imm32_b32 hwreg(HW_REG_MODE, 2, 2), 3
; GCN-NEXT:    v_interp_p2_f16 v0, v1, attr0.x, v2
; GCN-NEXT:    v_interp_p2_f16 v1, v1, attr0.y, v2
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %mul = fmul float %a, %b
 %fneg = fneg float %mul
 %intrp0 = call half @llvm.amdgcn.interp.p2.f16(float 4.0, float %fneg, i32 0, i32 0, i1 false, i32 0)
 %intrp1 = call half @llvm.amdgcn.interp.p2.f16(float 4.0, float %fneg, i32 1, i32 0, i1 false, i32 0)
 %insert.0 = insertvalue { half, half } poison, half %intrp0, 0
 %insert.1 = insertvalue { half, half } %insert.0, half %intrp1, 1
 ret { half, half } %insert.1
}

; ------------------------------------------------------------------------------
;
```

```
 arithmetic.fence tests
; ------------------------------------------------------------------------------

; FIXME: Legalization/promote is broken
define half @v_fneg_arithmetic_fence_f16(half %a) #0 {
; GCN-LABEL: v_fneg_arithmetic_fence_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    ;ARITH_FENCE
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_xor_b32_e32 v0, 0x8000, v0
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %fence = call half @llvm.arithmetic.fence.f16(half %a)
 %fneg = fneg half %fence
 ret half %fneg
}

define half @v_fneg_arithmetic_fence_fmul_f16(half %a, half %b) #0 {
; GCN-LABEL: v_fneg_arithmetic_fence_fmul_f16:
; GCN:       ; %bb.0:
; GCN-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; GCN-NEXT:    v_mul_f16_e32 v0, v0, v1
; GCN-NEXT:    ;ARITH_FENCE
; GCN-NEXT:    v_xor_b32_e32 v0, 0x8000, v0
; GCN-NEXT:    s_setpc_b64 s[30:31]
 %mul = fmul half %a, %b
 %fence = call half @llvm.arithmetic.fence.f16(half %mul)
 %fneg = fneg half %fence
 ret half
 %fneg
}

declare half @llvm.amdgcn.rcp.f16(half) #1
declare half @llvm.amdgcn.sin.f16(half) #1
declare half @llvm.arithmetic.fence.f16(half) #1
declare float @llvm.amdgcn.interp.p1.f16(float, i32, i32, i1, i32) #0
declare half @llvm.amdgcn.interp.p2.f16(float, float, i32, i32, i1, i32) #0

attributes #0 = { nounwind "denormal-fp-math-f32"="preserve-sign,preserve-sign" }
attributes #1 = { nounwind readnone }
attributes #2 = { nounwind "unsafe-fp-math"="true" }
attributes #3 = { nounwind "no-signed-zeros-fp-math"="true" }
attributes #4 = { nounwind "amdgpu-ieee"="false" "denormal-fp-math-f32"="preserve-sign,preserve-sign" }
;; NOTE: These prefixes are unused and the list is autogenerated. Do not add tests below this line:
; GCN-NSZ: {{.*}}
; GCN-SAFE: {{.*}}
; VI: {{.*}}
; VI-NSZ: {{.*}}
; VI-SAFE: {{.*}}
```

```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64IZbb

declare i4 @llvm.usub.sat.i4(i4, i4)
declare i8 @llvm.usub.sat.i8(i8, i8)
declare i16 @llvm.usub.sat.i16(i16, i16)
declare i32 @llvm.usub.sat.i32(i32, i32)
declare i64 @llvm.usub.sat.i64(i64, i64)

define i32 @func32(i32 %x, i32 %y, i32 %z) nounwind {
; RV64I-LABEL: func32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func32:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    mulw a1, a1, a2
; RV64IZbb-NEXT:    sext.w a0, a0
; RV64IZbb-NEXT:    maxu
 a0, a0, a1
; RV64IZbb-NEXT:    sub a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i32 %y, %z
 %tmp = call i32 @llvm.usub.sat.i32(i32 %x, i32 %a)
 ret i32 %tmp
}

define i64 @func64(i64 %x, i64 %y, i64 %z) nounwind {
; RV64I-LABEL: func64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sub a1, a0, a2
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func64:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    maxu a0, a0, a2
```

```
; RV64IZbb-NEXT:    sub a0, a0, a2
; RV64IZbb-NEXT:    ret
 %a = mul i64 %y, %z
 %tmp = call i64 @llvm.usub.sat.i64(i64 %x, i64 %z)
 ret i64 %tmp
}

define i16 @func16(i16 %x, i16 %y, i16 %z) nounwind {
; RV64I-LABEL: func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a3, 16
; RV64I-NEXT:    addi a3, a3, -1
; RV64I-NEXT:    and a0, a0, a3
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    and a1, a1, a3
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sltu
 a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    zext.h a0, a0
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    zext.h a1, a1
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    sub a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i16 %y, %z
 %tmp = call i16 @llvm.usub.sat.i16(i16 %x, i16 %a)
 ret i16 %tmp
}

define i8 @func8(i8 %x, i8 %y, i8 %z) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    andi a1, a1, 255
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
```

```
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    andi a0, a0, 255
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    andi a1, a1, 255
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:
   sub a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i8 %y, %z
 %tmp = call i8 @llvm.usub.sat.i8(i8 %x, i8 %a)
 ret i8 %tmp
}

define i4 @func4(i4 %x, i4 %y, i4 %z) nounwind {
; RV64I-LABEL: func4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 15
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    andi a1, a1, 15
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func4:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    andi a0, a0, 15
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    andi a1, a1, 15
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    sub a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i4 %y, %z
 %tmp = call i4 @llvm.usub.sat.i4(i4 %x, i4 %a)
 ret i4 %tmp
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64I
; RUN: llc -mtriple=riscv64 -mattr=+zbkb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64ZBKB

define signext i32 @pack_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: pack_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
```

```
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    slliw a1, a1, 16
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i32:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packw a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %shl = and i32 %a, 65535
 %shl1 = shl i32 %b, 16
 %or = or i32 %shl1, %shl
 ret i32 %or
}

define signext i32 @pack_i32_2(i16 zeroext %a, i16 zeroext %b) nounwind {
; RV64I-LABEL: pack_i32_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slliw
 a1, a1, 16
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i32_2:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packw a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %zexta = zext i16 %a to i32
 %zextb = zext i16 %b to i32
 %shl1 = shl i32 %zextb, 16
 %or = or i32 %shl1, %zexta
 ret i32 %or
}

; Test case where we don't have a sign_extend_inreg after the or.
define signext i32 @pack_i32_3(i16 zeroext %0, i16 zeroext %1, i32 signext %2) {
; RV64I-LABEL: pack_i32_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    addw a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i32_3:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packw a0, a1, a0
; RV64ZBKB-NEXT:    addw a0, a0, a2
; RV64ZBKB-NEXT:    ret
```

```
 %4 = zext i16 %0 to i32
 %5 = shl nuw i32 %4, 16
 %6 = zext i16 %1 to i32
 %7 = or i32 %5, %6
 %8 = add i32 %7, %2
 ret i32 %8
}

define i64 @pack_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: pack_i64:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i64:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    pack a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %shl = and i64 %a, 4294967295
 %shl1 = shl i64 %b, 32
 %or = or i64 %shl1, %shl
 ret i64 %or
}

define i64 @pack_i64_2(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: pack_i64_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i64_2:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    pack a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %zexta = zext i32 %a to i64
 %zextb = zext i32 %b to i64
 %shl1 = shl i64 %zextb, 32
 %or = or i64 %shl1, %zexta
 ret i64 %or
}
```

```
define i64 @pack_i64_3(ptr %0, ptr %1) {
; RV64I-LABEL: pack_i64_3:
; RV64I:       #
 %bb.0:
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    lwu a1, 0(a1)
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i64_3:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    lw a0, 0(a0)
; RV64ZBKB-NEXT:    lwu a1, 0(a1)
; RV64ZBKB-NEXT:    pack a0, a1, a0
; RV64ZBKB-NEXT:    ret
 %3 = load i32, ptr %0, align 4
 %4 = zext i32 %3 to i64
 %5 = shl i64 %4, 32
 %6 = load i32, ptr %1, align 4
 %7 = zext i32 %6 to i64
 %8 = or i64 %5, %7
 ret i64 %8
}

define signext i32 @packh_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: packh_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    slli a1, a1, 56
; RV64I-NEXT:    srli a1, a1, 48
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: packh_i32:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packh a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %and = and i32 %a, 255
 %and1 = shl i32 %b, 8
 %shl = and i32 %and1, 65280
 %or = or
 i32 %shl, %and
 ret i32 %or
}

define i32 @packh_i32_2(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: packh_i32_2:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    andi a1, a1, 255
; RV64I-NEXT:    slliw a1, a1, 8
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: packh_i32_2:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packh a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %and = and i32 %a, 255
 %and1 = and i32 %b, 255
 %shl = shl i32 %and1, 8
 %or = or i32 %shl, %and
 ret i32 %or
}

define i64 @packh_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: packh_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    slli a1, a1, 56
; RV64I-NEXT:    srli a1, a1, 48
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: packh_i64:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packh a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %and = and i64 %a, 255
 %and1 = shl i64 %b, 8
 %shl = and i64 %and1, 65280
 %or = or i64 %shl,
 %and
 ret i64 %or
}

define i64 @packh_i64_2(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: packh_i64_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    andi a1, a1, 255
; RV64I-NEXT:    slli a1, a1, 8
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
```

```
; RV64ZBKB-LABEL: packh_i64_2:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packh a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %and = and i64 %a, 255
 %and1 = and i64 %b, 255
 %shl = shl i64 %and1, 8
 %or = or i64 %shl, %and
 ret i64 %or
}

define zeroext i16 @packh_i16(i8 zeroext %a, i8 zeroext %b) nounwind {
; RV64I-LABEL: packh_i16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slliw a1, a1, 8
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: packh_i16:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    packh a0, a0, a1
; RV64ZBKB-NEXT:    ret
 %zext = zext i8 %a to i16
 %zext1 = zext i8 %b to i16
 %shl = shl i16 %zext1, 8
 %or = or i16 %shl, %zext
 ret i16 %or
}

define zeroext i16 @packh_i16_2(i8
 zeroext %0, i8 zeroext %1, i8 zeroext %2) {
; RV64I-LABEL: packh_i16_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    slli a0, a0, 8
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: packh_i16_2:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    add a0, a1, a0
; RV64ZBKB-NEXT:    packh a0, a2, a0
; RV64ZBKB-NEXT:    ret
 %4 = add i8 %1, %0
 %5 = zext i8 %4 to i16
 %6 = shl i16 %5, 8
```

```
 %7 = zext i8 %2 to i16
 %8 = or i16 %6, %7
 ret i16 %8
}

define i64 @pack_i64_allWUsers(i32 signext %0, i32 signext %1, i32 signext %2) {
; RV64I-LABEL: pack_i64_allWUsers:
; RV64I:       # %bb.0:
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    slli a2, a2, 32
; RV64I-NEXT:    srli a2, a2, 32
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i64_allWUsers:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    add a0, a1, a0
; RV64ZBKB-NEXT:
    pack a0, a2, a0
; RV64ZBKB-NEXT:    ret
 %4 = add i32 %1, %0
 %5 = zext i32 %4 to i64
 %6 = shl i64 %5, 32
 %7 = zext i32 %2 to i64
 %8 = or i64 %6, %7
 ret i64 %8
}

define signext i32 @pack_i32_allWUsers(i16 zeroext %0, i16 zeroext %1, i16 zeroext %2) {
; RV64I-LABEL: pack_i32_allWUsers:
; RV64I:       # %bb.0:
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    slliw a0, a0, 16
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: pack_i32_allWUsers:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    add a0, a1, a0
; RV64ZBKB-NEXT:    packw a0, a2, a0
; RV64ZBKB-NEXT:    ret
 %4 = add i16 %1, %0
 %5 = zext i16 %4 to i32
 %6 = shl i32 %5, 16
 %7 = zext i16 %2 to i32
 %8 = or i32 %6, %7
 ret i32 %8
```

```
}

define i64 @pack_i64_imm() {
; RV64I-LABEL: pack_i64_imm:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 65793
; RV64I-NEXT:    addiw a0, a0, 16
; RV64I-NEXT:    slli a1, a0, 32
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL:
 pack_i64_imm:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    lui a0, 65793
; RV64ZBKB-NEXT:    addi a0, a0, 16
; RV64ZBKB-NEXT:    pack a0, a0, a0
; RV64ZBKB-NEXT:    ret
 ret i64 1157442765409226768 ; 0x0101010101010101
}

define i32 @zexth_i32(i32 %a) nounwind {
; RV64I-LABEL: zexth_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: zexth_i32:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    zext.h a0, a0
; RV64ZBKB-NEXT:    ret
 %and = and i32 %a, 65535
 ret i32 %and
}

define i64 @zexth_i64(i64 %a) nounwind {
; RV64I-LABEL: zexth_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: zexth_i64:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    zext.h a0, a0
; RV64ZBKB-NEXT:    ret
 %and = and i64 %a, 65535
```

```
 ret i64 %and
}

define i32 @zext_i16_to_i32(i16 %a) nounwind {
; RV64I-LABEL: zext_i16_to_i32:
; RV64I:       #
 %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: zext_i16_to_i32:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    zext.h a0, a0
; RV64ZBKB-NEXT:    ret
 %1 = zext i16 %a to i32
 ret i32 %1
}

define i64 @zext_i16_to_i64(i16 %a) nounwind {
; RV64I-LABEL: zext_i16_to_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBKB-LABEL: zext_i16_to_i64:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    zext.h a0, a0
; RV64ZBKB-NEXT:    ret
 %1 = zext i16 %a to i64
 ret i64 %1
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64I
; RUN: llc -mtriple=riscv64 -mattr=+zbb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64ZBB-ZBKB,RV64ZBB
; RUN: llc -mtriple=riscv64 -mattr=+zbkb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64ZBB-
ZBKB,RV64ZBKB

define signext i32 @andn_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: andn_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
```

```
;
; RV64ZBB-ZBKB-LABEL: andn_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    andn a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %neg = xor i32 %b, -1
 %and = and i32 %neg, %a
 ret i32 %and
}

define i64 @andn_i64(i64 %a, i64
 %b) nounwind {
; RV64I-LABEL: andn_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: andn_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    andn a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %neg = xor i64 %b, -1
 %and = and i64 %neg, %a
 ret i64 %and
}

define signext i32 @orn_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: orn_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: orn_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    orn a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %neg = xor i32 %b, -1
 %or = or i32 %neg, %a
 ret i32 %or
}

define i64 @orn_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: orn_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    or a0, a1, a0
```

```
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: orn_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:
    orn a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %neg = xor i64 %b, -1
 %or = or i64 %neg, %a
 ret i64 %or
}

define signext i32 @xnor_i32(i32 signext %a, i32 signext %b) nounwind {
; CHECK-LABEL: xnor_i32:
; CHECK:       # %bb.0:
; CHECK-NEXT:    xor a0, a0, a1
; CHECK-NEXT:    not a0, a0
; CHECK-NEXT:    ret
 %neg = xor i32 %a, -1
 %xor = xor i32 %neg, %b
 ret i32 %xor
}

define i64 @xnor_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: xnor_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: xnor_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    xnor a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %neg = xor i64 %a, -1
 %xor = xor i64 %neg, %b
 ret i64 %xor
}

declare i32 @llvm.fshl.i32(i32, i32, i32)

define signext i32 @rol_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: rol_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sllw a2, a0, a1
; RV64I-NEXT:    negw a1, a1
; RV64I-NEXT:
```

```
    srlw a0, a0, a1
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rol_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rolw a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshl.i32(i32 %a, i32 %a, i32 %b)
 ret i32 %1
}

; Similar to rol_i32, but doesn't sign extend the result.
define void @rol_i32_nosext(i32 signext %a, i32 signext %b, ptr %x) nounwind {
; RV64I-LABEL: rol_i32_nosext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sllw a3, a0, a1
; RV64I-NEXT:    negw a1, a1
; RV64I-NEXT:    srlw a0, a0, a1
; RV64I-NEXT:    or a0, a3, a0
; RV64I-NEXT:    sw a0, 0(a2)
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rol_i32_nosext:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rolw a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    sw a0, 0(a2)
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshl.i32(i32 %a, i32 %a, i32 %b)
 store i32 %1, ptr %x
 ret void
}

define signext i32 @rol_i32_neg_constant_rhs(i32 signext %a) nounwind {
; RV64I-LABEL:
 rol_i32_neg_constant_rhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -2
; RV64I-NEXT:    sllw a2, a1, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    srlw a0, a1, a0
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rol_i32_neg_constant_rhs:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    li a1, -2
; RV64ZBB-ZBKB-NEXT:    rolw a0, a1, a0
```

```
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshl.i32(i32 -2, i32 -2, i32 %a)
 ret i32 %1
}

declare i64 @llvm.fshl.i64(i64, i64, i64)

define i64 @rol_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: rol_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sll a2, a0, a1
; RV64I-NEXT:    negw a1, a1
; RV64I-NEXT:    srl a0, a0, a1
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rol_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rol a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %or = tail call i64 @llvm.fshl.i64(i64 %a, i64 %a, i64 %b)
 ret i64 %or
}

declare i32 @llvm.fshr.i32(i32,
 i32, i32)

define signext i32 @ror_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: ror_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srlw a2, a0, a1
; RV64I-NEXT:    negw a1, a1
; RV64I-NEXT:    sllw a0, a0, a1
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: ror_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rorw a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshr.i32(i32 %a, i32 %a, i32 %b)
 ret i32 %1
}

; Similar to ror_i32, but doesn't sign extend the result.
define void @ror_i32_nosext(i32 signext %a, i32 signext %b, ptr %x) nounwind {
; RV64I-LABEL: ror_i32_nosext:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    srlw a3, a0, a1
; RV64I-NEXT:    negw a1, a1
; RV64I-NEXT:    sllw a0, a0, a1
; RV64I-NEXT:    or a0, a3, a0
; RV64I-NEXT:    sw a0, 0(a2)
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: ror_i32_nosext:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rorw a0, a0, a1
; RV64ZBB-ZBKB-NEXT:
   sw a0, 0(a2)
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshr.i32(i32 %a, i32 %a, i32 %b)
 store i32 %1, ptr %x
 ret void
}

define signext i32 @ror_i32_neg_constant_rhs(i32 signext %a) nounwind {
; RV64I-LABEL: ror_i32_neg_constant_rhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -2
; RV64I-NEXT:    srlw a2, a1, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    sllw a0, a1, a0
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: ror_i32_neg_constant_rhs:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    li a1, -2
; RV64ZBB-ZBKB-NEXT:    rorw a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshr.i32(i32 -2, i32 -2, i32 %a)
 ret i32 %1
}

declare i64 @llvm.fshr.i64(i64, i64, i64)

define i64 @ror_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: ror_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srl a2, a0, a1
; RV64I-NEXT:    negw a1, a1
; RV64I-NEXT:    sll a0, a0, a1
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
```

```
;
 RV64ZBB-ZBKB-LABEL: ror_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    ror a0, a0, a1
; RV64ZBB-ZBKB-NEXT:    ret
 %or = tail call i64 @llvm.fshr.i64(i64 %a, i64 %a, i64 %b)
 ret i64 %or
}

define signext i32 @rori_i32_fshl(i32 signext %a) nounwind {
; RV64I-LABEL: rori_i32_fshl:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    slliw a0, a0, 31
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rori_i32_fshl:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    roriw a0, a0, 1
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshl.i32(i32 %a, i32 %a, i32 31)
 ret i32 %1
}

; Similar to rori_i32_fshl, but doesn't sign extend the result.
define void @rori_i32_fshl_nosext(i32 signext %a, ptr %x) nounwind {
; RV64I-LABEL: rori_i32_fshl_nosext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a2, a0, 1
; RV64I-NEXT:    slli a0, a0, 31
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    sw a0, 0(a1)
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL:
 rori_i32_fshl_nosext:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    roriw a0, a0, 1
; RV64ZBB-ZBKB-NEXT:    sw a0, 0(a1)
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshl.i32(i32 %a, i32 %a, i32 31)
 store i32 %1, ptr %x
 ret void
}

define signext i32 @rori_i32_fshr(i32 signext %a) nounwind {
; RV64I-LABEL: rori_i32_fshr:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    slliw a1, a0, 1
; RV64I-NEXT:    srliw a0, a0, 31
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rori_i32_fshr:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    roriw a0, a0, 31
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshr.i32(i32 %a, i32 %a, i32 31)
 ret i32 %1
}

; Similar to rori_i32_fshr, but doesn't sign extend the result.
define void @rori_i32_fshr_nosext(i32 signext %a, ptr %x) nounwind {
; RV64I-LABEL: rori_i32_fshr_nosext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a2, a0, 1
; RV64I-NEXT:    srliw a0, a0, 31
; RV64I-NEXT:
 or a0, a0, a2
; RV64I-NEXT:    sw a0, 0(a1)
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rori_i32_fshr_nosext:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    roriw a0, a0, 31
; RV64ZBB-ZBKB-NEXT:    sw a0, 0(a1)
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i32 @llvm.fshr.i32(i32 %a, i32 %a, i32 31)
 store i32 %1, ptr %x
 ret void
}

; This test is similar to the type legalized version of the fshl/fshr tests, but
; instead of having the same input to both shifts it has different inputs. Make
; sure we don't match it as a roriw.
define signext i32 @not_rori_i32(i32 signext %x, i32 signext %y) nounwind {
; CHECK-LABEL: not_rori_i32:
; CHECK:       # %bb.0:
; CHECK-NEXT:    slliw a0, a0, 31
; CHECK-NEXT:    srliw a1, a1, 1
; CHECK-NEXT:    or a0, a0, a1
; CHECK-NEXT:    ret
 %a = shl i32 %x, 31
 %b = lshr i32 %y, 1
 %c = or i32 %a, %b
```

```
 ret i32 %c
}

; This is similar to the type legalized roriw pattern, but the and mask is more
; than 32 bits so the lshr doesn't shift zeroes
 into the lower 32 bits. Make
; sure we don't match it to roriw.
define i64 @roriw_bug(i64 %x) nounwind {
; CHECK-LABEL: roriw_bug:
; CHECK:       # %bb.0:
; CHECK-NEXT:    slli a1, a0, 31
; CHECK-NEXT:    andi a2, a0, -2
; CHECK-NEXT:    srli a0, a0, 1
; CHECK-NEXT:    or a0, a1, a0
; CHECK-NEXT:    sext.w a0, a0
; CHECK-NEXT:    xor a0, a2, a0
; CHECK-NEXT:    ret
 %a = shl i64 %x, 31
 %b = and i64 %x, 18446744073709551614
 %c = lshr i64 %b, 1
 %d = or i64 %a, %c
 %e = shl i64 %d, 32
 %f = ashr i64 %e, 32
 %g = xor i64 %b, %f ; to increase the use count on %b to disable SimplifyDemandedBits.
 ret i64 %g
}

define i64 @rori_i64_fshl(i64 %a) nounwind {
; RV64I-LABEL: rori_i64_fshl:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a0, 1
; RV64I-NEXT:    slli a0, a0, 63
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rori_i64_fshl:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rori a0, a0, 1
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i64 @llvm.fshl.i64(i64
 %a, i64 %a, i64 63)
 ret i64 %1
}

define i64 @rori_i64_fshr(i64 %a) nounwind {
; RV64I-LABEL: rori_i64_fshr:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 1
```

```
; RV64I-NEXT:    srli a0, a0, 63
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: rori_i64_fshr:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    rori a0, a0, 63
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = tail call i64 @llvm.fshr.i64(i64 %a, i64 %a, i64 63)
 ret i64 %1
}

define signext i32 @not_shl_one_i32(i32 signext %x) {
; RV64I-LABEL: not_shl_one_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    sllw a0, a1, a0
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: not_shl_one_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    li a1, -2
; RV64ZBB-ZBKB-NEXT:    rolw a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = shl i32 1, %x
 %2 = xor i32 %1, -1
 ret i32 %2
}

define i64 @not_shl_one_i64(i64 %x) {
; RV64I-LABEL: not_shl_one_i64:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    sll a0, a1, a0
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: not_shl_one_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    li a1, -2
; RV64ZBB-ZBKB-NEXT:    rol a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %1 = shl i64 1, %x
 %2 = xor i64 %1, -1
 ret i64 %2
}
```

```
define i8 @srli_i8(i8 %a) nounwind {
; CHECK-LABEL: srli_i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    slli a0, a0, 56
; CHECK-NEXT:    srli a0, a0, 62
; CHECK-NEXT:    ret
 %1 = lshr i8 %a, 6
 ret i8 %1
}

; We could use sext.b+srai, but slli+srai offers more opportunities for
; comppressed instructions.
define i8 @srai_i8(i8 %a) nounwind {
; RV64I-LABEL: srai_i8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a0, a0, 29
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: srai_i8:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    slli a0, a0, 56
; RV64ZBB-NEXT:    srai a0, a0, 61
; RV64ZBB-NEXT:    ret
;
; RV64ZBKB-LABEL:
 srai_i8:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    slli a0, a0, 24
; RV64ZBKB-NEXT:    sraiw a0, a0, 29
; RV64ZBKB-NEXT:    ret
 %1 = ashr i8 %a, 5
 ret i8 %1
}

; We could use zext.h+srli, but slli+srli offers more opportunities for
; comppressed instructions.
define i16 @srli_i16(i16 %a) nounwind {
; CHECK-LABEL: srli_i16:
; CHECK:       # %bb.0:
; CHECK-NEXT:    slli a0, a0, 48
; CHECK-NEXT:    srli a0, a0, 54
; CHECK-NEXT:    ret
 %1 = lshr i16 %a, 6
 ret i16 %1
}
```

```
; We could use sext.h+srai, but slli+srai offers more opportunities for
; comppressed instructions.
define i16 @srai_i16(i16 %a) nounwind {
; RV64I-LABEL: srai_i16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 25
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: srai_i16:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    slli a0, a0, 48
; RV64ZBB-NEXT:    srai a0, a0, 57
; RV64ZBB-NEXT:    ret
;
; RV64ZBKB-LABEL: srai_i16:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    slli
 a0, a0, 16
; RV64ZBKB-NEXT:    sraiw a0, a0, 25
; RV64ZBKB-NEXT:    ret
 %1 = ashr i16 %a, 9
 ret i16 %1
}

define i1 @andn_seqz_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: andn_seqz_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: andn_seqz_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    andn a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    seqz a0, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %and = and i32 %a, %b
 %cmpeq = icmp eq i32 %and, %b
 ret i1 %cmpeq
}

define i1 @andn_seqz_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: andn_seqz_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    xor a0, a0, a1
```

```
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: andn_seqz_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    andn a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    seqz a0, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %and
 = and i64 %a, %b
 %cmpeq = icmp eq i64 %and, %b
 ret i1 %cmpeq
}

define i1 @andn_snez_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: andn_snez_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: andn_snez_i32:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    andn a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    snez a0, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %and = and i32 %a, %b
 %cmpeq = icmp ne i32 %and, %b
 ret i1 %cmpeq
}

define i1 @andn_snez_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: andn_snez_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBB-ZBKB-LABEL: andn_snez_i64:
; RV64ZBB-ZBKB:       # %bb.0:
; RV64ZBB-ZBKB-NEXT:    andn a0, a1, a0
; RV64ZBB-ZBKB-NEXT:    snez a0, a0
; RV64ZBB-ZBKB-NEXT:    ret
 %and = and i64 %a, %b
 %cmpeq = icmp ne i64 %and,
```

```
 %b
 ret i1 %cmpeq
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s
--check-prefixes=CHECK,CHECK-SKX
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi |
FileCheck %s --check-prefixes=CHECK,CHECK-SKX,CHECK-SKX-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for
vpmaddwd+vpaddd
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s --check-
prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cascadelake | FileCheck %s --
check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cooperlake | FileCheck %s --
check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=cannonlake | FileCheck %s --check-
prefixes=CHECK,CHECK-VBMI,CHECK-VBMI1
;
 RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-client | FileCheck %s --
check-prefixes=CHECK,CHECK-VBMI,CHECK-GFNI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-server | FileCheck %s --
check-prefixes=CHECK,CHECK-VBMI,CHECK-GFNI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=tigerlake | FileCheck %s --check-
prefixes=CHECK,CHECK-VBMI,CHECK-GFNI

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the
legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define dso_local void @add256(ptr %a, ptr %b, ptr %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpaddd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:    vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:    vmovdqa
 %ymm0, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %d = load <16 x i32>, ptr %a
 %e = load <16 x i32>, ptr %b
 %f = add <16 x i32> %d, %e
 store <16 x i32> %f, ptr %c
 ret void
}
```

```
define dso_local void @add512(ptr %a, ptr %b, ptr %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: add512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:    vpaddd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:    vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %d = load <16 x i32>, ptr %a
 %e = load <16 x i32>, ptr %b
 %f = add <16 x i32> %d, %e
 store <16 x i32> %f, ptr %c
 ret void
}

define dso_local void @avg_v64i8_256(ptr %a, ptr %b) "min-legal-vector-width"="256" {
; CHECK-LABEL: avg_v64i8_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpavgb (%rsi), %ymm0, %ymm0
; CHECK-NEXT:    vpavgb 32(%rsi), %ymm1, %ymm1
;
 CHECK-NEXT:    vmovdqu %ymm1, (%rax)
; CHECK-NEXT:    vmovdqu %ymm0, (%rax)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %1 = load <64 x i8>, ptr %a
 %2 = load <64 x i8>, ptr %b
 %3 = zext <64 x i8> %1 to <64 x i32>
 %4 = zext <64 x i8> %2 to <64 x i32>
 %5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
 %6 = add nuw nsw <64 x i32> %5, %4
 %7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
 i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1>
 %8 = trunc <64 x i32> %7 to <64 x i8>
 store <64 x i8> %8, ptr undef, align 4
 ret void
}


define dso_local void @avg_v64i8_512(ptr %a, ptr %b) "min-legal-vector-width"="512" {
```

```
; CHECK-LABEL: avg_v64i8_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:    vpavgb (%rsi), %zmm0, %zmm0
; CHECK-NEXT:    vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %1 = load <64 x i8>, ptr %a
 %2 = load <64 x i8>, ptr %b
 %3 = zext <64 x i8> %1 to <64 x i32>
 %4 = zext <64 x i8> %2 to <64 x i32>
 %5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
 i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1>
 %6 = add nuw nsw <64 x i32> %5, %4
 %7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
 %8 = trunc <64 x i32> %7 to <64 x i8>
 store <64 x i8> %8, ptr undef, align 4
 ret void
}

define dso_local void @pmaddwd_32_256(ptr %APtr, ptr %BPtr, ptr %CPtr) "min-legal-vector-width"="256"
 {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpmaddwd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:    vpmaddwd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
  %A = load <32 x i16>, ptr %APtr
  %B = load <32 x i16>, ptr %BPtr
  %a = sext <32 x i16> %A to <32 x i32>
  %b = sext <32 x i16> %B to <32 x i32>
  %m = mul nsw <32 x i32> %a, %b
  %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
  %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
  %ret = add <16 x i32> %odd, %even
```

```
 store <16 x i32> %ret,
ptr %CPtr
 ret void
}

define dso_local void @pmaddwd_32_512(ptr %APtr, ptr %BPtr, ptr %CPtr) "min-legal-vector-width"="512" {
; CHECK-LABEL: pmaddwd_32_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:    vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:    vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %A = load <32 x i16>, ptr %APtr
 %B = load <32 x i16>, ptr %BPtr
 %a = sext <32 x i16> %A to <32 x i32>
 %b = sext <32 x i16> %B to <32 x i32>
 %m = mul nsw <32 x i32> %a, %b
 %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
 %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
 %ret = add <16 x i32> %odd, %even
 store <16 x i32> %ret, ptr
%CPtr
 ret void
}

define dso_local void @psubus_64i8_max_256(ptr %xptr, ptr %yptr, ptr %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: psubus_64i8_max_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:    vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <64 x i8>, ptr %xptr
 %y = load <64 x i8>, ptr %yptr
 %cmp = icmp ult <64 x i8> %x, %y
 %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
 %res = sub <64 x i8> %max, %y
 store <64 x i8> %res, ptr %zptr
 ret void
}
```

```
define dso_local void @psubus_64i8_max_512(ptr %xptr, ptr %yptr, ptr %zptr) "min-legal-vector-width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:    vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT:
   vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <64 x i8>, ptr %xptr
 %y = load <64 x i8>, ptr %yptr
 %cmp = icmp ult <64 x i8> %x, %y
 %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
 %res = sub <64 x i8> %max, %y
 store <64 x i8> %res, ptr %zptr
 ret void
}

define dso_local i32 @_Z9test_charPcS_i_256(ptr nocapture readonly, ptr nocapture readonly, i32) "min-legal-
vector-width"="256" {
; CHECK-SKX-LABEL: _Z9test_charPcS_i_256:
; CHECK-SKX:       # %bb.0: # %entry
; CHECK-SKX-NEXT:    movl %edx, %eax
; CHECK-SKX-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-SKX-NEXT:    xorl %ecx, %ecx
; CHECK-SKX-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-SKX-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-SKX-NEXT:    .p2align 4, 0x90
; CHECK-SKX-NEXT:  .LBB8_1: # %vector.body
; CHECK-SKX-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-SKX-NEXT:    vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-SKX-NEXT:    vpmovsxbw (%rdi,%rcx), %ymm4
; CHECK-SKX-NEXT:
   vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-SKX-NEXT:    vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-SKX-NEXT:    vpaddd %ymm2, %ymm3, %ymm2
; CHECK-SKX-NEXT:    vpmovsxbw (%rsi,%rcx), %ymm3
; CHECK-SKX-NEXT:    vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-SKX-NEXT:    vpaddd %ymm1, %ymm3, %ymm1
; CHECK-SKX-NEXT:    addq $32, %rcx
; CHECK-SKX-NEXT:    cmpq %rcx, %rax
; CHECK-SKX-NEXT:    jne .LBB8_1
; CHECK-SKX-NEXT:  # %bb.2: # %middle.block
; CHECK-SKX-NEXT:    vpaddd %ymm0, %ymm1, %ymm1
; CHECK-SKX-NEXT:    vpaddd %ymm0, %ymm2, %ymm0
; CHECK-SKX-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-SKX-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
```

```
; CHECK-SKX-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpsrlq $32, %xmm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vmovd %xmm0, %eax
; CHECK-SKX-NEXT:    vzeroupper
; CHECK-SKX-NEXT:    retq
;
; CHECK-AVX512-LABEL:
 _Z9test_charPcS_i_256:
; CHECK-AVX512:      # %bb.0: # %entry
; CHECK-AVX512-NEXT:    movl %edx, %eax
; CHECK-AVX512-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    xorl %ecx, %ecx
; CHECK-AVX512-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-AVX512-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-AVX512-NEXT:    .p2align 4, 0x90
; CHECK-AVX512-NEXT:  .LBB8_1: # %vector.body
; CHECK-AVX512-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-AVX512-NEXT:    vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-AVX512-NEXT:    vpmovsxbw (%rdi,%rcx), %ymm4
; CHECK-AVX512-NEXT:    vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-AVX512-NEXT:    vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-AVX512-NEXT:    vpaddd %ymm2, %ymm3, %ymm2
; CHECK-AVX512-NEXT:    vpmovsxbw (%rsi,%rcx), %ymm3
; CHECK-AVX512-NEXT:    vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-AVX512-NEXT:    vpaddd %ymm1, %ymm3, %ymm1
; CHECK-AVX512-NEXT:    addq $32, %rcx
; CHECK-AVX512-NEXT:    cmpq %rcx, %rax
; CHECK-AVX512-NEXT:    jne .LBB8_1
; CHECK-AVX512-NEXT:
  # %bb.2: # %middle.block
; CHECK-AVX512-NEXT:    vpaddd %ymm0, %ymm1, %ymm1
; CHECK-AVX512-NEXT:    vpaddd %ymm0, %ymm2, %ymm0
; CHECK-AVX512-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpsrlq $32, %xmm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vmovd %xmm0, %eax
; CHECK-AVX512-NEXT:    vzeroupper
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI-LABEL: _Z9test_charPcS_i_256:
; CHECK-VBMI:      # %bb.0: # %entry
; CHECK-VBMI-NEXT:    movl %edx, %eax
```

```
; CHECK-VBMI-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    xorl %ecx, %ecx
; CHECK-VBMI-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-VBMI-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-VBMI-NEXT:    .p2align 4, 0x90
; CHECK-VBMI-NEXT:  .LBB8_1: # %vector.body
; CHECK-VBMI-NEXT:
   # =>This Inner Loop Header: Depth=1
; CHECK-VBMI-NEXT:    vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-VBMI-NEXT:    vpmovsxbw (%rdi,%rcx), %ymm4
; CHECK-VBMI-NEXT:    vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-VBMI-NEXT:    vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-VBMI-NEXT:    vpaddd %ymm2, %ymm3, %ymm2
; CHECK-VBMI-NEXT:    vpmovsxbw (%rsi,%rcx), %ymm3
; CHECK-VBMI-NEXT:    vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-VBMI-NEXT:    vpaddd %ymm1, %ymm3, %ymm1
; CHECK-VBMI-NEXT:    addq $32, %rcx
; CHECK-VBMI-NEXT:    cmpq %rcx, %rax
; CHECK-VBMI-NEXT:    jne .LBB8_1
; CHECK-VBMI-NEXT:  # %bb.2: # %middle.block
; CHECK-VBMI-NEXT:    vpaddd %ymm0, %ymm1, %ymm1
; CHECK-VBMI-NEXT:    vpaddd %ymm0, %ymm2, %ymm0
; CHECK-VBMI-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-VBMI-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd
 {{.*#+}} xmm1 = xmm0[1,1,1,1]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vmovd %xmm0, %eax
; CHECK-VBMI-NEXT:    vzeroupper
; CHECK-VBMI-NEXT:    retq
entry:
 %3 = zext i32 %2 to i64
 br label %vector.body

vector.body:
 %index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
 %vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
 %4 = getelementptr inbounds i8, ptr %0, i64 %index
 %5 = bitcast ptr %4 to ptr
 %wide.load = load <32 x i8>, ptr %5, align 1
 %6 = sext <32 x i8> %wide.load to <32 x i32>
 %7 = getelementptr inbounds i8, ptr %1, i64 %index
 %8 = bitcast ptr %7 to ptr
 %wide.load14 = load <32 x i8>, ptr %8, align 1
 %9 = sext <32 x i8> %wide.load14 to <32 x i32>
```

```
 %10 = mul nsw <32 x i32> %9, %6
 %11 = add nsw <32 x i32> %10, %vec.phi
 %index.next = add i64 %index, 32
 %12 = icmp eq i64 %index.next, %3
 br i1 %12, label %middle.block, label %vector.body

middle.block:
 %rdx.shuf1 = shufflevector <32 x i32>
 %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32
26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
 %rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
 %rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef,
 i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
 %bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
 %rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
 %rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
 i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
 %13 = extractelement <32 x i32> %bin.rdx20, i32 0
 ret i32 %13
}

define dso_local i32 @_Z9test_charPcS_i_512(ptr nocapture readonly, ptr nocapture readonly, i32) "min-legal-
vector-width"="512" {
; CHECK-SKX-LABEL: _Z9test_charPcS_i_512:
; CHECK-SKX:       # %bb.0: # %entry
; CHECK-SKX-NEXT:    movl %edx, %eax
; CHECK-SKX-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-SKX-NEXT:    xorl %ecx, %ecx
; CHECK-SKX-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-SKX-NEXT:    .p2align 4, 0x90
; CHECK-SKX-NEXT:  .LBB9_1: # %vector.body
; CHECK-SKX-NEXT:    # =>This Inner Loop Header: Depth=1
```

```
; CHECK-SKX-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-SKX-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-SKX-NEXT:    vpmaddwd %zmm2, %zmm3,
 %zmm2
; CHECK-SKX-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
; CHECK-SKX-NEXT:    addq $32, %rcx
; CHECK-SKX-NEXT:    cmpq %rcx, %rax
; CHECK-SKX-NEXT:    jne .LBB9_1
; CHECK-SKX-NEXT:  # %bb.2: # %middle.block
; CHECK-SKX-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-SKX-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-SKX-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-SKX-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpsrlq $32, %xmm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vmovd %xmm0, %eax
; CHECK-SKX-NEXT:    vzeroupper
; CHECK-SKX-NEXT:    retq
;
; CHECK-AVX512-LABEL: _Z9test_charPcS_i_512:
; CHECK-AVX512:       # %bb.0: # %entry
; CHECK-AVX512-NEXT:    movl %edx, %eax
; CHECK-AVX512-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    xorl %ecx, %ecx
; CHECK-AVX512-NEXT:    vpxor
 %xmm1, %xmm1, %xmm1
; CHECK-AVX512-NEXT:    .p2align 4, 0x90
; CHECK-AVX512-NEXT:  .LBB9_1: # %vector.body
; CHECK-AVX512-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-AVX512-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-AVX512-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-AVX512-NEXT:    vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-AVX512-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
; CHECK-AVX512-NEXT:    addq $32, %rcx
; CHECK-AVX512-NEXT:    cmpq %rcx, %rax
; CHECK-AVX512-NEXT:    jne .LBB9_1
; CHECK-AVX512-NEXT:  # %bb.2: # %middle.block
; CHECK-AVX512-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-AVX512-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-AVX512-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpsrlq $32, %xmm0, %xmm1
```

```
; CHECK-AVX512-NEXT:    vpaddd %xmm1,
 %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vmovd %xmm0, %eax
; CHECK-AVX512-NEXT:    vzeroupper
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI-LABEL: _Z9test_charPcS_i_512:
; CHECK-VBMI:       # %bb.0: # %entry
; CHECK-VBMI-NEXT:    movl %edx, %eax
; CHECK-VBMI-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    xorl %ecx, %ecx
; CHECK-VBMI-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-VBMI-NEXT:    .p2align 4, 0x90
; CHECK-VBMI-NEXT:  .LBB9_1: # %vector.body
; CHECK-VBMI-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-VBMI-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-VBMI-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-VBMI-NEXT:    vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
; CHECK-VBMI-NEXT:    addq $32, %rcx
; CHECK-VBMI-NEXT:    cmpq %rcx, %rax
; CHECK-VBMI-NEXT:    jne .LBB9_1
; CHECK-VBMI-NEXT:  # %bb.2: # %middle.block
; CHECK-VBMI-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-VBMI-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-VBMI-NEXT:    vpaddd
 %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,1,1]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vmovd %xmm0, %eax
; CHECK-VBMI-NEXT:    vzeroupper
; CHECK-VBMI-NEXT:    retq
entry:
 %3 = zext i32 %2 to i64
 br label %vector.body

vector.body:
 %index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
 %vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
 %4 = getelementptr inbounds i8, ptr %0, i64 %index
 %5 = bitcast ptr %4 to ptr
 %wide.load = load <32 x i8>, ptr %5, align 1
 %6 = sext <32 x i8> %wide.load to <32 x i32>
 %7 = getelementptr inbounds i8, ptr %1, i64 %index
```

```
 %8 = bitcast ptr %7 to ptr
 %wide.load14 = load <32 x i8>, ptr %8, align 1
 %9 = sext <32 x i8> %wide.load14 to
 <32 x i32>
 %10 = mul nsw <32 x i32> %9, %6
 %11 = add nsw <32 x i32> %10, %vec.phi
 %index.next = add i64 %index, 32
 %12 = icmp eq i64 %index.next, %3
 br i1 %12, label %middle.block, label %vector.body

middle.block:
 %rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef>
 %bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
 %rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32
12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
 i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
 %rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
 %rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
 %rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
 %13 = extractelement <32 x i32> %bin.rdx20, i32 0
 ret i32 %13
}

@a = dso_local global [1024 x i8] zeroinitializer, align 16
@b = dso_local global [1024 x i8] zeroinitializer, align 16

define dso_local i32 @sad_16i8_256() "min-legal-vector-width"="256" {
; CHECK-SKX-LABEL: sad_16i8_256:
; CHECK-SKX:       # %bb.0: # %entry
; CHECK-SKX-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-SKX-NEXT:    movq $-1024, %rax # imm = 0xFC00
```

```
; CHECK-SKX-NEXT:    vpxor %xmm1,
 %xmm1, %xmm1
; CHECK-SKX-NEXT:    .p2align 4, 0x90
; CHECK-SKX-NEXT:  .LBB10_1: # %vector.body
; CHECK-SKX-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-SKX-NEXT:    vmovdqu a+1024(%rax), %xmm2
; CHECK-SKX-NEXT:    vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-SKX-NEXT:    vpaddd %ymm1, %ymm2, %ymm1
; CHECK-SKX-NEXT:    addq $4, %rax
; CHECK-SKX-NEXT:    jne .LBB10_1
; CHECK-SKX-NEXT:  # %bb.2: # %middle.block
; CHECK-SKX-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-SKX-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpsrlq $32, %xmm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vmovd %xmm0, %eax
; CHECK-SKX-NEXT:    vzeroupper
; CHECK-SKX-NEXT:    retq
;
; CHECK-AVX512-LABEL: sad_16i8_256:
; CHECK-AVX512:       # %bb.0: # %entry
; CHECK-AVX512-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-AVX512-NEXT:
   movq $-1024, %rax # imm = 0xFC00
; CHECK-AVX512-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-AVX512-NEXT:    .p2align 4, 0x90
; CHECK-AVX512-NEXT:  .LBB10_1: # %vector.body
; CHECK-AVX512-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-AVX512-NEXT:    vmovdqu a+1024(%rax), %xmm2
; CHECK-AVX512-NEXT:    vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-AVX512-NEXT:    vpaddd %ymm1, %ymm2, %ymm1
; CHECK-AVX512-NEXT:    addq $4, %rax
; CHECK-AVX512-NEXT:    jne .LBB10_1
; CHECK-AVX512-NEXT:  # %bb.2: # %middle.block
; CHECK-AVX512-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpsrlq $32, %xmm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vmovd %xmm0, %eax
; CHECK-AVX512-NEXT:    vzeroupper
; CHECK-AVX512-NEXT:
```

```
    retq
;
; CHECK-VBMI-LABEL: sad_16i8_256:
; CHECK-VBMI:       # %bb.0: # %entry
; CHECK-VBMI-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    movq $-1024, %rax # imm = 0xFC00
; CHECK-VBMI-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-VBMI-NEXT:    .p2align 4, 0x90
; CHECK-VBMI-NEXT:  .LBB10_1: # %vector.body
; CHECK-VBMI-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-VBMI-NEXT:    vmovdqu a+1024(%rax), %xmm2
; CHECK-VBMI-NEXT:    vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-VBMI-NEXT:    vpaddd %ymm1, %ymm2, %ymm1
; CHECK-VBMI-NEXT:    addq $4, %rax
; CHECK-VBMI-NEXT:    jne .LBB10_1
; CHECK-VBMI-NEXT:  # %bb.2: # %middle.block
; CHECK-VBMI-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-VBMI-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,1,1]
; CHECK-VBMI-NEXT:    vpaddd %xmm1,
 %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vmovd %xmm0, %eax
; CHECK-VBMI-NEXT:    vzeroupper
; CHECK-VBMI-NEXT:    retq
entry:
 br label %vector.body

vector.body:
 %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
 %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
 %0 = getelementptr inbounds [1024 x i8], ptr @a, i64 0, i64 %index
 %1 = bitcast ptr %0 to ptr
 %wide.load = load <16 x i8>, ptr %1, align 4
 %2 = zext <16 x i8> %wide.load to <16 x i32>
 %3 = getelementptr inbounds [1024 x i8], ptr @b, i64 0, i64 %index
 %4 = bitcast ptr %3 to ptr
 %wide.load1 = load <16 x i8>, ptr %4, align 4
 %5 = zext <16 x i8> %wide.load1 to <16 x i32>
 %6 = sub nsw <16 x i32> %2, %5
 %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
 %8 = sub nsw <16 x i32> zeroinitializer, %6
 %9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
 %10 = add
```

```
 nsw <16 x i32> %9, %vec.phi
 %index.next = add i64 %index, 4
 %11 = icmp eq i64 %index.next, 1024
 br i1 %11, label %middle.block, label %vector.body

middle.block:
 %rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32
13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx = add <16 x i32> %10, %rdx.shuf
 %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef>
 %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
 %rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
 %bin.rdx3 = add <16 x i32>
 %bin.rdx2, %rdx.shuf3
 %rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
i32 undef, i32 undef>
 %bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
 %12 = extractelement <16 x i32> %bin.rdx4, i32 0
 ret i32 %12
}

define dso_local i32 @sad_16i8_512() "min-legal-vector-width"="512" {
; CHECK-SKX-LABEL: sad_16i8_512:
; CHECK-SKX:       # %bb.0: # %entry
; CHECK-SKX-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-SKX-NEXT:    movq $-1024, %rax # imm = 0xFC00
; CHECK-SKX-NEXT:    .p2align 4, 0x90
; CHECK-SKX-NEXT:  .LBB11_1: # %vector.body
; CHECK-SKX-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-SKX-NEXT:    vmovdqu a+1024(%rax), %xmm1
; CHECK-SKX-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-SKX-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-SKX-NEXT:    addq $4, %rax
; CHECK-SKX-NEXT:    jne .LBB11_1
;
 CHECK-SKX-NEXT:  # %bb.2: # %middle.block
; CHECK-SKX-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-SKX-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-SKX-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vpsrlq $32, %xmm0, %xmm1
```

```
; CHECK-SKX-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-SKX-NEXT:    vmovd %xmm0, %eax
; CHECK-SKX-NEXT:    vzeroupper
; CHECK-SKX-NEXT:    retq
;
; CHECK-AVX512-LABEL: sad_16i8_512:
; CHECK-AVX512:       # %bb.0: # %entry
; CHECK-AVX512-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    movq $-1024, %rax # imm = 0xFC00
; CHECK-AVX512-NEXT:    .p2align 4, 0x90
; CHECK-AVX512-NEXT:  .LBB11_1: # %vector.body
; CHECK-AVX512-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-AVX512-NEXT:    vmovdqu a+1024(%rax), %xmm1
; CHECK-AVX512-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-AVX512-NEXT:
   vpaddd %zmm0, %zmm1, %zmm0
; CHECK-AVX512-NEXT:    addq $4, %rax
; CHECK-AVX512-NEXT:    jne .LBB11_1
; CHECK-AVX512-NEXT:  # %bb.2: # %middle.block
; CHECK-AVX512-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-AVX512-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vpsrlq $32, %xmm0, %xmm1
; CHECK-AVX512-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-AVX512-NEXT:    vmovd %xmm0, %eax
; CHECK-AVX512-NEXT:    vzeroupper
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI-LABEL: sad_16i8_512:
; CHECK-VBMI:       # %bb.0: # %entry
; CHECK-VBMI-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    movq $-1024, %rax # imm = 0xFC00
; CHECK-VBMI-NEXT:    .p2align 4, 0x90
; CHECK-VBMI-NEXT:  .LBB11_1: # %vector.body
; CHECK-VBMI-NEXT:    # =>This Inner Loop Header: Depth=1
;
 CHECK-VBMI-NEXT:    vmovdqu a+1024(%rax), %xmm1
; CHECK-VBMI-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-VBMI-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-VBMI-NEXT:    addq $4, %rax
; CHECK-VBMI-NEXT:    jne .LBB11_1
; CHECK-VBMI-NEXT:  # %bb.2: # %middle.block
; CHECK-VBMI-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-VBMI-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:    vextracti128 $1, %ymm0, %xmm1
```

```
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,2,3]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,1,1]
; CHECK-VBMI-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-VBMI-NEXT:    vmovd %xmm0, %eax
; CHECK-VBMI-NEXT:    vzeroupper
; CHECK-VBMI-NEXT:    retq
entry:
 br label %vector.body

vector.body:
 %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
 %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
 %0 = getelementptr
 inbounds [1024 x i8], ptr @a, i64 0, i64 %index
 %1 = bitcast ptr %0 to ptr
 %wide.load = load <16 x i8>, ptr %1, align 4
 %2 = zext <16 x i8> %wide.load to <16 x i32>
 %3 = getelementptr inbounds [1024 x i8], ptr @b, i64 0, i64 %index
 %4 = bitcast ptr %3 to ptr
 %wide.load1 = load <16 x i8>, ptr %4, align 4
 %5 = zext <16 x i8> %wide.load1 to <16 x i32>
 %6 = sub nsw <16 x i32> %2, %5
 %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
 %8 = sub nsw <16 x i32> zeroinitializer, %6
 %9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
 %10 = add nsw <16 x i32> %9, %vec.phi
 %index.next = add i64 %index, 4
 %11 = icmp eq i64 %index.next, 1024
 br i1 %11, label %middle.block, label %vector.body

middle.block:
 %rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32
 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx = add <16 x i32> %10, %rdx.shuf
 %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
 %rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
 %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
 %rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
```

```
i32 undef, i32 undef>
 %bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
  %12 = extractelement <16 x i32> %bin.rdx4, i32 0
 ret i32 %12
}

define dso_local void @sbto16f32_256(<16 x i16> %a, ptr %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    kshiftrw $8, %k0, %k1
; CHECK-NEXT:    vpmovm2d %k1, %ymm0
; CHECK-NEXT:    vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT:    vpmovm2d %k0, %ymm1
; CHECK-NEXT:    vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT:    vmovaps %ymm1, (%rdi)
; CHECK-NEXT:    vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = sitofp <16 x i1> %mask to <16 x float>
 store <16 x float> %1, ptr %res
 ret void
}

define dso_local void @sbto16f32_512(<16 x i16> %a, ptr %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    vpmovm2d %k0, %zmm0
; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:
    vmovaps %zmm0, (%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = sitofp <16 x i1> %mask to <16 x float>
 store <16 x float> %1, ptr %res
 ret void
}

define dso_local void @sbto16f64_256(<16 x i16> %a, ptr %res)  "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    kshiftrw $8, %k0, %k1
; CHECK-NEXT:    vpmovm2d %k1, %ymm0
; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm1
```

```
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:    vpmovm2d %k0, %ymm2
; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:    vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:    vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT:    vmovaps %ymm3, (%rdi)
; CHECK-NEXT:    vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT:    vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask
 = icmp slt <16 x i16> %a, zeroinitializer
 %1 = sitofp <16 x i1> %mask to <16 x double>
 store <16 x double> %1, ptr %res
 ret void
}

define dso_local void @sbto16f64_512(<16 x i16> %a, ptr %res)  "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f64_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    vpmovm2d %k0, %zmm0
; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT:    vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT:    vmovaps %zmm1, (%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = sitofp <16 x i1> %mask to <16 x double>
 store <16 x double> %1, ptr %res
 ret void
}

define dso_local void @ubto16f32_256(<16 x i16> %a, ptr %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    kshiftrw $8, %k0, %k1
; CHECK-NEXT:
   vpmovm2d %k1, %ymm0
; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:    vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT:    vpmovm2d %k0, %ymm1
; CHECK-NEXT:    vpsrld $31, %ymm1, %ymm1
```

```
; CHECK-NEXT:    vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT:    vmovaps %ymm1, (%rdi)
; CHECK-NEXT:    vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = uitofp <16 x i1> %mask to <16 x float>
 store <16 x float> %1, ptr %res
 ret void
}

define dso_local void @ubto16f32_512(<16 x i16> %a, ptr %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    vpmovm2d %k0, %zmm0
; CHECK-NEXT:    vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:    vmovaps %zmm0, (%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = uitofp <16 x i1> %mask to <16 x float>
 store
 <16 x float> %1, ptr %res
 ret void
}

define dso_local void @ubto16f64_256(<16 x i16> %a, ptr %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    kshiftrw $8, %k0, %k1
; CHECK-NEXT:    vpmovm2d %k1, %ymm0
; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:    vpmovm2d %k0, %ymm2
; CHECK-NEXT:    vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:    vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:    vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT:    vmovaps %ymm3, (%rdi)
; CHECK-NEXT:    vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT:    vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT:    vzeroupper
```

```
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = uitofp <16 x i1> %mask to <16 x double>
 store <16
 x double> %1, ptr %res
 ret void
}

define dso_local void @ubto16f64_512(<16 x i16> %a, ptr %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f64_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    vpmovm2d %k0, %zmm0
; CHECK-NEXT:    vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm1
; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT:    vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT:    vmovaps %zmm1, (%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer
 %1 = uitofp <16 x i1> %mask to <16 x double>
 store <16 x double> %1, ptr %res
 ret void
}

define <16 x i16> @test_16f32toub_256(ptr %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
; CHECK-LABEL: test_16f32toub_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:    vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:    vpmovd2m %ymm1, %k0
; CHECK-NEXT:
   vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:    vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:    vpmovd2m %ymm1, %k1
; CHECK-NEXT:    kunpckbw %k0, %k1, %k1
; CHECK-NEXT:    vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:    retq
 %a = load <16 x float>, ptr %ptr
 %mask = fptoui <16 x float> %a to <16 x i1>
 %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
 ret <16 x i16> %select
}

define <16 x i16> @test_16f32toub_512(ptr %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
; CHECK-LABEL: test_16f32toub_512:
```

```
; CHECK:       # %bb.0:
; CHECK-NEXT:    vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:    vpslld $31, %zmm1, %zmm1
; CHECK-NEXT:    vpmovd2m %zmm1, %k1
; CHECK-NEXT:    vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:    retq
 %a = load <16 x float>, ptr %ptr
 %mask = fptoui <16 x float> %a to <16 x i1>
 %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
 ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_256(ptr
 %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
; CHECK-LABEL: test_16f32tosb_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:    vpmovd2m %ymm1, %k0
; CHECK-NEXT:    vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:    vpmovd2m %ymm1, %k1
; CHECK-NEXT:    kunpckbw %k0, %k1, %k1
; CHECK-NEXT:    vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:    retq
 %a = load <16 x float>, ptr %ptr
 %mask = fptosi <16 x float> %a to <16 x i1>
 %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
 ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(ptr %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
; CHECK-LABEL: test_16f32tosb_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:    vpmovd2m %zmm1, %k1
; CHECK-NEXT:    vmovdqu16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT:    retq
 %a = load <16 x float>, ptr %ptr
 %mask = fptosi <16 x float> %a to <16 x i1>
 %select =
 select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
 ret <16 x i16> %select
}

define dso_local void @mul256(ptr %a, ptr %b, ptr %c) "min-legal-vector-width"="256" {
; CHECK-SKX-VBMI-LABEL: mul256:
; CHECK-SKX-VBMI:       # %bb.0:
; CHECK-SKX-VBMI-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-SKX-VBMI-NEXT:    vmovdqa 32(%rdi), %ymm1
```

```
; CHECK-SKX-VBMI-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-SKX-VBMI-NEXT:    vmovdqa 32(%rsi), %ymm3
; CHECK-SKX-VBMI-NEXT:    vpbroadcastw {{.*#+}} ymm4 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-SKX-VBMI-NEXT:    vpandn %ymm3, %ymm4, %ymm5
; CHECK-SKX-VBMI-NEXT:    vpmaddubsw %ymm5, %ymm1, %ymm5
; CHECK-SKX-VBMI-NEXT:    vpand %ymm3, %ymm4, %ymm3
; CHECK-SKX-VBMI-NEXT:    vpmaddubsw %ymm3, %ymm1, %ymm1
; CHECK-SKX-VBMI-NEXT:    vmovdqa {{.*#+}} ymm3 =
[0,32,2,34,4,36,6,38,8,40,10,42,12,44,14,46,16,48,18,50,20,52,22,54,24,56,26,58,28,60,30,62]
; CHECK-SKX-VBMI-NEXT:    vpermt2b %ymm5, %ymm3, %ymm1
; CHECK-SKX-VBMI-NEXT:
   vpandn %ymm2, %ymm4, %ymm5
; CHECK-SKX-VBMI-NEXT:    vpmaddubsw %ymm5, %ymm0, %ymm5
; CHECK-SKX-VBMI-NEXT:    vpand %ymm2, %ymm4, %ymm2
; CHECK-SKX-VBMI-NEXT:    vpmaddubsw %ymm2, %ymm0, %ymm0
; CHECK-SKX-VBMI-NEXT:    vpermt2b %ymm5, %ymm3, %ymm0
; CHECK-SKX-VBMI-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-SKX-VBMI-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-SKX-VBMI-NEXT:    vzeroupper
; CHECK-SKX-VBMI-NEXT:    retq
;
; CHECK-AVX512-LABEL: mul256:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-AVX512-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-AVX512-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-AVX512-NEXT:    vmovdqa 32(%rsi), %ymm3
; CHECK-AVX512-NEXT:    vpbroadcastw {{.*#+}} ymm4 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-AVX512-NEXT:    vpand %ymm3, %ymm4, %ymm5
; CHECK-AVX512-NEXT:    vpmaddubsw %ymm5, %ymm1, %ymm5
; CHECK-AVX512-NEXT:    vpandn %ymm3, %ymm4, %ymm3
; CHECK-AVX512-NEXT:    vpmaddubsw %ymm3, %ymm1,
 %ymm1
; CHECK-AVX512-NEXT:    vpsllw $8, %ymm1, %ymm1
; CHECK-AVX512-NEXT:    vpternlogq $248, %ymm4, %ymm5, %ymm1
; CHECK-AVX512-NEXT:    vpand %ymm2, %ymm4, %ymm3
; CHECK-AVX512-NEXT:    vpmaddubsw %ymm3, %ymm0, %ymm3
; CHECK-AVX512-NEXT:    vpandn %ymm2, %ymm4, %ymm2
; CHECK-AVX512-NEXT:    vpmaddubsw %ymm2, %ymm0, %ymm0
; CHECK-AVX512-NEXT:    vpsllw $8, %ymm0, %ymm0
; CHECK-AVX512-NEXT:    vpternlogq $248, %ymm4, %ymm3, %ymm0
; CHECK-AVX512-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-AVX512-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-AVX512-NEXT:    vzeroupper
; CHECK-AVX512-NEXT:    retq
;
```

```
; CHECK-VBMI-LABEL: mul256:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-VBMI-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-VBMI-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-VBMI-NEXT:    vmovdqa 32(%rsi), %ymm3
; CHECK-VBMI-NEXT:    vpbroadcastw {{.*#+}} ymm4 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-VBMI-NEXT:    vpandn %ymm3, %ymm4, %ymm5
;
 CHECK-VBMI-NEXT:    vpmaddubsw %ymm5, %ymm1, %ymm5
; CHECK-VBMI-NEXT:    vpand %ymm3, %ymm4, %ymm3
; CHECK-VBMI-NEXT:    vpmaddubsw %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT:    vmovdqa {{.*#+}} ymm3 =
[0,32,2,34,4,36,6,38,8,40,10,42,12,44,14,46,16,48,18,50,20,52,22,54,24,56,26,58,28,60,30,62]
; CHECK-VBMI-NEXT:    vpermt2b %ymm5, %ymm3, %ymm1
; CHECK-VBMI-NEXT:    vpandn %ymm2, %ymm4, %ymm5
; CHECK-VBMI-NEXT:    vpmaddubsw %ymm5, %ymm0, %ymm5
; CHECK-VBMI-NEXT:    vpand %ymm2, %ymm4, %ymm2
; CHECK-VBMI-NEXT:    vpmaddubsw %ymm2, %ymm0, %ymm0
; CHECK-VBMI-NEXT:    vpermt2b %ymm5, %ymm3, %ymm0
; CHECK-VBMI-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-VBMI-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-VBMI-NEXT:    vzeroupper
; CHECK-VBMI-NEXT:    retq
 %d = load <64 x i8>, ptr %a
 %e = load <64 x i8>, ptr %b
 %f = mul <64 x i8> %d, %e
 store <64 x i8> %f, ptr %c
 ret void
}

define dso_local void @mul512(ptr %a, ptr %b, ptr %c) "min-legal-vector-width"="512" {
; CHECK-SKX-VBMI-LABEL: mul512:
;
 CHECK-SKX-VBMI:       # %bb.0:
; CHECK-SKX-VBMI-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-SKX-VBMI-NEXT:    vmovdqa64 (%rsi), %zmm1
; CHECK-SKX-VBMI-NEXT:    vpbroadcastw {{.*#+}} zmm2 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-SKX-VBMI-NEXT:    vpandnq %zmm1, %zmm2, %zmm3
; CHECK-SKX-VBMI-NEXT:    vpmaddubsw %zmm3, %zmm0, %zmm3
; CHECK-SKX-VBMI-NEXT:    vpandq %zmm1, %zmm2, %zmm1
; CHECK-SKX-VBMI-NEXT:    vpmaddubsw %zmm1, %zmm0, %zmm0
; CHECK-SKX-VBMI-NEXT:    vmovdqa64 {{.*#+}} zmm1 =
[0,64,2,66,4,68,6,70,8,72,10,74,12,76,14,78,16,80,18,82,20,84,22,86,24,88,26,90,28,92,30,94,32,96,34,98,36,100,38,102,40,104,42,106,44,108,46,110,48,112,50,114,52,116,54,118,56,120,58,122,60,124,62,126]
```

```
; CHECK-SKX-VBMI-NEXT:    vpermi2b %zmm3, %zmm0, %zmm1
; CHECK-SKX-VBMI-NEXT:    vmovdqa64 %zmm1, (%rdx)
; CHECK-SKX-VBMI-NEXT:    vzeroupper
; CHECK-SKX-VBMI-NEXT:    retq
;
; CHECK-AVX512-LABEL: mul512:
;
 CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-AVX512-NEXT:    vmovdqa64 (%rsi), %zmm1
; CHECK-AVX512-NEXT:    vpbroadcastw {{.*#+}} zmm2 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
; CHECK-AVX512-NEXT:    vpandq %zmm1, %zmm2, %zmm3
; CHECK-AVX512-NEXT:    vpmaddubsw %zmm3, %zmm0, %zmm3
; CHECK-AVX512-NEXT:    vpandnq %zmm1, %zmm2, %zmm1
; CHECK-AVX512-NEXT:    vpmaddubsw %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:    vpsllw $8, %zmm0, %zmm0
; CHECK-AVX512-NEXT:    vpternlogq $248, %zmm2, %zmm3, %zmm0
; CHECK-AVX512-NEXT:    vmovdqa64 %zmm0, (%rdx)
; CHECK-AVX512-NEXT:    vzeroupper
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI-LABEL: mul512:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:    vmovdqa64 (%rdi), %zmm0
; CHECK-VBMI-NEXT:    vmovdqa64 (%rsi), %zmm1
; CHECK-VBMI-NEXT:    vpbroadcastw {{.*#+}} zmm2 =
[255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255,255]
;
 CHECK-VBMI-NEXT:    vpandnq %zmm1, %zmm2, %zmm3
; CHECK-VBMI-NEXT:    vpmaddubsw %zmm3, %zmm0, %zmm3
; CHECK-VBMI-NEXT:    vpandq %zmm1, %zmm2, %zmm1
; CHECK-VBMI-NEXT:    vpmaddubsw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:    vmovdqa64 {{.*#+}} zmm1 =
[0,64,2,66,4,68,6,70,8,72,10,74,12,76,14,78,16,80,18,82,20,84,22,86,24,88,26,90,28,92,30,94,32,96,34,98,36,100,38,102,40,104,42,106,44,108,46,110,48,112,50,114,52,116,54,118,56,120,58,122,60,124,62,126]
; CHECK-VBMI-NEXT:    vpermi2b %zmm3, %zmm0, %zmm1
; CHECK-VBMI-NEXT:    vmovdqa64 %zmm1, (%rdx)
; CHECK-VBMI-NEXT:    vzeroupper
; CHECK-VBMI-NEXT:    retq
 %d = load <64 x i8>, ptr %a
 %e = load <64 x i8>, ptr %b
 %f = mul <64 x i8> %d, %e
 store <64 x i8> %f, ptr %c
 ret void
}
```

```
; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, ptr %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {
; CHECK-LABEL: mload_v4i32:
; CHECK:
      # %bb.0:
; CHECK-NEXT:    vptestnmd %xmm0, %xmm0, %k1
; CHECK-NEXT:    vpblendmd (%rdi), %xmm1, %xmm0 {%k1}
; CHECK-NEXT:    retq
 %mask = icmp eq <4 x i32> %trigger, zeroinitializer
 %res = call <4 x i32> @llvm.masked.load.v4i32.p0(ptr %addr, i32 4, <4 x i1> %mask, <4 x i32> %dst)
 ret <4 x i32> %res
}
declare <4 x i32> @llvm.masked.load.v4i32.p0(ptr, i32, <4 x i1>, <4 x i32>)

define <16 x i32> @trunc_v16i64_v16i32(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i32:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT:    vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT:    vpmovqd %ymm0, %xmm0
; CHECK-NEXT:    vpmovqd %ymm1, %xmm1
; CHECK-NEXT:    vinserti128 $1, %xmm1, %ymm0, %ymm0
; CHECK-NEXT:    vpmovqd %ymm2, %xmm1
; CHECK-NEXT:    vpmovqd %ymm3, %xmm2
; CHECK-NEXT:    vinserti128 $1, %xmm2, %ymm1, %ymm1
; CHECK-NEXT:    retq
 %a = load <16
 x i64>, ptr %x
 %b = trunc <16 x i64> %a to <16 x i32>
 ret <16 x i32> %b
}

define <16 x i8> @trunc_v16i64_v16i8(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT:    vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT:    vpmovqb %ymm3, %xmm3
; CHECK-NEXT:    vpmovqb %ymm2, %xmm2
; CHECK-NEXT:    vpunpckldq {{.*#+}} xmm2 = xmm2[0],xmm3[0],xmm2[1],xmm3[1]
; CHECK-NEXT:    vpmovqb %ymm1, %xmm1
; CHECK-NEXT:    vpmovqb %ymm0, %xmm0
; CHECK-NEXT:    vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
```

```
; CHECK-NEXT:    vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm2[0]
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = load <16 x i64>, ptr %x
 %b = trunc <16 x i64> %a to <16 x i8>
 ret <16 x i8> %b
}

define <16 x i8> @trunc_v16i32_v16i8(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL:
 trunc_v16i32_v16i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpmovdb %ymm1, %xmm1
; CHECK-NEXT:    vpmovdb %ymm0, %xmm0
; CHECK-NEXT:    vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = load <16 x i32>, ptr %x
 %b = trunc <16 x i32> %a to <16 x i8>
 ret <16 x i8> %b
}

define <8 x i8> @trunc_v8i64_v8i8(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpmovqb %ymm1, %xmm1
; CHECK-NEXT:    vpmovqb %ymm0, %xmm0
; CHECK-NEXT:    vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = load <8 x i64>, ptr %x
 %b = trunc <8 x i64> %a to <8 x i8>
 ret <8 x i8> %b
}

define <8 x i16> @trunc_v8i64_v8i16(ptr %x) nounwind "min-legal-vector-width"="256"
 {
; CHECK-LABEL: trunc_v8i64_v8i16:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:    vpmovqw %ymm1, %xmm1
; CHECK-NEXT:    vpmovqw %ymm0, %xmm0
; CHECK-NEXT:    vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
```

```
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = load <8 x i64>, ptr %x
 %b = trunc <8 x i64> %a to <8 x i16>
 ret <8 x i16> %b
}

define <8 x i32> @trunc_v8i64_v8i32_zeroes(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpsrlq $48, 32(%rdi), %ymm0
; CHECK-NEXT:    vpsrlq $48, (%rdi), %ymm1
; CHECK-NEXT:    vpackusdw %ymm0, %ymm1, %ymm0
; CHECK-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT:    retq
 %a = load <8 x i64>, ptr %x
 %b = lshr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
 %c = trunc <8 x i64> %b to <8 x i32>
 ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(ptr
 %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-NEXT:    vpmovsxbw {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT:    vpermi2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT:    retq
 %a = load <16 x i32>, ptr %x
 %b = lshr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32
16, i32 16, i32 16, i32 16, i32 16>
 %c = trunc <16 x i32> %b to <16 x i16>
 ret <16 x i16> %c
}

define <32 x i8> @trunc_v32i16_v32i8_zeroes(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-SKX-VBMI-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-SKX-VBMI:       # %bb.0:
; CHECK-SKX-VBMI-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-SKX-VBMI-NEXT:    vmovdqa {{.*#+}} ymm0 =
[1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-SKX-VBMI-NEXT:    vpermi2b 32(%rdi), %ymm1,
 %ymm0
; CHECK-SKX-VBMI-NEXT:    retq
;
; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-AVX512:       # %bb.0:
```

```
; CHECK-AVX512-NEXT:    vpsrlw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT:    vpsrlw $8, (%rdi), %ymm1
; CHECK-AVX512-NEXT:    vpackuswb %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:    vmovdqa {{.*#+}} ymm0 =
[1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT:    vpermi2b 32(%rdi), %ymm1, %ymm0
; CHECK-VBMI-NEXT:    retq
 %a = load <32 x i16>, ptr %x
 %b = lshr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8,
i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
 %c = trunc
 <32 x i16> %b to <32 x i8>
 ret <32 x i8> %c
}

define <8 x i32> @trunc_v8i64_v8i32_sign(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i32_sign:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpsraq $48, 32(%rdi), %ymm0
; CHECK-NEXT:    vpsraq $48, (%rdi), %ymm1
; CHECK-NEXT:    vpackssdw %ymm0, %ymm1, %ymm0
; CHECK-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT:    retq
 %a = load <8 x i64>, ptr %x
 %b = ashr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
 %c = trunc <8 x i64> %b to <8 x i32>
 ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_sign(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i16_sign:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-NEXT:    vpmovsxbw {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT:    vpermi2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT:    retq
 %a = load <16 x i32>, ptr %x
 %b = ashr <16
 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32
16, i32 16, i32 16>
 %c = trunc <16 x i32> %b to <16 x i16>
 ret <16 x i16> %c
```

```
}

define <32 x i8> @trunc_v32i16_v32i8_sign(ptr %x) nounwind "min-legal-vector-width"="256" {
; CHECK-SKX-VBMI-LABEL: trunc_v32i16_v32i8_sign:
; CHECK-SKX-VBMI:       # %bb.0:
; CHECK-SKX-VBMI-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-SKX-VBMI-NEXT:    vmovdqa {{.*#+}} ymm0 =
[1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-SKX-VBMI-NEXT:    vpermi2b 32(%rdi), %ymm1, %ymm0
; CHECK-SKX-VBMI-NEXT:    retq
;
; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_sign:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:    vpsrlw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT:    vpsrlw $8, (%rdi), %ymm1
; CHECK-AVX512-NEXT:    vpackuswb %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI-LABEL:
 trunc_v32i16_v32i8_sign:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:    vmovdqa {{.*#+}} ymm0 =
[1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT:    vpermi2b 32(%rdi), %ymm1, %ymm0
; CHECK-VBMI-NEXT:    retq
 %a = load <32 x i16>, ptr %x
 %b = ashr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8,
i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
 %c = trunc <32 x i16> %b to <32 x i8>
 ret <32 x i8> %c
}

define dso_local void @zext_v16i8_v16i64(<16 x i8> %x, ptr %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: zext_v16i8_v16i64:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovzxbw {{.*#+}} ymm1 =
xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zer
o,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xm
m0[15],zero
;
 CHECK-NEXT:    vpshufd {{.*#+}} xmm2 = xmm1[2,3,2,3]
; CHECK-NEXT:    vpmovzxwq {{.*#+}} ymm2 =
xmm2[0],zero,zero,zero,xmm2[1],zero,zero,zero,xmm2[2],zero,zero,zero,xmm2[3],zero,zero,zero
; CHECK-NEXT:    vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT:    vpshufd {{.*#+}} xmm3 = xmm1[2,3,2,3]
; CHECK-NEXT:    vpmovzxwq {{.*#+}} ymm3 =
```

```
xmm3[0],zero,zero,zero,xmm3[1],zero,zero,zero,xmm3[2],zero,zero,zero,xmm3[3],zero,zero,zero
; CHECK-NEXT:    vpmovzxwq {{.*#+}} ymm1 =
xmm1[0],zero,zero,zero,xmm1[1],zero,zero,zero,xmm1[2],zero,zero,zero,xmm1[3],zero,zero,zero
; CHECK-NEXT:    vpmovzxbq {{.*#+}} ymm0 =
xmm0[0],zero,zero,zero,zero,zero,zero,zero,xmm0[1],zero,zero,zero,zero,zero,zero,zero,xmm0[2],zero,zero,zero,zer
o,zero,zero,zero,xmm0[3],zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT:    vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:    vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT:    vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT:
   vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = zext <16 x i8> %x to <16 x i64>
 store <16 x i64> %a, ptr %y
 ret void
}

define dso_local void @sext_v16i8_v16i64(<16 x i8> %x, ptr %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpmovsxbw %xmm0, %ymm1
; CHECK-NEXT:    vpshufd {{.*#+}} xmm2 = xmm1[2,3,2,3]
; CHECK-NEXT:    vpmovsxwq %xmm2, %ymm2
; CHECK-NEXT:    vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT:    vpshufd {{.*#+}} xmm3 = xmm1[2,3,2,3]
; CHECK-NEXT:    vpmovsxwq %xmm3, %ymm3
; CHECK-NEXT:    vpmovsxwq %xmm1, %ymm1
; CHECK-NEXT:    vpmovsxbq %xmm0, %ymm0
; CHECK-NEXT:    vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:    vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT:    vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT:    vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = sext <16 x i8> %x to <16 x i64>
 store <16 x i64> %a, ptr %y
 ret void
}

define dso_local void @vselect_split_v8i16_setcc(<8
 x i16> %s, <8 x i16> %t, ptr %p, ptr %q, ptr %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i16_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:    vpcmpeqw %xmm1, %xmm0, %k1
; CHECK-NEXT:    kshiftrb $4, %k1, %k2
```

```
; CHECK-NEXT:    vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT:    vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT:    vmovdqa %ymm2, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <8 x i64>, ptr %p
 %y = load <8 x i64>, ptr %q
 %a = icmp eq <8 x i16> %s, %t
 %b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
 store <8 x i64> %b, ptr %r
 ret void
}

define dso_local void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, ptr %p, ptr %q, ptr %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i32_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-NEXT:
   vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:    vpcmpeqd %ymm1, %ymm0, %k1
; CHECK-NEXT:    kshiftrb $4, %k1, %k2
; CHECK-NEXT:    vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT:    vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT:    vmovdqa %ymm2, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <8 x i64>, ptr %p
 %y = load <8 x i64>, ptr %q
 %a = icmp eq <8 x i32> %s, %t
 %b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
 store <8 x i64> %b, ptr %r
 ret void
}

define dso_local void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, ptr %p, ptr %q, ptr %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i8_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:    vpcmpeqb %xmm1, %xmm0, %k1
; CHECK-NEXT:    kshiftrw $8, %k1, %k2
; CHECK-NEXT:    vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT:    vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT:
```

```
   vmovdqa %ymm2, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <16 x i32>, ptr %p
 %y = load <16 x i32>, ptr %q
 %a = icmp eq <16 x i8> %s, %t
 %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
 store <16 x i32> %b, ptr %r
 ret void
}

define dso_local void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, ptr %p, ptr %q, ptr %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i16_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:    vpcmpeqw %ymm1, %ymm0, %k1
; CHECK-NEXT:    kshiftrw $8, %k1, %k2
; CHECK-NEXT:    vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT:    vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT:    vmovdqa %ymm2, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <16 x i32>, ptr %p
 %y = load <16 x i32>, ptr %q
 %a = icmp eq <16 x i16> %s,
 %t
 %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
 store <16 x i32> %b, ptr %r
 ret void
}

define <16 x i8> @trunc_packus_v16i32_v16i8(ptr %p) "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_packus_v16i32_v16i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT:    vpmovuswb %ymm0, %xmm0
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = load <16 x i32>, ptr %p
 %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
 %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32
```

```
255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
 %d = icmp sgt <16 x i32> %c, zeroinitializer
 %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
  %f = trunc <16 x i32> %e to <16 x i8>
 ret <16 x i8> %f
}

define dso_local void @trunc_packus_v16i32_v16i8_store(ptr %p, ptr %q) "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
; CHECK-NEXT:    vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT:    vpmovuswb %ymm0, (%rsi)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %a = load <16 x i32>, ptr %p
 %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32
255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
 %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32
255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
 %d = icmp sgt <16 x i32> %c, zeroinitializer
 %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
  %f = trunc <16 x i32> %e to <16 x i8>
 store <16 x i8> %f, ptr %q
 ret void
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_argument_return:
; CHECK:       # %bb.0:
; CHECK-NEXT:    retq
 ret <64 x i1> %x
}

define dso_local void @v64i1_shuffle(ptr %x, ptr %y) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_shuffle:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm0
; CHECK-NEXT:    vptestnmb %ymm1, %ymm1, %k0
; CHECK-NEXT:    kshiftrd $1, %k0, %k1
; CHECK-NEXT:    kshiftlq $63, %k0, %k2
; CHECK-NEXT:    kshiftrq $62, %k2, %k2
; CHECK-NEXT:    kshiftlq $63, %k1, %k1
; CHECK-NEXT:    kshiftrq $63, %k1, %k1
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-5, %rax
```

```
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $3, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq
 $61, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-9, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $2, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $60, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-17, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $5, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $59, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-33, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $4, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $58, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-65, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $7, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2,
 %k2
; CHECK-NEXT:    kshiftrq $57, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-129, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $6, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $56, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-257, %rax # imm = 0xFEFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $9, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
```

```
; CHECK-NEXT:    kshiftrq $55, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-513, %rax # imm = 0xFDFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $8, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $54, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1,
 %k1
; CHECK-NEXT:    kshiftrd $11, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $53, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $10, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $52, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-4097, %rax # imm = 0xEFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $13, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $51, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-8193, %rax # imm = 0xDFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $12, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $50, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:
   movq $-16385, %rax # imm = 0xBFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $15, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $49, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-32769, %rax # imm = 0xFFFF7FFF
; CHECK-NEXT:    kmovq %rax, %k2
```

```
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $14, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $48, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-65537, %rax # imm = 0xFFFEFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $17, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $47, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-131073, %rax # imm = 0xFFFDFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $16, %k0, %k2
; CHECK-NEXT:
   kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $46, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-262145, %rax # imm = 0xFFFBFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $19, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $45, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-524289, %rax # imm = 0xFFF7FFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $18, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $44, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1048577, %rax # imm = 0xFFEFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $21, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $43, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-2097153, %rax #
 imm = 0xFFDFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $20, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $42, %k2, %k2
```

```
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-4194305, %rax # imm = 0xFFBFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $23, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $41, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-8388609, %rax # imm = 0xFF7FFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $22, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $40, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-16777217, %rax # imm = 0xFEFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $25, %k0, %k2
; CHECK-NEXT:    kshiftlq
 $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $39, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-33554433, %rax # imm = 0xFDFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $24, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $38, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-67108865, %rax # imm = 0xFBFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $27, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $37, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-134217729, %rax # imm = 0xF7FFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $26, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $36, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-268435457, %rax # imm = 0xEFFFFFFF
;
 CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
```

```
; CHECK-NEXT:    kshiftrd $29, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $35, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-536870913, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $28, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $34, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1073741825, %rax # imm = 0xBFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $31, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $33, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movabsq $-2147483649, %rax # imm = 0xFFFFFFFF7FFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k2
; CHECK-NEXT:    vptestnmb %ymm0, %ymm0, %k1
; CHECK-NEXT:
   kshiftrd $30, %k0, %k0
; CHECK-NEXT:    kshiftlq $63, %k0, %k0
; CHECK-NEXT:    kshiftrq $32, %k0, %k0
; CHECK-NEXT:    korq %k0, %k2, %k0
; CHECK-NEXT:    movabsq $-4294967297, %rax # imm = 0xFFFFFFFEFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $1, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $31, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-8589934593, %rax # imm = 0xFFFFFFFDFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftlq $63, %k1, %k2
; CHECK-NEXT:    kshiftrq $30, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-17179869185, %rax # imm = 0xFFFFFFFBFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $3, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $29, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:
```

```
   movabsq $-34359738369, %rax # imm = 0xFFFFFFF7FFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $2, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $28, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-68719476737, %rax # imm = 0xFFFFFFEFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $5, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $27, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-137438953473, %rax # imm = 0xFFFFFFDFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $4, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $26, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-274877906945, %rax # imm = 0xFFFFFFBFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:
   kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $7, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $25, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-549755813889, %rax # imm = 0xFFFFFF7FFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $6, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $24, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-1099511627777, %rax # imm = 0xFFFFFEFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $9, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $23, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-2199023255553, %rax # imm = 0xFFFFFDFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $8, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2,
```

```
%k2
; CHECK-NEXT:    kshiftrq $22, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-4398046511105, %rax # imm = 0xFFFFFBFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $11, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $21, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-8796093022209, %rax # imm = 0xFFFFF7FFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $10, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $20, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-17592186044417, %rax # imm = 0xFFFFEFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $13, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $19, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq
 $-35184372088833, %rax # imm = 0xFFFFDFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $12, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $18, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-70368744177665, %rax # imm = 0xFFFFBFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $15, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $17, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-140737488355329, %rax # imm = 0xFFFF7FFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $14, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $16, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-281474976710657, %rax # imm = 0xFFFEFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
```

```
; CHECK-NEXT:
   kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $17, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $15, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-562949953421313, %rax # imm = 0xFFFDFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $16, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $14, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-1125899906842625, %rax # imm = 0xFFFBFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $19, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $13, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-2251799813685249, %rax # imm = 0xFFF7FFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $18, %k1, %k2
; CHECK-NEXT:    kshiftlq
 $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $12, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-4503599627370497, %rax # imm = 0xFFEFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $21, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $11, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-9007199254740993, %rax # imm = 0xFFDFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $20, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $10, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-18014398509481985, %rax # imm = 0xFFBFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $23, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $9, %k2, %k2
```

```
; CHECK-NEXT:    korq %k2, %k0,
 %k0
; CHECK-NEXT:    movabsq $-36028797018963969, %rax # imm = 0xFF7FFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $22, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $8, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-72057594037927937, %rax # imm = 0xFEFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $25, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $7, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-144115188075855873, %rax # imm = 0xFDFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $24, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $6, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-288230376151711745, %rax # imm = 0xFBFFFFFFFFFFFFF
; CHECK-NEXT:
   kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $27, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $5, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $26, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $4, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-1152921504606846977, %rax # imm = 0xEFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $29, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $3, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-2305843009213693953, %rax # imm = 0xDFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
```

```
; CHECK-NEXT:    kshiftrd
 $28, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $2, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-4611686018427387905, %rax # imm = 0xBFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $31, %k1, %k2
; CHECK-NEXT:    kshiftlq $62, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $30, %k1, %k1
; CHECK-NEXT:    kshiftlq $1, %k0, %k0
; CHECK-NEXT:    kshiftrq $1, %k0, %k0
; CHECK-NEXT:    kshiftlq $63, %k1, %k1
; CHECK-NEXT:    korq %k1, %k0, %k1
; CHECK-NEXT:    vmovdqu8 %ymm1, (%rsi) {%k1}
; CHECK-NEXT:    kshiftrq $32, %k1, %k1
; CHECK-NEXT:    vmovdqu8 %ymm0, 32(%rsi) {%k1}
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
entry:
 %a = load <64 x i8>, ptr %x
 %b = icmp eq <64 x i8> %a, zeroinitializer
 %shuf = shufflevector <64 x i1> %b, <64 x i1> undef, <64 x i32> <i32 1, i32 0, i32 3, i32 2, i32 5, i32 4, i32 7, i32
6, i32 9, i32 8, i32 11,
 i32 10, i32 13, i32 12, i32 15, i32 14, i32 17, i32 16, i32 19, i32 18, i32 21, i32 20, i32 23, i32 22, i32 25, i32 24,
i32 27, i32 26, i32 29, i32 28, i32 31, i32 30, i32 33, i32 32, i32 35, i32 34, i32 37, i32 36, i32 39, i32 38, i32 41, i32
40, i32 43, i32 42, i32 45, i32 44, i32 47, i32 46, i32 49, i32 48, i32 51, i32 50, i32 53, i32 52, i32 55, i32 54, i32 57,
i32 56, i32 59, i32 58, i32 61, i32 60, i32 63, i32 62>
 call void @llvm.masked.store.v64i8.p0(<64 x i8> %a, ptr %y, i32 1, <64 x i1> %shuf)
 ret void
}
declare void @llvm.masked.store.v64i8.p0(<64 x i8>, ptr, i32, <64 x i1>)

@mem64_dst = dso_local global i64 0, align 8
@mem64_src = dso_local global i64 0, align 8
define dso_local i32 @v64i1_inline_asm() "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_inline_asm:
; CHECK:       # %bb.0:
; CHECK-NEXT:    kmovq mem64_src(%rip), %k0
; CHECK-NEXT:    #APP
; CHECK-NEXT:    #NO_APP
; CHECK-NEXT:    kmovq %k0, mem64_dst(%rip)
; CHECK-NEXT:    movl -{{[0-9]+}}(%rsp), %eax
;
 CHECK-NEXT:    retq
 %1 = alloca i32, align 4
```

```
 %2 = load i64, ptr @mem64_src, align 8
 %3 = call i64 asm "", "=k,k,~{dirflag},~{fpsr},~{flags}"(i64 %2)
 store i64 %3, ptr @mem64_dst, align 8
 %4 = load i32, ptr %1, align 4
 ret i32 %4
}

define dso_local void @cmp_v8i64_sext(ptr %xptr, ptr %yptr, ptr %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rsi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT:    vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT:    vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <8 x i64>, ptr %xptr
 %y = load <8 x i64>, ptr %yptr
 %cmp = icmp slt <8 x i64> %x, %y
 %ext = sext <8 x i1> %cmp to <8 x i64>
 store <8 x i64> %ext, ptr %zptr
 ret void
}

define dso_local void @cmp_v8i64_zext(ptr %xptr, ptr %yptr, ptr %zptr) "min-legal-vector-width"="256"
 {
; CHECK-LABEL: cmp_v8i64_zext:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vmovdqa (%rsi), %ymm0
; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT:    vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT:    vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT:    vpsrlq $63, %ymm1, %ymm1
; CHECK-NEXT:    vpsrlq $63, %ymm0, %ymm0
; CHECK-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
 %x = load <8 x i64>, ptr %xptr
 %y = load <8 x i64>, ptr %yptr
 %cmp = icmp slt <8 x i64> %x, %y
 %ext = zext <8 x i1> %cmp to <8 x i64>
 store <8 x i64> %ext, ptr %zptr
 ret void
}
```

```
define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: var_rotate_v16i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpandd {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to4}, %xmm1, %xmm1
; CHECK-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT:    vpunpckhbw {{.*#+}} xmm2 =
xmm1[8],xmm2[8],xmm1[9],xmm2[9],xmm1[10],xmm2[10],xmm1[11],xmm2[11],xmm1[12],xmm2[12],xmm1[13
],xmm2[13],xmm1[14],xmm2[14],xmm1[15],xmm2[15]
;
 CHECK-NEXT:    vpunpckhbw {{.*#+}} xmm3 = xmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15]
; CHECK-NEXT:    vpsllvw %xmm2, %xmm3, %xmm2
; CHECK-NEXT:    vpsrlw $8, %xmm2, %xmm2
; CHECK-NEXT:    vpunpcklbw {{.*#+}} xmm0 = xmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7]
; CHECK-NEXT:    vpmovzxbw {{.*#+}} xmm1 =
xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zer
o
; CHECK-NEXT:    vpsllvw %xmm1, %xmm0, %xmm0
; CHECK-NEXT:    vpsrlw $8, %xmm0, %xmm0
; CHECK-NEXT:    vpackuswb %xmm2, %xmm0, %xmm0
; CHECK-NEXT:    retq
 %b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
 %shl = shl <16 x i8> %a, %b
 %lshr = lshr <16 x i8> %a, %b8
 %or = or <16 x i8> %shl, %lshr
 ret <16 x i8> %or
}

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256"
 {
; CHECK-LABEL: var_rotate_v32i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpandd {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm1, %ymm1
; CHECK-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT:    vpunpckhbw {{.*#+}} ymm3 =
ymm1[8],ymm2[8],ymm1[9],ymm2[9],ymm1[10],ymm2[10],ymm1[11],ymm2[11],ymm1[12],ymm2[12],ymm1[13
],ymm2[13],ymm1[14],ymm2[14],ymm1[15],ymm2[15],ymm1[24],ymm2[24],ymm1[25],ymm2[25],ymm1[26],ym
m2[26],ymm1[27],ymm2[27],ymm1[28],ymm2[28],ymm1[29],ymm2[29],ymm1[30],ymm2[30],ymm1[31],ymm2[
31]
; CHECK-NEXT:    vpunpckhbw {{.*#+}} ymm4 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT:    vpsllvw %ymm3, %ymm4, %ymm3
; CHECK-NEXT:    vpsrlw $8, %ymm3, %ymm3
; CHECK-NEXT:    vpunpcklbw {{.*#+}} ymm1 =
ymm1[0],ymm2[0],ymm1[1],ymm2[1],ymm1[2],ymm2[2],ymm1[3],ymm2[3],ymm1[4],ymm2[4],ymm1[5],ymm2[
5],ymm1[6],ymm2[6],ymm1[7],ymm2[7],ymm1[16],ymm2[16],ymm1[17],ymm2[17],ymm1[18],ymm2[18],ymm1[
19],ymm2[19],ymm1[20],ymm2[20],ymm1[21],ymm2[21],ymm1[22],ymm2[22],ymm1[23],ymm2[23]
;
```

```
 CHECK-NEXT:    vpunpcklbw {{.*#+}} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT:    vpsllvw %ymm1, %ymm0, %ymm0
; CHECK-NEXT:    vpsrlw $8, %ymm0, %ymm0
; CHECK-NEXT:    vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-NEXT:    retq
 %b8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
 %shl = shl <32 x i8> %a, %b
 %lshr = lshr <32 x i8> %a, %b8
 %or = or <32 x i8> %shl, %lshr
 ret <32 x i8> %or
}

define <32 x i8> @splatvar_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256"
{
; CHECK-LABEL: splatvar_rotate_v32i8:
; CHECK:       # %bb.0:
; CHECK-NEXT:    vpunpckhbw {{.*#+}} ymm2 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
;
 CHECK-NEXT:    vpand {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %xmm1, %xmm1
; CHECK-NEXT:    vpsllw %xmm1, %ymm2, %ymm2
; CHECK-NEXT:    vpsrlw $8, %ymm2, %ymm2
; CHECK-NEXT:    vpunpcklbw {{.*#+}} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT:    vpsllw %xmm1, %ymm0, %ymm0
; CHECK-NEXT:    vpsrlw $8, %ymm0, %ymm0
; CHECK-NEXT:    vpackuswb %ymm2, %ymm0, %ymm0
; CHECK-NEXT:    retq
 %splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
 %splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %splat
 %shl = shl <32 x i8> %a, %splat
 %lshr = lshr <32 x i8> %a, %splat8
 %or = or <32 x i8> %shl, %lshr
 ret <32 x i8> %or
}

define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: constant_rotate_v32i8:
;
 CHECK:       # %bb.0:
; CHECK-NEXT:    vpunpckhbw {{.*#+}} ymm1 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT:    vpsllvw {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %ymm1, %ymm1
; CHECK-NEXT:    vpsrlw $8, %ymm1, %ymm1
; CHECK-NEXT:    vpunpcklbw {{.*#+}} ymm0 =
```

```
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT:    vpsllvw {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %ymm0, %ymm0
; CHECK-NEXT:    vpsrlw $8, %ymm0, %ymm0
; CHECK-NEXT:    vpackuswb %ymm1, %ymm0, %ymm0
; CHECK-NEXT:    retq
 %shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8
1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1>
 %lshr = lshr <32 x i8> %a, <i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8,
i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8 1,
 i8 2, i8 3, i8 4, i8 5, i8 6, i8 7>
 %or = or <32 x i8> %shl, %lshr
 ret <32 x i8> %or
}

define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
; CHECK-SKX-LABEL: splatconstant_rotate_v32i8:
; CHECK-SKX:       # %bb.0:
; CHECK-SKX-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-SKX-NEXT:    vpsrlw $4, %ymm0, %ymm0
; CHECK-SKX-NEXT:    vpternlogd $216, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm1, %ymm0
; CHECK-SKX-NEXT:    retq
;
; CHECK-AVX512-LABEL: splatconstant_rotate_v32i8:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-AVX512-NEXT:    vpsrlw $4, %ymm0, %ymm0
; CHECK-AVX512-NEXT:    vpternlogd $216, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI1-LABEL: splatconstant_rotate_v32i8:
; CHECK-VBMI1:       # %bb.0:
; CHECK-VBMI1-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-VBMI1-NEXT:    vpsrlw $4, %ymm0, %ymm0
; CHECK-VBMI1-NEXT:    vpternlogd $216, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8},
 %ymm1, %ymm0
; CHECK-VBMI1-NEXT:    retq
;
; CHECK-GFNI-LABEL: splatconstant_rotate_v32i8:
; CHECK-GFNI:       # %bb.0:
; CHECK-GFNI-NEXT:    vgf2p8affineqb $0, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to4}, %ymm0, %ymm0
; CHECK-GFNI-NEXT:    retq
 %shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8
4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>
 %lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4,
i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>
 %or = or <32 x i8> %shl, %lshr
 ret <32 x i8> %or
}
```

```
define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
; CHECK-SKX-LABEL: splatconstant_rotate_mask_v32i8:
; CHECK-SKX:       # %bb.0:
; CHECK-SKX-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-SKX-NEXT:
   vpsrlw $4, %ymm0, %ymm0
; CHECK-SKX-NEXT:    vpternlogd $216, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm1, %ymm0
; CHECK-SKX-NEXT:    vpandd {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm0, %ymm0
; CHECK-SKX-NEXT:    retq
;
; CHECK-AVX512-LABEL: splatconstant_rotate_mask_v32i8:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-AVX512-NEXT:    vpsrlw $4, %ymm0, %ymm0
; CHECK-AVX512-NEXT:    vpternlogd $216, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    vpandd {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm0, %ymm0
; CHECK-AVX512-NEXT:    retq
;
; CHECK-VBMI1-LABEL: splatconstant_rotate_mask_v32i8:
; CHECK-VBMI1:       # %bb.0:
; CHECK-VBMI1-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-VBMI1-NEXT:    vpsrlw $4, %ymm0, %ymm0
; CHECK-VBMI1-NEXT:    vpternlogd $216, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm1, %ymm0
; CHECK-VBMI1-NEXT:    vpandd {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm0, %ymm0
; CHECK-VBMI1-NEXT:    retq
;
;
 CHECK-GFNI-LABEL: splatconstant_rotate_mask_v32i8:
; CHECK-GFNI:       # %bb.0:
; CHECK-GFNI-NEXT:    vgf2p8affineqb $0, {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to4}, %ymm0, %ymm0
; CHECK-GFNI-NEXT:    vpandd {{\.?LCPI[0-9]+_[0-9]+}}(%rip){1to8}, %ymm0, %ymm0
; CHECK-GFNI-NEXT:    retq
 %shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>
 %lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>
 %rmask = and <32 x i8> %lshr, <i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55>
 %lmask = and <32
 x i8> %shl, <i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33>
 %or = or <32 x i8> %lmask, %rmask
 ret <32 x i8> %or
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
```

```
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=RV64I %s

; Check indexed and unindexed, sext, zext and anyext loads

define void @lb(ptr %a, ptr %b) nounwind {
; RV64I-LABEL: lb:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lb a2, 1(a0)
; RV64I-NEXT:    lbu zero, 0(a0)
; RV64I-NEXT:    sw a2, 0(a1)
; RV64I-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i32 1
 %2 = load i8, ptr %1
 %3 = sext i8 %2 to i32
 ; the unused load will produce an anyext for selection
 %4 = load volatile i8, ptr %a
 store i32 %3, ptr %b
 ret void
}

define void @lbu(ptr %a, ptr %b) nounwind {
; RV64I-LABEL: lbu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lbu a0, 1(a0)
; RV64I-NEXT:    sw a0, 0(a1)
; RV64I-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i32 1
 %2 = load i8, ptr %1
 %3 = zext i8 %2 to i32
 store i32 %3, ptr %b
 ret void
}

define void @lh(ptr
 %a, ptr %b) nounwind {
; RV64I-LABEL: lh:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lh a2, 2(a0)
; RV64I-NEXT:    lh zero, 0(a0)
; RV64I-NEXT:    sw a2, 0(a1)
; RV64I-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i32 1
 %2 = load i16, ptr %1
 %3 = sext i16 %2 to i32
 ; the unused load will produce an anyext for selection
 %4 = load volatile i16, ptr %a
 store i32 %3, ptr %b
 ret void
```

```
}

define void @lhu(ptr %a, ptr %b) nounwind {
; RV64I-LABEL: lhu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lhu a0, 2(a0)
; RV64I-NEXT:    sw a0, 0(a1)
; RV64I-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i32 1
 %2 = load i16, ptr %1
 %3 = zext i16 %2 to i32
 store i32 %3, ptr %b
 ret void
}

define void @lw(ptr %a, ptr %b) nounwind {
; RV64I-LABEL: lw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lw a2, 4(a0)
; RV64I-NEXT:    lw zero, 0(a0)
; RV64I-NEXT:    sd a2, 0(a1)
; RV64I-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i64 1
 %2 = load i32, ptr %1
 %3 = sext i32 %2 to i64
 ; the unused load
 will produce an anyext for selection
 %4 = load volatile i32, ptr %a
 store i64 %3, ptr %b
 ret void
}

define void @lwu(ptr %a, ptr %b) nounwind {
; RV64I-LABEL: lwu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lwu a0, 4(a0)
; RV64I-NEXT:    sd a0, 0(a1)
; RV64I-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i64 1
 %2 = load i32, ptr %1
 %3 = zext i32 %2 to i64
 store i64 %3, ptr %b
 ret void
}
```

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```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+xtheadmac -mattr=+m -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64XTHEADMAC

define i32 @mula_i32(i32 %a, i32 %b, i32 %c) {
; RV64XTHEADMAC-LABEL: mula_i32:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulaw a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = mul i32 %b, %c
 %e = add i32 %a, %d
 ret i32 %e
}

define i32 @muls_i32(i32 %a, i32 %b, i32 %c) {
; RV64XTHEADMAC-LABEL: muls_i32:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulsw a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = mul i32 %b, %c
 %e = sub i32 %a, %d
 ret i32 %e
}

define i64 @mula_i64(i64 %a, i64 %b, i64 %c) {
; RV64XTHEADMAC-LABEL: mula_i64:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mula a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = mul i64 %b, %c
 %f = add i64 %a, %d
 ret i64 %f
}

define i64 @mulaw_i64(i32
 %a, i32 %b, i32 %c) {
; RV64XTHEADMAC-LABEL: mulaw_i64:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulaw a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = mul i32 %b, %c
 %e = add i32 %a, %d
 %f = sext i32 %e to i64
```

```
 ret i64 %f
}

define i64 @mulah_i64(i32 %a, i16 %b, i16 %c) {
; RV64XTHEADMAC-LABEL: mulah_i64:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulah a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = sext i16 %b to i32
 %e = sext i16 %c to i32
 %f = mul i32 %d, %e
 %g = add i32 %a, %f
 %h = sext i32 %g to i64
 ret i64 %h
}

define i64 @muls_i64(i64 %a, i64 %b, i64 %c) {
; RV64XTHEADMAC-LABEL: muls_i64:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.muls a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = mul i64 %b, %c
 %f = sub i64 %a, %d
 ret i64 %f
}

define i64 @mulsw_i64(i32 %a, i32 %b, i32 %c) {
; RV64XTHEADMAC-LABEL: mulsw_i64:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulsw a0, a1, a2
; RV64XTHEADMAC-NEXT:
   ret
 %d = mul i32 %b, %c
 %e = sub i32 %a, %d
 %f = sext i32 %e to i64
 ret i64 %f
}

define i64 @mulsh_i64(i32 %a, i16 %b, i16 %c) {
; RV64XTHEADMAC-LABEL: mulsh_i64:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulsh a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %d = sext i16 %b to i32
 %e = sext i16 %c to i32
 %f = mul i32 %d, %e
 %g = sub i32 %a, %f
 %h = sext i32 %g to i64
```

```
 ret i64 %h
}

define i32 @commutative1(i32 %A, i32 %B, i32 %C) {
; RV64XTHEADMAC-LABEL: commutative1:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulaw a2, a1, a0
; RV64XTHEADMAC-NEXT:    mv a0, a2
; RV64XTHEADMAC-NEXT:    ret
 %mul = mul nsw i32 %B, %A
 %add = add i32 %mul, %C
 ret i32 %add
}

define i32 @commutative2(i32 %A, i32 %B, i32 %C) {
; RV64XTHEADMAC-LABEL: commutative2:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulaw a0, a1, a2
; RV64XTHEADMAC-NEXT:    ret
 %mul = mul nsw i32 %B, %C
 %add = add i32 %mul, %A
 ret i32 %add
}

define
 i32 @commutative3(i32 %A, i32 %B, i32 %C) {
; RV64XTHEADMAC-LABEL: commutative3:
; RV64XTHEADMAC:       # %bb.0:
; RV64XTHEADMAC-NEXT:    th.mulaw a1, a2, a0
; RV64XTHEADMAC-NEXT:    mv a0, a1
; RV64XTHEADMAC-NEXT:    ret
 %mul = mul nsw i32 %C, %A
 %add = add i32 %mul, %B
 ret i32 %add
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64IZbb

declare i4 @llvm.uadd.sat.i4(i4, i4)
declare i8 @llvm.uadd.sat.i8(i8, i8)
declare i16 @llvm.uadd.sat.i16(i16, i16)
declare i32 @llvm.uadd.sat.i32(i32, i32)
declare i64 @llvm.uadd.sat.i64(i64, i64)

define signext i32 @func(i32 signext %x, i32 signext %y) nounwind {
```

```
; RV64I-LABEL: func:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a1, a0, a1
; RV64I-NEXT:    sltu a0, a1, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    not a2, a1
; RV64IZbb-NEXT:    minu a0, a0, a2
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i32 @llvm.uadd.sat.i32(i32
 %x, i32 %y);
 ret i32 %tmp;
}

define i64 @func2(i64 %x, i64 %y) nounwind {
; RV64I-LABEL: func2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    add a1, a0, a1
; RV64I-NEXT:    sltu a0, a1, a0
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func2:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    not a2, a1
; RV64IZbb-NEXT:    minu a0, a0, a2
; RV64IZbb-NEXT:    add a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i64 @llvm.uadd.sat.i64(i64 %x, i64 %y);
 ret i64 %tmp;
}

define zeroext i16 @func16(i16 zeroext %x, i16 zeroext %y) nounwind {
; RV64I-LABEL: func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 16
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    bltu a0, a1, .LBB2_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB2_2:
```

```
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
;
 RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 16
; RV64IZbb-NEXT:    addiw a1, a1, -1
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i16 @llvm.uadd.sat.i16(i16 %x, i16 %y);
 ret i16 %tmp;
}

define zeroext i8 @func8(i8 zeroext %x, i8 zeroext %y) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 255
; RV64I-NEXT:    bltu a0, a1, .LBB3_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a0, 255
; RV64I-NEXT:  .LBB3_2:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 255
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i8 @llvm.uadd.sat.i8(i8 %x, i8 %y);
 ret i8 %tmp;
}

define zeroext i4 @func3(i4 zeroext %x, i4 zeroext %y) nounwind {
; RV64I-LABEL: func3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0,
 a0, a1
; RV64I-NEXT:    li a1, 15
; RV64I-NEXT:    bltu a0, a1, .LBB4_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a0, 15
```

```
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func3:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 15
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i4 @llvm.uadd.sat.i4(i4 %x, i4 %y);
 ret i4 %tmp;
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64I
; RUN: llc -mtriple=riscv64 -mattr=+zbs -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64ZBS

define signext i32 @bclr_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: bclr_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclr_i32:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    andi a1, a1, 31
; RV64ZBS-NEXT:    bclr a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %and = and i32 %b, 31
 %shl = shl nuw i32 1, %and
 %neg = xor i32 %shl, -1
 %and1 = and i32 %neg, %a
 ret i32 %and1
}

define signext i32 @bclr_i32_no_mask(i32 signext %a,
 i32 signext %b) nounwind {
; RV64I-LABEL: bclr_i32_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
```

```
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclr_i32_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclr a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %shl = shl i32 1, %b
 %neg = xor i32 %shl, -1
 %and1 = and i32 %neg, %a
 ret i32 %and1
}

define signext i32 @bclr_i32_load(ptr %p, i32 signext %b) nounwind {
; RV64I-LABEL: bclr_i32_load:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclr_i32_load:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    lw a0, 0(a0)
; RV64ZBS-NEXT:    bclr a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %a = load i32, ptr %p
 %shl = shl i32 1, %b
 %neg
 = xor i32 %shl, -1
 %and1 = and i32 %neg, %a
 ret i32 %and1
}

define i64 @bclr_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: bclr_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sll a1, a2, a1
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
```

```
; RV64ZBS-LABEL: bclr_i64:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclr a0, a0, a1
; RV64ZBS-NEXT:    ret
 %and = and i64 %b, 63
 %shl = shl nuw i64 1, %and
 %neg = xor i64 %shl, -1
 %and1 = and i64 %neg, %a
 ret i64 %and1
}

define i64 @bclr_i64_no_mask(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: bclr_i64_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sll a1, a2, a1
; RV64I-NEXT:    not a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclr_i64_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclr a0, a0, a1
; RV64ZBS-NEXT:    ret
 %shl = shl i64 1, %b
 %neg = xor i64 %shl, -1
 %and1 = and i64 %neg, %a
 ret i64
 %and1
}

define signext i32 @bset_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: bset_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i32:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    andi a1, a1, 31
; RV64ZBS-NEXT:    bset a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %and = and i32 %b, 31
 %shl = shl nuw i32 1, %and
 %or = or i32 %shl, %a
```

```
 ret i32 %or
}

define signext i32 @bset_i32_no_mask(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: bset_i32_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i32_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %shl = shl i32 1, %b
 %or = or i32 %shl, %a
 ret i32 %or
}

define signext i32
 @bset_i32_load(ptr %p, i32 signext %b) nounwind {
; RV64I-LABEL: bset_i32_load:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i32_load:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    lw a0, 0(a0)
; RV64ZBS-NEXT:    bset a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %a = load i32, ptr %p
 %shl = shl i32 1, %b
 %or = or i32 %shl, %a
 ret i32 %or
}

; We can use bsetw for 1 << x by setting the first source to zero.
define signext i32 @bset_i32_zero(i32 signext %a) nounwind {
; RV64I-LABEL: bset_i32_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
```

```
; RV64I-NEXT:    sllw a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i32_zero:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, zero, a0
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %shl = shl i32 1, %a
 ret i32 %shl
}

define i64 @bset_i64(i64 %a,
 i64 %b) nounwind {
; RV64I-LABEL: bset_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sll a1, a2, a1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i64:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, a0, a1
; RV64ZBS-NEXT:    ret
 %conv = and i64 %b, 63
 %shl = shl nuw i64 1, %conv
 %or = or i64 %shl, %a
 ret i64 %or
}

define i64 @bset_i64_no_mask(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: bset_i64_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sll a1, a2, a1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i64_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, a0, a1
; RV64ZBS-NEXT:    ret
 %shl = shl i64 1, %b
 %or = or i64 %shl, %a
 ret i64 %or
}
```

```
; We can use bsetw for 1 << x by setting the first source to zero.
define signext i64 @bset_i64_zero(i64 signext %a) nounwind {
; RV64I-LABEL: bset_i64_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li
 a1, 1
; RV64I-NEXT:    sll a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_i64_zero:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, zero, a0
; RV64ZBS-NEXT:    ret
 %shl = shl i64 1, %a
 ret i64 %shl
}

define signext i32 @binv_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: binv_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binv_i32:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    andi a1, a1, 31
; RV64ZBS-NEXT:    binv a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %and = and i32 %b, 31
 %shl = shl nuw i32 1, %and
 %xor = xor i32 %shl, %a
 ret i32 %xor
}

define signext i32 @binv_i32_no_mask(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: binv_i32_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binv_i32_no_mask:
;
```

```
 RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binv a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %shl = shl i32 1, %b
 %xor = xor i32 %shl, %a
 ret i32 %xor
}

define signext i32 @binv_i32_load(ptr %p, i32 signext %b) nounwind {
; RV64I-LABEL: binv_i32_load:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sllw a1, a2, a1
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binv_i32_load:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    lw a0, 0(a0)
; RV64ZBS-NEXT:    binv a0, a0, a1
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %a = load i32, ptr %p
 %shl = shl i32 1, %b
 %xor = xor i32 %shl, %a
 ret i32 %xor
}

define i64 @binv_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: binv_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sll a1, a2, a1
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binv_i64:
; RV64ZBS:       # %bb.0:
;
 RV64ZBS-NEXT:    binv a0, a0, a1
; RV64ZBS-NEXT:    ret
 %conv = and i64 %b, 63
 %shl = shl nuw i64 1, %conv
 %xor = xor i64 %shl, %a
 ret i64 %xor
}
```

```
define i64 @binv_i64_no_mask(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: binv_i64_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    sll a1, a2, a1
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binv_i64_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binv a0, a0, a1
; RV64ZBS-NEXT:    ret
 %shl = shl nuw i64 1, %b
 %xor = xor i64 %shl, %a
 ret i64 %xor
}

define signext i32 @bext_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: bext_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srlw a0, a0, a1
; RV64I-NEXT:    andi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bext_i32:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    andi a1, a1, 31
; RV64ZBS-NEXT:    bext a0, a0, a1
; RV64ZBS-NEXT:    ret
 %and = and i32 %b, 31
 %shr = lshr i32 %a, %and
 %and1 = and i32
 %shr, 1
 ret i32 %and1
}

define signext i32 @bext_i32_no_mask(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: bext_i32_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srlw a0, a0, a1
; RV64I-NEXT:    andi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bext_i32_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bext a0, a0, a1
; RV64ZBS-NEXT:    ret
```

```
 %shr = lshr i32 %a, %b
 %and1 = and i32 %shr, 1
 ret i32 %and1
}

; This gets previous converted to (i1 (truncate (srl X, Y)). Make sure we are
; able to use bext.
define void @bext_i32_trunc(i32 signext %0, i32 signext %1) {
; RV64I-LABEL: bext_i32_trunc:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srlw a0, a0, a1
; RV64I-NEXT:    andi a0, a0, 1
; RV64I-NEXT:    beqz a0, .LBB19_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB19_2:
; RV64I-NEXT:    tail bar
;
; RV64ZBS-LABEL: bext_i32_trunc:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bext a0, a0, a1
; RV64ZBS-NEXT:    beqz a0, .LBB19_2
; RV64ZBS-NEXT:
  # %bb.1:
; RV64ZBS-NEXT:    ret
; RV64ZBS-NEXT:  .LBB19_2:
; RV64ZBS-NEXT:    tail bar
 %3 = shl i32 1, %1
 %4 = and i32 %3, %0
 %5 = icmp eq i32 %4, 0
 br i1 %5, label %6, label %7

6:                                        ; preds = %2
 tail call void @bar()
 br label %7

7:                                        ; preds = %6, %2
 ret void
}

declare void @bar()

define i64 @bext_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: bext_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srl a0, a0, a1
; RV64I-NEXT:    andi a0, a0, 1
; RV64I-NEXT:    ret
```

```
;
; RV64ZBS-LABEL: bext_i64:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bext a0, a0, a1
; RV64ZBS-NEXT:    ret
 %conv = and i64 %b, 63
 %shr = lshr i64 %a, %conv
 %and1 = and i64 %shr, 1
 ret i64 %and1
}

define i64 @bext_i64_no_mask(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: bext_i64_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srl a0, a0, a1
; RV64I-NEXT:    andi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL:
 bext_i64_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bext a0, a0, a1
; RV64ZBS-NEXT:    ret
 %shr = lshr i64 %a, %b
 %and1 = and i64 %shr, 1
 ret i64 %and1
}

define signext i32 @bexti_i32(i32 signext %a) nounwind {
; RV64I-LABEL: bexti_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 58
; RV64I-NEXT:    srli a0, a0, 63
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bexti_i32:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bexti a0, a0, 5
; RV64ZBS-NEXT:    ret
 %shr = lshr i32 %a, 5
 %and = and i32 %shr, 1
 ret i32 %and
}

define i64 @bexti_i64(i64 %a) nounwind {
; RV64I-LABEL: bexti_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 58
```

```
; RV64I-NEXT:    srli a0, a0, 63
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bexti_i64:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bexti a0, a0, 5
; RV64ZBS-NEXT:    ret
 %shr = lshr i64 %a, 5
 %and = and i64 %shr, 1
 ret i64 %and
}

define signext i32 @bexti_i32_cmp(i32 signext %a) nounwind {
; CHECK-LABEL: bexti_i32_cmp:
;
 CHECK:       # %bb.0:
; CHECK-NEXT:    andi a0, a0, 32
; CHECK-NEXT:    snez a0, a0
; CHECK-NEXT:    ret
 %and = and i32 %a, 32
 %cmp = icmp ne i32 %and, 0
 %zext = zext i1 %cmp to i32
 ret i32 %zext
}

define i64 @bexti_i64_cmp(i64 %a) nounwind {
; RV64I-LABEL: bexti_i64_cmp:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 58
; RV64I-NEXT:    srli a0, a0, 63
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bexti_i64_cmp:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bexti a0, a0, 5
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, 32
 %cmp = icmp ne i64 %and, 0
 %zext = zext i1 %cmp to i64
 ret i64 %zext
}

define signext i32 @bclri_i32_10(i32 signext %a) nounwind {
; CHECK-LABEL: bclri_i32_10:
; CHECK:       # %bb.0:
; CHECK-NEXT:    andi a0, a0, -1025
; CHECK-NEXT:    ret
 %and = and i32 %a, -1025
```

```
 ret i32 %and
}

define signext i32 @bclri_i32_11(i32 signext %a) nounwind {
; RV64I-LABEL: bclri_i32_11:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1048575
; RV64I-NEXT:    addiw a1,
 a1, 2047
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i32_11:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 11
; RV64ZBS-NEXT:    ret
 %and = and i32 %a, -2049
 ret i32 %and
}

define signext i32 @bclri_i32_30(i32 signext %a) nounwind {
; RV64I-LABEL: bclri_i32_30:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 786432
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i32_30:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 30
; RV64ZBS-NEXT:    ret
 %and = and i32 %a, -1073741825
 ret i32 %and
}

define signext i32 @bclri_i32_31(i32 signext %a) nounwind {
; RV64I-LABEL: bclri_i32_31:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 33
; RV64I-NEXT:    srli a0, a0, 33
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i32_31:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 31
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
```

```
 %and = and i32 %a,
 -2147483649
 ret i32 %and
}

define i64 @bclri_i64_10(i64 %a) nounwind {
; CHECK-LABEL: bclri_i64_10:
; CHECK:       # %bb.0:
; CHECK-NEXT:    andi a0, a0, -1025
; CHECK-NEXT:    ret
 %and = and i64 %a, -1025
 ret i64 %and
}

define i64 @bclri_i64_11(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_11:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1048575
; RV64I-NEXT:    addiw a1, a1, 2047
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i64_11:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 11
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, -2049
 ret i64 %and
}

define i64 @bclri_i64_30(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_30:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 786432
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i64_30:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 30
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, -1073741825
 ret i64 %and
}

define
```

```
 i64 @bclri_i64_31(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_31:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    addi a1, a1, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i64_31:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 31
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, -2147483649
 ret i64 %and
}

define i64 @bclri_i64_62(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_62:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    slli a1, a1, 62
; RV64I-NEXT:    addi a1, a1, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i64_62:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 62
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, -4611686018427387905
 ret i64 %and
}

define i64 @bclri_i64_63(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_63:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 1
; RV64I-NEXT:    srli a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL:
 bclri_i64_63:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 63
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, -9223372036854775809
 ret i64 %and
}
```

```
define i64 @bclri_i64_large0(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_large0:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1044480
; RV64I-NEXT:    addiw a1, a1, -256
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i64_large0:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    andi a0, a0, -256
; RV64ZBS-NEXT:    bclri a0, a0, 24
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, -16777472
 ret i64 %and
}

define i64 @bclri_i64_large1(i64 %a) nounwind {
; RV64I-LABEL: bclri_i64_large1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1044464
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bclri_i64_large1:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bclri a0, a0, 16
; RV64ZBS-NEXT:    bclri a0, a0, 24
; RV64ZBS-NEXT:    ret
 %and = and i64 %a,
 -16842753
 ret i64 %and
}

define signext i32 @bseti_i32_10(i32 signext %a) nounwind {
; CHECK-LABEL: bseti_i32_10:
; CHECK:       # %bb.0:
; CHECK-NEXT:    ori a0, a0, 1024
; CHECK-NEXT:    ret
 %or = or i32 %a, 1024
 ret i32 %or
}

define signext i32 @bseti_i32_11(i32 signext %a) nounwind {
; RV64I-LABEL: bseti_i32_11:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slliw a1, a1, 11
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i32_11:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 11
; RV64ZBS-NEXT:    ret
 %or = or i32 %a, 2048
 ret i32 %or
}

define signext i32 @bseti_i32_30(i32 signext %a) nounwind {
; RV64I-LABEL: bseti_i32_30:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 262144
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i32_30:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 30
; RV64ZBS-NEXT:    ret
 %or = or i32 %a, 1073741824
 ret i32 %or
}

define signext i32
 @bseti_i32_31(i32 signext %a) nounwind {
; RV64I-LABEL: bseti_i32_31:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i32_31:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 31
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %or = or i32 %a, 2147483648
 ret i32 %or
}

define i64 @bseti_i64_10(i64 %a) nounwind {
; CHECK-LABEL: bseti_i64_10:
; CHECK:       # %bb.0:
```

```
; CHECK-NEXT:    ori a0, a0, 1024
; CHECK-NEXT:    ret
 %or = or i64 %a, 1024
 ret i64 %or
}

define i64 @bseti_i64_11(i64 %a) nounwind {
; RV64I-LABEL: bseti_i64_11:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli a1, a1, 11
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i64_11:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 11
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 2048
 ret i64 %or
}

define i64 @bseti_i64_30(i64 %a) nounwind {
; RV64I-LABEL: bseti_i64_30:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 262144
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i64_30:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 30
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 1073741824
 ret i64 %or
}

define i64 @bseti_i64_31(i64 %a) nounwind {
; RV64I-LABEL: bseti_i64_31:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli a1, a1, 31
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i64_31:
; RV64ZBS:       # %bb.0:
```

```
; RV64ZBS-NEXT:    bseti a0, a0, 31
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 2147483648
 ret i64 %or
}

define i64 @bseti_i64_62(i64 %a) nounwind {
; RV64I-LABEL: bseti_i64_62:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli a1, a1, 62
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i64_62:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 62
; RV64ZBS-NEXT:    ret
 %or = or i64 %a,
 4611686018427387904
 ret i64 %or
}

define i64 @bseti_i64_63(i64 %a) nounwind {
; RV64I-LABEL: bseti_i64_63:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    slli a1, a1, 63
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bseti_i64_63:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 63
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 9223372036854775808
 ret i64 %or
}

define signext i32 @binvi_i32_10(i32 signext %a) nounwind {
; CHECK-LABEL: binvi_i32_10:
; CHECK:       # %bb.0:
; CHECK-NEXT:    xori a0, a0, 1024
; CHECK-NEXT:    ret
 %xor = xor i32 %a, 1024
 ret i32 %xor
}
```

```
define signext i32 @binvi_i32_11(i32 signext %a) nounwind {
; RV64I-LABEL: binvi_i32_11:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slliw a1, a1, 11
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i32_11:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 11
; RV64ZBS-NEXT:    ret
 %xor = xor i32 %a,
 2048
 ret i32 %xor
}

define signext i32 @binvi_i32_30(i32 signext %a) nounwind {
; RV64I-LABEL: binvi_i32_30:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 262144
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i32_30:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 30
; RV64ZBS-NEXT:    ret
 %xor = xor i32 %a, 1073741824
 ret i32 %xor
}

define signext i32 @binvi_i32_31(i32 signext %a) nounwind {
; RV64I-LABEL: binvi_i32_31:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i32_31:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 31
; RV64ZBS-NEXT:    sext.w a0, a0
; RV64ZBS-NEXT:    ret
 %xor = xor i32 %a, 2147483648
 ret i32 %xor
}
```

```
define i64 @binvi_i64_10(i64 %a) nounwind {
; CHECK-LABEL: binvi_i64_10:
; CHECK:       # %bb.0:
; CHECK-NEXT:    xori a0, a0, 1024
; CHECK-NEXT:    ret
 %xor = xor i64 %a, 1024
 ret i64 %xor
}

define i64 @binvi_i64_11(i64
 %a) nounwind {
; RV64I-LABEL: binvi_i64_11:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli a1, a1, 11
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i64_11:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 11
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 2048
 ret i64 %xor
}

define i64 @binvi_i64_30(i64 %a) nounwind {
; RV64I-LABEL: binvi_i64_30:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 262144
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i64_30:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 30
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 1073741824
 ret i64 %xor
}

define i64 @binvi_i64_31(i64 %a) nounwind {
; RV64I-LABEL: binvi_i64_31:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli a1, a1, 31
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
```

```
;
; RV64ZBS-LABEL: binvi_i64_31:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi
 a0, a0, 31
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 2147483648
 ret i64 %xor
}

define i64 @binvi_i64_62(i64 %a) nounwind {
; RV64I-LABEL: binvi_i64_62:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli a1, a1, 62
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i64_62:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 62
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 4611686018427387904
 ret i64 %xor
}

define i64 @binvi_i64_63(i64 %a) nounwind {
; RV64I-LABEL: binvi_i64_63:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    slli a1, a1, 63
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: binvi_i64_63:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 63
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 9223372036854775808
 ret i64 %xor
}

define i64 @xor_i64_large(i64 %a) nounwind {
; RV64I-LABEL: xor_i64_large:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 1
; RV64I-NEXT:    slli
```

```
 a1, a1, 32
; RV64I-NEXT:    addi a1, a1, 1
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: xor_i64_large:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    binvi a0, a0, 0
; RV64ZBS-NEXT:    binvi a0, a0, 32
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 4294967297
 ret i64 %xor
}

define i64 @xor_i64_4099(i64 %a) nounwind {
; RV64I-LABEL: xor_i64_4099:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1
; RV64I-NEXT:    addiw a1, a1, 3
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: xor_i64_4099:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    xori a0, a0, 3
; RV64ZBS-NEXT:    binvi a0, a0, 12
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 4099
 ret i64 %xor
}

define i64 @xor_i64_96(i64 %a) nounwind {
; CHECK-LABEL: xor_i64_96:
; CHECK:       # %bb.0:
; CHECK-NEXT:    xori a0, a0, 96
; CHECK-NEXT:    ret
 %xor = xor i64 %a, 96
 ret i64 %xor
}

define i64 @or_i64_large(i64 %a) nounwind {
; RV64I-LABEL: or_i64_large:
; RV64I:       # %bb.0:
; RV64I-NEXT:
   li a1, 1
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    addi a1, a1, 1
; RV64I-NEXT:    or a0, a0, a1
```

```
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: or_i64_large:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bseti a0, a0, 0
; RV64ZBS-NEXT:    bseti a0, a0, 32
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 4294967297
 ret i64 %or
}

define i64 @xor_i64_66901(i64 %a) nounwind {
; RV64I-LABEL: xor_i64_66901:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 16
; RV64I-NEXT:    addiw a1, a1, 1365
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: xor_i64_66901:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    xori a0, a0, 1365
; RV64ZBS-NEXT:    binvi a0, a0, 16
; RV64ZBS-NEXT:    ret
 %xor = xor i64 %a, 66901
 ret i64 %xor
}

define i64 @or_i64_4099(i64 %a) nounwind {
; RV64I-LABEL: or_i64_4099:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1
; RV64I-NEXT:    addiw a1, a1, 3
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: or_i64_4099:
; RV64ZBS:
      # %bb.0:
; RV64ZBS-NEXT:    ori a0, a0, 3
; RV64ZBS-NEXT:    bseti a0, a0, 12
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 4099
 ret i64 %or
}

define i64 @or_i64_96(i64 %a) nounwind {
; CHECK-LABEL: or_i64_96:
```

```
; CHECK:       # %bb.0:
; CHECK-NEXT:    ori a0, a0, 96
; CHECK-NEXT:    ret
 %or = or i64 %a, 96
 ret i64 %or
}

define i64 @or_i64_66901(i64 %a) nounwind {
; RV64I-LABEL: or_i64_66901:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 16
; RV64I-NEXT:    addiw a1, a1, 1365
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: or_i64_66901:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    ori a0, a0, 1365
; RV64ZBS-NEXT:    bseti a0, a0, 16
; RV64ZBS-NEXT:    ret
 %or = or i64 %a, 66901
 ret i64 %or
}

define signext i32 @bset_trailing_ones_i32_mask(i32 signext %a) nounwind {
; RV64I-LABEL: bset_trailing_ones_i32_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    sllw a0, a1, a0
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
;
 RV64ZBS-LABEL: bset_trailing_ones_i32_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    andi a0, a0, 31
; RV64ZBS-NEXT:    bset a0, zero, a0
; RV64ZBS-NEXT:    addiw a0, a0, -1
; RV64ZBS-NEXT:    ret
 %and = and i32 %a, 31
 %shift = shl nsw i32 -1, %and
 %not = xor i32 %shift, -1
 ret i32 %not
}

define signext i32 @bset_trailing_ones_i32_no_mask(i32 signext %a) nounwind {
; RV64I-LABEL: bset_trailing_ones_i32_no_mask:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    sllw a0, a1, a0
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_trailing_ones_i32_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, zero, a0
; RV64ZBS-NEXT:    addiw a0, a0, -1
; RV64ZBS-NEXT:    ret
 %shift = shl nsw i32 -1, %a
 %not = xor i32 %shift, -1
 ret i32 %not
}

define signext i64 @bset_trailing_ones_i64_mask(i64 signext %a) nounwind {
; RV64I-LABEL: bset_trailing_ones_i64_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    sll
 a0, a1, a0
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_trailing_ones_i64_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, zero, a0
; RV64ZBS-NEXT:    addi a0, a0, -1
; RV64ZBS-NEXT:    ret
 %and = and i64 %a, 63
 %shift = shl nsw i64 -1, %and
 %not = xor i64 %shift, -1
 ret i64 %not
}

define signext i64 @bset_trailing_ones_i64_no_mask(i64 signext %a) nounwind {
; RV64I-LABEL: bset_trailing_ones_i64_no_mask:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    sll a0, a1, a0
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64ZBS-LABEL: bset_trailing_ones_i64_no_mask:
; RV64ZBS:       # %bb.0:
; RV64ZBS-NEXT:    bset a0, zero, a0
; RV64ZBS-NEXT:    addi a0, a0, -1
; RV64ZBS-NEXT:    ret
```

```
 %shift = shl nsw i64 -1, %a
 %not = xor i64 %shift, -1
 ret i64 %not
}
```

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```
; RUN: opt -passes=consthoist -S -o - %s | FileCheck %s
target datalayout = "e-m:e-p:32:32-i64:64-v128:64:128-a:0:32-n32-S64"
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG:  %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
; CHECK-DAG:  %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK-DAG:  %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
; CHECK-DAG:  %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
; CHECK:  %0 = inttoptr i32 %[[C2]] to ptr
; CHECK-NEXT:  %1 = load volatile i8, ptr %0
; CHECK-NEXT:  %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT:  %2 = inttoptr i32 %[[M1]] to ptr
; CHECK-NEXT:  %3 = load volatile i8, ptr %2
; CHECK-NEXT:  %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 31
; CHECK-NEXT:  %4 = inttoptr i32 %[[M2]] to ptr
; CHECK-NEXT:  %5 = load volatile
 i8, ptr %4
; CHECK-NEXT:  %6 = inttoptr i32 %[[C1]] to ptr
; CHECK-NEXT:  %7 = load volatile i8, ptr %6
; CHECK-NEXT:  %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 7
; CHECK-NEXT:  %8 = inttoptr i32 %[[M3]] to ptr
; CHECK-NEXT:  %9 = load volatile i8, ptr %8
; CHECK-NEXT:  %10 = inttoptr i32 %[[C4]] to ptr
; CHECK-NEXT:  store i8 %9, ptr %10
```

```
; CHECK-NEXT:  %[[M4:const_mat[0-9]?]] = add i32 %[[C4]], 31
; CHECK-NEXT:  %11 = inttoptr i32 %[[M4]] to ptr
; CHECK-NEXT:  store i8 %7, ptr %11
; CHECK-NEXT:  %12 = inttoptr i32 %[[C3]] to ptr
; CHECK-NEXT:  store i8 %5, ptr %12
; CHECK-NEXT:  %[[M5:const_mat[0-9]?]] = add i32 %[[C3]], 7
; CHECK-NEXT:  %13 = inttoptr i32 %[[M5]] to ptr
; CHECK-NEXT:  store i8 %3, ptr %13
; CHECK-NEXT:  %[[M6:const_mat[0-9]?]] = add i32 %[[C1]], 80
; CHECK-NEXT:  %14 = inttoptr i32 %[[M6]] to ptr
; CHECK-NEXT:  store ptr %14, ptr @goo

@goo = global ptr undef

define void @foo_i8() {
entry:
 %0 = load volatile i8, ptr inttoptr (i32 805874688 to ptr)
 %1 = load volatile
 i8, ptr inttoptr (i32 805874692 to ptr)
 %2 = load volatile i8, ptr inttoptr (i32 805874719 to ptr)
 %3 = load volatile i8, ptr inttoptr (i32 805874720 to ptr)
 %4 = load volatile i8, ptr inttoptr (i32 805874727 to ptr)
 store i8 %4, ptr inttoptr(i32 805873688 to ptr)
 store i8 %3, ptr inttoptr(i32 805873719 to ptr)
 store i8 %2, ptr inttoptr(i32 805873720 to ptr)
 store i8 %1, ptr inttoptr(i32 805873727 to ptr)
 store ptr inttoptr(i32 805874800 to ptr), ptr @goo
 ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to ptr
; CHECK-NEXT: %1 = load volatile i16, ptr %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to ptr
; CHECK-NEXT: %3 = load volatile i16, ptr %2, align 2
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add
 i32 %[[C2]], 32
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to ptr
; CHECK-NEXT: %5 = load volatile i16, ptr %4, align 2
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C2]], 62
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to ptr
; CHECK-NEXT: %7 = load volatile i16, ptr %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to ptr
; CHECK-NEXT: %9 = load volatile i16, ptr %8, align 2
```

```
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 22
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to ptr
; CHECK-NEXT: %11 = load volatile i16, ptr %10, align 2

define void @foo_i16() {
entry:
 %0 = load volatile i16, ptr inttoptr (i32 805874688 to ptr), align 2
 %1 = load volatile i16, ptr inttoptr (i32 805874692 to ptr), align 2
 %2 = load volatile i16, ptr inttoptr (i32 805874720 to ptr), align 2
 %3 = load volatile i16, ptr inttoptr (i32 805874750 to ptr), align 2
 %4 = load volatile i16, ptr inttoptr (i32 805874752 to ptr), align 2
 %5 = load volatile i16, ptr inttoptr (i32 805874774 to ptr), align
 2
 ret void
}

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG:  %[[C1:const[0-9]?]] = bitcast i32 805874816 to i32
; CHECK-DAG:  %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK:  %0 = inttoptr i32 %[[C2]] to ptr
; CHECK-NEXT:  %1 = load volatile i32, ptr %0, align 4
; CHECK-NEXT:  %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT:  %2 = inttoptr i32 %[[M1]] to ptr
; CHECK-NEXT:  %3 = load volatile i32, ptr %2, align 4
; CHECK-NEXT:  %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 124
; CHECK-NEXT:  %4 = inttoptr i32 %[[M2]] to ptr
; CHECK-NEXT:  %5 = load volatile i32, ptr %4, align 4
; CHECK-NEXT:  %6 = inttoptr i32 %[[C1]] to ptr
; CHECK-NEXT:  %7 = load volatile i32, ptr %6, align 4
; CHECK-NEXT:  %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 8
; CHECK-NEXT:  %8 = inttoptr i32 %[[M3]] to ptr
; CHECK-NEXT:  %9 = load volatile i32, ptr %8, align 4
; CHECK-NEXT:  %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 12
; CHECK-NEXT:  %10 = inttoptr
 i32 %[[M4]] to ptr
; CHECK-NEXT:  %11 = load volatile i32, ptr %10, align 4

define void @foo_i32() {
entry:
 %0 = load volatile i32, ptr inttoptr (i32 805874688 to ptr), align 4
 %1 = load volatile i32, ptr inttoptr (i32 805874692 to ptr), align 4
 %2 = load volatile i32, ptr inttoptr (i32 805874812 to ptr), align 4
 %3 = load volatile i32, ptr inttoptr (i32 805874816 to ptr), align 4
 %4 = load volatile i32, ptr inttoptr (i32 805874824 to ptr), align 4
 %5 = load volatile i32, ptr inttoptr (i32 805874828 to ptr), align 4
 ret void
}
```

```
; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { ptr, i8, %union.anon.0 }
%union.anon.0 = type { ptr }

define hidden fastcc void @fred() unnamed_addr #0 {
entry:
 br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph:                                 ; preds = %entry
 br label %while.body

while.body:                                       ; preds = %exit.2, %while.body.lr.ph
 %lsr.iv = phi ptr [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
 switch i32 undef, label %exit [
   i32 1, label %sw.bb.i
   i32 2, label %sw.bb3.i
 ]

sw.bb.i:                                          ; preds = %while.body
 unreachable

sw.bb3.i:                                         ; preds = %while.body
 unreachable

exit:
                                                  ; preds = %while.body
 switch i32 undef, label %exit.2 [
   i32 1, label %sw.bb.i17
   i32 2, label %sw.bb3.i20
 ]

sw.bb.i17:                                        ; preds = %.exit
 %0 = load i32, ptr %lsr.iv, align 4
 unreachable

sw.bb3.i20:                                       ; preds = %exit
 %1 = load ptr, ptr %lsr.iv, align 4
 unreachable

exit.2:                                           ; preds = %exit
```

```
 %cgep22 = getelementptr %struct.0, ptr %lsr.iv, i32 1
 br label %while.body

while.end:                                        ; preds = %entry
 ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
; RUN: opt < %s -passes=argpromotion -S | FileCheck %s

; CHECK-LABEL: define i32 @foo() #0 {
; CHECK-NEXT:      %.val = load <32 x half>, ptr undef, align 4
; CHECK-NEXT:      call void @bar(<32 x half> %.val)
; CHECK-NEXT:      ret i32 0
; CHECK-NEXT:    }

; CHECK-LABEL: define internal void @bar(<32 x half> %.0.val) #0 {
; CHECK-NEXT:      ret void
; CHECK-NEXT:    }

; CHECK:    attributes #0 = { uwtable "min-legal-vector-width"="512" }

define i32 @foo() #0 {
 call void @bar(ptr undef)
 ret i32 0
}

define internal void @bar(ptr) #0 {
 %2 = load <32 x half>, ptr %0, align 4
 ret void
}

attributes #0 = { uwtable "min-legal-vector-width"="0" }
```

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+zbb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64ZBB

declare i32 @llvm.riscv.orc.b.i32(i32)

define signext i32 @orcb32(i32 signext %a) nounwind {
; RV64ZBB-LABEL: orcb32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    orc.b a0, a0
; RV64ZBB-NEXT:    sext.w a0, a0
; RV64ZBB-NEXT:    ret
 %tmp = call i32 @llvm.riscv.orc.b.i32(i32 %a)
 ret i32 %tmp
}

define zeroext i32 @orcb32_zext(i32 zeroext %a) nounwind {
; RV64ZBB-LABEL: orcb32_zext:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    orc.b a0, a0
; RV64ZBB-NEXT:    ret
 %tmp = call i32 @llvm.riscv.orc.b.i32(i32 %a)
 ret i32 %tmp
}

; Second and+or is redundant with the first, make sure we remove them.
define signext i32 @orcb32_knownbits(i32 signext %a) nounwind {
; RV64ZBB-LABEL: orcb32_knownbits:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    lui a1, 1044480
; RV64ZBB-NEXT:
    and a0, a0, a1
; RV64ZBB-NEXT:    lui a1, 2048
; RV64ZBB-NEXT:    addi a1, a1, 1
; RV64ZBB-NEXT:    or a0, a0, a1
; RV64ZBB-NEXT:    orc.b a0, a0
; RV64ZBB-NEXT:    sext.w a0, a0
; RV64ZBB-NEXT:    ret
 %tmp = and i32 %a, 4278190080 ; 0xFF000000
```

```
 %tmp2 = or i32 %tmp, 8388609 ; 0x800001
 %tmp3 = call i32 @llvm.riscv.orc.b.i32(i32 %tmp2)
 %tmp4 = and i32 %tmp3, 4278190080 ; 0xFF000000
 %tmp5 = or i32 %tmp4, 16711935 ; 0xFF00FF
 ret i32 %tmp5
}

declare i64 @llvm.riscv.orc.b.i64(i64)

define i64 @orcb64(i64 %a) nounwind {
; RV64ZBB-LABEL: orcb64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    orc.b a0, a0
; RV64ZBB-NEXT:    ret
 %tmp = call i64 @llvm.riscv.orc.b.i64(i64 %a)
 ret i64 %tmp
}

; Second and+or is redundant with the first, make sure we remove them.
define i64 @orcb64_knownbits(i64 %a) nounwind {
; RV64ZBB-LABEL: orcb64_knownbits:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    lui a1, 65535
; RV64ZBB-NEXT:    slli a1, a1, 12
; RV64ZBB-NEXT:    and a0, a0, a1
;
 RV64ZBB-NEXT:    lui a1, 256
; RV64ZBB-NEXT:    addiw a1, a1, 8
; RV64ZBB-NEXT:    slli a2, a1, 42
; RV64ZBB-NEXT:    add a1, a1, a2
; RV64ZBB-NEXT:    or a0, a0, a1
; RV64ZBB-NEXT:    orc.b a0, a0
; RV64ZBB-NEXT:    ret
 %tmp = and i64 %a, 1099494850560 ; 0x000000ffff000000
 %tmp2 = or i64 %tmp, 4611721202800525320 ; 0x4000200000100008
 %tmp3 = call i64 @llvm.riscv.orc.b.i64(i64 %tmp2)
 %tmp4 = and i64 %tmp3, 1099494850560 ; 0x000000ffff000000
 %tmp5 = or i64 %tmp4, 18374966855153418495 ; 0xff00ff0000ff00ff
 ret i64 %tmp5
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64IZbb

declare i4 @llvm.usub.sat.i4(i4, i4)
declare i8 @llvm.usub.sat.i8(i8, i8)
```

```
declare i16 @llvm.usub.sat.i16(i16, i16)
declare i32 @llvm.usub.sat.i32(i32, i32)
declare i64 @llvm.usub.sat.i64(i64, i64)

define signext i32 @func(i32 signext %x, i32 signext %y) nounwind {
; RV64I-LABEL: func:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addiw a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i32 @llvm.usub.sat.i32(i32 %x, i32 %y);
  ret i32 %tmp;
}

define i64 @func2(i64 %x, i64 %y) nounwind {
; RV64I-LABEL: func2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sub a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func2:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    sub a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i64 @llvm.usub.sat.i64(i64 %x, i64 %y);
 ret i64 %tmp;
}

define zeroext i16 @func16(i16 zeroext %x, i16 zeroext %y) nounwind {
; RV64I-LABEL: func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    slli a0, a0, 32
```

```
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    slli a0, a0, 32
; RV64IZbb-NEXT:
   srli a0, a0, 32
; RV64IZbb-NEXT:    ret
 %tmp = call i16 @llvm.usub.sat.i16(i16 %x, i16 %y);
 ret i16 %tmp;
}

define zeroext i8 @func8(i8 zeroext %x, i8 zeroext %y) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    slli a0, a0, 32
; RV64IZbb-NEXT:    srli a0, a0, 32
; RV64IZbb-NEXT:    ret
 %tmp = call i8 @llvm.usub.sat.i8(i8 %x, i8 %y);
 ret i8 %tmp;
}

define zeroext i4 @func3(i4 zeroext %x, i4 zeroext %y) nounwind {
; RV64I-LABEL: func3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a1, a0, a1
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    slli
 a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
```

```
;
; RV64IZbb-LABEL: func3:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    maxu a0, a0, a1
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    slli a0, a0, 32
; RV64IZbb-NEXT:    srli a0, a0, 32
; RV64IZbb-NEXT:    ret
 %tmp = call i4 @llvm.usub.sat.i4(i4 %x, i4 %y);
 ret i4 %tmp;
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+zbc -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64ZBC

declare i64 @llvm.riscv.clmulr.i64(i64 %a, i64 %b)

define i64 @clmul64r(i64 %a, i64 %b) nounwind {
; RV64ZBC-LABEL: clmul64r:
; RV64ZBC:       # %bb.0:
; RV64ZBC-NEXT:    clmulr a0, a0, a1
; RV64ZBC-NEXT:    ret
 %tmp = call i64 @llvm.riscv.clmulr.i64(i64 %a, i64 %b)
 ret i64 %tmp
}

declare i32 @llvm.riscv.clmulr.i32(i32 %a, i32 %b)

define signext i32 @clmul32r(i32 signext %a, i32 signext %b) nounwind {
; RV64ZBC-LABEL: clmul32r:
; RV64ZBC:       # %bb.0:
; RV64ZBC-NEXT:    slli a1, a1, 32
; RV64ZBC-NEXT:    slli a0, a0, 32
; RV64ZBC-NEXT:    clmulr a0, a0, a1
; RV64ZBC-NEXT:    srai a0, a0, 32
; RV64ZBC-NEXT:    ret
 %tmp = call i32 @llvm.riscv.clmulr.i32(i32 %a, i32 %b)
 ret i32 %tmp
}

; FIXME: We could avoid the slli instructions by using clmul+srli+sext.w
 since
; the inputs are zero extended.
define signext i32 @clmul32r_zext(i32 zeroext %a, i32 zeroext %b) nounwind {
; RV64ZBC-LABEL: clmul32r_zext:
; RV64ZBC:       # %bb.0:
; RV64ZBC-NEXT:    slli a1, a1, 32
; RV64ZBC-NEXT:    slli a0, a0, 32
```

```
; RV64ZBC-NEXT:    clmulr a0, a0, a1
; RV64ZBC-NEXT:    srai a0, a0, 32
; RV64ZBC-NEXT:    ret
 %tmp = call i32 @llvm.riscv.clmulr.i32(i32 %a, i32 %b)
 ret i32 %tmp
}
# People who have agreed to one of the CLAs and can contribute patches.
# The AUTHORS file lists the copyright holders; this file
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# but not in AUTHORS, because Google holds the copyright.
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# https://developers.google.com/open-source/cla/corporate
#
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# individual or corporate CLA was used.
#
# Names should be added to this file as:
#     Name <email address>
#
# Please keep the list sorted.

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; RUN: opt %s -passes=inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
 ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
 ret void
}

define internal void @innerNoAttribute() {
 ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
 call void @innerLarge()
 ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
 call void @innerLarge()
 ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
 call void @innerSmall()
 ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
 call void @innerNoAttribute()
 ret void
}
```

```
; CHECK: define void @outerNoAttribute()
 {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
; RUN: opt -mtriple=aarch64-linux-gnu -mattr=+sve -passes=scalarize-masked-mem-intrin -S < %s | FileCheck %s

; Testing that masked scatters operating on scalable vectors that are
; packed in SVE registers are not scalarized.

; CHECK-LABEL: @masked_scatter_nxv4i32(
; CHECK: call void @llvm.masked.scatter.nxv4i32
define void @masked_scatter_nxv4i32(<vscale x 4 x i32> %data, <vscale x 4 x ptr> %ptrs, <vscale x 4 x i1>
%masks) {
 call void @llvm.masked.scatter.nxv4i32(<vscale x 4 x i32> %data, <vscale x 4 x ptr> %ptrs, i32 0, <vscale x 4 x
i1> %masks)
 ret void
}

; Testing that masked scatters operating on scalable vectors of FP
; data that is packed in SVE registers are not scalarized.

; CHECK-LABEL: @masked_scatter_nxv2f64(
; CHECK: call void @llvm.masked.scatter.nxv2f64
define void @masked_scatter_nxv2f64(<vscale x 2 x double> %data, <vscale x 2 x ptr> %ptrs, <vscale x 2 x i1>
%masks) {
 call void @llvm.masked.scatter.nxv2f64(<vscale x 2 x double> %data, <vscale x 2 x ptr> %ptrs,
 i32 0, <vscale x 2 x i1> %masks)
 ret void
}

; Testing that masked scatters operating on scalable vectors of FP
; data that is unpacked in SVE registers are not scalarized.

; CHECK-LABEL: @masked_scatter_nxv2f16(
; CHECK: call void @llvm.masked.scatter.nxv2f16
define void @masked_scatter_nxv2f16(<vscale x 2 x half> %data, <vscale x 2 x ptr> %ptrs, <vscale x 2 x i1>
%masks) {
 call void @llvm.masked.scatter.nxv2f16(<vscale x 2 x half> %data, <vscale x 2 x ptr> %ptrs, i32 0, <vscale x 2 x
i1> %masks)
 ret void
}

; Testing that masked scatters operating on 64-bit fixed vectors are
; scalarized because NEON doesn't have support for masked scatter
; instructions.
```

```
; CHECK-LABEL: @masked_scatter_v2f32(
; CHECK-NOT: @llvm.masked.scatter.v2f32(
define void @masked_scatter_v2f32(<2 x float> %data, <2 x ptr> %ptrs, <2 x i1> %masks) {
 call void @llvm.masked.scatter.v2f32(<2 x float> %data, <2 x ptr> %ptrs, i32 0, <2 x i1> %masks)
 ret void
}

; Testing that masked scatters operating on 128-bit
 fixed vectors are
; scalarized because NEON doesn't have support for masked scatter
; instructions and because we are not targeting fixed width SVE.

; CHECK-LABEL: @masked_scatter_v4i32(
; CHECK-NOT: @llvm.masked.scatter.v4i32(
define void @masked_scatter_v4i32(<4 x i32> %data, <4 x ptr> %ptrs, <4 x i1> %masks) {
 call void @llvm.masked.scatter.v4i32(<4 x i32> %data, <4 x ptr> %ptrs, i32 0, <4 x i1> %masks)
 ret void
}

declare void @llvm.masked.scatter.nxv4i32(<vscale x 4 x i32> %data, <vscale x 4 x ptr> %ptrs, i32 %align,
<vscale x 4 x i1> %masks)
declare void @llvm.masked.scatter.nxv2f64(<vscale x 2 x double> %data, <vscale x 2 x ptr> %ptrs, i32 %align,
<vscale x 2 x i1> %masks)
declare void @llvm.masked.scatter.nxv2f16(<vscale x 2 x half> %data, <vscale x 2 x ptr> %ptrs, i32 %align,
<vscale x 2 x i1> %masks)
declare void @llvm.masked.scatter.v2f32(<2 x float> %data, <2 x ptr> %ptrs, i32 %align, <2 x i1> %masks)
declare void @llvm.masked.scatter.v4i32(<4 x i32> %data, <4 x
 ptr> %ptrs, i32 %align, <4 x i1> %masks)
//===-- ObjectContainerBSDArchive.h ----------------------------*- C++ -*-===//
//
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//
//===----------------------------------------------------------------------===//

#ifndef
LLDB_SOURCE_PLUGINS_OBJECTCONTAINER_BSD_ARCHIVE_OBJECTCONTAINERBSDARCHIVE_H
#define
LLDB_SOURCE_PLUGINS_OBJECTCONTAINER_BSD_ARCHIVE_OBJECTCONTAINERBSDARCHIVE_H

#include "lldb/Core/UniqueCStringMap.h"
#include "lldb/Symbol/ObjectContainer.h"
#include "lldb/Utility/ArchSpec.h"
#include "lldb/Utility/ConstString.h"
#include "lldb/Utility/FileSpec.h"
```

```
#include "llvm/Object/Archive.h"
#include "llvm/Support/Chrono.h"
#include "llvm/Support/Path.h"

#include <map>
#include <memory>
#include <mutex>

enum class ArchiveType { Invalid, Archive, ThinArchive };

class ObjectContainerBSDArchive : public lldb_private::ObjectContainer
 {
public:
 ObjectContainerBSDArchive(const lldb::ModuleSP &module_sp,
                           lldb::DataBufferSP &data_sp,
                           lldb::offset_t data_offset,
                           const lldb_private::FileSpec *file,
                           lldb::offset_t offset, lldb::offset_t length,
                           ArchiveType archive_type);

 ~ObjectContainerBSDArchive() override;

 // Static Functions
 static void Initialize();

 static void Terminate();

 static llvm::StringRef GetPluginNameStatic() { return "bsd-archive"; }

 static llvm::StringRef GetPluginDescriptionStatic() {
   return "BSD Archive object container reader.";
 }

 static lldb_private::ObjectContainer *
 CreateInstance(const lldb::ModuleSP &module_sp, lldb::DataBufferSP &data_sp,
                lldb::offset_t data_offset, const lldb_private::FileSpec *file,
                lldb::offset_t offset, lldb::offset_t length);

 static size_t GetModuleSpecifications(const
 lldb_private::FileSpec &file,
                                       lldb::DataBufferSP &data_sp,
                                       lldb::offset_t data_offset,
                                       lldb::offset_t file_offset,
                                       lldb::offset_t length,
                                       lldb_private::ModuleSpecList &specs);

 static ArchiveType MagicBytesMatch(const lldb_private::DataExtractor &data);
```

```
 // Member Functions
 bool ParseHeader() override;

 size_t GetNumObjects() const override {
   if (m_archive_sp)
     return m_archive_sp->GetNumObjects();
   return 0;
 }

 lldb::ObjectFileSP GetObjectFile(const lldb_private::FileSpec *file) override;

 // PluginInterface protocol
 llvm::StringRef GetPluginName() override { return GetPluginNameStatic(); }

protected:
 struct Object {
   Object();

   void Clear();

   lldb::offset_t ExtractFromThin(const lldb_private::DataExtractor &data,

       lldb::offset_t offset,
                              llvm::StringRef stringTable);

   lldb::offset_t Extract(const lldb_private::DataExtractor &data,
                          lldb::offset_t offset);
   /// Object name in the archive.
   lldb_private::ConstString ar_name;

   /// Object modification time in the archive.
   uint32_t modification_time = 0;

   /// Object size in bytes in the archive.
   uint32_t size = 0;

   /// File offset in bytes from the beginning of the file of the object data.
   lldb::offset_t file_offset = 0;

   /// Length of the object data.
   lldb::offset_t file_size = 0;

   void Dump() const;
 };

 class Archive {
 public:
   typedef std::shared_ptr<Archive> shared_ptr;
```

```
  typedef std::multimap<lldb_private::FileSpec, shared_ptr> Map;

  Archive(const lldb_private::ArchSpec &arch,
          const llvm::sys::TimePoint<> &mod_time, lldb::offset_t file_offset,
          lldb_private::DataExtractor &data, ArchiveType archive_type);

  ~Archive();

  static Map &GetArchiveCache();

  static std::recursive_mutex &GetArchiveCacheMutex();

  static Archive::shared_ptr FindCachedArchive(
     const lldb_private::FileSpec &file, const lldb_private::ArchSpec &arch,
     const llvm::sys::TimePoint<> &mod_time, lldb::offset_t file_offset);

  static Archive::shared_ptr ParseAndCacheArchiveForFile(
     const lldb_private::FileSpec &file, const lldb_private::ArchSpec &arch,
     const llvm::sys::TimePoint<> &mod_time, lldb::offset_t file_offset,
     lldb_private::DataExtractor &data, ArchiveType archive_type);

  size_t GetNumObjects() const { return m_objects.size(); }

  const Object *GetObjectAtIndex(size_t idx) {
    if (idx < m_objects.size())
      return &m_objects[idx];
    return nullptr;
  }

  size_t ParseObjects();

  Object *FindObject(lldb_private::ConstString object_name,
                     const llvm::sys::TimePoint<> &object_mod_time);

  lldb::offset_t
GetFileOffset() const { return m_file_offset; }

  const llvm::sys::TimePoint<> &GetModificationTime() {
    return m_modification_time;
  }

  const lldb_private::ArchSpec &GetArchitecture() const { return m_arch; }

  void SetArchitecture(const lldb_private::ArchSpec &arch) { m_arch = arch; }

  bool HasNoExternalReferences() const;

  lldb_private::DataExtractor &GetData() { return m_data; }
```

```
  ArchiveType GetArchiveType() { return m_archive_type; }

 protected:
  typedef lldb_private::UniqueCStringMap<uint32_t> ObjectNameToIndexMap;
  // Member Variables
  lldb_private::ArchSpec m_arch;
  llvm::sys::TimePoint<> m_modification_time;
  lldb::offset_t m_file_offset;
  std::vector<Object> m_objects;
  ObjectNameToIndexMap m_object_name_to_index_map;
  lldb_private::DataExtractor m_data; ///< The data for this object container
                                      ///so we don't lose data if the .a files
                                      ///gets
 modified
  ArchiveType m_archive_type;
 };

 void SetArchive(Archive::shared_ptr &archive_sp);

 Archive::shared_ptr m_archive_sp;

 ArchiveType m_archive_type;
};

#endif //
LLDB_SOURCE_PLUGINS_OBJECTCONTAINER_BSD_ARCHIVE_OBJECTCONTAINERBSDARCHIVE_H
! RUN: %python %S/../test_errors.py %s %flang -fopenmp -Werror -pedantic
! OpenMP Version 5.0
! 2.19.4.4 firstprivate Clause
! 2.19.4.5 lastprivate Clause
! 2.19.6.1 copyin Clause
! 2.19.6.2 copyprivate Clause
! If the list item is a polymorphic variable with the allocatable attribute,
! the behavior is unspecified.

subroutine firstprivate()
 class(*), allocatable, save :: x

 !PORTABILITY: If a polymorphic variable with allocatable attribute 'x' is in FIRSTPRIVATE clause, the behavior
is unspecified
 !$omp parallel firstprivate(x)
  call sub()
 !$omp end parallel

end

subroutine lastprivate()
```

```
 class(*), allocatable, save :: x

 !PORTABILITY: If a polymorphic variable with allocatable attribute 'x' is in LASTPRIVATE clause, the behavior
is unspecified
 !$omp do lastprivate(x)
 do i = 1, 10
   call sub()
 enddo
 !$omp end do

end

subroutine copyin()
 class(*), allocatable, save :: x
 !$omp threadprivate(x)

 !PORTABILITY: If a polymorphic variable with allocatable attribute
 'x' is in COPYIN clause, the behavior is unspecified
 !$omp parallel copyin(x)
   call sub()
 !$omp end parallel

end

subroutine copyprivate()
 class(*), allocatable, save :: x
 !$omp threadprivate(x)

 !$omp single
   call sub()
 !PORTABILITY: If a polymorphic variable with allocatable attribute 'x' is in COPYPRIVATE clause, the behavior
is unspecified
 !$omp end single copyprivate(x)

end
```

```
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```

```
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THE SOFTWARE.
static_library("BSD-Archive") {
 output_name = "lldbPluginObjectContainerBSDArchive"
 configs += ["//llvm/utils/gn/build:lldb_code"]
 deps = [
   "//lldb/source/Core",
   "//lldb/source/Host",
   "//lldb/source/Symbol",
   "//llvm/lib/Support",
 ]
 sources = ["ObjectContainerBSDArchive.cpp"]
}
; RUN: opt -mtriple=aarch64-linux-gnu -mattr=+sve -passes=scalarize-masked-mem-intrin -S < %s | FileCheck %s

; Testing that masked gathers operating on scalable vectors that are
; packed in SVE registers are not scalarized.

; CHECK-LABEL: @masked_gather_nxv4i32(
; CHECK: call <vscale x 4 x i32> @llvm.masked.gather.nxv4i32
define <vscale x 4 x i32> @masked_gather_nxv4i32(<vscale x 4 x ptr> %ld, <vscale x 4 x i1> %masks, <vscale x 4 x i32> %passthru) {
 %res = call <vscale x 4 x i32> @llvm.masked.gather.nxv4i32(<vscale x 4 x ptr> %ld, i32 0, <vscale x 4 x i1> %masks, <vscale x 4 x i32> %passthru)
 ret <vscale x 4 x i32> %res
}

```
; Testing that masked gathers operating on scalable vectors of FP data
; that is packed in SVE registers are not scalarized.

; CHECK-LABEL: @masked_gather_nxv2f64(
; CHECK: call <vscale x 2 x double> @llvm.masked.gather.nxv2f64
define <vscale x 2 x double> @masked_gather_nxv2f64(<vscale x 2 x ptr> %ld, <vscale x 2 x i1> %masks, <vscale
x 2 x double> %passthru)
 {
 %res = call <vscale x 2 x double> @llvm.masked.gather.nxv2f64(<vscale x 2 x ptr> %ld, i32 0, <vscale x 2 x i1>
%masks, <vscale x 2 x double> %passthru)
 ret <vscale x 2 x double> %res
}

; Testing that masked gathers operating on scalable vectors of FP data
; that is unpacked in SVE registers are not scalarized.

; CHECK-LABEL: @masked_gather_nxv2f16(
; CHECK: call <vscale x 2 x half> @llvm.masked.gather.nxv2f16
define <vscale x 2 x half> @masked_gather_nxv2f16(<vscale x 2 x ptr> %ld, <vscale x 2 x i1> %masks, <vscale x
2 x half> %passthru) {
 %res = call <vscale x 2 x half> @llvm.masked.gather.nxv2f16(<vscale x 2 x ptr> %ld, i32 0, <vscale x 2 x i1>
%masks, <vscale x 2 x half> %passthru)
 ret <vscale x 2 x half> %res
}

; Testing that masked gathers operating on 64-bit fixed vectors are
; scalarized because NEON doesn't have support for masked gather
; instructions.

; CHECK-LABEL: @masked_gather_v2f32(
; CHECK-NOT: @llvm.masked.gather.v2f32(
define <2 x float> @masked_gather_v2f32(<2
 x ptr> %ld, <2 x i1> %masks, <2 x float> %passthru) {
 %res = call <2 x float> @llvm.masked.gather.v2f32(<2 x ptr> %ld, i32 0, <2 x i1> %masks, <2 x float> %passthru)
 ret <2 x float> %res
}

; Testing that masked gathers operating on 128-bit fixed vectors are
; scalarized because NEON doesn't have support for masked gather
; instructions and because we are not targeting fixed width SVE.

; CHECK-LABEL: @masked_gather_v4i32(
; CHECK-NOT: @llvm.masked.gather.v4i32(
define <4 x i32> @masked_gather_v4i32(<4 x ptr> %ld, <4 x i1> %masks, <4 x i32> %passthru) {
 %res = call <4 x i32> @llvm.masked.gather.v4i32(<4 x ptr> %ld, i32 0, <4 x i1> %masks, <4 x i32> %passthru)
 ret <4 x i32> %res
}
```

```
declare <vscale x 4 x i32> @llvm.masked.gather.nxv4i32(<vscale x 4 x ptr> %ptrs, i32 %align, <vscale x 4 x i1>
%masks, <vscale x 4 x i32> %passthru)
declare <vscale x 2 x double> @llvm.masked.gather.nxv2f64(<vscale x 2 x ptr> %ptrs, i32 %align, <vscale x 2 x
i1> %masks, <vscale x 2 x double>
 %passthru)
declare <vscale x 2 x half> @llvm.masked.gather.nxv2f16(<vscale x 2 x ptr> %ptrs, i32 %align, <vscale x 2 x i1>
%masks, <vscale x 2 x half> %passthru)
declare <2 x float> @llvm.masked.gather.v2f32(<2 x ptr> %ptrs, i32 %align, <2 x i1> %masks, <2 x float>
%passthru)
declare <4 x i32> @llvm.masked.gather.v4i32(<4 x ptr> %ptrs, i32 %align, <4 x i1> %masks, <4 x i32> %passthru)
# RUN: llc -O0 -mtriple=m68k -start-after=prologepilog -verify-machineinstrs %s -o - | FileCheck %s

name: is-pcrel-register-operand-legal
body:             |
 bb.0.entry:
   ; CHECK: move.l  (0,%pc,%a0), (%a1)
   ; CHECK: move.l  (%a0), (0,%pc,%a1)

   MOV32jk $a1,  0, $a0, implicit-def $ccr
   MOV32kj 0,  $a1, $a0, implicit-def $ccr
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: sed 's/iXLen/i64/g' %s | llc -mtriple=riscv64 -verify-machineinstrs \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=LP64-LP64F-LP64D-FPELIM %s
; RUN: sed 's/iXLen/i64/g' %s | llc -mtriple=riscv64 -mattr=+d -target-abi lp64f \
; RUN:     -verify-machineinstrs -riscv-experimental-rv64-legal-i32 \
; RUN:   | FileCheck -check-prefix=LP64-LP64F-LP64D-FPELIM %s
; RUN: sed 's/iXLen/i64/g' %s | llc -mtriple=riscv64 -mattr=+d -target-abi lp64d \
; RUN:     -verify-machineinstrs -riscv-experimental-rv64-legal-i32 \
; RUN:   | FileCheck -check-prefix=LP64-LP64F-LP64D-FPELIM %s
; RUN: sed 's/iXLen/i64/g' %s | llc -mtriple=riscv64 -verify-machineinstrs -frame-pointer=all \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=LP64-LP64F-LP64D-WITHFP %s
; RUN: sed 's/iXLen/i64/g' %s | llc -mtriple=riscv64 -target-abi lp64e \
; RUN:     -verify-machineinstrs -riscv-experimental-rv64-legal-i32
 \
; RUN:   | FileCheck -check-prefix=LP64E-FPELIM %s
; RUN: sed 's/iXLen/i64/g' %s | llc -mtriple=riscv64 -target-abi lp64e -frame-pointer=all \
; RUN:     -verify-machineinstrs -riscv-experimental-rv64-legal-i32 \
; RUN:   | FileCheck -check-prefix=LP64E-WITHFP %s

; The same vararg calling convention is used for ilp32/ilp32f/ilp32d and for
; lp64/lp64f/lp64d. Different CHECK lines are required for RV32D due to slight
; codegen differences due to the way the f64 load operations are lowered.
; The nounwind attribute is omitted for some of the tests, to check that CFI
; directives are correctly generated.

declare void @llvm.va_start(ptr)
declare void @llvm.va_end(ptr)
```

```
declare void @notdead(ptr)

; Although frontends are recommended to not generate va_arg due to the lack of
; support for aggregate types, we test simple cases here to ensure they are
; lowered correctly

define i32 @va1(ptr %fmt, ...) {
; LP64-LP64F-LP64D-FPELIM-LABEL: va1:
; LP64-LP64F-LP64D-FPELIM:
  # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -80
; LP64-LP64F-LP64D-FPELIM-NEXT:    .cfi_def_cfa_offset 80
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 72(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 64(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a1, sp, 28
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va1:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_def_cfa_offset 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0,
 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_offset ra, -72
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_offset s0, -80
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_def_cfa s0, 64
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, s0, 12
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
```

```
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va1:
;
 LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -56
; LP64E-FPELIM-NEXT:    .cfi_def_cfa_offset 56
; LP64E-FPELIM-NEXT:    mv a0, a1
; LP64E-FPELIM-NEXT:    sd a5, 48(sp)
; LP64E-FPELIM-NEXT:    sd a4, 40(sp)
; LP64E-FPELIM-NEXT:    sd a3, 32(sp)
; LP64E-FPELIM-NEXT:    sd a2, 24(sp)
; LP64E-FPELIM-NEXT:    sd a1, 16(sp)
; LP64E-FPELIM-NEXT:    addi a1, sp, 20
; LP64E-FPELIM-NEXT:    sd a1, 0(sp)
; LP64E-FPELIM-NEXT:    addi sp, sp, 56
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va1:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -72
; LP64E-WITHFP-NEXT:    .cfi_def_cfa_offset 72
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    .cfi_offset ra, -56
; LP64E-WITHFP-NEXT:    .cfi_offset s0, -64
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    .cfi_def_cfa s0, 48
; LP64E-WITHFP-NEXT:    mv a0, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:
   sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a1, s0, 12
; LP64E-WITHFP-NEXT:    sd a1, -24(s0)
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 72
; LP64E-WITHFP-NEXT:    ret
%va = alloca ptr
call void @llvm.va_start(ptr %va)
%argp.cur = load ptr, ptr %va, align 4
%argp.next = getelementptr inbounds i8, ptr %argp.cur, i32 4
store ptr %argp.next, ptr %va, align 4
```

```
 %1 = load i32, ptr %argp.cur, align 4
 call void @llvm.va_end(ptr %va)
 ret i32 %1
}

define i32 @va1_va_arg(ptr %fmt, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va1_va_arg:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -80
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 72(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:
   sd a6, 64(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a1, sp, 32
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va1_va_arg:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:
   sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, s0, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va1_va_arg:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -56
```

```
; LP64E-FPELIM-NEXT:    mv a0, a1
; LP64E-FPELIM-NEXT:    sd a5, 48(sp)
; LP64E-FPELIM-NEXT:    sd a4, 40(sp)
; LP64E-FPELIM-NEXT:    sd a3, 32(sp)
; LP64E-FPELIM-NEXT:    sd a2, 24(sp)
; LP64E-FPELIM-NEXT:    sd a1, 16(sp)
; LP64E-FPELIM-NEXT:    addi a1, sp, 24
; LP64E-FPELIM-NEXT:    sd a1, 0(sp)
; LP64E-FPELIM-NEXT:    addi sp, sp, 56
; LP64E-FPELIM-NEXT:
 ret
;
; LP64E-WITHFP-LABEL: va1_va_arg:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -72
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    mv a0, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:    sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a1, s0, 16
; LP64E-WITHFP-NEXT:    sd a1, -24(s0)
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 72
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %1 = va_arg ptr %va, i32
 call void @llvm.va_end(ptr %va)
 ret i32 %1
}

; Ensure the adjustment when restoring the stack pointer using the frame
; pointer is correct
define i32 @va1_va_arg_alloca(ptr
 %fmt, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va1_va_arg_alloca:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi s0, sp, 32
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv s1, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a0, s0, 16
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, -32(s0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    slli a0, a1, 32
; LP64-LP64F-LP64D-FPELIM-NEXT:
   srli a0, a0, 32
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a0, a0, 15
; LP64-LP64F-LP64D-FPELIM-NEXT:    andi a0, a0, -16
; LP64-LP64F-LP64D-FPELIM-NEXT:    sub a0, sp, a0
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv sp, a0
; LP64-LP64F-LP64D-FPELIM-NEXT:    call notdead
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv a0, s1
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, s0, -32
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va1_va_arg_alloca:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:
   sd s1, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv s1, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a0, s0, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, -32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    slli a0, a1, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    srli a0, a0, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a0, a0, 15
; LP64-LP64F-LP64D-WITHFP-NEXT:    andi a0, a0, -16
```

```
; LP64-LP64F-LP64D-WITHFP-NEXT:    sub a0, sp, a0
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv sp, a0
; LP64-LP64F-LP64D-WITHFP-NEXT:    call notdead
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv a0, s1
; LP64-LP64F-LP64D-WITHFP-NEXT:
   addi sp, s0, -32
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va1_va_arg_alloca:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -80
; LP64E-FPELIM-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    addi s0, sp, 32
; LP64E-FPELIM-NEXT:    mv s1, a1
; LP64E-FPELIM-NEXT:    sd a5, 40(s0)
; LP64E-FPELIM-NEXT:    sd a4, 32(s0)
; LP64E-FPELIM-NEXT:    sd a3, 24(s0)
; LP64E-FPELIM-NEXT:    sd a2, 16(s0)
; LP64E-FPELIM-NEXT:    sd a1, 8(s0)
; LP64E-FPELIM-NEXT:    addi a0, s0, 16
; LP64E-FPELIM-NEXT:    sd a0, -32(s0)
; LP64E-FPELIM-NEXT:
 slli a0, a1, 32
; LP64E-FPELIM-NEXT:    srli a0, a0, 32
; LP64E-FPELIM-NEXT:    addi a0, a0, 7
; LP64E-FPELIM-NEXT:    andi a0, a0, -8
; LP64E-FPELIM-NEXT:    sub a0, sp, a0
; LP64E-FPELIM-NEXT:    mv sp, a0
; LP64E-FPELIM-NEXT:    call notdead
; LP64E-FPELIM-NEXT:    mv a0, s1
; LP64E-FPELIM-NEXT:    addi sp, s0, -32
; LP64E-FPELIM-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    addi sp, sp, 80
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va1_va_arg_alloca:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -80
; LP64E-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
```

```
; LP64E-WITHFP-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 32
; LP64E-WITHFP-NEXT:    mv s1, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:
 sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a0, s0, 16
; LP64E-WITHFP-NEXT:    sd a0, -32(s0)
; LP64E-WITHFP-NEXT:    slli a0, a1, 32
; LP64E-WITHFP-NEXT:    srli a0, a0, 32
; LP64E-WITHFP-NEXT:    addi a0, a0, 7
; LP64E-WITHFP-NEXT:    andi a0, a0, -8
; LP64E-WITHFP-NEXT:    sub a0, sp, a0
; LP64E-WITHFP-NEXT:    mv sp, a0
; LP64E-WITHFP-NEXT:    call notdead
; LP64E-WITHFP-NEXT:    mv a0, s1
; LP64E-WITHFP-NEXT:    addi sp, s0, -32
; LP64E-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 80
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %1 = va_arg ptr %va, i32
 %2 = alloca i8, i32 %1
 call void @notdead(ptr %2)
 call void @llvm.va_end(ptr %va)
 ret i32 %1
}

define void @va1_caller()
 nounwind {
; Pass a double, as a float would be promoted by a C/C++ frontend
; LP64-LP64F-LP64D-FPELIM-LABEL: va1_caller:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -16
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a1, 1023
; LP64-LP64F-LP64D-FPELIM-NEXT:    slli a1, a1, 52
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a2, 2
; LP64-LP64F-LP64D-FPELIM-NEXT:    call va1
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 16
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
```

```
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va1_caller:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:
   li a1, 1023
; LP64-LP64F-LP64D-WITHFP-NEXT:    slli a1, a1, 52
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a2, 2
; LP64-LP64F-LP64D-WITHFP-NEXT:    call va1
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va1_caller:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -8
; LP64E-FPELIM-NEXT:    sd ra, 0(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    li a1, 1023
; LP64E-FPELIM-NEXT:    slli a1, a1, 52
; LP64E-FPELIM-NEXT:    li a2, 2
; LP64E-FPELIM-NEXT:    call va1
; LP64E-FPELIM-NEXT:    ld ra, 0(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    addi sp, sp, 8
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va1_caller:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -16
; LP64E-WITHFP-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:
   sd s0, 0(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 16
; LP64E-WITHFP-NEXT:    li a1, 1023
; LP64E-WITHFP-NEXT:    slli a1, a1, 52
; LP64E-WITHFP-NEXT:    li a2, 2
; LP64E-WITHFP-NEXT:    call va1
; LP64E-WITHFP-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 16
; LP64E-WITHFP-NEXT:    ret
 %1 = call i32 (ptr, ...) @va1(ptr undef, double 1.0, i32 2)
 ret void
}
```

```
; Ensure that 2x xlen size+alignment varargs are accessed via an "aligned"
; register pair (where the first register is even-numbered).

define i64 @va2(ptr %fmt, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va2:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -80
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 72(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 64(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 56(sp)
;
 LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a1, sp, 39
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va2:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3,
 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, s0, 23
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va2:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -56
; LP64E-FPELIM-NEXT:    mv a0, a1
; LP64E-FPELIM-NEXT:    sd a5, 48(sp)
```

```
; LP64E-FPELIM-NEXT:    sd a4, 40(sp)
; LP64E-FPELIM-NEXT:    sd a3, 32(sp)
; LP64E-FPELIM-NEXT:    sd a2, 24(sp)
; LP64E-FPELIM-NEXT:    sd a1, 16(sp)
; LP64E-FPELIM-NEXT:    addi a1, sp, 31
; LP64E-FPELIM-NEXT:    sd a1, 0(sp)
; LP64E-FPELIM-NEXT:    addi sp, sp, 56
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va2:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp,
 sp, -72
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    mv a0, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:    sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a1, s0, 23
; LP64E-WITHFP-NEXT:    sd a1, -24(s0)
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 72
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %argp.cur = load ptr, ptr %va
 %ptrint = ptrtoint ptr %argp.cur to iXLen
 %1 = add iXLen %ptrint, 7
 %2 = and iXLen %1, -8
 %argp.cur.aligned = inttoptr iXLen %1 to ptr
 %argp.next = getelementptr inbounds i8, ptr %argp.cur.aligned, i32 8
 store ptr %argp.next, ptr %va
 %3 =
 inttoptr iXLen %2 to ptr
 %4 = load double, ptr %3, align 8
 %5 = bitcast double %4 to i64
 call void @llvm.va_end(ptr %va)
 ret i64 %5
}

define i64 @va2_va_arg(ptr %fmt, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va2_va_arg:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -80
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 72(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 64(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a1, sp, 32
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va2_va_arg:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:
   addi sp, sp, -96
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, s0, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va2_va_arg:
;
 LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -56
; LP64E-FPELIM-NEXT:    mv a0, a1
; LP64E-FPELIM-NEXT:    sd a5, 48(sp)
; LP64E-FPELIM-NEXT:    sd a4, 40(sp)
; LP64E-FPELIM-NEXT:    sd a3, 32(sp)
; LP64E-FPELIM-NEXT:    sd a2, 24(sp)
; LP64E-FPELIM-NEXT:    sd a1, 16(sp)
; LP64E-FPELIM-NEXT:    addi a1, sp, 24
; LP64E-FPELIM-NEXT:    sd a1, 0(sp)
```

```
; LP64E-FPELIM-NEXT:    addi sp, sp, 56
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va2_va_arg:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -72
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    mv a0, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:    sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a1, s0, 16
; LP64E-WITHFP-NEXT:    sd a1,
 -24(s0)
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 72
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %1 = va_arg ptr %va, double
 call void @llvm.va_end(ptr %va)
 %2 = bitcast double %1 to i64
 ret i64 %2
}

define void @va2_caller() nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va2_caller:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -16
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a1, 1023
; LP64-LP64F-LP64D-FPELIM-NEXT:    slli a1, a1, 52
; LP64-LP64F-LP64D-FPELIM-NEXT:    call va2
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 16
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va2_caller:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
;
 LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
```

```
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a1, 1023
; LP64-LP64F-LP64D-WITHFP-NEXT:    slli a1, a1, 52
; LP64-LP64F-LP64D-WITHFP-NEXT:    call va2
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va2_caller:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -8
; LP64E-FPELIM-NEXT:    sd ra, 0(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    li a1, 1023
; LP64E-FPELIM-NEXT:    slli a1, a1, 52
; LP64E-FPELIM-NEXT:    call va2
; LP64E-FPELIM-NEXT:    ld ra, 0(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    addi sp, sp, 8
; LP64E-FPELIM-NEXT:
   ret
;
; LP64E-WITHFP-LABEL: va2_caller:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -16
; LP64E-WITHFP-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 16
; LP64E-WITHFP-NEXT:    li a1, 1023
; LP64E-WITHFP-NEXT:    slli a1, a1, 52
; LP64E-WITHFP-NEXT:    call va2
; LP64E-WITHFP-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 16
; LP64E-WITHFP-NEXT:    ret
%1 = call i64 (ptr, ...) @va2(ptr undef, double 1.000000e+00)
ret void
}

; On RV32, Ensure a named 2*xlen argument is passed in a1 and a2, while the
; vararg double is passed in a4 and a5 (rather than a3 and a4)

define i64 @va3(i32 %a, i64 %b, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va3:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -64
; LP64-LP64F-LP64D-FPELIM-NEXT:
   sd a7, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 48(sp)
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 16(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a3, sp, 31
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a0, a1, a2
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 64
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va3:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -80
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:
   sd a4, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 0(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a3, s0, 15
; LP64-LP64F-LP64D-WITHFP-NEXT:    add a0, a1, a2
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va3:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -40
; LP64E-FPELIM-NEXT:    sd a5, 32(sp)
; LP64E-FPELIM-NEXT:    sd a4, 24(sp)
; LP64E-FPELIM-NEXT:    sd a3, 16(sp)
; LP64E-FPELIM-NEXT:    sd a2, 8(sp)
; LP64E-FPELIM-NEXT:    addi a3, sp, 23
; LP64E-FPELIM-NEXT:    add a0, a1, a2
; LP64E-FPELIM-NEXT:    sd a3, 0(sp)
; LP64E-FPELIM-NEXT:    addi sp, sp, 40
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va3:
; LP64E-WITHFP:
      # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -56
```

```
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    sd a5, 24(s0)
; LP64E-WITHFP-NEXT:    sd a4, 16(s0)
; LP64E-WITHFP-NEXT:    sd a3, 8(s0)
; LP64E-WITHFP-NEXT:    sd a2, 0(s0)
; LP64E-WITHFP-NEXT:    addi a3, s0, 15
; LP64E-WITHFP-NEXT:    add a0, a1, a2
; LP64E-WITHFP-NEXT:    sd a3, -24(s0)
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 56
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %argp.cur = load ptr, ptr %va
 %ptrint = ptrtoint ptr %argp.cur to iXLen
 %1 = add iXLen %ptrint, 7
 %2 = and iXLen %1, -8
 %argp.cur.aligned = inttoptr iXLen %1 to ptr
 %argp.next = getelementptr inbounds i8, ptr %argp.cur.aligned, i32 8
 store ptr %argp.next, ptr
 %va
 %3 = inttoptr iXLen %2 to ptr
 %4 = load double, ptr %3, align 8
 call void @llvm.va_end(ptr %va)
 %5 = bitcast double %4 to i64
 %6 = add i64 %b, %5
 ret i64 %6
}

define i64 @va3_va_arg(i32 %a, i64 %b, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va3_va_arg:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -64
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 16(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a3, sp, 24
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a0, a1, a2
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 64
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
```

```
; LP64-LP64F-LP64D-WITHFP-LABEL: va3_va_arg:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:
   addi sp, sp, -80
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 0(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a3, s0, 8
; LP64-LP64F-LP64D-WITHFP-NEXT:    add a0, a1, a2
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va3_va_arg:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:
   addi sp, sp, -40
; LP64E-FPELIM-NEXT:    sd a5, 32(sp)
; LP64E-FPELIM-NEXT:    sd a4, 24(sp)
; LP64E-FPELIM-NEXT:    sd a3, 16(sp)
; LP64E-FPELIM-NEXT:    sd a2, 8(sp)
; LP64E-FPELIM-NEXT:    addi a3, sp, 16
; LP64E-FPELIM-NEXT:    add a0, a1, a2
; LP64E-FPELIM-NEXT:    sd a3, 0(sp)
; LP64E-FPELIM-NEXT:    addi sp, sp, 40
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va3_va_arg:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -56
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    sd a5, 24(s0)
; LP64E-WITHFP-NEXT:    sd a4, 16(s0)
; LP64E-WITHFP-NEXT:    sd a3, 8(s0)
; LP64E-WITHFP-NEXT:    sd a2, 0(s0)
; LP64E-WITHFP-NEXT:    addi a3, s0, 8
; LP64E-WITHFP-NEXT:    add a0, a1, a2
; LP64E-WITHFP-NEXT:    sd a3, -24(s0)
```

```
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded
 Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 56
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %1 = va_arg ptr %va, double
 call void @llvm.va_end(ptr %va)
 %2 = bitcast double %1 to i64
 %3 = add i64 %b, %2
 ret i64 %3
}

define void @va3_caller() nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va3_caller:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -16
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a2, 1
; LP64-LP64F-LP64D-FPELIM-NEXT:    slli a2, a2, 62
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a0, 2
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a1, 1111
; LP64-LP64F-LP64D-FPELIM-NEXT:    call va3
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 16
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va3_caller:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:
   addi sp, sp, -16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a2, 1
; LP64-LP64F-LP64D-WITHFP-NEXT:    slli a2, a2, 62
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a0, 2
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a1, 1111
; LP64-LP64F-LP64D-WITHFP-NEXT:    call va3
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va3_caller:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -8
```

```
; LP64E-FPELIM-NEXT:    sd ra, 0(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    li a2, 1
; LP64E-FPELIM-NEXT:    slli a2, a2, 62
; LP64E-FPELIM-NEXT:    li a0, 2
; LP64E-FPELIM-NEXT:    li a1,
 1111
; LP64E-FPELIM-NEXT:    call va3
; LP64E-FPELIM-NEXT:    ld ra, 0(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    addi sp, sp, 8
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va3_caller:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -16
; LP64E-WITHFP-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 16
; LP64E-WITHFP-NEXT:    li a2, 1
; LP64E-WITHFP-NEXT:    slli a2, a2, 62
; LP64E-WITHFP-NEXT:    li a0, 2
; LP64E-WITHFP-NEXT:    li a1, 1111
; LP64E-WITHFP-NEXT:    call va3
; LP64E-WITHFP-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 16
; LP64E-WITHFP-NEXT:    ret
%1 = call i64 (i32, i64, ...) @va3(i32 2, i64 1111, double 2.000000e+00)
ret void
}

declare void @llvm.va_copy(ptr, ptr)

define i32 @va4_va_copy(i32 %argno, ...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL:
 va4_va_copy:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv s0, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 88(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 80(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 72(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 64(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a0, sp, 48
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, 0(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    call notdead
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld a0, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a0, a0, 3
; LP64-LP64F-LP64D-FPELIM-NEXT:    andi a0, a0, -4
; LP64-LP64F-LP64D-FPELIM-NEXT:
  addi a1, a0, 8
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lw a1, 0(a0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a0, a0, 11
; LP64-LP64F-LP64D-FPELIM-NEXT:    andi a0, a0, -4
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a2, a0, 8
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lw a2, 0(a0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a0, a0, 11
; LP64-LP64F-LP64D-FPELIM-NEXT:    andi a0, a0, -4
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a3, a0, 8
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a3, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lw a0, 0(a0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a1, a1, s0
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a1, a1, a2
; LP64-LP64F-LP64D-FPELIM-NEXT:    addw a0, a1, a0
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-FPELIM-NEXT:
  ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va4_va_copy:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -112
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 40(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 32(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s1, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 48
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv s1, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a0, s0, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, -32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, -40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    call
```

```
 notdead
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld a0, -32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a0, a0, 3
; LP64-LP64F-LP64D-WITHFP-NEXT:    andi a0, a0, -4
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, a0, 8
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, -32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lw a1, 0(a0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a0, a0, 11
; LP64-LP64F-LP64D-WITHFP-NEXT:    andi a0, a0, -4
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a2, a0, 8
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, -32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lw a2, 0(a0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a0, a0, 11
; LP64-LP64F-LP64D-WITHFP-NEXT:    andi a0, a0, -4
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a3, a0, 8
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, -32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lw a0, 0(a0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    add a1, a1, s1
; LP64-LP64F-LP64D-WITHFP-NEXT:    add a1, a1, a2
; LP64-LP64F-LP64D-WITHFP-NEXT:    addw a0, a1, a0
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra,
 40(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 32(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s1, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 112
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va4_va_copy:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -80
; LP64E-FPELIM-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    mv s0, a1
; LP64E-FPELIM-NEXT:    sd a5, 72(sp)
; LP64E-FPELIM-NEXT:    sd a4, 64(sp)
; LP64E-FPELIM-NEXT:    sd a3, 56(sp)
; LP64E-FPELIM-NEXT:    sd a2, 48(sp)
; LP64E-FPELIM-NEXT:    sd a1, 40(sp)
; LP64E-FPELIM-NEXT:    addi a0, sp, 48
; LP64E-FPELIM-NEXT:    sd a0, 8(sp)
; LP64E-FPELIM-NEXT:    sd a0, 0(sp)
; LP64E-FPELIM-NEXT:    call notdead
; LP64E-FPELIM-NEXT:    ld a0, 8(sp)
; LP64E-FPELIM-NEXT:    addi a0, a0, 3
; LP64E-FPELIM-NEXT:    andi a0, a0, -4
; LP64E-FPELIM-NEXT:
   addi a1, a0, 8
; LP64E-FPELIM-NEXT:    sd a1, 8(sp)
```

```
; LP64E-FPELIM-NEXT:    lw a1, 0(a0)
; LP64E-FPELIM-NEXT:    addi a0, a0, 11
; LP64E-FPELIM-NEXT:    andi a0, a0, -4
; LP64E-FPELIM-NEXT:    addi a2, a0, 8
; LP64E-FPELIM-NEXT:    sd a2, 8(sp)
; LP64E-FPELIM-NEXT:    lw a2, 0(a0)
; LP64E-FPELIM-NEXT:    addi a0, a0, 11
; LP64E-FPELIM-NEXT:    andi a0, a0, -4
; LP64E-FPELIM-NEXT:    addi a3, a0, 8
; LP64E-FPELIM-NEXT:    sd a3, 8(sp)
; LP64E-FPELIM-NEXT:    lw a0, 0(a0)
; LP64E-FPELIM-NEXT:    add a1, a1, s0
; LP64E-FPELIM-NEXT:    add a1, a1, a2
; LP64E-FPELIM-NEXT:    addw a0, a1, a0
; LP64E-FPELIM-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    addi sp, sp, 80
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va4_va_copy:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -88
; LP64E-WITHFP-NEXT:    sd ra, 32(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 24(sp) #
 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s1, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 40
; LP64E-WITHFP-NEXT:    mv s1, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:    sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a0, s0, 16
; LP64E-WITHFP-NEXT:    sd a0, -32(s0)
; LP64E-WITHFP-NEXT:    sd a0, -40(s0)
; LP64E-WITHFP-NEXT:    call notdead
; LP64E-WITHFP-NEXT:    ld a0, -32(s0)
; LP64E-WITHFP-NEXT:    addi a0, a0, 3
; LP64E-WITHFP-NEXT:    andi a0, a0, -4
; LP64E-WITHFP-NEXT:    addi a1, a0, 8
; LP64E-WITHFP-NEXT:    sd a1, -32(s0)
; LP64E-WITHFP-NEXT:    lw a1, 0(a0)
; LP64E-WITHFP-NEXT:    addi a0, a0, 11
; LP64E-WITHFP-NEXT:    andi a0, a0, -4
; LP64E-WITHFP-NEXT:    addi a2, a0, 8
; LP64E-WITHFP-NEXT:    sd a2, -32(s0)
; LP64E-WITHFP-NEXT:    lw a2, 0(a0)
```

```
; LP64E-WITHFP-NEXT:    addi a0, a0, 11
; LP64E-WITHFP-NEXT:    andi a0,
 a0, -4
; LP64E-WITHFP-NEXT:    addi a3, a0, 8
; LP64E-WITHFP-NEXT:    sd a3, -32(s0)
; LP64E-WITHFP-NEXT:    lw a0, 0(a0)
; LP64E-WITHFP-NEXT:    add a1, a1, s1
; LP64E-WITHFP-NEXT:    add a1, a1, a2
; LP64E-WITHFP-NEXT:    addw a0, a1, a0
; LP64E-WITHFP-NEXT:    ld ra, 32(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 24(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s1, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 88
; LP64E-WITHFP-NEXT:    ret
 %vargs = alloca ptr
 %wargs = alloca ptr
 call void @llvm.va_start(ptr %vargs)
 %1 = va_arg ptr %vargs, i32
 call void @llvm.va_copy(ptr %wargs, ptr %vargs)
 %2 = load ptr, ptr %wargs, align 4
 call void @notdead(ptr %2)
 %3 = va_arg ptr %vargs, i32
 %4 = va_arg ptr %vargs, i32
 %5 = va_arg ptr %vargs, i32
 call void @llvm.va_end(ptr %vargs)
 call void @llvm.va_end(ptr %wargs)
 %add1 = add i32 %3, %1
 %add2 = add i32 %add1, %4
 %add3 = add i32 %add2, %5
 ret i32 %add3
}

; Check 2x*xlen
 values are aligned appropriately when passed on the stack in a vararg call

declare i32 @va5_aligned_stack_callee(i32, ...)

define void @va5_aligned_stack_caller() nounwind {
; The double should be 8-byte aligned on the stack, but the two-element array
; should only be 4-byte aligned
; LP64-LP64F-LP64D-FPELIM-LABEL: va5_aligned_stack_caller:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -48
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd ra, 40(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a0, 17
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a0, 16
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, 16(sp)
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a0, 15
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a0, %hi(.LCPI11_0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld t0, %lo(.LCPI11_0)(a0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a0, %hi(.LCPI11_1)
; LP64-LP64F-LP64D-FPELIM-NEXT:
   ld a2, %lo(.LCPI11_1)(a0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a0, %hi(.LCPI11_2)
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld a3, %lo(.LCPI11_2)(a0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a0, 2384
; LP64-LP64F-LP64D-FPELIM-NEXT:    addiw a6, a0, 761
; LP64-LP64F-LP64D-FPELIM-NEXT:    slli a6, a6, 11
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a0, 1
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a1, 11
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a4, 12
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a5, 13
; LP64-LP64F-LP64D-FPELIM-NEXT:    li a7, 14
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd t0, 0(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    call va5_aligned_stack_callee
; LP64-LP64F-LP64D-FPELIM-NEXT:    ld ra, 40(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 48
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va5_aligned_stack_caller:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -48
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 40(sp)
 # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 32(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 48
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a0, 17
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, 24(sp)
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a0, 16
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, 16(sp)
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a0, 15
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, 8(sp)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a0, %hi(.LCPI11_0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld t0, %lo(.LCPI11_0)(a0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a0, %hi(.LCPI11_1)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld a2, %lo(.LCPI11_1)(a0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a0, %hi(.LCPI11_2)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld a3, %lo(.LCPI11_2)(a0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a0, 2384
; LP64-LP64F-LP64D-WITHFP-NEXT:    addiw a6, a0, 761
; LP64-LP64F-LP64D-WITHFP-NEXT:    slli a6, a6, 11
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a0, 1
; LP64-LP64F-LP64D-WITHFP-NEXT:
```

```
   li a1, 11
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a4, 12
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a5, 13
; LP64-LP64F-LP64D-WITHFP-NEXT:    li a7, 14
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd t0, 0(sp)
; LP64-LP64F-LP64D-WITHFP-NEXT:    call va5_aligned_stack_callee
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 40(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 32(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 48
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va5_aligned_stack_caller:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -56
; LP64E-FPELIM-NEXT:    sd ra, 48(sp) # 8-byte Folded Spill
; LP64E-FPELIM-NEXT:    li a0, 17
; LP64E-FPELIM-NEXT:    sd a0, 40(sp)
; LP64E-FPELIM-NEXT:    li a0, 16
; LP64E-FPELIM-NEXT:    lui a1, %hi(.LCPI11_0)
; LP64E-FPELIM-NEXT:    ld a1, %lo(.LCPI11_0)(a1)
; LP64E-FPELIM-NEXT:    sd a0, 32(sp)
; LP64E-FPELIM-NEXT:    li a0, 15
; LP64E-FPELIM-NEXT:    sd a0, 24(sp)
;
 LP64E-FPELIM-NEXT:    sd a1, 16(sp)
; LP64E-FPELIM-NEXT:    li a0, 14
; LP64E-FPELIM-NEXT:    sd a0, 8(sp)
; LP64E-FPELIM-NEXT:    lui a0, 2384
; LP64E-FPELIM-NEXT:    addiw a0, a0, 761
; LP64E-FPELIM-NEXT:    slli a6, a0, 11
; LP64E-FPELIM-NEXT:    lui a0, %hi(.LCPI11_1)
; LP64E-FPELIM-NEXT:    ld a2, %lo(.LCPI11_1)(a0)
; LP64E-FPELIM-NEXT:    lui a0, %hi(.LCPI11_2)
; LP64E-FPELIM-NEXT:    ld a3, %lo(.LCPI11_2)(a0)
; LP64E-FPELIM-NEXT:    li a0, 1
; LP64E-FPELIM-NEXT:    li a1, 11
; LP64E-FPELIM-NEXT:    li a4, 12
; LP64E-FPELIM-NEXT:    li a5, 13
; LP64E-FPELIM-NEXT:    sd a6, 0(sp)
; LP64E-FPELIM-NEXT:    call va5_aligned_stack_callee
; LP64E-FPELIM-NEXT:    ld ra, 48(sp) # 8-byte Folded Reload
; LP64E-FPELIM-NEXT:    addi sp, sp, 56
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va5_aligned_stack_caller:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -64
; LP64E-WITHFP-NEXT:    sd ra, 56(sp) # 8-byte Folded Spill
```

```
; LP64E-WITHFP-NEXT:    sd s0, 48(sp)
 # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 64
; LP64E-WITHFP-NEXT:    li a0, 17
; LP64E-WITHFP-NEXT:    sd a0, 40(sp)
; LP64E-WITHFP-NEXT:    li a0, 16
; LP64E-WITHFP-NEXT:    lui a1, %hi(.LCPI11_0)
; LP64E-WITHFP-NEXT:    ld a1, %lo(.LCPI11_0)(a1)
; LP64E-WITHFP-NEXT:    sd a0, 32(sp)
; LP64E-WITHFP-NEXT:    li a0, 15
; LP64E-WITHFP-NEXT:    sd a0, 24(sp)
; LP64E-WITHFP-NEXT:    sd a1, 16(sp)
; LP64E-WITHFP-NEXT:    li a0, 14
; LP64E-WITHFP-NEXT:    sd a0, 8(sp)
; LP64E-WITHFP-NEXT:    lui a0, 2384
; LP64E-WITHFP-NEXT:    addiw a0, a0, 761
; LP64E-WITHFP-NEXT:    slli a6, a0, 11
; LP64E-WITHFP-NEXT:    lui a0, %hi(.LCPI11_1)
; LP64E-WITHFP-NEXT:    ld a2, %lo(.LCPI11_1)(a0)
; LP64E-WITHFP-NEXT:    lui a0, %hi(.LCPI11_2)
; LP64E-WITHFP-NEXT:    ld a3, %lo(.LCPI11_2)(a0)
; LP64E-WITHFP-NEXT:    li a0, 1
; LP64E-WITHFP-NEXT:    li a1, 11
; LP64E-WITHFP-NEXT:    li a4, 12
; LP64E-WITHFP-NEXT:    li a5, 13
; LP64E-WITHFP-NEXT:    sd a6, 0(sp)
; LP64E-WITHFP-NEXT:    call va5_aligned_stack_callee
;
 LP64E-WITHFP-NEXT:    ld ra, 56(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 48(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 64
; LP64E-WITHFP-NEXT:    ret
 %1 = call i32 (i32, ...) @va5_aligned_stack_callee(i32 1, i32 11,
   fp128 0xLEB851EB851EB851F400091EB851EB851, i32 12, i32 13, i64 20000000000,
   i32 14, double 2.720000e+00, i32 15, [2 x i32] [i32 16, i32 17])
 ret void
}

; A function with no fixed arguments is not valid C, but can be
; specified in LLVM IR. We must ensure the vararg save area is
; still set up correctly.

define i32 @va6_no_fixed_args(...) nounwind {
; LP64-LP64F-LP64D-FPELIM-LABEL: va6_no_fixed_args:
; LP64-LP64F-LP64D-FPELIM:       # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, -80
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 72(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 64(sp)
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 56(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 48(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:
   sd a3, 40(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 32(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 24(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a0, 16(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi a1, sp, 24
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    addi sp, sp, 80
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va6_no_fixed_args:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -96
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 32
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
;
 LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a0, 0(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, s0, 8
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, -24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 96
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va6_no_fixed_args:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    addi sp, sp, -56
; LP64E-FPELIM-NEXT:    sd a5, 48(sp)
; LP64E-FPELIM-NEXT:    sd a4, 40(sp)
; LP64E-FPELIM-NEXT:    sd a3, 32(sp)
; LP64E-FPELIM-NEXT:    sd a2, 24(sp)
; LP64E-FPELIM-NEXT:    sd a1, 16(sp)
; LP64E-FPELIM-NEXT:    sd a0, 8(sp)
; LP64E-FPELIM-NEXT:    addi a1, sp, 16
; LP64E-FPELIM-NEXT:    sd a1, 0(sp)
; LP64E-FPELIM-NEXT:    addi sp, sp, 56
; LP64E-FPELIM-NEXT:    ret
;
```

```
; LP64E-WITHFP-LABEL: va6_no_fixed_args:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:
   addi sp, sp, -72
; LP64E-WITHFP-NEXT:    sd ra, 16(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 8(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    addi s0, sp, 24
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:    sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    sd a0, 0(s0)
; LP64E-WITHFP-NEXT:    addi a1, s0, 8
; LP64E-WITHFP-NEXT:    sd a1, -24(s0)
; LP64E-WITHFP-NEXT:    ld ra, 16(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 8(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 72
; LP64E-WITHFP-NEXT:    ret
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %1 = va_arg ptr %va, i32
 call void @llvm.va_end(ptr %va)
 ret i32 %1
}

; TODO: improve constant materialization of stack addresses

define i32 @va_large_stack(ptr %fmt, ...) {
; LP64-LP64F-LP64D-FPELIM-LABEL: va_large_stack:
; LP64-LP64F-LP64D-FPELIM:
      # %bb.0:
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a0, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    addiw a0, a0, 336
; LP64-LP64F-LP64D-FPELIM-NEXT:    sub sp, sp, a0
; LP64-LP64F-LP64D-FPELIM-NEXT:    .cfi_def_cfa_offset 100000080
; LP64-LP64F-LP64D-FPELIM-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui t0, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add t0, sp, t0
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a7, 328(t0)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a7, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a7, sp, a7
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a6, 320(a7)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a6, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a6, sp, a6
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a5, 312(a6)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a5, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a5, sp, a5
```

```
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a4, 304(a5)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a4, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a4, sp, a4
; LP64-LP64F-LP64D-FPELIM-NEXT:
   sd a3, 296(a4)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a3, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a3, sp, a3
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a2, 288(a3)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a2, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a2, sp, a2
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 280(a2)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a1, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    addiw a1, a1, 284
; LP64-LP64F-LP64D-FPELIM-NEXT:    add a1, sp, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    sd a1, 8(sp)
; LP64-LP64F-LP64D-FPELIM-NEXT:    lui a1, 24414
; LP64-LP64F-LP64D-FPELIM-NEXT:    addiw a1, a1, 336
; LP64-LP64F-LP64D-FPELIM-NEXT:    add sp, sp, a1
; LP64-LP64F-LP64D-FPELIM-NEXT:    ret
;
; LP64-LP64F-LP64D-WITHFP-LABEL: va_large_stack:
; LP64-LP64F-LP64D-WITHFP:       # %bb.0:
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, -2032
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_def_cfa_offset 2032
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd ra, 1960(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:
   sd s0, 1952(sp) # 8-byte Folded Spill
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_offset ra, -72
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_offset s0, -80
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi s0, sp, 1968
; LP64-LP64F-LP64D-WITHFP-NEXT:    .cfi_def_cfa s0, 64
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a0, 24414
; LP64-LP64F-LP64D-WITHFP-NEXT:    addiw a0, a0, -1680
; LP64-LP64F-LP64D-WITHFP-NEXT:    sub sp, sp, a0
; LP64-LP64F-LP64D-WITHFP-NEXT:    mv a0, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a7, 56(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a6, 48(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a5, 40(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a4, 32(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a3, 24(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a2, 16(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    sd a1, 8(s0)
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi a1, s0, 12
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a2, 24414
; LP64-LP64F-LP64D-WITHFP-NEXT:    sub a2, s0, a2
; LP64-LP64F-LP64D-WITHFP-NEXT:
   sd a1, -288(a2)
; LP64-LP64F-LP64D-WITHFP-NEXT:    lui a1, 24414
```

```
; LP64-LP64F-LP64D-WITHFP-NEXT:    addiw a1, a1, -1680
; LP64-LP64F-LP64D-WITHFP-NEXT:    add sp, sp, a1
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld ra, 1960(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    ld s0, 1952(sp) # 8-byte Folded Reload
; LP64-LP64F-LP64D-WITHFP-NEXT:    addi sp, sp, 2032
; LP64-LP64F-LP64D-WITHFP-NEXT:    ret
;
; LP64E-FPELIM-LABEL: va_large_stack:
; LP64E-FPELIM:       # %bb.0:
; LP64E-FPELIM-NEXT:    lui a0, 24414
; LP64E-FPELIM-NEXT:    addiw a0, a0, 320
; LP64E-FPELIM-NEXT:    sub sp, sp, a0
; LP64E-FPELIM-NEXT:    .cfi_def_cfa_offset 100000064
; LP64E-FPELIM-NEXT:    mv a0, a1
; LP64E-FPELIM-NEXT:    lui a6, 24414
; LP64E-FPELIM-NEXT:    add a6, sp, a6
; LP64E-FPELIM-NEXT:    sd a5, 312(a6)
; LP64E-FPELIM-NEXT:    lui a5, 24414
; LP64E-FPELIM-NEXT:    add a5, sp, a5
; LP64E-FPELIM-NEXT:    sd a4, 304(a5)
; LP64E-FPELIM-NEXT:    lui a4, 24414
; LP64E-FPELIM-NEXT:    add a4,
 sp, a4
; LP64E-FPELIM-NEXT:    sd a3, 296(a4)
; LP64E-FPELIM-NEXT:    lui a3, 24414
; LP64E-FPELIM-NEXT:    add a3, sp, a3
; LP64E-FPELIM-NEXT:    sd a2, 288(a3)
; LP64E-FPELIM-NEXT:    lui a2, 24414
; LP64E-FPELIM-NEXT:    add a2, sp, a2
; LP64E-FPELIM-NEXT:    sd a1, 280(a2)
; LP64E-FPELIM-NEXT:    lui a1, 24414
; LP64E-FPELIM-NEXT:    addiw a1, a1, 284
; LP64E-FPELIM-NEXT:    add a1, sp, a1
; LP64E-FPELIM-NEXT:    sd a1, 8(sp)
; LP64E-FPELIM-NEXT:    lui a1, 24414
; LP64E-FPELIM-NEXT:    addiw a1, a1, 320
; LP64E-FPELIM-NEXT:    add sp, sp, a1
; LP64E-FPELIM-NEXT:    ret
;
; LP64E-WITHFP-LABEL: va_large_stack:
; LP64E-WITHFP:       # %bb.0:
; LP64E-WITHFP-NEXT:    addi sp, sp, -2040
; LP64E-WITHFP-NEXT:    .cfi_def_cfa_offset 2040
; LP64E-WITHFP-NEXT:    sd ra, 1984(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    sd s0, 1976(sp) # 8-byte Folded Spill
; LP64E-WITHFP-NEXT:    .cfi_offset ra, -56
; LP64E-WITHFP-NEXT:    .cfi_offset s0, -64
; LP64E-WITHFP-NEXT:    addi s0, sp, 1992
```

```
;
 LP64E-WITHFP-NEXT:    .cfi_def_cfa s0, 48
; LP64E-WITHFP-NEXT:    lui a0, 24414
; LP64E-WITHFP-NEXT:    addiw a0, a0, -1704
; LP64E-WITHFP-NEXT:    sub sp, sp, a0
; LP64E-WITHFP-NEXT:    mv a0, a1
; LP64E-WITHFP-NEXT:    sd a5, 40(s0)
; LP64E-WITHFP-NEXT:    sd a4, 32(s0)
; LP64E-WITHFP-NEXT:    sd a3, 24(s0)
; LP64E-WITHFP-NEXT:    sd a2, 16(s0)
; LP64E-WITHFP-NEXT:    sd a1, 8(s0)
; LP64E-WITHFP-NEXT:    addi a1, s0, 12
; LP64E-WITHFP-NEXT:    lui a2, 24414
; LP64E-WITHFP-NEXT:    sub a2, s0, a2
; LP64E-WITHFP-NEXT:    sd a1, -288(a2)
; LP64E-WITHFP-NEXT:    lui a1, 24414
; LP64E-WITHFP-NEXT:    addiw a1, a1, -1704
; LP64E-WITHFP-NEXT:    add sp, sp, a1
; LP64E-WITHFP-NEXT:    ld ra, 1984(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    ld s0, 1976(sp) # 8-byte Folded Reload
; LP64E-WITHFP-NEXT:    addi sp, sp, 2040
; LP64E-WITHFP-NEXT:    ret
 %large = alloca [ 100000000 x i8 ]
 %va = alloca ptr
 call void @llvm.va_start(ptr %va)
 %argp.cur = load ptr, ptr %va, align 4
 %argp.next
 = getelementptr inbounds i8, ptr %argp.cur, i32 4
 store ptr %argp.next, ptr %va, align 4
 %1 = load i32, ptr %argp.cur, align 4
 call void @llvm.va_end(ptr %va)
 ret i32 %1
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+d -mattr=+xtheadmemidx -mattr=+m -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64XTHEADMEMIDX

define ptr @lbia(ptr %base, ptr %addr.2, i8 %a) {
; RV64XTHEADMEMIDX-LABEL: lbia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lbia a3, (a0), -1, 0
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sb a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i8, ptr %base, i8 0
 %ld = load i8, ptr %addr
 %addr.1 = getelementptr i8, ptr %base, i8 -1
 %res = add i8 %ld, %a
```

```
 store i8 %res, ptr %addr.2
 ret ptr %addr.1
}

define ptr @lbib(ptr %base, i8 %a) {
; RV64XTHEADMEMIDX-LABEL: lbib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lbib a2, (a0), 1, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a2, a1
; RV64XTHEADMEMIDX-NEXT:    sb a1, 1(a0)
; RV64XTHEADMEMIDX-NEXT:
   ret
 %addr = getelementptr i8, ptr %base, i8 1
 %ld = load i8, ptr %addr
 %addr.1 = getelementptr i8, ptr %base, i8 2
 %res = add i8 %ld, %a
 store i8 %res, ptr %addr.1
 ret ptr %addr
}

define ptr @lbuia(ptr %base, ptr %addr.2, i64 %a) {
; RV64XTHEADMEMIDX-LABEL: lbuia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lbuia a3, (a0), -1, 0
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sd a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i8, ptr %base, i8 0
 %ld = load i8, ptr %addr
 %zext = zext i8 %ld to i64
 %addr.1 = getelementptr i8, ptr %base, i8 -1
 %res = add i64 %zext, %a
 store i64 %res, ptr %addr.2
 ret ptr %addr.1
}

define ptr @lbuib(ptr %base, i64 %a, ptr %addr.1) {
; RV64XTHEADMEMIDX-LABEL: lbuib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lbuib a3, (a0), 1, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a3, a1
; RV64XTHEADMEMIDX-NEXT:    sd a1, 0(a2)
; RV64XTHEADMEMIDX-NEXT:
  ret
 %addr = getelementptr i8, ptr %base, i8 1
 %ld = load i8, ptr %addr
 %zext = zext i8 %ld to i64
 %res = add i64 %zext, %a
```

```
 store i64 %res, ptr %addr.1
 ret ptr %addr
}

define ptr @lhia(ptr %base, ptr %addr.2, i16 %a) {
; RV64XTHEADMEMIDX-LABEL: lhia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lhia a3, (a0), -16, 1
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sh a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i16, ptr %base, i16 0
 %ld = load i16, ptr %addr
 %addr.1 = getelementptr i16, ptr %base, i16 -16
 %res = add i16 %ld, %a
 store i16 %res, ptr %addr.2
 ret ptr %addr.1
}

define ptr @lhib(ptr %base, i16 %a) {
; RV64XTHEADMEMIDX-LABEL: lhib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lhib a2, (a0), 2, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a2, a1
; RV64XTHEADMEMIDX-NEXT:    sh a1, 2(a0)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i16, ptr %base, i16 1
 %ld = load i16,
 ptr %addr
 %addr.1 = getelementptr i16, ptr %base, i16 2
 %res = add i16 %ld, %a
 store i16 %res, ptr %addr.1
 ret ptr %addr
}

define ptr @lhuia(ptr %base, ptr %addr.2, i64 %a) {
; RV64XTHEADMEMIDX-LABEL: lhuia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lhuia a3, (a0), -16, 1
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sd a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i16, ptr %base, i16 0
 %ld = load i16, ptr %addr
 %zext = zext i16 %ld to i64
 %addr.1 = getelementptr i16, ptr %base, i16 -16
 %res = add i64 %zext, %a
 store i64 %res, ptr %addr.2
```

```
 ret ptr %addr.1
}

define ptr @lhuib(ptr %base, i64 %a, ptr %addr.1) {
; RV64XTHEADMEMIDX-LABEL: lhuib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lhuib a3, (a0), 2, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a3, a1
; RV64XTHEADMEMIDX-NEXT:    sd a1, 0(a2)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i16, ptr %base, i16 1
 %ld =
 load i16, ptr %addr
 %zext = zext i16 %ld to i64
 %res = add i64 %zext, %a
 store i64 %res, ptr %addr.1
 ret ptr %addr
}

define ptr @lwia(ptr %base, ptr %addr.2, i32 %a) {
; RV64XTHEADMEMIDX-LABEL: lwia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lwia a3, (a0), -16, 2
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sw a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i32, ptr %base, i32 0
 %ld = load i32, ptr %addr
 %addr.1 = getelementptr i32, ptr %base, i32 -16
 %res = add i32 %ld, %a
 store i32 %res, ptr %addr.2
 ret ptr %addr.1
}

define ptr @lwib(ptr %base, i32 %a) {
; RV64XTHEADMEMIDX-LABEL: lwib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lwib a2, (a0), 4, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a2, a1
; RV64XTHEADMEMIDX-NEXT:    sw a1, 4(a0)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i32, ptr %base, i32 1
 %ld = load i32, ptr %addr
 %addr.1 = getelementptr i32, ptr %base,
 i32 2
 %res = add i32 %ld, %a
 store i32 %res, ptr %addr.1
 ret ptr %addr
```

```
}

define ptr @lwuia(ptr %base, ptr %addr.2, i64 %a) {
; RV64XTHEADMEMIDX-LABEL: lwuia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lwuia a3, (a0), -16, 2
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sd a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i32, ptr %base, i32 0
 %ld = load i32, ptr %addr
 %zext = zext i32 %ld to i64
 %addr.1 = getelementptr i32, ptr %base, i32 -16
 %res = add i64 %zext, %a
 store i64 %res, ptr %addr.2
 ret ptr %addr.1
}

define ptr @lwuib(ptr %base, i64 %a, ptr %addr.1) {
; RV64XTHEADMEMIDX-LABEL: lwuib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lwuib a3, (a0), 4, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a3, a1
; RV64XTHEADMEMIDX-NEXT:    sd a1, 0(a2)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i32, ptr %base, i32 1
 %ld = load i32, ptr %addr
 %zext = zext i32 %ld to i64
 %res
 = add i64 %zext, %a
 store i64 %res, ptr %addr.1
 ret ptr %addr
}

define ptr @ldia(ptr %base, ptr %addr.2, i64 %a) {
; RV64XTHEADMEMIDX-LABEL: ldia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.ldia a3, (a0), -16, 3
; RV64XTHEADMEMIDX-NEXT:    add a2, a3, a2
; RV64XTHEADMEMIDX-NEXT:    sd a2, 0(a1)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i64, ptr %base, i64 0
 %ld = load i64, ptr %addr
 %addr.1 = getelementptr i64, ptr %base, i64 -16
 %res = add i64 %ld, %a
 store i64 %res, ptr %addr.2
 ret ptr %addr.1
}
```

```
define ptr @ldib(ptr %base, i64 %a) {
; RV64XTHEADMEMIDX-LABEL: ldib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.ldib a2, (a0), 8, 0
; RV64XTHEADMEMIDX-NEXT:    add a1, a2, a1
; RV64XTHEADMEMIDX-NEXT:    sd a1, 8(a0)
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr = getelementptr i64, ptr %base, i64 1
 %ld = load i64, ptr %addr
 %addr.1 = getelementptr i64, ptr %base, i64 2
 %res = add i64 %ld, %a
 store i64 %res, ptr %addr.1
  ret ptr %addr
}

define ptr @sbia(ptr %base, i8 %a, i8 %b) {
; RV64XTHEADMEMIDX-LABEL: sbia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.sbia a1, (a0), 1, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i8, ptr %base, i8 1
 %res = add i8 %a, %b
 store i8 %res, ptr %base
 ret ptr %addr.1
}

define ptr @sbib(ptr %base, i8 %a, i8 %b) {
; RV64XTHEADMEMIDX-LABEL: sbib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.sbib a1, (a0), 1, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i8, ptr %base, i8 1
 %res = add i8 %a, %b
 store i8 %res, ptr %addr.1
 ret ptr %addr.1
}

define ptr @shia(ptr %base, i16 %a, i16 %b) {
; RV64XTHEADMEMIDX-LABEL: shia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.shia a1, (a0), -9, 1
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr
```

```
 i16, ptr %base, i16 -9
 %res = add i16 %a, %b
 store i16 %res, ptr %base
 ret ptr %addr.1
}

define ptr @shib(ptr %base, i16 %a, i16 %b) {
; RV64XTHEADMEMIDX-LABEL: shib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.shib a1, (a0), 2, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i16, ptr %base, i16 1
 %res = add i16 %a, %b
 store i16 %res, ptr %addr.1
 ret ptr %addr.1
}

define ptr @swia(ptr %base, i32 %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: swia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.swia a1, (a0), 8, 2
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i32, ptr %base, i32 8
 %res = add i32 %a, %b
 store i32 %res, ptr %base
 ret ptr %addr.1
}

define ptr @swib(ptr %base, i32 %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: swib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:
   th.swib a1, (a0), -13, 3
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i32, ptr %base, i32 -26
 %res = add i32 %a, %b
 store i32 %res, ptr %addr.1
 ret ptr %addr.1
}

define ptr @sdia(ptr %base, i64 %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: sdia:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    add a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.sdia a1, (a0), 8, 3
```

```
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i64, ptr %base, i64 8
 %res = add i64 %a, %b
 store i64 %res, ptr %base
 ret ptr %addr.1
}

define ptr @sdib(ptr %base, i64 %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: sdib:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    add a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.sdib a1, (a0), 8, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i64, ptr %base, i64 1
 %res = add i64 %a, %b
 store i64 %res, ptr %addr.1
 ret ptr %addr.1
}

define i8 @lrb_anyext(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrb_anyext:
; RV64XTHEADMEMIDX:
      # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrb a0, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i64 %b
 %2 = load i8, ptr %1, align 1
 ret i8 %2
}

define i64 @lrb(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrb:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrb a0, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i64 %b
 %2 = load i8, ptr %1, align 1
 %3 = sext i8 %2 to i64
 %4 = add i64 %3, %3
 ret i64 %4
}

define i8 @lurb_anyext(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurb_anyext:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurb a0, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    ret
```

```
 %1 = zext i32 %b to i64
 %2 = getelementptr i8, ptr %a, i64 %1
 %3 = load i8, ptr %2, align 1
 ret i8 %3
}

define i64 @lurb(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurb:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurb
 a0, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i8, ptr %a, i64 %1
 %3 = load i8, ptr %2, align 1
 %4 = sext i8 %3 to i64
 %5 = add i64 %4, %4
 ret i64 %5
}

define i64 @lrbu(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrbu:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrbu a0, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i64 %b
 %2 = load i8, ptr %1, align 1
 %3 = zext i8 %2 to i64
 %4 = add i64 %3, %3
 ret i64 %4
}

define i64 @lurbu(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurbu:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurbu a0, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i8, ptr %a, i64 %1
 %3 = load i8, ptr %2, align 1
 %4 = zext i8 %3 to i64
 %5 = add i64 %4, %4
 ret i64 %5
}
```

```
define
 i16 @lrh_anyext(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrh_anyext:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrh a0, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i64 %b
 %2 = load i16, ptr %1, align 2
 ret i16 %2
}

define i64 @lrh(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrh:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrh a0, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i64 %b
 %2 = load i16, ptr %1, align 2
 %3 = sext i16 %2 to i64
 %4 = add i64 %3, %3
 ret i64 %4
}

define i16 @lurh_anyext(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurh_anyext:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurh a0, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i16, ptr %a, i64 %1
 %3 = load i16, ptr %2, align 2
 ret i16 %3
}

define i64 @lurh(ptr %a, i32 %b) {
;
 RV64XTHEADMEMIDX-LABEL: lurh:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurh a0, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i16, ptr %a, i64 %1
 %3 = load i16, ptr %2, align 2
 %4 = sext i16 %3 to i64
 %5 = add i64 %4, %4
 ret i64 %5
```

```
}

define i64 @lrhu(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrhu:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrhu a0, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i64 %b
 %2 = load i16, ptr %1, align 2
 %3 = zext i16 %2 to i64
 %4 = add i64 %3, %3
 ret i64 %4
}

define i64 @lurhu(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurhu:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurhu a0, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i16,
 ptr %a, i64 %1
 %3 = load i16, ptr %2, align 2
 %4 = zext i16 %3 to i64
 %5 = add i64 %4, %4
 ret i64 %5
}

define i32 @lrw_anyext(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrw_anyext:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrw a0, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i64 %b
 %2 = load i32, ptr %1, align 4
 ret i32 %2
}

define i64 @lrw(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrw:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrw a0, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i64 %b
 %2 = load i32, ptr %1, align 4
```

```
 %3 = sext i32 %2 to i64
 %4 = add i64 %3, %3
 ret i64 %4
}

define i32 @lurw_anyext(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurw_anyext:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurw a0, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr
 i32, ptr %a, i64 %1
 %3 = load i32, ptr %2, align 4
 ret i32 %3
}

define i64 @lurw(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurw:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurw a0, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i32, ptr %a, i64 %1
 %3 = load i32, ptr %2, align 4
 %4 = sext i32 %3 to i64
 %5 = add i64 %4, %4
 ret i64 %5
}

define i64 @lrwu(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrwu:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrwu a0, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i64 %b
 %2 = load i32, ptr %1, align 4
 %3 = zext i32 %2 to i64
 %4 = add i64 %3, %3
 ret i64 %4
}

define i64 @lurwu(ptr %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: lurwu:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurwu a0, a0, a1, 2
```

```
; RV64XTHEADMEMIDX-NEXT:
   add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i32, ptr %a, i64 %1
 %3 = load i32, ptr %2, align 4
 %4 = zext i32 %3 to i64
 %5 = add i64 %4, %4
 ret i64 %5
}

define i64 @lrd(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrd:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lrd a0, a0, a1, 3
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = getelementptr i64, ptr %a, i64 %b
 %2 = load i64, ptr %1, align 8
 %3 = add i64 %2, %2
 ret i64 %3
}

define i64 @lrd_2(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrd_2:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addi a0, a0, 96
; RV64XTHEADMEMIDX-NEXT:    th.lrd a0, a0, a1, 3
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add i64 %b, 12
 %2 = getelementptr i64, ptr %a, i64 %1
 %3 = load i64, ptr %2, align 8
 %4 = add i64 %3, %3
 ret i64 %4
}

define i64 @lurd(ptr %a, i32 %b)
 {
; RV64XTHEADMEMIDX-LABEL: lurd:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    th.lurd a0, a0, a1, 3
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = getelementptr i64, ptr %a, i64 %1
 %3 = load i64, ptr %2, align 8
 %4 = add i64 %3, %3
```

```
 ret i64 %4
}

define void @srb(ptr %a, i64 %b, i8 %c) {
; RV64XTHEADMEMIDX-LABEL: srb:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.srb a2, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add i8 %c, %c
 %2 = getelementptr i8, ptr %a, i64 %b
 store i8 %1, ptr %2, align 1
 ret void
}

define void @surb(ptr %a, i32 %b, i8 %c) {
; RV64XTHEADMEMIDX-LABEL: surb:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.surb a2, a0, a1, 0
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = add i8 %c, %c
 %3 = getelementptr i8, ptr %a, i64 %1
 store
 i8 %2, ptr %3, align 1
 ret void
}

define void @srh(ptr %a, i64 %b, i16 %c) {
; RV64XTHEADMEMIDX-LABEL: srh:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.srh a2, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add i16 %c, %c
 %2 = getelementptr i16, ptr %a, i64 %b
 store i16 %1, ptr %2, align 2
 ret void
}

define void @surh(ptr %a, i32 %b, i16 %c) {
; RV64XTHEADMEMIDX-LABEL: surh:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.surh a2, a0, a1, 1
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
```

```
 %2 = add i16 %c, %c
 %3 = getelementptr i16, ptr %a, i64 %1
 store i16 %2, ptr %3, align 2
 ret void
}

define void @srw(ptr %a, i64 %b, i32 %c) {
; RV64XTHEADMEMIDX-LABEL: srw:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.srw a2, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add
 i32 %c, %c
 %2 = getelementptr i32, ptr %a, i64 %b
 store i32 %1, ptr %2, align 4
 ret void
}

define void @surw(ptr %a, i32 %b, i32 %c) {
; RV64XTHEADMEMIDX-LABEL: surw:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.surw a2, a0, a1, 2
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = add i32 %c, %c
 %3 = getelementptr i32, ptr %a, i64 %1
 store i32 %2, ptr %3, align 4
 ret void
}

define void @srd(ptr %a, i64 %b, i64 %c) {
; RV64XTHEADMEMIDX-LABEL: srd:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    add a2, a2, a2
; RV64XTHEADMEMIDX-NEXT:    th.srd a2, a0, a1, 3
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add i64 %c, %c
 %2 = getelementptr i64, ptr %a, i64 %b
 store i64 %1, ptr %2, align 8
 ret void
}

define void @surd(ptr %a, i32 %b, i64 %c) {
; RV64XTHEADMEMIDX-LABEL: surd:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    add a2, a2, a2
```

```
; RV64XTHEADMEMIDX-NEXT:    th.surd
 a2, a0, a1, 3
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = zext i32 %b to i64
 %2 = add i64 %c, %c
 %3 = getelementptr i64, ptr %a, i64 %1
 store i64 %2, ptr %3, align 8
 ret void
}

define ptr @test_simm5(ptr %base, i32 %a, i32 %b) {
; RV64XTHEADMEMIDX-LABEL: test_simm5:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    addw a1, a1, a2
; RV64XTHEADMEMIDX-NEXT:    th.swia a1, (a0), -12, 2
; RV64XTHEADMEMIDX-NEXT:    ret
 %addr.1 = getelementptr i32, ptr %base, i32 -12
 %res = add i32 %a, %b
 store i32 %res, ptr %base
 ret ptr %addr.1
}

define i64 @lrd_large_shift(ptr %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrd_large_shift:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    slli a1, a1, 5
; RV64XTHEADMEMIDX-NEXT:    add a0, a1, a0
; RV64XTHEADMEMIDX-NEXT:    ld a0, 384(a0)
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add i64 %b, 12
 %2 = shl i64 %1, 2
 %3 = getelementptr i64, ptr %a, i64 %2
 %4 = load i64, ptr %3, align 8
 ret i64 %4
}

define i64 @lrd_large_offset(ptr
 %a, i64 %b) {
; RV64XTHEADMEMIDX-LABEL: lrd_large_offset:
; RV64XTHEADMEMIDX:       # %bb.0:
; RV64XTHEADMEMIDX-NEXT:    slli a1, a1, 3
; RV64XTHEADMEMIDX-NEXT:    add a0, a1, a0
; RV64XTHEADMEMIDX-NEXT:    lui a1, 23
; RV64XTHEADMEMIDX-NEXT:    add a0, a0, a1
; RV64XTHEADMEMIDX-NEXT:    ld a0, 1792(a0)
; RV64XTHEADMEMIDX-NEXT:    ret
 %1 = add i64 %b, 12000
 %2 = getelementptr i64, ptr %a, i64 %1
```

```
 %3 = load i64, ptr %2, align 8
 ret i64 %3
}
```

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```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m -verify-machineinstrs \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zba -verify-machineinstrs \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64ZBA
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zicond -verify-machineinstrs \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64ZICOND

;
; Get the actual value of the overflow bit.
;
define zeroext i1 @saddo1.i32(i32 signext %v1, i32 signext %v2, ptr %res) {
; RV64-LABEL: saddo1.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a3, a0, a1
; RV64-NEXT:    add a1, a0, a1
; RV64-NEXT:    xor a3, a1, a3
; RV64-NEXT:    snez a0, a3
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo1.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a3, a0, a1
; RV64ZBA-NEXT:
   add a1, a0, a1
; RV64ZBA-NEXT:    xor a3, a1, a3
; RV64ZBA-NEXT:    snez a0, a3
; RV64ZBA-NEXT:    sw a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo1.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a3, a0, a1
; RV64ZICOND-NEXT:    add a1, a0, a1
; RV64ZICOND-NEXT:    xor a3, a1, a3
; RV64ZICOND-NEXT:    snez a0, a3
; RV64ZICOND-NEXT:    sw a1, 0(a2)
; RV64ZICOND-NEXT:    ret
```

```
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

; Test the immediate version.
define zeroext i1 @saddo2.i32(i32 signext %v1, ptr %res) {
; RV64-LABEL: saddo2.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addiw a2, a0, 4
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo2.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addiw a2, a0, 4
; RV64ZBA-NEXT:
  slt a0, a2, a0
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo2.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addiw a2, a0, 4
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 4)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

; Test negative immediates.
define zeroext i1 @saddo3.i32(i32 signext %v1, ptr %res) {
; RV64-LABEL: saddo3.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addiw a2, a0, -4
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: saddo3.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addiw a2, a0, -4
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL:
 saddo3.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addiw a2, a0, -4
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 -4)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

; Test immediates that are too large to be encoded.
define zeroext i1 @saddo4.i32(i32 signext %v1, ptr %res) {
; RV64-LABEL: saddo4.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    lui a2, 4096
; RV64-NEXT:    addi a2, a2, -1
; RV64-NEXT:    addw a2, a0, a2
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo4.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    lui a2, 4096
; RV64ZBA-NEXT:    addi a2, a2, -1
; RV64ZBA-NEXT:    addw a2, a0, a2
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    sw a2,
 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo4.i32:
; RV64ZICOND:       # %bb.0: # %entry
```

```
; RV64ZICOND-NEXT:    lui a2, 4096
; RV64ZICOND-NEXT:    addi a2, a2, -1
; RV64ZICOND-NEXT:    addw a2, a0, a2
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 16777215)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @saddo1.i64(i64 %v1, i64 %v2, ptr %res) {
; RV64-LABEL: saddo1.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a3, a0, a1
; RV64-NEXT:    slt a0, a3, a0
; RV64-NEXT:    slti a1, a1, 0
; RV64-NEXT:    xor a0, a1, a0
; RV64-NEXT:    sd a3, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo1.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a3, a0, a1
; RV64ZBA-NEXT:    slt a0, a3, a0
; RV64ZBA-NEXT:    slti a1, a1, 0
; RV64ZBA-NEXT:    xor a0,
 a1, a0
; RV64ZBA-NEXT:    sd a3, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo1.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a3, a0, a1
; RV64ZICOND-NEXT:    slt a0, a3, a0
; RV64ZICOND-NEXT:    slti a1, a1, 0
; RV64ZICOND-NEXT:    xor a0, a1, a0
; RV64ZICOND-NEXT:    sd a3, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.sadd.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
```

```
}

define zeroext i1 @saddo2.i64(i64 %v1, ptr %res) {
; RV64-LABEL: saddo2.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addi a2, a0, 4
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo2.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addi a2, a0, 4
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    sd a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo2.i64:
; RV64ZICOND:
    # %bb.0: # %entry
; RV64ZICOND-NEXT:    addi a2, a0, 4
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.sadd.with.overflow.i64(i64 %v1, i64 4)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @saddo3.i64(i64 %v1, ptr %res) {
; RV64-LABEL: saddo3.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addi a2, a0, -4
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo3.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addi a2, a0, -4
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    sd a2, 0(a1)
; RV64ZBA-NEXT:    ret
```

```
;
; RV64ZICOND-LABEL: saddo3.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addi a2, a0, -4
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    xori
 a0, a0, 1
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.sadd.with.overflow.i64(i64 %v1, i64 -4)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @uaddo.i32(i32 signext %v1, i32 signext %v2, ptr %res) {
; RV64-LABEL: uaddo.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a1, a0, a1
; RV64-NEXT:    sltu a0, a1, a0
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a1, a0
; RV64ZBA-NEXT:    sw a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a1, a0
; RV64ZICOND-NEXT:    sw a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.uadd.with.overflow.i32(i32 %v1, i32 %v2)
 %val =
 extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @uaddo.i32.constant(i32 signext %v1, ptr %res) {
```

```
; RV64-LABEL: uaddo.i32.constant:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addiw a2, a0, -2
; RV64-NEXT:    sltu a0, a2, a0
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i32.constant:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addiw a2, a0, -2
; RV64ZBA-NEXT:    sltu a0, a2, a0
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i32.constant:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addiw a2, a0, -2
; RV64ZICOND-NEXT:    sltu a0, a2, a0
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.uadd.with.overflow.i32(i32 %v1, i32 -2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @uaddo.i32.constant_one(i32
 signext %v1, ptr %res) {
; RV64-LABEL: uaddo.i32.constant_one:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addiw a2, a0, 1
; RV64-NEXT:    seqz a0, a2
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i32.constant_one:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addiw a2, a0, 1
; RV64ZBA-NEXT:    seqz a0, a2
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i32.constant_one:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addiw a2, a0, 1
; RV64ZICOND-NEXT:    seqz a0, a2
```

```
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.uadd.with.overflow.i32(i32 %v1, i32 1)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @uaddo.i64(i64 %v1, i64 %v2, ptr %res) {
; RV64-LABEL: uaddo.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a1, a0, a1
; RV64-NEXT:
 sltu a0, a1, a0
; RV64-NEXT:    sd a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a1, a0
; RV64ZBA-NEXT:    sd a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a1, a0
; RV64ZICOND-NEXT:    sd a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @uaddo.i64.constant_one(i64 %v1, ptr %res) {
; RV64-LABEL: uaddo.i64.constant_one:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addi a2, a0, 1
; RV64-NEXT:    seqz a0, a2
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
```

```
; RV64ZBA-LABEL: uaddo.i64.constant_one:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addi
 a2, a0, 1
; RV64ZBA-NEXT:    seqz a0, a2
; RV64ZBA-NEXT:    sd a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i64.constant_one:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addi a2, a0, 1
; RV64ZICOND-NEXT:    seqz a0, a2
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 1)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @ssubo1.i32(i32 signext %v1, i32 signext %v2, ptr %res) {
; RV64-LABEL: ssubo1.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a3, a0, a1
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    xor a3, a1, a3
; RV64-NEXT:    snez a0, a3
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo1.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a3, a0, a1
; RV64ZBA-NEXT:    sub a1, a0, a1
; RV64ZBA-NEXT:    xor a3, a1, a3
; RV64ZBA-NEXT:
   snez a0, a3
; RV64ZBA-NEXT:    sw a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo1.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a3, a0, a1
; RV64ZICOND-NEXT:    sub a1, a0, a1
; RV64ZICOND-NEXT:    xor a3, a1, a3
; RV64ZICOND-NEXT:    snez a0, a3
```

```
; RV64ZICOND-NEXT:    sw a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.ssub.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @ssubo2.i32(i32 signext %v1, ptr %res) {
; RV64-LABEL: ssubo2.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addiw a2, a0, 4
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo2.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addiw a2, a0, 4
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo2.i32:
;
 RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addiw a2, a0, 4
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.ssub.with.overflow.i32(i32 %v1, i32 -4)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @ssubo.i64(i64 %v1, i64 %v2, ptr %res) {
; RV64-LABEL: ssubo.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sgtz a3, a1
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    slt a0, a1, a0
; RV64-NEXT:    xor a0, a3, a0
; RV64-NEXT:    sd a1, 0(a2)
```

```
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sgtz a3, a1
; RV64ZBA-NEXT:    sub a1, a0, a1
; RV64ZBA-NEXT:    slt a0, a1, a0
; RV64ZBA-NEXT:    xor a0, a3, a0
; RV64ZBA-NEXT:    sd a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:
   sgtz a3, a1
; RV64ZICOND-NEXT:    sub a1, a0, a1
; RV64ZICOND-NEXT:    slt a0, a1, a0
; RV64ZICOND-NEXT:    xor a0, a3, a0
; RV64ZICOND-NEXT:    sd a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.ssub.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @usubo.i32(i32 signext %v1, i32 signext %v2, ptr %res) {
; RV64-LABEL: usubo.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a1, a0, a1
; RV64-NEXT:    sltu a0, a0, a1
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a0, a1
; RV64ZBA-NEXT:    sw a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a0, a1
; RV64ZICOND-NEXT:    sw a1, 0(a2)
```

```
;
 RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.usub.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @usubo.i32.constant.rhs(i32 signext %v1, ptr %res) {
; RV64-LABEL: usubo.i32.constant.rhs:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addiw a2, a0, 2
; RV64-NEXT:    sltu a0, a0, a2
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.i32.constant.rhs:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addiw a2, a0, 2
; RV64ZBA-NEXT:    sltu a0, a0, a2
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.i32.constant.rhs:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addiw a2, a0, 2
; RV64ZICOND-NEXT:    sltu a0, a0, a2
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.usub.with.overflow.i32(i32 %v1, i32 -2)
 %val = extractvalue
 {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @usubo.i32.constant.lhs(i32 signext %v1, ptr %res) {
; RV64-LABEL: usubo.i32.constant.lhs:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    li a2, -2
; RV64-NEXT:    subw a2, a2, a0
; RV64-NEXT:    addi a0, a2, 1
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    sw a2, 0(a1)
```

```
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.i32.constant.lhs:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    li a2, -2
; RV64ZBA-NEXT:    subw a2, a2, a0
; RV64ZBA-NEXT:    addi a0, a2, 1
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.i32.constant.lhs:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    li a2, -2
; RV64ZICOND-NEXT:    subw a2, a2, a0
; RV64ZICOND-NEXT:    addi a0, a2, 1
; RV64ZICOND-NEXT:    seqz a0, a0
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.usub.with.overflow.i32(i32
 -2, i32 %v1)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @usubo.i64(i64 %v1, i64 %v2, ptr %res) {
; RV64-LABEL: usubo.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    sltu a0, a0, a1
; RV64-NEXT:    sd a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sub a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a0, a1
; RV64ZBA-NEXT:    sd a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    sub a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a0, a1
; RV64ZICOND-NEXT:    sd a1, 0(a2)
```

```
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.usub.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @smulo.i32(i32
 signext %v1, i32 signext %v2, ptr %res) {
; RV64-LABEL: smulo.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulw a3, a0, a1
; RV64-NEXT:    mul a1, a0, a1
; RV64-NEXT:    xor a3, a1, a3
; RV64-NEXT:    snez a0, a3
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulw a3, a0, a1
; RV64ZBA-NEXT:    mul a1, a0, a1
; RV64ZBA-NEXT:    xor a3, a1, a3
; RV64ZBA-NEXT:    snez a0, a3
; RV64ZBA-NEXT:    sw a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulw a3, a0, a1
; RV64ZICOND-NEXT:    mul a1, a0, a1
; RV64ZICOND-NEXT:    xor a3, a1, a3
; RV64ZICOND-NEXT:    snez a0, a3
; RV64ZICOND-NEXT:    sw a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.smul.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define
 zeroext i1 @smulo2.i32(i32 signext %v1, ptr %res) {
; RV64-LABEL: smulo2.i32:
```

```
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    li a2, 13
; RV64-NEXT:    mulw a3, a0, a2
; RV64-NEXT:    mul a2, a0, a2
; RV64-NEXT:    xor a3, a2, a3
; RV64-NEXT:    snez a0, a3
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo2.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sh1add a2, a0, a0
; RV64ZBA-NEXT:    sh2add a2, a2, a0
; RV64ZBA-NEXT:    sext.w a0, a2
; RV64ZBA-NEXT:    xor a0, a2, a0
; RV64ZBA-NEXT:    snez a0, a0
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo2.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    li a2, 13
; RV64ZICOND-NEXT:    mulw a3, a0, a2
; RV64ZICOND-NEXT:    mul a2, a0, a2
; RV64ZICOND-NEXT:    xor a3, a2, a3
; RV64ZICOND-NEXT:    snez a0, a3
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.smul.with.overflow.i32(i32 %v1, i32 13)
  %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @smulo.i64(i64 %v1, i64 %v2, ptr %res) {
; RV64-LABEL: smulo.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulh a3, a0, a1
; RV64-NEXT:    mul a1, a0, a1
; RV64-NEXT:    srai a0, a1, 63
; RV64-NEXT:    xor a0, a3, a0
; RV64-NEXT:    snez a0, a0
; RV64-NEXT:    sd a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.i64:
```

```
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulh a3, a0, a1
; RV64ZBA-NEXT:    mul a1, a0, a1
; RV64ZBA-NEXT:    srai a0, a1, 63
; RV64ZBA-NEXT:    xor a0, a3, a0
; RV64ZBA-NEXT:    snez a0, a0
; RV64ZBA-NEXT:    sd a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulh a3, a0, a1
; RV64ZICOND-NEXT:    mul a1, a0, a1
; RV64ZICOND-NEXT:    srai a0, a1, 63
; RV64ZICOND-NEXT:    xor a0, a3, a0
; RV64ZICOND-NEXT:    snez a0, a0
; RV64ZICOND-NEXT:
   sd a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.smul.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @smulo2.i64(i64 %v1, ptr %res) {
; RV64-LABEL: smulo2.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    li a2, 13
; RV64-NEXT:    mulh a3, a0, a2
; RV64-NEXT:    mul a2, a0, a2
; RV64-NEXT:    srai a0, a2, 63
; RV64-NEXT:    xor a0, a3, a0
; RV64-NEXT:    snez a0, a0
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo2.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    li a2, 13
; RV64ZBA-NEXT:    mulh a2, a0, a2
; RV64ZBA-NEXT:    sh1add a3, a0, a0
; RV64ZBA-NEXT:    sh2add a3, a3, a0
; RV64ZBA-NEXT:    srai a0, a3, 63
; RV64ZBA-NEXT:    xor a0, a2, a0
; RV64ZBA-NEXT:    snez a0, a0
```

```
; RV64ZBA-NEXT:    sd a3, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo2.i64:
; RV64ZICOND:       # %bb.0:
 # %entry
; RV64ZICOND-NEXT:    li a2, 13
; RV64ZICOND-NEXT:    mulh a3, a0, a2
; RV64ZICOND-NEXT:    mul a2, a0, a2
; RV64ZICOND-NEXT:    srai a0, a2, 63
; RV64ZICOND-NEXT:    xor a0, a3, a0
; RV64ZICOND-NEXT:    snez a0, a0
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.smul.with.overflow.i64(i64 %v1, i64 13)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @umulo.i32(i32 signext %v1, i32 signext %v2, ptr %res) {
; RV64-LABEL: umulo.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    slli a1, a1, 32
; RV64-NEXT:    slli a0, a0, 32
; RV64-NEXT:    mulhu a1, a0, a1
; RV64-NEXT:    srai a0, a1, 32
; RV64-NEXT:    snez a0, a0
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    mul a1, a0, a1
; RV64ZBA-NEXT:
   srai a0, a1, 32
; RV64ZBA-NEXT:    snez a0, a0
; RV64ZBA-NEXT:    sw a1, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    slli a1, a1, 32
; RV64ZICOND-NEXT:    slli a0, a0, 32
```

```
; RV64ZICOND-NEXT:    mulhu a1, a0, a1
; RV64ZICOND-NEXT:    srai a0, a1, 32
; RV64ZICOND-NEXT:    snez a0, a0
; RV64ZICOND-NEXT:    sw a1, 0(a2)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.umul.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @umulo2.i32(i32 signext %v1, ptr %res) {
; RV64-LABEL: umulo2.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    li a2, 13
; RV64-NEXT:    slli a2, a2, 32
; RV64-NEXT:    slli a0, a0, 32
; RV64-NEXT:    mulhu a2, a0, a2
; RV64-NEXT:    srli a0, a2, 32
; RV64-NEXT:    snez a0, a0
; RV64-NEXT:    sw a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo2.i32:
;
 RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    zext.w a2, a0
; RV64ZBA-NEXT:    sh1add.uw a0, a0, a2
; RV64ZBA-NEXT:    sh2add a2, a0, a2
; RV64ZBA-NEXT:    srli a0, a2, 32
; RV64ZBA-NEXT:    snez a0, a0
; RV64ZBA-NEXT:    sw a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo2.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    li a2, 13
; RV64ZICOND-NEXT:    slli a2, a2, 32
; RV64ZICOND-NEXT:    slli a0, a0, 32
; RV64ZICOND-NEXT:    mulhu a2, a0, a2
; RV64ZICOND-NEXT:    srli a0, a2, 32
; RV64ZICOND-NEXT:    snez a0, a0
; RV64ZICOND-NEXT:    sw a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.umul.with.overflow.i32(i32 %v1, i32 13)
```

```
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 store i32 %val, ptr %res
 ret i1 %obit
}

; Similar to umulo.i32, but storing the overflow and returning the result.
define signext i32 @umulo3.i32(i32 signext %0, i32 signext %1, ptr %2) {
; RV64-LABEL: umulo3.i32:
;
 RV64:       # %bb.0:
; RV64-NEXT:    slli a1, a1, 32
; RV64-NEXT:    slli a0, a0, 32
; RV64-NEXT:    mulhu a0, a0, a1
; RV64-NEXT:    srai a1, a0, 32
; RV64-NEXT:    snez a1, a1
; RV64-NEXT:    sext.w a0, a0
; RV64-NEXT:    sw a1, 0(a2)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo3.i32:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    mul a3, a0, a1
; RV64ZBA-NEXT:    srai a3, a3, 32
; RV64ZBA-NEXT:    snez a3, a3
; RV64ZBA-NEXT:    mulw a0, a0, a1
; RV64ZBA-NEXT:    sw a3, 0(a2)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo3.i32:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    slli a1, a1, 32
; RV64ZICOND-NEXT:    slli a0, a0, 32
; RV64ZICOND-NEXT:    mulhu a0, a0, a1
; RV64ZICOND-NEXT:    srai a1, a0, 32
; RV64ZICOND-NEXT:    snez a1, a1
; RV64ZICOND-NEXT:    sext.w a0, a0
; RV64ZICOND-NEXT:    sw a1, 0(a2)
; RV64ZICOND-NEXT:    ret
 %4 = tail call { i32, i1 } @llvm.umul.with.overflow.i32(i32 %0, i32 %1)
 %5 = extractvalue
 { i32, i1 } %4, 1
 %6 = extractvalue { i32, i1 } %4, 0
 %7 = zext i1 %5 to i32
 store i32 %7, ptr %2, align 4
 ret i32 %6
```

```
}

define zeroext i1 @umulo.i64(i64 %v1, i64 %v2, ptr %res) {
; RV64-LABEL: umulo.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulhu a3, a0, a1
; RV64-NEXT:    snez a3, a3
; RV64-NEXT:    mul a0, a0, a1
; RV64-NEXT:    sd a0, 0(a2)
; RV64-NEXT:    mv a0, a3
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulhu a3, a0, a1
; RV64ZBA-NEXT:    snez a3, a3
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    sd a0, 0(a2)
; RV64ZBA-NEXT:    mv a0, a3
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulhu a3, a0, a1
; RV64ZICOND-NEXT:    snez a3, a3
; RV64ZICOND-NEXT:    mul a0, a0, a1
; RV64ZICOND-NEXT:    sd a0, 0(a2)
; RV64ZICOND-NEXT:    mv a0, a3
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.umul.with.overflow.i64(i64
 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @umulo2.i64(i64 %v1, ptr %res) {
; RV64-LABEL: umulo2.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    li a3, 13
; RV64-NEXT:    mulhu a2, a0, a3
; RV64-NEXT:    snez a2, a2
; RV64-NEXT:    mul a0, a0, a3
; RV64-NEXT:    sd a0, 0(a1)
; RV64-NEXT:    mv a0, a2
; RV64-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: umulo2.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    li a2, 13
; RV64ZBA-NEXT:    mulhu a2, a0, a2
; RV64ZBA-NEXT:    snez a2, a2
; RV64ZBA-NEXT:    sh1add a3, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a3, a0
; RV64ZBA-NEXT:    sd a0, 0(a1)
; RV64ZBA-NEXT:    mv a0, a2
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo2.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    li a3, 13
; RV64ZICOND-NEXT:    mulhu a2, a0, a3
; RV64ZICOND-NEXT:    snez a2, a2
; RV64ZICOND-NEXT:    mul a0, a0, a3
; RV64ZICOND-NEXT:
   sd a0, 0(a1)
; RV64ZICOND-NEXT:    mv a0, a2
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.umul.with.overflow.i64(i64 %v1, i64 13)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}



;
; Check the use of the overflow bit in combination with a select instruction.
;
define i32 @saddo.select.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: saddo.select.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a2, a0, a1
; RV64-NEXT:    add a3, a0, a1
; RV64-NEXT:    bne a3, a2, .LBB28_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB28_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo.select.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a2, a0, a1
```

```
; RV64ZBA-NEXT:    add a3, a0, a1
; RV64ZBA-NEXT:    bne a3, a2, .LBB28_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB28_2: # %entry
; RV64ZBA-NEXT:    ret
;
;
 RV64ZICOND-LABEL: saddo.select.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a2, a0, a1
; RV64ZICOND-NEXT:    add a3, a0, a1
; RV64ZICOND-NEXT:    xor a2, a3, a2
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = select i1 %obit, i32 %v1, i32 %v2
 ret i32 %ret
}

define i1 @saddo.not.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: saddo.not.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a2, a0, a1
; RV64-NEXT:    add a0, a0, a1
; RV64-NEXT:    xor a0, a0, a2
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo.not.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a2, a0, a1
; RV64ZBA-NEXT:    add a0, a0, a1
; RV64ZBA-NEXT:    xor a0, a0, a2
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    ret
;
;
 RV64ZICOND-LABEL: saddo.not.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a2, a0, a1
; RV64ZICOND-NEXT:    add a0, a0, a1
; RV64ZICOND-NEXT:    xor a0, a0, a2
; RV64ZICOND-NEXT:    seqz a0, a0
```

```
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i64 @saddo.select.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: saddo.select.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a2, a0, a1
; RV64-NEXT:    slt a2, a2, a0
; RV64-NEXT:    slti a3, a1, 0
; RV64-NEXT:    bne a3, a2, .LBB30_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB30_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo.select.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a2, a0, a1
; RV64ZBA-NEXT:    slt a2, a2, a0
; RV64ZBA-NEXT:    slti a3, a1, 0
; RV64ZBA-NEXT:    bne a3, a2, .LBB30_2
; RV64ZBA-NEXT:  # %bb.1:
 # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB30_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo.select.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a2, a0, a1
; RV64ZICOND-NEXT:    slt a2, a2, a0
; RV64ZICOND-NEXT:    slti a3, a1, 0
; RV64ZICOND-NEXT:    xor a2, a3, a2
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.sadd.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = select i1 %obit, i64 %v1, i64 %v2
 ret i64 %ret
}
```

```
define i1 @saddo.not.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: saddo.not.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a2, a0, a1
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    slti a1, a1, 0
; RV64-NEXT:    xor a0, a1, a0
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo.not.i64:
; RV64ZBA:       # %bb.0: # %entry
;
 RV64ZBA-NEXT:    add a2, a0, a1
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    slti a1, a1, 0
; RV64ZBA-NEXT:    xor a0, a1, a0
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo.not.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a2, a0, a1
; RV64ZICOND-NEXT:    slt a0, a2, a0
; RV64ZICOND-NEXT:    slti a1, a1, 0
; RV64ZICOND-NEXT:    xor a0, a1, a0
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.sadd.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i32 @uaddo.select.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: uaddo.select.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a2, a0, a1
; RV64-NEXT:    bltu a2, a0, .LBB32_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB32_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.select.i32:
; RV64ZBA:
```

```
   # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a2, a0, a1
; RV64ZBA-NEXT:    bltu a2, a0, .LBB32_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB32_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.select.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a2, a0, a1
; RV64ZICOND-NEXT:    sltu a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.uadd.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = select i1 %obit, i32 %v1, i32 %v2
 ret i32 %ret
}

define i1 @uaddo.not.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: uaddo.not.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a1, a0, a1
; RV64-NEXT:    sltu a0, a1, a0
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.not.i32:
; RV64ZBA:       # %bb.0: # %entry
;
 RV64ZBA-NEXT:    addw a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a1, a0
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.not.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a1, a0
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.uadd.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
```

```
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i64 @uaddo.select.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: uaddo.select.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a2, a0, a1
; RV64-NEXT:    bltu a2, a0, .LBB34_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB34_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.select.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a2, a0, a1
; RV64ZBA-NEXT:    bltu a2, a0, .LBB34_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv
 a0, a1
; RV64ZBA-NEXT:  .LBB34_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.select.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a2, a0, a1
; RV64ZICOND-NEXT:    sltu a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = select i1 %obit, i64 %v1, i64 %v2
 ret i64 %ret
}

define i1 @uaddo.not.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: uaddo.not.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a1, a0, a1
; RV64-NEXT:    sltu a0, a1, a0
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.not.i64:
```

```
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a1, a0
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.not.i64:
; RV64ZICOND:
      # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a1, a0
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i32 @ssubo.select.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: ssubo.select.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a2, a0, a1
; RV64-NEXT:    sub a3, a0, a1
; RV64-NEXT:    bne a3, a2, .LBB36_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB36_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo.select.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a2, a0, a1
; RV64ZBA-NEXT:    sub a3, a0, a1
; RV64ZBA-NEXT:    bne a3, a2, .LBB36_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB36_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo.select.i32:
;
 RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a2, a0, a1
; RV64ZICOND-NEXT:    sub a3, a0, a1
; RV64ZICOND-NEXT:    xor a2, a3, a2
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
```

```
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.ssub.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = select i1 %obit, i32 %v1, i32 %v2
 ret i32 %ret
}

define i1 @ssubo.not.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: ssubo.not.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a2, a0, a1
; RV64-NEXT:    sub a0, a0, a1
; RV64-NEXT:    xor a0, a0, a2
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo.not.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a2, a0, a1
; RV64ZBA-NEXT:    sub a0, a0, a1
; RV64ZBA-NEXT:    xor a0, a0, a2
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL:
 ssubo.not.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a2, a0, a1
; RV64ZICOND-NEXT:    sub a0, a0, a1
; RV64ZICOND-NEXT:    xor a0, a0, a2
; RV64ZICOND-NEXT:    seqz a0, a0
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.ssub.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i64 @ssubo.select.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: ssubo.select.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sgtz a2, a1
; RV64-NEXT:    sub a3, a0, a1
; RV64-NEXT:    slt a3, a3, a0
```

```
; RV64-NEXT:    bne a2, a3, .LBB38_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB38_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo.select.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sgtz a2, a1
; RV64ZBA-NEXT:    sub a3, a0, a1
; RV64ZBA-NEXT:    slt a3, a3, a0
; RV64ZBA-NEXT:    bne a2, a3, .LBB38_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:
   mv a0, a1
; RV64ZBA-NEXT:  .LBB38_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo.select.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    sgtz a2, a1
; RV64ZICOND-NEXT:    sub a3, a0, a1
; RV64ZICOND-NEXT:    slt a3, a3, a0
; RV64ZICOND-NEXT:    xor a2, a2, a3
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.ssub.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = select i1 %obit, i64 %v1, i64 %v2
 ret i64 %ret
}

define i1 @ssub.not.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: ssub.not.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sgtz a2, a1
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    slt a0, a1, a0
; RV64-NEXT:    xor a0, a2, a0
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssub.not.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sgtz a2, a1
```

```
; RV64ZBA-NEXT:
   sub a1, a0, a1
; RV64ZBA-NEXT:    slt a0, a1, a0
; RV64ZBA-NEXT:    xor a0, a2, a0
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssub.not.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    sgtz a2, a1
; RV64ZICOND-NEXT:    sub a1, a0, a1
; RV64ZICOND-NEXT:    slt a0, a1, a0
; RV64ZICOND-NEXT:    xor a0, a2, a0
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.ssub.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i32 @usubo.select.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: usubo.select.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a2, a0, a1
; RV64-NEXT:    bltu a0, a2, .LBB40_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB40_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.select.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw
 a2, a0, a1
; RV64ZBA-NEXT:    bltu a0, a2, .LBB40_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB40_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.select.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a2, a0, a1
; RV64ZICOND-NEXT:    sltu a2, a0, a2
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
```

```
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.usub.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = select i1 %obit, i32 %v1, i32 %v2
 ret i32 %ret
}

define i1 @usubo.not.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: usubo.not.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a1, a0, a1
; RV64-NEXT:    sltu a0, a0, a1
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.not.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a1, a0, a1
; RV64ZBA-NEXT:
   sltu a0, a0, a1
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.not.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a0, a1
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.usub.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i64 @usubo.select.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: usubo.select.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sub a2, a0, a1
; RV64-NEXT:    bltu a0, a2, .LBB42_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB42_2: # %entry
; RV64-NEXT:    ret
;
```

```
; RV64ZBA-LABEL: usubo.select.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sub a2, a0, a1
; RV64ZBA-NEXT:    bltu a0, a2, .LBB42_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB42_2: # %entry
;
 RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.select.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    sub a2, a0, a1
; RV64ZICOND-NEXT:    sltu a2, a0, a2
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.usub.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = select i1 %obit, i64 %v1, i64 %v2
 ret i64 %ret
}

define i1 @usubo.not.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: usubo.not.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    sltu a0, a0, a1
; RV64-NEXT:    xori a0, a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.not.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sub a1, a0, a1
; RV64ZBA-NEXT:    sltu a0, a0, a1
; RV64ZBA-NEXT:    xori a0, a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.not.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:
   sub a1, a0, a1
; RV64ZICOND-NEXT:    sltu a0, a0, a1
; RV64ZICOND-NEXT:    xori a0, a0, 1
; RV64ZICOND-NEXT:    ret
entry:
```

```
 %t = call {i64, i1} @llvm.usub.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i32 @smulo.select.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: smulo.select.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulw a2, a0, a1
; RV64-NEXT:    mul a3, a0, a1
; RV64-NEXT:    bne a3, a2, .LBB44_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB44_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.select.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulw a2, a0, a1
; RV64ZBA-NEXT:    mul a3, a0, a1
; RV64ZBA-NEXT:    bne a3, a2, .LBB44_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB44_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.select.i32:
; RV64ZICOND:       # %bb.0:
 # %entry
; RV64ZICOND-NEXT:    mulw a2, a0, a1
; RV64ZICOND-NEXT:    mul a3, a0, a1
; RV64ZICOND-NEXT:    xor a2, a3, a2
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.smul.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = select i1 %obit, i32 %v1, i32 %v2
 ret i32 %ret
}

define i1 @smulo.not.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: smulo.not.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulw a2, a0, a1
```

```
; RV64-NEXT:    mul a0, a0, a1
; RV64-NEXT:    xor a0, a0, a2
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.not.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulw a2, a0, a1
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    xor a0, a0, a2
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.not.i32:
; RV64ZICOND:       # %bb.0:
 # %entry
; RV64ZICOND-NEXT:    mulw a2, a0, a1
; RV64ZICOND-NEXT:    mul a0, a0, a1
; RV64ZICOND-NEXT:    xor a0, a0, a2
; RV64ZICOND-NEXT:    seqz a0, a0
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.smul.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i64 @smulo.select.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: smulo.select.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulh a2, a0, a1
; RV64-NEXT:    mul a3, a0, a1
; RV64-NEXT:    srai a3, a3, 63
; RV64-NEXT:    bne a2, a3, .LBB46_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB46_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.select.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulh a2, a0, a1
; RV64ZBA-NEXT:    mul a3, a0, a1
; RV64ZBA-NEXT:    srai a3, a3, 63
; RV64ZBA-NEXT:    bne a2, a3, .LBB46_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
```

```
; RV64ZBA-NEXT:  .LBB46_2:
 # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.select.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulh a2, a0, a1
; RV64ZICOND-NEXT:    mul a3, a0, a1
; RV64ZICOND-NEXT:    srai a3, a3, 63
; RV64ZICOND-NEXT:    xor a2, a2, a3
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.smul.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = select i1 %obit, i64 %v1, i64 %v2
 ret i64 %ret
}

define i1 @smulo.not.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: smulo.not.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulh a2, a0, a1
; RV64-NEXT:    mul a0, a0, a1
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    xor a0, a2, a0
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.not.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulh a2, a0, a1
; RV64ZBA-NEXT:    mul a0,
 a0, a1
; RV64ZBA-NEXT:    srai a0, a0, 63
; RV64ZBA-NEXT:    xor a0, a2, a0
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.not.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulh a2, a0, a1
; RV64ZICOND-NEXT:    mul a0, a0, a1
; RV64ZICOND-NEXT:    srai a0, a0, 63
; RV64ZICOND-NEXT:    xor a0, a2, a0
; RV64ZICOND-NEXT:    seqz a0, a0
```

```
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.smul.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i32 @umulo.select.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: umulo.select.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    slli a2, a1, 32
; RV64-NEXT:    slli a3, a0, 32
; RV64-NEXT:    mulhu a2, a3, a2
; RV64-NEXT:    srai a2, a2, 32
; RV64-NEXT:    bnez a2, .LBB48_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB48_2: # %entry
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL:
 umulo.select.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    zext.w a2, a1
; RV64ZBA-NEXT:    zext.w a3, a0
; RV64ZBA-NEXT:    mul a2, a3, a2
; RV64ZBA-NEXT:    srai a2, a2, 32
; RV64ZBA-NEXT:    bnez a2, .LBB48_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB48_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.select.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    slli a2, a1, 32
; RV64ZICOND-NEXT:    slli a3, a0, 32
; RV64ZICOND-NEXT:    mulhu a2, a3, a2
; RV64ZICOND-NEXT:    srai a2, a2, 32
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.umul.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = select i1 %obit, i32 %v1, i32 %v2
```

```
 ret i32 %ret
}

define i1 @umulo.not.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: umulo.not.i32:
; RV64:
      # %bb.0: # %entry
; RV64-NEXT:    slli a1, a1, 32
; RV64-NEXT:    slli a0, a0, 32
; RV64-NEXT:    mulhu a0, a0, a1
; RV64-NEXT:    srai a0, a0, 32
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo.not.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    srai a0, a0, 32
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.not.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    slli a1, a1, 32
; RV64ZICOND-NEXT:    slli a0, a0, 32
; RV64ZICOND-NEXT:    mulhu a0, a0, a1
; RV64ZICOND-NEXT:    srai a0, a0, 32
; RV64ZICOND-NEXT:    seqz a0, a0
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.umul.with.overflow.i32(i32 %v1, i32 %v2)
 %obit = extractvalue {i32, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
}

define i64 @umulo.select.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: umulo.select.i64:
; RV64:
   # %bb.0: # %entry
; RV64-NEXT:    mulhu a2, a0, a1
; RV64-NEXT:    bnez a2, .LBB50_2
; RV64-NEXT:  # %bb.1: # %entry
; RV64-NEXT:    mv a0, a1
; RV64-NEXT:  .LBB50_2: # %entry
; RV64-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: umulo.select.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulhu a2, a0, a1
; RV64ZBA-NEXT:    bnez a2, .LBB50_2
; RV64ZBA-NEXT:  # %bb.1: # %entry
; RV64ZBA-NEXT:    mv a0, a1
; RV64ZBA-NEXT:  .LBB50_2: # %entry
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.select.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulhu a2, a0, a1
; RV64ZICOND-NEXT:    czero.nez a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.umul.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = select i1 %obit, i64 %v1, i64 %v2
 ret i64 %ret
}

define i1 @umulo.not.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: umulo.not.i64:
; RV64:       # %bb.0:
 # %entry
; RV64-NEXT:    mulhu a0, a0, a1
; RV64-NEXT:    seqz a0, a0
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo.not.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulhu a0, a0, a1
; RV64ZBA-NEXT:    seqz a0, a0
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.not.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulhu a0, a0, a1
; RV64ZICOND-NEXT:    seqz a0, a0
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.umul.with.overflow.i64(i64 %v1, i64 %v2)
 %obit = extractvalue {i64, i1} %t, 1
 %ret = xor i1 %obit, true
 ret i1 %ret
```

```
}


;
; Check the use of the overflow bit in combination with a branch instruction.
;
define zeroext i1 @saddo.br.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: saddo.br.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a2, a0, a1
; RV64-NEXT:    add a0, a0, a1
; RV64-NEXT:    beq a0, a2, .LBB52_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB52_2:
 # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo.br.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a2, a0, a1
; RV64ZBA-NEXT:    add a0, a0, a1
; RV64ZBA-NEXT:    beq a0, a2, .LBB52_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB52_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo.br.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a2, a0, a1
; RV64ZICOND-NEXT:    add a0, a0, a1
; RV64ZICOND-NEXT:    beq a0, a2, .LBB52_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB52_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.sadd.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 br i1 %obit, label %overflow, label %continue
```

```
overflow:
  ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @saddo.br.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: saddo.br.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a2, a0, a1
; RV64-NEXT:    slt a0, a2, a0
; RV64-NEXT:    slti a1, a1, 0
; RV64-NEXT:    beq a1, a0, .LBB53_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB53_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: saddo.br.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a2, a0, a1
; RV64ZBA-NEXT:    slt a0, a2, a0
; RV64ZBA-NEXT:    slti a1, a1, 0
; RV64ZBA-NEXT:    beq a1, a0, .LBB53_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB53_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: saddo.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a2, a0, a1
; RV64ZICOND-NEXT:    slt a0, a2, a0
;
 RV64ZICOND-NEXT:    slti a1, a1, 0
; RV64ZICOND-NEXT:    beq a1, a0, .LBB53_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB53_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
```

```
entry:
 %t = call {i64, i1} @llvm.sadd.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @uaddo.br.i32(i32 %v1, i32 %v2) {
; RV64-LABEL: uaddo.br.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addw a1, a0, a1
; RV64-NEXT:    sext.w a0, a0
; RV64-NEXT:    bgeu a1, a0, .LBB54_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB54_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.br.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addw a1,
 a0, a1
; RV64ZBA-NEXT:    sext.w a0, a0
; RV64ZBA-NEXT:    bgeu a1, a0, .LBB54_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB54_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.br.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addw a1, a0, a1
; RV64ZICOND-NEXT:    sext.w a0, a0
; RV64ZICOND-NEXT:    bgeu a1, a0, .LBB54_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB54_2: # %continue
```

```
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.uadd.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @uaddo.br.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: uaddo.br.i64:
; RV64:       #
 %bb.0: # %entry
; RV64-NEXT:    add a1, a0, a1
; RV64-NEXT:    bgeu a1, a0, .LBB55_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB55_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.br.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a1, a0, a1
; RV64ZBA-NEXT:    bgeu a1, a0, .LBB55_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB55_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a1, a0, a1
; RV64ZICOND-NEXT:    bgeu a1, a0, .LBB55_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB55_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
```

```
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 %v2)
 %val
 = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @ssubo.br.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: ssubo.br.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a2, a0, a1
; RV64-NEXT:    sub a0, a0, a1
; RV64-NEXT:    beq a0, a2, .LBB56_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB56_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo.br.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a2, a0, a1
; RV64ZBA-NEXT:    sub a0, a0, a1
; RV64ZBA-NEXT:    beq a0, a2, .LBB56_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB56_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo.br.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:
   subw a2, a0, a1
; RV64ZICOND-NEXT:    sub a0, a0, a1
; RV64ZICOND-NEXT:    beq a0, a2, .LBB56_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
```

```
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB56_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.ssub.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @ssubo.br.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: ssubo.br.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sgtz a2, a1
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    slt a0, a1, a0
; RV64-NEXT:    beq a2, a0, .LBB57_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB57_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: ssubo.br.i64:
;
 RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sgtz a2, a1
; RV64ZBA-NEXT:    sub a1, a0, a1
; RV64ZBA-NEXT:    slt a0, a1, a0
; RV64ZBA-NEXT:    beq a2, a0, .LBB57_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB57_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: ssubo.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    sgtz a2, a1
```

```
; RV64ZICOND-NEXT:    sub a1, a0, a1
; RV64ZICOND-NEXT:    slt a0, a1, a0
; RV64ZICOND-NEXT:    beq a2, a0, .LBB57_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB57_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.ssub.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
  ret i1 true
}

define zeroext i1 @usubo.br.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: usubo.br.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    subw a1, a0, a1
; RV64-NEXT:    bgeu a0, a1, .LBB58_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB58_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.br.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    subw a1, a0, a1
; RV64ZBA-NEXT:    bgeu a0, a1, .LBB58_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB58_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: usubo.br.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    subw a1, a0, a1
```

```
; RV64ZICOND-NEXT:    bgeu a0, a1, .LBB58_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB58_2: # %continue
;
 RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.usub.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @usubo.br.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: usubo.br.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    sub a1, a0, a1
; RV64-NEXT:    bgeu a0, a1, .LBB59_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB59_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: usubo.br.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    sub a1, a0, a1
; RV64ZBA-NEXT:    bgeu a0, a1, .LBB59_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB59_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL:
 usubo.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    sub a1, a0, a1
```

```
; RV64ZICOND-NEXT:    bgeu a0, a1, .LBB59_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB59_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.usub.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @smulo.br.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: smulo.br.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulw a2, a0, a1
; RV64-NEXT:    mul a0, a0, a1
; RV64-NEXT:    beq a0, a2, .LBB60_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB60_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.br.i32:
;
 RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulw a2, a0, a1
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    beq a0, a2, .LBB60_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB60_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.br.i32:
; RV64ZICOND:       # %bb.0: # %entry
```

```
; RV64ZICOND-NEXT:    mulw a2, a0, a1
; RV64ZICOND-NEXT:    mul a0, a0, a1
; RV64ZICOND-NEXT:    beq a0, a2, .LBB60_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB60_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.smul.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @smulo.br.i64(i64
 %v1, i64 %v2) {
; RV64-LABEL: smulo.br.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    mulh a2, a0, a1
; RV64-NEXT:    mul a0, a0, a1
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    beq a2, a0, .LBB61_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB61_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo.br.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    mulh a2, a0, a1
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    srai a0, a0, 63
; RV64ZBA-NEXT:    beq a2, a0, .LBB61_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB61_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
```

```
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulh a2, a0, a1
; RV64ZICOND-NEXT:    mul a0, a0, a1
; RV64ZICOND-NEXT:    srai a0, a0, 63
; RV64ZICOND-NEXT:    beq a2, a0, .LBB61_2
;
 RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB61_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.smul.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @smulo2.br.i64(i64 %v1) {
; RV64-LABEL: smulo2.br.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    li a1, -13
; RV64-NEXT:    mulh a2, a0, a1
; RV64-NEXT:    mul a0, a0, a1
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    beq a2, a0, .LBB62_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB62_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: smulo2.br.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    li a1, -13
; RV64ZBA-NEXT:    mulh
```

```
 a2, a0, a1
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    srai a0, a0, 63
; RV64ZBA-NEXT:    beq a2, a0, .LBB62_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB62_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: smulo2.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    li a1, -13
; RV64ZICOND-NEXT:    mulh a2, a0, a1
; RV64ZICOND-NEXT:    mul a0, a0, a1
; RV64ZICOND-NEXT:    srai a0, a0, 63
; RV64ZICOND-NEXT:    beq a2, a0, .LBB62_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB62_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.smul.with.overflow.i64(i64 %v1, i64 -13)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
  ret i1 true
}

define zeroext i1 @umulo.br.i32(i32 signext %v1, i32 signext %v2) {
; RV64-LABEL: umulo.br.i32:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    slli a1, a1, 32
; RV64-NEXT:    slli a0, a0, 32
; RV64-NEXT:    mulhu a0, a0, a1
; RV64-NEXT:    srai a0, a0, 32
; RV64-NEXT:    beqz a0, .LBB63_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
```

```
; RV64-NEXT:  .LBB63_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo.br.i32:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    mul a0, a0, a1
; RV64ZBA-NEXT:    srai a0, a0, 32
; RV64ZBA-NEXT:    beqz a0, .LBB63_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB63_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.br.i32:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    slli a1, a1, 32
;
 RV64ZICOND-NEXT:    slli a0, a0, 32
; RV64ZICOND-NEXT:    mulhu a0, a0, a1
; RV64ZICOND-NEXT:    srai a0, a0, 32
; RV64ZICOND-NEXT:    beqz a0, .LBB63_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB63_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i32, i1} @llvm.umul.with.overflow.i32(i32 %v1, i32 %v2)
 %val = extractvalue {i32, i1} %t, 0
 %obit = extractvalue {i32, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @umulo.br.i64(i64 %v1, i64 %v2) {
; RV64-LABEL: umulo.br.i64:
; RV64:       # %bb.0: # %entry
```

```
; RV64-NEXT:    mulhu a0, a0, a1
; RV64-NEXT:    beqz a0, .LBB64_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB64_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo.br.i64:
; RV64ZBA:       #
 %bb.0: # %entry
; RV64ZBA-NEXT:    mulhu a0, a0, a1
; RV64ZBA-NEXT:    beqz a0, .LBB64_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB64_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    mulhu a0, a0, a1
; RV64ZICOND-NEXT:    beqz a0, .LBB64_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB64_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.umul.with.overflow.i64(i64 %v1, i64 %v2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @umulo2.br.i64(i64 %v1) {
; RV64-LABEL: umulo2.br.i64:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    add a1,
```

```
 a0, a0
; RV64-NEXT:    bgeu a1, a0, .LBB65_2
; RV64-NEXT:  # %bb.1: # %overflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:    ret
; RV64-NEXT:  .LBB65_2: # %continue
; RV64-NEXT:    li a0, 1
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: umulo2.br.i64:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    add a1, a0, a0
; RV64ZBA-NEXT:    bgeu a1, a0, .LBB65_2
; RV64ZBA-NEXT:  # %bb.1: # %overflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:    ret
; RV64ZBA-NEXT:  .LBB65_2: # %continue
; RV64ZBA-NEXT:    li a0, 1
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: umulo2.br.i64:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    add a1, a0, a0
; RV64ZICOND-NEXT:    bgeu a1, a0, .LBB65_2
; RV64ZICOND-NEXT:  # %bb.1: # %overflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:    ret
; RV64ZICOND-NEXT:  .LBB65_2: # %continue
; RV64ZICOND-NEXT:    li a0, 1
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.umul.with.overflow.i64(i64 %v1, i64 2)
 %val = extractvalue {i64, i1} %t, 0
 %obit
 = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %overflow, label %continue

overflow:
 ret i1 false

continue:
 ret i1 true
}

define zeroext i1 @uaddo.i64.constant(i64 %v1, ptr %res) {
; RV64-LABEL: uaddo.i64.constant:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addi a2, a0, 2
```

```
; RV64-NEXT:    sltu a0, a2, a0
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i64.constant:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addi a2, a0, 2
; RV64ZBA-NEXT:    sltu a0, a2, a0
; RV64ZBA-NEXT:    sd a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i64.constant:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addi a2, a0, 2
; RV64ZICOND-NEXT:    sltu a0, a2, a0
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext
 i1 @uaddo.i64.constant_2048(i64 %v1, ptr %res) {
; RV64-LABEL: uaddo.i64.constant_2048:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addi a2, a0, 2047
; RV64-NEXT:    addi a2, a2, 1
; RV64-NEXT:    sltu a0, a2, a0
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i64.constant_2048:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addi a2, a0, 2047
; RV64ZBA-NEXT:    addi a2, a2, 1
; RV64ZBA-NEXT:    sltu a0, a2, a0
; RV64ZBA-NEXT:    sd a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i64.constant_2048:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addi a2, a0, 2047
; RV64ZICOND-NEXT:    addi a2, a2, 1
; RV64ZICOND-NEXT:    sltu a0, a2, a0
```

```
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 2048)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define zeroext i1 @uaddo.i64.constant_2049(i64
 %v1, ptr %res) {
; RV64-LABEL: uaddo.i64.constant_2049:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    addi a2, a0, 2047
; RV64-NEXT:    addi a2, a2, 2
; RV64-NEXT:    sltu a0, a2, a0
; RV64-NEXT:    sd a2, 0(a1)
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i64.constant_2049:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    addi a2, a0, 2047
; RV64ZBA-NEXT:    addi a2, a2, 2
; RV64ZBA-NEXT:    sltu a0, a2, a0
; RV64ZBA-NEXT:    sd a2, 0(a1)
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i64.constant_2049:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    addi a2, a0, 2047
; RV64ZICOND-NEXT:    addi a2, a2, 2
; RV64ZICOND-NEXT:    sltu a0, a2, a0
; RV64ZICOND-NEXT:    sd a2, 0(a1)
; RV64ZICOND-NEXT:    ret
entry:
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 2049)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 store i64 %val, ptr %res
 ret i1 %obit
}

define i64 @uaddo.i64.constant_setcc_on_overflow_flag(ptr
 %p) {
; RV64-LABEL: uaddo.i64.constant_setcc_on_overflow_flag:
; RV64:       # %bb.0: # %entry
; RV64-NEXT:    ld a1, 0(a0)
```

```
; RV64-NEXT:    addi a0, a1, 2
; RV64-NEXT:    bltu a0, a1, .LBB69_2
; RV64-NEXT:  # %bb.1: # %IfOverflow
; RV64-NEXT:    li a0, 0
; RV64-NEXT:  .LBB69_2: # %IfNoOverflow
; RV64-NEXT:    ret
;
; RV64ZBA-LABEL: uaddo.i64.constant_setcc_on_overflow_flag:
; RV64ZBA:       # %bb.0: # %entry
; RV64ZBA-NEXT:    ld a1, 0(a0)
; RV64ZBA-NEXT:    addi a0, a1, 2
; RV64ZBA-NEXT:    bltu a0, a1, .LBB69_2
; RV64ZBA-NEXT:  # %bb.1: # %IfOverflow
; RV64ZBA-NEXT:    li a0, 0
; RV64ZBA-NEXT:  .LBB69_2: # %IfNoOverflow
; RV64ZBA-NEXT:    ret
;
; RV64ZICOND-LABEL: uaddo.i64.constant_setcc_on_overflow_flag:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    ld a1, 0(a0)
; RV64ZICOND-NEXT:    addi a0, a1, 2
; RV64ZICOND-NEXT:    bltu a0, a1, .LBB69_2
; RV64ZICOND-NEXT:  # %bb.1: # %IfOverflow
; RV64ZICOND-NEXT:    li a0, 0
; RV64ZICOND-NEXT:  .LBB69_2: # %IfNoOverflow
; RV64ZICOND-NEXT:
   ret
entry:
 %v1 = load i64, ptr %p
 %t = call {i64, i1} @llvm.uadd.with.overflow.i64(i64 %v1, i64 2)
 %val = extractvalue {i64, i1} %t, 0
 %obit = extractvalue {i64, i1} %t, 1
 br i1 %obit, label %IfNoOverflow, label %IfOverflow
IfOverflow:
 ret i64 0
IfNoOverflow:
 ret i64 %val
}

declare {i32, i1} @llvm.sadd.with.overflow.i32(i32, i32) nounwind readnone
declare {i64, i1} @llvm.sadd.with.overflow.i64(i64, i64) nounwind readnone
declare {i32, i1} @llvm.uadd.with.overflow.i32(i32, i32) nounwind readnone
declare {i64, i1} @llvm.uadd.with.overflow.i64(i64, i64) nounwind readnone
declare {i32, i1} @llvm.ssub.with.overflow.i32(i32, i32) nounwind readnone
declare {i64, i1} @llvm.ssub.with.overflow.i64(i64, i64) nounwind readnone
declare {i32, i1} @llvm.usub.with.overflow.i32(i32, i32) nounwind readnone
declare {i64, i1} @llvm.usub.with.overflow.i64(i64, i64) nounwind readnone
declare {i32, i1} @llvm.smul.with.overflow.i32(i32, i32) nounwind readnone
```

```
declare {i64, i1}
 @llvm.smul.with.overflow.i64(i64, i64) nounwind readnone
declare {i32, i1} @llvm.umul.with.overflow.i32(i32, i32) nounwind readnone
declare {i64, i1} @llvm.umul.with.overflow.i64(i64, i64) nounwind readnone
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64IZbb

declare i4 @llvm.ssub.sat.i4(i4, i4)
declare i8 @llvm.ssub.sat.i8(i8, i8)
declare i16 @llvm.ssub.sat.i16(i16, i16)
declare i32 @llvm.ssub.sat.i32(i32, i32)
declare i64 @llvm.ssub.sat.i64(i64, i64)

define i32 @func32(i32 %x, i32 %y, i32 %z) nounwind {
; RV64I-LABEL: func32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mulw a1, a1, a2
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    subw a2, a0, a1
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    beq a0, a2, .LBB0_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    sraiw a0, a0, 31
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:  .LBB0_2:
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL:
 func32:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    mulw a1, a1, a2
; RV64IZbb-NEXT:    sext.w a0, a0
; RV64IZbb-NEXT:    sub a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 524288
; RV64IZbb-NEXT:    addiw a2, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a2
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i32 %y, %z
 %tmp = call i32 @llvm.ssub.sat.i32(i32 %x, i32 %a)
 ret i32 %tmp
}

define i64 @func64(i64 %x, i64 %y, i64 %z) nounwind {
```

```
; RV64-LABEL: func64:
; RV64:       # %bb.0:
; RV64-NEXT:    mv a1, a0
; RV64-NEXT:    sgtz a3, a2
; RV64-NEXT:    sub a0, a0, a2
; RV64-NEXT:    slt a1, a0, a1
; RV64-NEXT:    beq a3, a1, .LBB1_2
; RV64-NEXT:  # %bb.1:
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    li a1, -1
; RV64-NEXT:    slli a1, a1, 63
; RV64-NEXT:    xor a0, a0, a1
; RV64-NEXT:  .LBB1_2:
; RV64-NEXT:    ret
 %a = mul i64 %y, %z
 %tmp = call i64 @llvm.ssub.sat.i64(i64 %x, i64 %z)
 ret i64 %tmp
}

define i16 @func16(i16 %x, i16 %y, i16 %z) nounwind {
; RV64I-LABEL:
 func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    slli a1, a1, 16
; RV64I-NEXT:    sraiw a1, a1, 16
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 8
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    bge a0, a1, .LBB2_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    bge a1, a0, .LBB2_4
; RV64I-NEXT:  .LBB2_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB2_3:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    blt a1, a0, .LBB2_2
; RV64I-NEXT:  .LBB2_4:
; RV64I-NEXT:    lui a0, 1048568
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    sext.h a0, a0
```

```
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    sext.h a1, a1
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 8
; RV64IZbb-NEXT:    addiw a1, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 1048568
;
 RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i16 %y, %z
 %tmp = call i16 @llvm.ssub.sat.i16(i16 %x, i16 %a)
 ret i16 %tmp
}

define i8 @func8(i8 %x, i8 %y, i8 %z) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a0, a0, 24
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    slli a1, a1, 24
; RV64I-NEXT:    sraiw a1, a1, 24
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    li a1, 127
; RV64I-NEXT:    bge a0, a1, .LBB3_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -127
; RV64I-NEXT:    blt a0, a1, .LBB3_4
; RV64I-NEXT:  .LBB3_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB3_3:
; RV64I-NEXT:    li a0, 127
; RV64I-NEXT:    li a1, -127
; RV64I-NEXT:    bge a0, a1, .LBB3_2
; RV64I-NEXT:  .LBB3_4:
; RV64I-NEXT:    li a0, -128
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    sext.b a0, a0
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    sext.b a1, a1
; RV64IZbb-NEXT:
   subw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 127
; RV64IZbb-NEXT:    min a0, a0, a1
```

```
; RV64IZbb-NEXT:    li a1, -128
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i8 %y, %z
 %tmp = call i8 @llvm.ssub.sat.i8(i8 %x, i8 %a)
 ret i8 %tmp
}

define i4 @func4(i4 %x, i4 %y, i4 %z) nounwind {
; RV64I-LABEL: func4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 28
; RV64I-NEXT:    sraiw a0, a0, 28
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    slli a1, a1, 28
; RV64I-NEXT:    sraiw a1, a1, 28
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    li a1, 7
; RV64I-NEXT:    bge a0, a1, .LBB4_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -7
; RV64I-NEXT:    blt a0, a1, .LBB4_4
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB4_3:
; RV64I-NEXT:    li a0, 7
; RV64I-NEXT:    li a1, -7
; RV64I-NEXT:    bge a0, a1, .LBB4_2
; RV64I-NEXT:  .LBB4_4:
; RV64I-NEXT:    li a0, -8
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func4:
; RV64IZbb:       # %bb.0:
;
 RV64IZbb-NEXT:    slli a0, a0, 28
; RV64IZbb-NEXT:    sraiw a0, a0, 28
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    slli a1, a1, 28
; RV64IZbb-NEXT:    sraiw a1, a1, 28
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 7
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -8
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i4 %y, %z
 %tmp = call i4 @llvm.ssub.sat.i4(i4 %x, i4 %a)
```

```
 ret i4 %tmp
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64IZbb

declare i4 @llvm.sadd.sat.i4(i4, i4)
declare i8 @llvm.sadd.sat.i8(i8, i8)
declare i16 @llvm.sadd.sat.i16(i16, i16)
declare i32 @llvm.sadd.sat.i32(i32, i32)
declare i64 @llvm.sadd.sat.i64(i64, i64)

define signext i32 @func(i32 signext %x, i32 signext %y) nounwind {
; RV64I-LABEL: func:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a2, a0, a1
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    beq a0, a2, .LBB0_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    sraiw a0, a0, 31
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:  .LBB0_2:
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func:
; RV64IZbb:
     # %bb.0:
; RV64IZbb-NEXT:    add a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 524288
; RV64IZbb-NEXT:    addiw a2, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a2
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i32 @llvm.sadd.sat.i32(i32 %x, i32 %y);
 ret i32 %tmp;
}

define i64 @func2(i64 %x, i64 %y) nounwind {
; RV64-LABEL: func2:
; RV64:       # %bb.0:
; RV64-NEXT:    mv a2, a0
; RV64-NEXT:    add a0, a0, a1
; RV64-NEXT:    slt a2, a0, a2
; RV64-NEXT:    slti a1, a1, 0
```

```
; RV64-NEXT:    beq a1, a2, .LBB1_2
; RV64-NEXT:  # %bb.1:
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    li a1, -1
; RV64-NEXT:    slli a1, a1, 63
; RV64-NEXT:    xor a0, a0, a1
; RV64-NEXT:  .LBB1_2:
; RV64-NEXT:    ret
 %tmp = call i64 @llvm.sadd.sat.i64(i64 %x, i64 %y);
 ret i64 %tmp;
}

define signext i16 @func16(i16 signext %x, i16 signext %y) nounwind {
; RV64I-LABEL: func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 8
; RV64I-NEXT:    addiw a1, a1,
 -1
; RV64I-NEXT:    bge a0, a1, .LBB2_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    bge a1, a0, .LBB2_4
; RV64I-NEXT:  .LBB2_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB2_3:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    blt a1, a0, .LBB2_2
; RV64I-NEXT:  .LBB2_4:
; RV64I-NEXT:    lui a0, 1048568
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 8
; RV64IZbb-NEXT:    addiw a1, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 1048568
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i16 @llvm.sadd.sat.i16(i16 %x, i16 %y);
 ret i16 %tmp;
}

define signext i8 @func8(i8 signext %x, i8 signext %y) nounwind {
; RV64I-LABEL: func8:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 127
; RV64I-NEXT:    bge a0, a1, .LBB3_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -127
;
 RV64I-NEXT:    blt a0, a1, .LBB3_4
; RV64I-NEXT:  .LBB3_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB3_3:
; RV64I-NEXT:    li a0, 127
; RV64I-NEXT:    li a1, -127
; RV64I-NEXT:    bge a0, a1, .LBB3_2
; RV64I-NEXT:  .LBB3_4:
; RV64I-NEXT:    li a0, -128
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 127
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -128
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i8 @llvm.sadd.sat.i8(i8 %x, i8 %y);
 ret i8 %tmp;
}

define signext i4 @func3(i4 signext %x, i4 signext %y) nounwind {
; RV64I-LABEL: func3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 7
; RV64I-NEXT:    bge a0, a1, .LBB4_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -7
; RV64I-NEXT:    blt a0, a1, .LBB4_4
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB4_3:
; RV64I-NEXT:    li a0, 7
; RV64I-NEXT:    li
 a1, -7
; RV64I-NEXT:    bge a0, a1, .LBB4_2
; RV64I-NEXT:  .LBB4_4:
; RV64I-NEXT:    li a0, -8
```

```
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func3:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 7
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -8
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i4 @llvm.sadd.sat.i4(i4 %x, i4 %y);
 ret i4 %tmp;
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefix=RV64IZbb

declare i4 @llvm.uadd.sat.i4(i4, i4)
declare i8 @llvm.uadd.sat.i8(i8, i8)
declare i16 @llvm.uadd.sat.i16(i16, i16)
declare i32 @llvm.uadd.sat.i32(i32, i32)
declare i64 @llvm.uadd.sat.i64(i64, i64)

define i32 @func32(i32 %x, i32 %y, i32 %z) nounwind {
; RV64I-LABEL: func32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    addw a1, a0, a1
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sltu a0, a1, a0
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func32:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    mulw a1, a1, a2
; RV64IZbb-NEXT:    not a2, a1
; RV64IZbb-NEXT:    sext.w a0, a0
;
 RV64IZbb-NEXT:    minu a0, a0, a2
; RV64IZbb-NEXT:    add a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i32 %y, %z
 %tmp = call i32 @llvm.uadd.sat.i32(i32 %x, i32 %a)
 ret i32 %tmp
```

```
}

define i64 @func64(i64 %x, i64 %y, i64 %z) nounwind {
; RV64I-LABEL: func64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    add a2, a0, a2
; RV64I-NEXT:    sltu a0, a2, a0
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func64:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    not a1, a2
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    add a0, a0, a2
; RV64IZbb-NEXT:    ret
 %a = mul i64 %y, %z
 %tmp = call i64 @llvm.uadd.sat.i64(i64 %x, i64 %z)
 ret i64 %tmp
}

define i16 @func16(i16 %x, i16 %y, i16 %z) nounwind {
; RV64I-LABEL: func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a3, 16
; RV64I-NEXT:    addiw a3, a3, -1
; RV64I-NEXT:    and a0, a0, a3
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    and a1, a1, a3
; RV64I-NEXT:    addw a0, a0, a1
;
 RV64I-NEXT:    bltu a0, a3, .LBB2_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a3
; RV64I-NEXT:  .LBB2_2:
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    zext.h a0, a0
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    zext.h a1, a1
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 16
; RV64IZbb-NEXT:    addiw a1, a1, -1
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    ret
```

```
 %a = mul i16 %y, %z
 %tmp = call i16 @llvm.uadd.sat.i16(i16 %x, i16 %a)
 ret i16 %tmp
}

define i8 @func8(i8 %x, i8 %y, i8 %z) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    andi a1, a1, 255
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 255
; RV64I-NEXT:    bltu a0, a1, .LBB3_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a0, 255
; RV64I-NEXT:  .LBB3_2:
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:
   andi a0, a0, 255
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    andi a1, a1, 255
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 255
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i8 %y, %z
 %tmp = call i8 @llvm.uadd.sat.i8(i8 %x, i8 %a)
 ret i8 %tmp
}

define i4 @func4(i4 %x, i4 %y, i4 %z) nounwind {
; RV64I-LABEL: func4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 15
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    andi a1, a1, 15
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 15
; RV64I-NEXT:    bltu a0, a1, .LBB4_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a0, 15
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    ret
;
```

```
; RV64IZbb-LABEL: func4:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    andi a0, a0, 15
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    andi a1, a1, 15
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 15
; RV64IZbb-NEXT:    minu a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul
 i4 %y, %z
 %tmp = call i4 @llvm.uadd.sat.i4(i4 %x, i4 %a)
 ret i4 %tmp
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64I
; RUN: llc -mtriple=riscv64 -mattr=+xtheadbb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64XTHEADBB

declare i32 @llvm.ctlz.i32(i32, i1)

define signext i32 @ctlz_i32(i32 signext %a) nounwind {
; RV64I-LABEL: ctlz_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB0_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:
   not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
```

```
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB0_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: ctlz_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.extu a0, a0, 31, 0
; RV64XTHEADBB-NEXT:    th.ff1 a0,
 a0
; RV64XTHEADBB-NEXT:    addi a0, a0, -32
; RV64XTHEADBB-NEXT:    ret
 %1 = call i32 @llvm.ctlz.i32(i32 %a, i1 false)
 ret i32 %1
}

define signext i32 @log2_i32(i32 signext %a) nounwind {
; RV64I-LABEL: log2_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB1_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
```

```
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:
 and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    j .LBB1_3
; RV64I-NEXT:  .LBB1_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:  .LBB1_3: # %cond.end
; RV64I-NEXT:    li a1, 31
; RV64I-NEXT:    subw a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: log2_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.extu a0, a0, 31, 0
; RV64XTHEADBB-NEXT:    th.ff1 a0, a0
; RV64XTHEADBB-NEXT:    addi a0, a0, -32
; RV64XTHEADBB-NEXT:    li a1, 31
; RV64XTHEADBB-NEXT:    subw a0, a1, a0
; RV64XTHEADBB-NEXT:    ret
 %1 = call i32 @llvm.ctlz.i32(i32
 %a, i1 false)
 %2 = sub i32 31, %1
 ret i32 %2
}

define signext i32 @log2_ceil_i32(i32 signext %a) nounwind {
; RV64I-LABEL: log2_ceil_i32:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    addiw a0, a0, -1
; RV64I-NEXT:    li s0, 32
; RV64I-NEXT:    li a1, 32
; RV64I-NEXT:    beqz a0, .LBB2_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:
   lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a1, a0, 24
; RV64I-NEXT:  .LBB2_2: # %cond.end
; RV64I-NEXT:    subw a0, s0, a1
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
```

```
; RV64XTHEADBB-LABEL: log2_ceil_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    addi a0, a0, -1
; RV64XTHEADBB-NEXT:    slli a0, a0, 32
; RV64XTHEADBB-NEXT:    srli a0, a0, 32
; RV64XTHEADBB-NEXT:    th.ff1 a0, a0
; RV64XTHEADBB-NEXT:    addi a0, a0, -32
; RV64XTHEADBB-NEXT:
   li a1, 32
; RV64XTHEADBB-NEXT:    subw a0, a1, a0
; RV64XTHEADBB-NEXT:    ret
 %1 = sub i32 %a, 1
 %2 = call i32 @llvm.ctlz.i32(i32 %1, i1 false)
 %3 = sub i32 32, %2
 ret i32 %3
}

define signext i32 @findLastSet_i32(i32 signext %a) nounwind {
; RV64I-LABEL: findLastSet_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a0
; RV64I-NEXT:    srliw a0, a0, 1
; RV64I-NEXT:    or a0, s0, a0
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:
   lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
```

```
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    xori a0, a0, 31
; RV64I-NEXT:    snez a1, s0
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: findLastSet_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    slli a1, a0, 32
; RV64XTHEADBB-NEXT:    th.ff1 a1, a1
; RV64XTHEADBB-NEXT:    xori a1, a1, 31
; RV64XTHEADBB-NEXT:    snez a0, a0
; RV64XTHEADBB-NEXT:
   addi a0, a0, -1
; RV64XTHEADBB-NEXT:    or a0, a0, a1
; RV64XTHEADBB-NEXT:    sext.w a0, a0
; RV64XTHEADBB-NEXT:    ret
 %1 = call i32 @llvm.ctlz.i32(i32 %a, i1 true)
 %2 = xor i32 31, %1
 %3 = icmp eq i32 %a, 0
 %4 = select i1 %3, i32 -1, i32 %2
 ret i32 %4
}

define i32 @ctlz_lshr_i32(i32 signext %a) {
; RV64I-LABEL: ctlz_lshr_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a0, a0, 1
; RV64I-NEXT:    beqz a0, .LBB4_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    .cfi_def_cfa_offset 16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    .cfi_offset ra, -8
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
```

```
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
;
 RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: ctlz_lshr_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    srliw a0, a0, 1
; RV64XTHEADBB-NEXT:    th.ff1 a0, a0
; RV64XTHEADBB-NEXT:
   addi a0, a0, -32
; RV64XTHEADBB-NEXT:    ret
 %1 = lshr i32 %a, 1
 %2 = call i32 @llvm.ctlz.i32(i32 %1, i1 false)
 ret i32 %2
}
```

```
declare i64 @llvm.ctlz.i64(i64, i1)

define i64 @ctlz_i64(i64 %a) nounwind {
; RV64I-LABEL: ctlz_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB5_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    srli a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 32
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srli a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addiw a2, a2, 1365
; RV64I-NEXT:    slli a3, a2, 32
; RV64I-NEXT:    add a2, a2, a3
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
;
 RV64I-NEXT:    addiw a1, a1, 819
; RV64I-NEXT:    slli a2, a1, 32
; RV64I-NEXT:    add a1, a1, a2
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srli a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srli a1, a0, 4
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    slli a2, a1, 32
; RV64I-NEXT:    add a1, a1, a2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 16
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 32
```

```
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    srli a0, a0, 56
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB5_2:
; RV64I-NEXT:    li a0, 64
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: ctlz_i64:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.ff1 a0, a0
; RV64XTHEADBB-NEXT:    ret
 %1 = call i64 @llvm.ctlz.i64(i64 %a, i1 false)
 ret i64 %1
}

declare i32 @llvm.cttz.i32(i32,
 i1)

define signext i32 @cttz_i32(i32 signext %a) nounwind {
; RV64I-LABEL: cttz_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB6_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    negw a1, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI6_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI6_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB6_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: cttz_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    beqz a0, .LBB6_2
; RV64XTHEADBB-NEXT:  # %bb.1: # %cond.false
; RV64XTHEADBB-NEXT:    addi sp, sp, -16
; RV64XTHEADBB-NEXT:
```

```
    sd ra, 8(sp) # 8-byte Folded Spill
; RV64XTHEADBB-NEXT:    negw a1, a0
; RV64XTHEADBB-NEXT:    and a0, a0, a1
; RV64XTHEADBB-NEXT:    lui a1, 30667
; RV64XTHEADBB-NEXT:    addiw a1, a1, 1329
; RV64XTHEADBB-NEXT:    call __muldi3
; RV64XTHEADBB-NEXT:    srliw a0, a0, 27
; RV64XTHEADBB-NEXT:    lui a1, %hi(.LCPI6_0)
; RV64XTHEADBB-NEXT:    addi a1, a1, %lo(.LCPI6_0)
; RV64XTHEADBB-NEXT:    add a0, a1, a0
; RV64XTHEADBB-NEXT:    lbu a0, 0(a0)
; RV64XTHEADBB-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64XTHEADBB-NEXT:    addi sp, sp, 16
; RV64XTHEADBB-NEXT:    ret
; RV64XTHEADBB-NEXT:  .LBB6_2:
; RV64XTHEADBB-NEXT:    li a0, 32
; RV64XTHEADBB-NEXT:    ret
; RV64ZBB-LABEL: cttz_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    ctzw a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 false)
 ret i32 %1
}

define signext i32 @cttz_zero_undef_i32(i32 signext %a) nounwind {
; RV64I-LABEL: cttz_zero_undef_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp,
 -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    negw a1, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI7_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI7_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: cttz_zero_undef_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    addi sp, sp, -16
```

```
; RV64XTHEADBB-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64XTHEADBB-NEXT:    negw a1, a0
; RV64XTHEADBB-NEXT:    and a0, a0, a1
; RV64XTHEADBB-NEXT:    lui a1, 30667
; RV64XTHEADBB-NEXT:    addiw a1, a1, 1329
; RV64XTHEADBB-NEXT:    call __muldi3
; RV64XTHEADBB-NEXT:    srliw a0, a0, 27
; RV64XTHEADBB-NEXT:    lui a1, %hi(.LCPI7_0)
; RV64XTHEADBB-NEXT:    addi a1, a1, %lo(.LCPI7_0)
;
 RV64XTHEADBB-NEXT:    add a0, a1, a0
; RV64XTHEADBB-NEXT:    lbu a0, 0(a0)
; RV64XTHEADBB-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64XTHEADBB-NEXT:    addi sp, sp, 16
; RV64XTHEADBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 true)
 ret i32 %1
}

define signext i32 @findFirstSet_i32(i32 signext %a) nounwind {
; RV64I-LABEL: findFirstSet_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    and a0, s0, a0
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI8_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI8_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    snez a1, s0
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    or a0, a1, a0
;
 RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: findFirstSet_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    addi sp, sp, -16
```

```
; RV64XTHEADBB-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64XTHEADBB-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64XTHEADBB-NEXT:    mv s0, a0
; RV64XTHEADBB-NEXT:    negw a0, a0
; RV64XTHEADBB-NEXT:    and a0, s0, a0
; RV64XTHEADBB-NEXT:    lui a1, 30667
; RV64XTHEADBB-NEXT:    addiw a1, a1, 1329
; RV64XTHEADBB-NEXT:    call __muldi3
; RV64XTHEADBB-NEXT:    srliw a0, a0, 27
; RV64XTHEADBB-NEXT:    lui a1, %hi(.LCPI8_0)
; RV64XTHEADBB-NEXT:    addi a1, a1, %lo(.LCPI8_0)
; RV64XTHEADBB-NEXT:    add a0, a1, a0
; RV64XTHEADBB-NEXT:    lbu a0, 0(a0)
; RV64XTHEADBB-NEXT:    snez a1, s0
; RV64XTHEADBB-NEXT:    addiw a1, a1, -1
; RV64XTHEADBB-NEXT:    or a0, a1, a0
; RV64XTHEADBB-NEXT:    ld ra, 8(sp) # 8-byte Folded
 Reload
; RV64XTHEADBB-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64XTHEADBB-NEXT:    addi sp, sp, 16
; RV64XTHEADBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 true)
 %2 = icmp eq i32 %a, 0
 %3 = select i1 %2, i32 -1, i32 %1
 ret i32 %3
}

define signext i32 @ffs_i32(i32 signext %a) nounwind {
; RV64I-LABEL: ffs_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    and a0, s0, a0
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI9_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI9_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    addiw a0, a0, 1
; RV64I-NEXT:    seqz a1, s0
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    and a0, a1, a0
```

```
;
 RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: ffs_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    addi sp, sp, -16
; RV64XTHEADBB-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64XTHEADBB-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64XTHEADBB-NEXT:    mv s0, a0
; RV64XTHEADBB-NEXT:    negw a0, a0
; RV64XTHEADBB-NEXT:    and a0, s0, a0
; RV64XTHEADBB-NEXT:    lui a1, 30667
; RV64XTHEADBB-NEXT:    addiw a1, a1, 1329
; RV64XTHEADBB-NEXT:    call __muldi3
; RV64XTHEADBB-NEXT:    srliw a0, a0, 27
; RV64XTHEADBB-NEXT:    lui a1, %hi(.LCPI9_0)
; RV64XTHEADBB-NEXT:    addi a1, a1, %lo(.LCPI9_0)
; RV64XTHEADBB-NEXT:    add a0, a1, a0
; RV64XTHEADBB-NEXT:    lbu a0, 0(a0)
; RV64XTHEADBB-NEXT:    addiw a0, a0, 1
; RV64XTHEADBB-NEXT:    seqz a1, s0
; RV64XTHEADBB-NEXT:    addiw a1, a1, -1
; RV64XTHEADBB-NEXT:    and a0, a1, a0
; RV64XTHEADBB-NEXT:
   ld ra, 8(sp) # 8-byte Folded Reload
; RV64XTHEADBB-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64XTHEADBB-NEXT:    addi sp, sp, 16
; RV64XTHEADBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 true)
 %2 = add i32 %1, 1
 %3 = icmp eq i32 %a, 0
 %4 = select i1 %3, i32 0, i32 %2
 ret i32 %4
}

declare i64 @llvm.cttz.i64(i64, i1)

define i64 @cttz_i64(i64 %a) nounwind {
; RV64I-LABEL: cttz_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB10_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    neg a1, a0
```

```
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, %hi(.LCPI10_0)
; RV64I-NEXT:    ld a1, %lo(.LCPI10_0)(a1)
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srli a0, a0, 58
; RV64I-NEXT:    lui a1, %hi(.LCPI10_1)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI10_1)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded
 Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB10_2:
; RV64I-NEXT:    li a0, 64
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: cttz_i64:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    beqz a0, .LBB10_2
; RV64XTHEADBB-NEXT:  # %bb.1: # %cond.false
; RV64XTHEADBB-NEXT:    addi a1, a0, -1
; RV64XTHEADBB-NEXT:    not a0, a0
; RV64XTHEADBB-NEXT:    and a0, a0, a1
; RV64XTHEADBB-NEXT:    th.ff1 a0, a0
; RV64XTHEADBB-NEXT:    li a1, 64
; RV64XTHEADBB-NEXT:    sub a0, a1, a0
; RV64XTHEADBB-NEXT:    ret
; RV64XTHEADBB-NEXT:  .LBB10_2:
; RV64XTHEADBB-NEXT:    li a0, 64
; RV64XTHEADBB-NEXT:    ret
 %1 = call i64 @llvm.cttz.i64(i64 %a, i1 false)
 ret i64 %1
}

define signext i32 @sextb_i32(i32 signext %a) nounwind {
; RV64I-LABEL: sextb_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    srai a0, a0, 56
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: sextb_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.ext a0, a0, 7,
 0
; RV64XTHEADBB-NEXT:    ret
 %shl = shl i32 %a, 24
```

```
 %shr = ashr exact i32 %shl, 24
 ret i32 %shr
}

define i64 @sextb_i64(i64 %a) nounwind {
; RV64I-LABEL: sextb_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    srai a0, a0, 56
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: sextb_i64:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.ext a0, a0, 7, 0
; RV64XTHEADBB-NEXT:    ret
 %shl = shl i64 %a, 56
 %shr = ashr exact i64 %shl, 56
 ret i64 %shr
}

define signext i32 @sexth_i32(i32 signext %a) nounwind {
; RV64I-LABEL: sexth_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srai a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: sexth_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.ext a0, a0, 15, 0
; RV64XTHEADBB-NEXT:    ret
 %shl = shl i32 %a, 16
 %shr = ashr exact i32 %shl, 16
 ret i32 %shr
}

define signext i32 @no_sexth_i32(i32 signext %a) nounwind {
; RV64I-LABEL: no_sexth_i32:
; RV64I:
      # %bb.0:
; RV64I-NEXT:    slli a0, a0, 17
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: no_sexth_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    slli a0, a0, 17
; RV64XTHEADBB-NEXT:    sraiw a0, a0, 16
```

```
; RV64XTHEADBB-NEXT:    ret
 %shl = shl i32 %a, 17
 %shr = ashr exact i32 %shl, 16
 ret i32 %shr
}

define i64 @sexth_i64(i64 %a) nounwind {
; RV64I-LABEL: sexth_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srai a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: sexth_i64:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.ext a0, a0, 15, 0
; RV64XTHEADBB-NEXT:    ret
 %shl = shl i64 %a, 48
 %shr = ashr exact i64 %shl, 48
 ret i64 %shr
}

define i64 @no_sexth_i64(i64 %a) nounwind {
; RV64I-LABEL: no_sexth_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 49
; RV64I-NEXT:    srai a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: no_sexth_i64:
; RV64XTHEADBB:       # %bb.0:
;
 RV64XTHEADBB-NEXT:    slli a0, a0, 49
; RV64XTHEADBB-NEXT:    srai a0, a0, 48
; RV64XTHEADBB-NEXT:    ret
 %shl = shl i64 %a, 49
 %shr = ashr exact i64 %shl, 48
 ret i64 %shr
}

define i32 @zexth_i32(i32 %a) nounwind {
; RV64I-LABEL: zexth_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: zexth_i32:
```

```
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.extu a0, a0, 15, 0
; RV64XTHEADBB-NEXT:    ret
 %and = and i32 %a, 65535
 ret i32 %and
}

define i64 @zexth_i64(i64 %a) nounwind {
; RV64I-LABEL: zexth_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: zexth_i64:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.extu a0, a0, 15, 0
; RV64XTHEADBB-NEXT:    ret
 %and = and i64 %a, 65535
 ret i64 %and
}

define i64 @zext_bf_i64(i64 %a) nounwind {
; RV64I-LABEL: zext_bf_i64:
; RV64I:       #
 %bb.0:
; RV64I-NEXT:    slli a0, a0, 47
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: zext_bf_i64:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.extu a0, a0, 16, 1
; RV64XTHEADBB-NEXT:    ret
 %1 = lshr i64 %a, 1
 %and = and i64 %1, 65535
 ret i64 %and
}

define i64 @zext_i64_srliw(i64 %a) nounwind {
; RV64I-LABEL: zext_i64_srliw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a0, a0, 16
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: zext_i64_srliw:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    srliw a0, a0, 16
```

```
; RV64XTHEADBB-NEXT:    ret
 %1 = lshr i64 %a, 16
 %and = and i64 %1, 65535
 ret i64 %and
}

declare i32 @llvm.bswap.i32(i32)

define signext i32 @bswap_i32(i32 signext %a) nounwind {
; RV64I-LABEL: bswap_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    lui a2, 16
; RV64I-NEXT:    addiw a2, a2, -256
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    srliw a3, a0, 24
; RV64I-NEXT:    or a1, a1, a3
;
 RV64I-NEXT:    and a2, a0, a2
; RV64I-NEXT:    slliw a2, a2, 8
; RV64I-NEXT:    slliw a0, a0, 24
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: bswap_i32:
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.revw a0, a0
; RV64XTHEADBB-NEXT:    ret
 %1 = tail call i32 @llvm.bswap.i32(i32 %a)
 ret i32 %1
}

; Similar to bswap_i32 but the result is not sign extended.
define void @bswap_i32_nosext(i32 signext %a, ptr %x) nounwind {
; RV64I-LABEL: bswap_i32_nosext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a2, a0, 8
; RV64I-NEXT:    lui a3, 16
; RV64I-NEXT:    addi a3, a3, -256
; RV64I-NEXT:    and a2, a2, a3
; RV64I-NEXT:    srliw a4, a0, 24
; RV64I-NEXT:    or a2, a2, a4
; RV64I-NEXT:    and a3, a0, a3
; RV64I-NEXT:    slli a3, a3, 8
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    or a0, a0, a3
; RV64I-NEXT:    or a0, a0, a2
```

```
; RV64I-NEXT:    sw a0, 0(a1)
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: bswap_i32_nosext:
;
 RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.revw a0, a0
; RV64XTHEADBB-NEXT:    sw a0, 0(a1)
; RV64XTHEADBB-NEXT:    ret
 %1 = tail call i32 @llvm.bswap.i32(i32 %a)
 store i32 %1, ptr %x
 ret void
}

declare i64 @llvm.bswap.i64(i64)

define i64 @bswap_i64(i64 %a) {
; RV64I-LABEL: bswap_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a0, 40
; RV64I-NEXT:    lui a2, 16
; RV64I-NEXT:    addiw a2, a2, -256
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    srli a3, a0, 56
; RV64I-NEXT:    or a1, a1, a3
; RV64I-NEXT:    srli a3, a0, 24
; RV64I-NEXT:    lui a4, 4080
; RV64I-NEXT:    and a3, a3, a4
; RV64I-NEXT:    srli a5, a0, 8
; RV64I-NEXT:    srliw a5, a5, 24
; RV64I-NEXT:    slli a5, a5, 24
; RV64I-NEXT:    or a3, a5, a3
; RV64I-NEXT:    or a1, a3, a1
; RV64I-NEXT:    and a4, a0, a4
; RV64I-NEXT:    slli a4, a4, 24
; RV64I-NEXT:    srliw a3, a0, 24
; RV64I-NEXT:    slli a3, a3, 32
; RV64I-NEXT:    or a3, a4, a3
; RV64I-NEXT:    and a2, a0, a2
; RV64I-NEXT:    slli
 a2, a2, 40
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    or a0, a0, a3
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XTHEADBB-LABEL: bswap_i64:
```

```
; RV64XTHEADBB:       # %bb.0:
; RV64XTHEADBB-NEXT:    th.rev a0, a0
; RV64XTHEADBB-NEXT:    ret
 %1 = call i64 @llvm.bswap.i64(i64 %a)
 ret i64 %1
}
```

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
;
; D31946
; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK:       # %bb.0:
; CHECK-NEXT:    pushq %rbx
; CHECK-NEXT:    .cfi_def_cfa_offset 16
; CHECK-NEXT:    subq $32, %rsp
; CHECK-NEXT:    .cfi_def_cfa_offset 48
; CHECK-NEXT:    .cfi_offset %rbx, -16
; CHECK-NEXT:    movaps %xmm1, {{[-0-9]+}}(%r{{[sb]}}p) # 16-byte Spill
; CHECK-NEXT:    movaps %xmm0, (%rsp) # 16-byte Spill
; CHECK-NEXT:    callq __gttf2@PLT
; CHECK-NEXT:    movl %eax, %ebx
; CHECK-NEXT:    movaps (%rsp), %xmm0 # 16-byte Reload
; CHECK-NEXT:    movaps {{[-0-9]+}}(%r{{[sb]}}p),
 %xmm1 # 16-byte Reload
; CHECK-NEXT:    callq __subtf3@PLT
; CHECK-NEXT:    testl %ebx, %ebx
; CHECK-NEXT:    jg .LBB0_2
; CHECK-NEXT:  # %bb.1:
; CHECK-NEXT:    xorps %xmm0, %xmm0
; CHECK-NEXT:  .LBB0_2:
; CHECK-NEXT:    addq $32, %rsp
; CHECK-NEXT:    .cfi_def_cfa_offset 16
; CHECK-NEXT:    popq %rbx
; CHECK-NEXT:    .cfi_def_cfa_offset 8
; CHECK-NEXT:    retq
 %cmp = fcmp ogt fp128 %a, %b
 %sub = fsub fp128 %a, %b
 %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
 ret fp128 %res
}

define fp128 @TestFabs(fp128 %a) {
; CHECK-LABEL: TestFabs:
```

```
; CHECK:       # %bb.0:
; CHECK-NEXT:    andps {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %xmm0
; CHECK-NEXT:    retq
 %res = call fp128 @llvm.fabs.f128(fp128 %a)
 ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestCopysign:
; CHECK:       # %bb.0:
; CHECK-NEXT:    andps {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %xmm1
; CHECK-NEXT:    andps {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %xmm0
; CHECK-NEXT:
   orps %xmm1, %xmm0
; CHECK-NEXT:    retq
 %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
 ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
; CHECK-LABEL: TestFneg:
; CHECK:       # %bb.0:
; CHECK-NEXT:    pushq %rax
; CHECK-NEXT:    .cfi_def_cfa_offset 16
; CHECK-NEXT:    movaps %xmm0, %xmm1
; CHECK-NEXT:    callq __multf3@PLT
; CHECK-NEXT:    xorps {{\.?LCPI[0-9]+_[0-9]+}}(%rip), %xmm0
; CHECK-NEXT:    popq %rax
; CHECK-NEXT:    .cfi_def_cfa_offset 8
; CHECK-NEXT:    retq
 %mul = fmul fp128 %a, %a
 %res = fsub fp128 0xL00000000000000008000000000000000, %mul
 ret fp128 %res
}
```

---

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```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64I
; RUN: llc -mtriple=riscv64 -mattr=+zbb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64ZBB

declare i32 @llvm.ctlz.i32(i32, i1)

define signext i32 @ctlz_i32(i32 signext %a) nounwind {
; RV64I-LABEL: ctlz_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB0_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
```

```
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:
  not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB0_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ctlz_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    clzw a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.ctlz.i32(i32 %a, i1 false)
  ret i32 %1
}
```

```
define signext i32 @log2_i32(i32 signext %a) nounwind {
; RV64I-LABEL: log2_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB1_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:
   srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    j .LBB1_3
; RV64I-NEXT:  .LBB1_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:  .LBB1_3: # %cond.end
; RV64I-NEXT:    li a1, 31
; RV64I-NEXT:    subw a0, a1, a0
; RV64I-NEXT:    ret
```

```
;
; RV64ZBB-LABEL: log2_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    clzw a0, a0
; RV64ZBB-NEXT:    li a1, 31
; RV64ZBB-NEXT:    subw a0, a1, a0
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.ctlz.i32(i32 %a, i1 false)
 %2 = sub i32 31, %1
 ret i32 %2
}

define signext i32 @log2_ceil_i32(i32 signext %a) nounwind {
; RV64I-LABEL: log2_ceil_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra,
 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    addiw a0, a0, -1
; RV64I-NEXT:    li s0, 32
; RV64I-NEXT:    li a1, 32
; RV64I-NEXT:    beqz a0, .LBB2_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:
```

```
  addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a1, a0, 24
; RV64I-NEXT:  .LBB2_2: # %cond.end
; RV64I-NEXT:    subw a0, s0, a1
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: log2_ceil_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    addi a0, a0, -1
; RV64ZBB-NEXT:    clzw a0, a0
; RV64ZBB-NEXT:    li a1, 32
; RV64ZBB-NEXT:    subw a0, a1, a0
; RV64ZBB-NEXT:    ret
 %1 = sub i32 %a, 1
 %2 = call i32 @llvm.ctlz.i32(i32 %1, i1 false)
 %3 = sub i32 32, %2
 ret i32 %3
}

define signext i32 @findLastSet_i32(i32 signext %a) nounwind {
; RV64I-LABEL: findLastSet_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
;
 RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a0
; RV64I-NEXT:    srliw a0, a0, 1
; RV64I-NEXT:    or a0, s0, a0
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
```

```
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:
  addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    xori a0, a0, 31
; RV64I-NEXT:    snez a1, s0
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: findLastSet_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    clzw a1, a0
; RV64ZBB-NEXT:    xori a1, a1, 31
; RV64ZBB-NEXT:    snez a0, a0
; RV64ZBB-NEXT:    addiw a0, a0, -1
; RV64ZBB-NEXT:    or a0, a0, a1
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.ctlz.i32(i32 %a, i1 true)
 %2 = xor i32 31, %1
 %3 = icmp eq i32 %a, 0
 %4 = select i1 %3, i32 -1, i32 %2
 ret i32 %4
}

define i32 @ctlz_lshr_i32(i32 signext %a) {
; RV64I-LABEL: ctlz_lshr_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a0, a0, 1
```

```
; RV64I-NEXT:    beqz a0, .LBB4_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp,
 -16
; RV64I-NEXT:    .cfi_def_cfa_offset 16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    .cfi_offset ra, -8
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srliw a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0,
 a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ctlz_lshr_i32:
; RV64ZBB:       # %bb.0:
```

```
; RV64ZBB-NEXT:    srliw a0, a0, 1
; RV64ZBB-NEXT:    clzw a0, a0
; RV64ZBB-NEXT:    ret
 %1 = lshr i32 %a, 1
 %2 = call i32 @llvm.ctlz.i32(i32 %1, i1 false)
 ret i32 %2
}

declare i64 @llvm.ctlz.i64(i64, i1)

define i64 @ctlz_i64(i64 %a) nounwind {
; RV64I-LABEL: ctlz_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB5_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    srli a1, a0, 1
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 4
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 8
; RV64I-NEXT:    or a0, a0,
 a1
; RV64I-NEXT:    srli a1, a0, 16
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    srli a1, a0, 32
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    srli a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addiw a2, a2, 1365
; RV64I-NEXT:    slli a3, a2, 32
; RV64I-NEXT:    add a2, a2, a3
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addiw a1, a1, 819
; RV64I-NEXT:    slli a2, a1, 32
; RV64I-NEXT:    add a1, a1, a2
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srli a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srli a1, a0, 4
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
```

```
; RV64I-NEXT:    slli a2, a1, 32
; RV64I-NEXT:    add a1, a1, a2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 16
; RV64I-NEXT:    add a0, a0,
 a1
; RV64I-NEXT:    slli a1, a0, 32
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    srli a0, a0, 56
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB5_2:
; RV64I-NEXT:    li a0, 64
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ctlz_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    clz a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i64 @llvm.ctlz.i64(i64 %a, i1 false)
 ret i64 %1
}

declare i32 @llvm.cttz.i32(i32, i1)

define signext i32 @cttz_i32(i32 signext %a) nounwind {
; RV64I-LABEL: cttz_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB6_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    negw a1, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI6_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI6_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0,
 0(a0)
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB6_2:
```

```
; RV64I-NEXT:    li a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: cttz_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    ctzw a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 false)
 ret i32 %1
}

define signext i32 @cttz_zero_undef_i32(i32 signext %a) nounwind {
; RV64I-LABEL: cttz_zero_undef_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    negw a1, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI7_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI7_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
;
 RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: cttz_zero_undef_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    ctzw a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 true)
 ret i32 %1
}

define signext i32 @findFirstSet_i32(i32 signext %a) nounwind {
; RV64I-LABEL: findFirstSet_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    and a0, s0, a0
```

```
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI8_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI8_0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    snez a1, s0
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0,
 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: findFirstSet_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    ctzw a1, a0
; RV64ZBB-NEXT:    snez a0, a0
; RV64ZBB-NEXT:    addiw a0, a0, -1
; RV64ZBB-NEXT:    or a0, a0, a1
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 true)
 %2 = icmp eq i32 %a, 0
 %3 = select i1 %2, i32 -1, i32 %1
 ret i32 %3
}

define signext i32 @ffs_i32(i32 signext %a) nounwind {
; RV64I-LABEL: ffs_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a0
; RV64I-NEXT:    negw a0, a0
; RV64I-NEXT:    and a0, s0, a0
; RV64I-NEXT:    lui a1, 30667
; RV64I-NEXT:    addiw a1, a1, 1329
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 27
; RV64I-NEXT:    lui a1, %hi(.LCPI9_0)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI9_0)
; RV64I-NEXT:    add a0, a1, a0
;
```

```
 RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    addiw a0, a0, 1
; RV64I-NEXT:    seqz a1, s0
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ffs_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    ctzw a1, a0
; RV64ZBB-NEXT:    addiw a1, a1, 1
; RV64ZBB-NEXT:    seqz a0, a0
; RV64ZBB-NEXT:    addiw a0, a0, -1
; RV64ZBB-NEXT:    and a0, a0, a1
; RV64ZBB-NEXT:    ret
 %1 = call i32 @llvm.cttz.i32(i32 %a, i1 true)
 %2 = add i32 %1, 1
 %3 = icmp eq i32 %a, 0
 %4 = select i1 %3, i32 0, i32 %2
 ret i32 %4
}

declare i64 @llvm.cttz.i64(i64, i1)

define i64 @cttz_i64(i64 %a) nounwind {
; RV64I-LABEL: cttz_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB10_2
; RV64I-NEXT:  # %bb.1: # %cond.false
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:
   neg a1, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, %hi(.LCPI10_0)
; RV64I-NEXT:    ld a1, %lo(.LCPI10_0)(a1)
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srli a0, a0, 58
; RV64I-NEXT:    lui a1, %hi(.LCPI10_1)
; RV64I-NEXT:    addi a1, a1, %lo(.LCPI10_1)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
```

```
; RV64I-NEXT:  .LBB10_2:
; RV64I-NEXT:    li a0, 64
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: cttz_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    ctz a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i64 @llvm.cttz.i64(i64 %a, i1 false)
 ret i64 %1
}

declare i32 @llvm.ctpop.i32(i32)

define signext i32 @ctpop_i32(i32 signext %a) nounwind {
; RV64I-LABEL: ctpop_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:
   addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ctpop_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    cpopw a0, a0
; RV64ZBB-NEXT:    ret
```

```
 %1 = call i32 @llvm.ctpop.i32(i32 %a)
 ret i32 %1
}

define signext i32 @ctpop_i32_load(ptr %p) nounwind {
; RV64I-LABEL: ctpop_i32_load:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
;
 RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    srliw a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addi a2, a2, 1365
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addi a1, a1, 819
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srliw a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srliw a1, a0, 4
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    lui a1, 4112
; RV64I-NEXT:    addiw a1, a1, 257
; RV64I-NEXT:    call __muldi3
; RV64I-NEXT:    srliw a0, a0, 24
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ctpop_i32_load:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    lw a0, 0(a0)
; RV64ZBB-NEXT:    cpopw a0, a0
; RV64ZBB-NEXT:    ret
 %a =
 load i32, ptr %p
 %1 = call i32 @llvm.ctpop.i32(i32 %a)
 ret i32 %1
}

declare i64 @llvm.ctpop.i64(i64)
```

```
define i64 @ctpop_i64(i64 %a) nounwind {
; RV64I-LABEL: ctpop_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a0, 1
; RV64I-NEXT:    lui a2, 349525
; RV64I-NEXT:    addiw a2, a2, 1365
; RV64I-NEXT:    slli a3, a2, 32
; RV64I-NEXT:    add a2, a2, a3
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    lui a1, 209715
; RV64I-NEXT:    addiw a1, a1, 819
; RV64I-NEXT:    slli a2, a1, 32
; RV64I-NEXT:    add a1, a1, a2
; RV64I-NEXT:    and a2, a0, a1
; RV64I-NEXT:    srli a0, a0, 2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    srli a1, a0, 4
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lui a1, 61681
; RV64I-NEXT:    addiw a1, a1, -241
; RV64I-NEXT:    slli a2, a1, 32
; RV64I-NEXT:    add a1, a1, a2
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli
 a1, a0, 16
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli a1, a0, 32
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    srli a0, a0, 56
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: ctpop_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    cpop a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i64 @llvm.ctpop.i64(i64 %a)
 ret i64 %1
}

define signext i32 @sextb_i32(i32 signext %a) nounwind {
; RV64I-LABEL: sextb_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    srai a0, a0, 56
```

```
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: sextb_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    sext.b a0, a0
; RV64ZBB-NEXT:    ret
 %shl = shl i32 %a, 24
 %shr = ashr exact i32 %shl, 24
 ret i32 %shr
}

define i64 @sextb_i64(i64 %a) nounwind {
; RV64I-LABEL: sextb_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    srai a0, a0, 56
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: sextb_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    sext.b a0, a0
; RV64ZBB-NEXT:    ret
  %shl = shl i64 %a, 56
 %shr = ashr exact i64 %shl, 56
 ret i64 %shr
}

define signext i32 @sexth_i32(i32 signext %a) nounwind {
; RV64I-LABEL: sexth_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srai a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: sexth_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    sext.h a0, a0
; RV64ZBB-NEXT:    ret
 %shl = shl i32 %a, 16
 %shr = ashr exact i32 %shl, 16
 ret i32 %shr
}

define i64 @sexth_i64(i64 %a) nounwind {
; RV64I-LABEL: sexth_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srai a0, a0, 48
```

```
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: sexth_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    sext.h a0, a0
; RV64ZBB-NEXT:    ret
 %shl = shl i64 %a, 48
 %shr = ashr exact i64 %shl, 48
 ret i64 %shr
}

define signext i32 @min_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: min_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    blt a0, a1, .LBB18_2
; RV64I-NEXT:  # %bb.1:
;
 RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB18_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: min_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    min a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp slt i32 %a, %b
 %cond = select i1 %cmp, i32 %a, i32 %b
 ret i32 %cond
}

define i64 @min_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: min_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    blt a0, a1, .LBB19_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB19_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: min_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    min a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp slt i64 %a, %b
 %cond = select i1 %cmp, i64 %a, i64 %b
 ret i64 %cond
}
```

```
define signext i32 @max_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: max_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    blt a1, a0, .LBB20_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB20_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: max_i32:
; RV64ZBB:
    # %bb.0:
; RV64ZBB-NEXT:    max a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp sgt i32 %a, %b
 %cond = select i1 %cmp, i32 %a, i32 %b
 ret i32 %cond
}

define i64 @max_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: max_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    blt a1, a0, .LBB21_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB21_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: max_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    max a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp sgt i64 %a, %b
 %cond = select i1 %cmp, i64 %a, i64 %b
 ret i64 %cond
}

define signext i32 @minu_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: minu_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bltu a0, a1, .LBB22_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB22_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: minu_i32:
; RV64ZBB:       # %bb.0:
```

```
; RV64ZBB-NEXT:    minu a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp ult i32 %a, %b
 %cond = select
 i1 %cmp, i32 %a, i32 %b
 ret i32 %cond
}

define i64 @minu_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: minu_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bltu a0, a1, .LBB23_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB23_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: minu_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    minu a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp ult i64 %a, %b
 %cond = select i1 %cmp, i64 %a, i64 %b
 ret i64 %cond
}

define signext i32 @maxu_i32(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: maxu_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bltu a1, a0, .LBB24_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB24_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: maxu_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    maxu a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp ugt i32 %a, %b
 %cond = select i1 %cmp, i32 %a, i32 %b
 ret i32 %cond
}

define i64 @maxu_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL:
 maxu_i64:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    bltu a1, a0, .LBB25_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB25_2:
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: maxu_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    maxu a0, a0, a1
; RV64ZBB-NEXT:    ret
 %cmp = icmp ugt i64 %a, %b
 %cond = select i1 %cmp, i64 %a, i64 %b
 ret i64 %cond
}

declare i32 @llvm.abs.i32(i32, i1 immarg)

define i32 @abs_i32(i32 %x) {
; RV64I-LABEL: abs_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraiw a1, a0, 31
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: abs_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    sraiw a1, a0, 31
; RV64ZBB-NEXT:    xor a0, a0, a1
; RV64ZBB-NEXT:    subw a0, a0, a1
; RV64ZBB-NEXT:    ret
 %abs = tail call i32 @llvm.abs.i32(i32 %x, i1 true)
 ret i32 %abs
}

define signext i32 @abs_i32_sext(i32 signext %x) {
; RV64I-LABEL: abs_i32_sext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraiw a1,
 a0, 31
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: abs_i32_sext:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    sraiw a1, a0, 31
; RV64ZBB-NEXT:    xor a0, a0, a1
```

```
; RV64ZBB-NEXT:    subw a0, a0, a1
; RV64ZBB-NEXT:    ret
 %abs = tail call i32 @llvm.abs.i32(i32 %x, i1 true)
 ret i32 %abs
}

declare i64 @llvm.abs.i64(i64, i1 immarg)

define i64 @abs_i64(i64 %x) {
; RV64I-LABEL: abs_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srai a1, a0, 63
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: abs_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    neg a1, a0
; RV64ZBB-NEXT:    max a0, a0, a1
; RV64ZBB-NEXT:    ret
 %abs = tail call i64 @llvm.abs.i64(i64 %x, i1 true)
 ret i64 %abs
}

define i32 @zexth_i32(i32 %a) nounwind {
; RV64I-LABEL: zexth_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL:
 zexth_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    zext.h a0, a0
; RV64ZBB-NEXT:    ret
 %and = and i32 %a, 65535
 ret i32 %and
}

define i64 @zexth_i64(i64 %a) nounwind {
; RV64I-LABEL: zexth_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    ret
;
```

```
; RV64ZBB-LABEL: zexth_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    zext.h a0, a0
; RV64ZBB-NEXT:    ret
 %and = and i64 %a, 65535
 ret i64 %and
}

declare i32 @llvm.bswap.i32(i32)

define signext i32 @bswap_i32(i32 signext %a) nounwind {
; RV64I-LABEL: bswap_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a0, 8
; RV64I-NEXT:    lui a2, 16
; RV64I-NEXT:    addiw a2, a2, -256
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    srliw a3, a0, 24
; RV64I-NEXT:    or a1, a1, a3
; RV64I-NEXT:    and a2, a0, a2
; RV64I-NEXT:    slliw a2, a2, 8
; RV64I-NEXT:    slliw a0, a0, 24
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL:
 bswap_i32:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    rev8 a0, a0
; RV64ZBB-NEXT:    srai a0, a0, 32
; RV64ZBB-NEXT:    ret
 %1 = tail call i32 @llvm.bswap.i32(i32 %a)
 ret i32 %1
}

; Similar to bswap_i32 but the result is not sign extended.
define void @bswap_i32_nosext(i32 signext %a, ptr %x) nounwind {
; RV64I-LABEL: bswap_i32_nosext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a2, a0, 8
; RV64I-NEXT:    lui a3, 16
; RV64I-NEXT:    addi a3, a3, -256
; RV64I-NEXT:    and a2, a2, a3
; RV64I-NEXT:    srliw a4, a0, 24
; RV64I-NEXT:    or a2, a2, a4
; RV64I-NEXT:    and a3, a0, a3
; RV64I-NEXT:    slli a3, a3, 8
```

```
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    or a0, a0, a3
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    sw a0, 0(a1)
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL: bswap_i32_nosext:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    rev8 a0, a0
; RV64ZBB-NEXT:    srli a0, a0, 32
; RV64ZBB-NEXT:    sw a0, 0(a1)
; RV64ZBB-NEXT:    ret
 %1 = tail call i32 @llvm.bswap.i32(i32
 %a)
 store i32 %1, ptr %x
 ret void
}

declare i64 @llvm.bswap.i64(i64)

define i64 @bswap_i64(i64 %a) {
; RV64I-LABEL: bswap_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a0, 40
; RV64I-NEXT:    lui a2, 16
; RV64I-NEXT:    addiw a2, a2, -256
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    srli a3, a0, 56
; RV64I-NEXT:    or a1, a1, a3
; RV64I-NEXT:    srli a3, a0, 24
; RV64I-NEXT:    lui a4, 4080
; RV64I-NEXT:    and a3, a3, a4
; RV64I-NEXT:    srli a5, a0, 8
; RV64I-NEXT:    srliw a5, a5, 24
; RV64I-NEXT:    slli a5, a5, 24
; RV64I-NEXT:    or a3, a5, a3
; RV64I-NEXT:    or a1, a3, a1
; RV64I-NEXT:    and a4, a0, a4
; RV64I-NEXT:    slli a4, a4, 24
; RV64I-NEXT:    srliw a3, a0, 24
; RV64I-NEXT:    slli a3, a3, 32
; RV64I-NEXT:    or a3, a4, a3
; RV64I-NEXT:    and a2, a0, a2
; RV64I-NEXT:    slli a2, a2, 40
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    or a0, a0, a2
; RV64I-NEXT:    or a0, a0, a3
; RV64I-NEXT:    or a0, a0, a1
```

```
; RV64I-NEXT:    ret
;
; RV64ZBB-LABEL:
 bswap_i64:
; RV64ZBB:       # %bb.0:
; RV64ZBB-NEXT:    rev8 a0, a0
; RV64ZBB-NEXT:    ret
 %1 = call i64 @llvm.bswap.i64(i64 %a)
 ret i64 %1
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+zbkb -verify-machineinstrs < %s \
; RUN:   | FileCheck %s -check-prefix=RV64ZBKB

declare i64 @llvm.riscv.brev8.i64(i64)

define i64 @brev8(i64 %a) nounwind {
; RV64ZBKB-LABEL: brev8:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    brev8 a0, a0
; RV64ZBKB-NEXT:    ret
 %val = call i64 @llvm.riscv.brev8.i64(i64 %a)
 ret i64 %val
}

; Test that rev8 is recognized as preserving zero extension.
define zeroext i16 @brev8_knownbits(i16 zeroext %a) nounwind {
; RV64ZBKB-LABEL: brev8_knownbits:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    brev8 a0, a0
; RV64ZBKB-NEXT:    ret
 %zext = zext i16 %a to i64
 %val = call i64 @llvm.riscv.brev8.i64(i64 %zext)
 %trunc = trunc i64 %val to i16
 ret i16 %trunc
}

declare i64 @llvm.bswap.i64(i64)

define i64 @rev8_i64(i64 %a) {
; RV64ZBKB-LABEL: rev8_i64:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    rev8 a0, a0
; RV64ZBKB-NEXT:    ret
  %1 = call i64 @llvm.bswap.i64(i64 %a)
 ret i64 %1
}
```

```
declare i32 @llvm.riscv.brev8.i32(i32)

define signext i32 @brev8_i32(i32 signext %a) nounwind {
; RV64ZBKB-LABEL: brev8_i32:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    brev8 a0, a0
; RV64ZBKB-NEXT:    sext.w a0, a0
; RV64ZBKB-NEXT:    ret
 %val = call i32 @llvm.riscv.brev8.i32(i32 %a)
 ret i32 %val
}

; Test that rev8 is recognized as preserving zero extension.
define zeroext i16 @brev8_i32_knownbits(i16 zeroext %a) nounwind {
; RV64ZBKB-LABEL: brev8_i32_knownbits:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    brev8 a0, a0
; RV64ZBKB-NEXT:    ret
 %zext = zext i16 %a to i32
 %val = call i32 @llvm.riscv.brev8.i32(i32 %zext)
 %trunc = trunc i32 %val to i16
 ret i16 %trunc
}

declare i32 @llvm.bswap.i32(i32)

define signext i32 @rev8_i32(i32 signext %a) {
; RV64ZBKB-LABEL: rev8_i32:
; RV64ZBKB:       # %bb.0:
; RV64ZBKB-NEXT:    rev8 a0, a0
; RV64ZBKB-NEXT:    srai a0, a0, 32
; RV64ZBKB-NEXT:    ret
 %1 = call i32 @llvm.bswap.i32(i32
 %a)
 ret i32 %1
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=amdgcn -mcpu=fiji < %s | FileCheck -check-prefixes=VI %s

; FIXME: This one should fold to rcp
define half @select_fneg_posk_src_rcp_f16(i32 %c, half %x, half %y) {
; VI-LABEL: select_fneg_posk_src_rcp_f16:
; VI:       ; %bb.0:
; VI-NEXT:    s_waitcnt vmcnt(0) expcnt(0) lgkmcnt(0)
; VI-NEXT:    v_rcp_f16_e64 v1, -v1
; VI-NEXT:    v_mov_b32_e32 v2, 0x4000
; VI-NEXT:    v_cmp_eq_u32_e32 vcc, 0, v0
; VI-NEXT:    v_cndmask_b32_e32 v0, v2, v1, vcc
```

```
; VI-NEXT:    s_setpc_b64 s[30:31]
 %cmp = icmp eq i32 %c, 0
 %rcp = call half @llvm.amdgcn.rcp.f16(half %x)
 %fneg = fneg half %rcp
 %select = select i1 %cmp, half %fneg, half 2.0
 ret half %select
}

declare half @llvm.amdgcn.rcp.f16(half) #0

attributes #0 = { nocallback nofree nosync nounwind speculatable willreturn memory(none) }
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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+m -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64I
; RUN: llc -mtriple=riscv64 -mattr=+m,+zba -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-
prefixes=CHECK,RV64ZBA,RV64ZBANOZBB
; RUN: llc -mtriple=riscv64 -mattr=+m,+zba,+zbb -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=CHECK,RV64ZBA,RV64ZBAZBB

define i64 @slliuw(i64 %a) nounwind {
; RV64I-LABEL: slliuw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 31
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: slliuw:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 1
; RV64ZBA-NEXT:    ret
 %conv1 = shl i64 %a, 1
 %shl = and i64 %conv1, 8589934590
 ret i64 %shl
}

define i128 @slliuw_2(i32 signext %0, ptr %1) {
; RV64I-LABEL:
 slliuw_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 28
; RV64I-NEXT:    add a1, a1, a0
; RV64I-NEXT:    ld a0, 0(a1)
; RV64I-NEXT:    ld a1, 8(a1)
; RV64I-NEXT:    ret
;
```

```
; RV64ZBA-LABEL: slliuw_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 4
; RV64ZBA-NEXT:    add a1, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a1)
; RV64ZBA-NEXT:    ld a1, 8(a1)
; RV64ZBA-NEXT:    ret
 %3 = zext i32 %0 to i64
 %4 = getelementptr inbounds i128, ptr %1, i64 %3
 %5 = load i128, ptr %4
 ret i128 %5
}

define i128 @slliuw_3(i32 signext %0, ptr %1) {
; RV64I-LABEL: slliuw_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 28
; RV64I-NEXT:    add a1, a1, a0
; RV64I-NEXT:    ld a0, 0(a1)
; RV64I-NEXT:    ld a1, 8(a1)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: slliuw_3:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    addi a0, a0, 1
; RV64ZBA-NEXT:    slli.uw a0, a0, 4
; RV64ZBA-NEXT:
   add a1, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a1)
; RV64ZBA-NEXT:    ld a1, 8(a1)
; RV64ZBA-NEXT:    ret
 %add = add i32 %0, 1
 %3 = zext i32 %add to i64
 %4 = getelementptr inbounds i128, ptr %1, i64 %3
 %5 = load i128, ptr %4
 ret i128 %5
}

define i64 @adduw(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: adduw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 32
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ret
;
```

```
; RV64ZBA-LABEL: adduw:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    add.uw a0, a1, a0
; RV64ZBA-NEXT:    ret
 %and = and i64 %b, 4294967295
 %add = add i64 %and, %a
 ret i64 %add
}

define signext i8 @adduw_2(i32 signext %0, ptr %1) {
; RV64I-LABEL: adduw_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lb a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: adduw_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:
   lb a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = zext i32 %0 to i64
 %4 = getelementptr inbounds i8, ptr %1, i64 %3
 %5 = load i8, ptr %4
 ret i8 %5
}

define signext i8 @adduw_3(i32 signext %0, ptr %1) {
; RV64I-LABEL: adduw_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lb a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: adduw_3:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    addi a0, a0, 1
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:    lb a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %add = add i32 %0, 1
 %3 = zext i32 %add to i64
```

```
 %4 = getelementptr inbounds i8, ptr %1, i64 %3
 %5 = load i8, ptr %4
 ret i8 %5
}

define i64 @zextw_i64(i64 %a) nounwind {
; RV64I-LABEL: zextw_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zextw_i64:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:
   zext.w a0, a0
; RV64ZBA-NEXT:    ret
 %and = and i64 %a, 4294967295
 ret i64 %and
}

; This makes sure targetShrinkDemandedConstant changes the and immmediate to
; allow zext.w or slli+srli.
define i64 @zextw_demandedbits_i64(i64 %0) {
; RV64I-LABEL: zextw_demandedbits_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    ori a0, a0, 1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zextw_demandedbits_i64:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    ori a0, a0, 1
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    ret
 %2 = and i64 %0, 4294967294
 %3 = or i64 %2, 1
 ret i64 %3
}

define signext i16 @sh1add(i64 %0, ptr %1) {
; RV64I-LABEL: sh1add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 1
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: sh1add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a1
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:
   ret
 %3 = getelementptr inbounds i16, ptr %1, i64 %0
 %4 = load i16, ptr %3
 ret i16 %4
}

define signext i32 @sh2add(i64 %0, ptr %1) {
; RV64I-LABEL: sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 2
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh2add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = getelementptr inbounds i32, ptr %1, i64 %0
 %4 = load i32, ptr %3
 ret i32 %4
}

define i64 @sh3add(i64 %0, ptr %1) {
; RV64I-LABEL: sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 3
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a1
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = getelementptr inbounds i64, ptr %1, i64 %0
 %4 = load i64, ptr %3
 ret i64 %4
}
```

```
define signext i16 @sh1adduw(i32
 signext %0, ptr %1) {
; RV64I-LABEL: sh1adduw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 31
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh1adduw:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add.uw a0, a0, a1
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = zext i32 %0 to i64
 %4 = getelementptr inbounds i16, ptr %1, i64 %3
 %5 = load i16, ptr %4
 ret i16 %5
}

define i64 @sh1adduw_2(i64 %0, i64 %1) {
; RV64I-LABEL: sh1adduw_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 31
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh1adduw_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %3 = shl i64 %0, 1
 %4 = and i64 %3, 8589934590
 %5 = add i64 %4, %1
 ret i64 %5
}

define signext i32 @sh2adduw(i32 signext %0, ptr %1) {
; RV64I-LABEL: sh2adduw:
; RV64I:
      # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 30
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: sh2adduw:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add.uw a0, a0, a1
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = zext i32 %0 to i64
 %4 = getelementptr inbounds i32, ptr %1, i64 %3
 %5 = load i32, ptr %4
 ret i32 %5
}

define i64 @sh2adduw_2(i64 %0, i64 %1) {
; RV64I-LABEL: sh2adduw_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 30
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh2adduw_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %3 = shl i64 %0, 2
 %4 = and i64 %3, 17179869180
 %5 = add i64 %4, %1
 ret i64 %5
}

define i64 @sh3adduw(i32 signext %0, ptr %1) {
; RV64I-LABEL: sh3adduw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:
 srli a0, a0, 29
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh3adduw:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add.uw a0, a0, a1
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = zext i32 %0 to i64
 %4 = getelementptr inbounds i64, ptr %1, i64 %3
 %5 = load i64, ptr %4
```

```
 ret i64 %5
}

define i64 @sh3adduw_2(i64 %0, i64 %1) {
; RV64I-LABEL: sh3adduw_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 29
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh3adduw_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %3 = shl i64 %0, 3
 %4 = and i64 %3, 34359738360
 %5 = add i64 %4, %1
 ret i64 %5
}

; Type legalization inserts a sext_inreg after the first add. That add will be
; selected as sh2add which does not sign extend. SimplifyDemandedBits is unable
; to remove the sext_inreg because it has multiple uses.
 The ashr will use the
; sext_inreg to become sraiw. This leaves the sext_inreg only used by the shl.
; If the shl is selected as sllw, we don't need the sext_inreg.
define i64 @sh2add_extra_sext(i32 %x, i32 %y, i32 %z) {
; RV64I-LABEL: sh2add_extra_sext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    sllw a1, a2, a0
; RV64I-NEXT:    sraiw a0, a0, 2
; RV64I-NEXT:    mul a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh2add_extra_sext:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    sllw a1, a2, a0
; RV64ZBA-NEXT:    sraiw a0, a0, 2
; RV64ZBA-NEXT:    mul a0, a1, a0
; RV64ZBA-NEXT:    ret
 %a = shl i32 %x, 2
 %b = add i32 %a, %y
 %c = shl i32 %z, %b
 %d = ashr i32 %b, 2
```

```
 %e = sext i32 %c to i64
 %f = sext i32 %d to i64
 %g = mul i64 %e, %f
 ret i64 %g
}

define i64 @addmul6(i64 %a, i64 %b) {
; RV64I-LABEL: addmul6:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a2, a0, 1
; RV64I-NEXT:
   slli a0, a0, 3
; RV64I-NEXT:    sub a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul6:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a0
; RV64ZBA-NEXT:    sh1add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 6
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul10(i64 %a, i64 %b) {
; RV64I-LABEL: addmul10:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 10
; RV64I-NEXT:    mul a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul10:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    sh1add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 10
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul12(i64 %a, i64 %b) {
; RV64I-LABEL: addmul12:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a2, a0, 2
```

```
; RV64I-NEXT:    slli a0, a0, 4
; RV64I-NEXT:    sub a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul12:
; RV64ZBA:
      # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 12
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul18(i64 %a, i64 %b) {
; RV64I-LABEL: addmul18:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 18
; RV64I-NEXT:    mul a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul18:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    sh1add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 18
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul20(i64 %a, i64 %b) {
; RV64I-LABEL: addmul20:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 20
; RV64I-NEXT:    mul a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul20:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 20
```

```
 %d = add i64 %c, %b
 ret i64 %d
}

define
 i64 @addmul24(i64 %a, i64 %b) {
; RV64I-LABEL: addmul24:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a2, a0, 3
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    sub a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul24:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 24
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul36(i64 %a, i64 %b) {
; RV64I-LABEL: addmul36:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 36
; RV64I-NEXT:    mul a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul36:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 36
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul40(i64 %a, i64 %b) {
; RV64I-LABEL: addmul40:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 40
; RV64I-NEXT:    mul a0,
```

```
 a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul40:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 40
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @addmul72(i64 %a, i64 %b) {
; RV64I-LABEL: addmul72:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 72
; RV64I-NEXT:    mul a0, a0, a2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addmul72:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 72
 %d = add i64 %c, %b
 ret i64 %d
}

define i64 @mul96(i64 %a) {
; RV64I-LABEL: mul96:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 5
; RV64I-NEXT:    slli a0, a0, 7
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul96:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a0
; RV64ZBA-NEXT:    slli a0, a0, 5
;
 RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 96
 ret i64 %c
```

```
}

define i64 @mul160(i64 %a) {
; RV64I-LABEL: mul160:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 160
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul160:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    slli a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 160
 ret i64 %c
}

define i64 @mul288(i64 %a) {
; RV64I-LABEL: mul288:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 288
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul288:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    slli a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 288
 ret i64 %c
}

define i64 @zext_mul96(i32 signext %a) {
; RV64I-LABEL: zext_mul96:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a1, a0, 27
; RV64I-NEXT:    srli a0, a0, 25
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL:
 zext_mul96:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 5
; RV64ZBA-NEXT:    sh1add a0, a0, a0
```

```
; RV64ZBA-NEXT:    ret
 %b = zext i32 %a to i64
 %c = mul i64 %b, 96
 ret i64 %c
}

define i64 @zext_mul160(i32 signext %a) {
; RV64I-LABEL: zext_mul160:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    slli a1, a1, 37
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    mulhu a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zext_mul160:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 5
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    ret
 %b = zext i32 %a to i64
 %c = mul i64 %b, 160
 ret i64 %c
}

define i64 @zext_mul288(i32 signext %a) {
; RV64I-LABEL: zext_mul288:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 9
; RV64I-NEXT:    slli a1, a1, 37
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    mulhu a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zext_mul288:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0,
 5
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    ret
 %b = zext i32 %a to i64
 %c = mul i64 %b, 288
 ret i64 %c
}

; We can't use slli.uw becaues the shift amount is more than 31.
define i64 @zext_mul12884901888(i32 signext %a) {
; RV64I-LABEL: zext_mul12884901888:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 32
; RV64I-NEXT:    slli a0, a0, 34
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zext_mul12884901888:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a0
; RV64ZBA-NEXT:    slli a0, a0, 32
; RV64ZBA-NEXT:    ret
 %b = zext i32 %a to i64
 %c = mul i64 %b, 12884901888
 ret i64 %c
}

; We can't use slli.uw becaues the shift amount is more than 31.
define i64 @zext_mul21474836480(i32 signext %a) {
; RV64I-LABEL: zext_mul21474836480:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zext_mul21474836480:
; RV64ZBA:
  # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    slli a0, a0, 32
; RV64ZBA-NEXT:    ret
 %b = zext i32 %a to i64
 %c = mul i64 %b, 21474836480
 ret i64 %c
}

; We can't use slli.uw becaues the shift amount is more than 31.
define i64 @zext_mul38654705664(i32 signext %a) {
; RV64I-LABEL: zext_mul38654705664:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 9
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: zext_mul38654705664:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a0
```

```
; RV64ZBA-NEXT:    slli a0, a0, 32
; RV64ZBA-NEXT:    ret
 %b = zext i32 %a to i64
 %c = mul i64 %b, 38654705664
 ret i64 %c
}

define i64 @sh1add_imm(i64 %0) {
; CHECK-LABEL: sh1add_imm:
; CHECK:       # %bb.0:
; CHECK-NEXT:    slli a0, a0, 1
; CHECK-NEXT:    addi a0, a0, 5
; CHECK-NEXT:    ret
 %a = shl i64 %0, 1
 %b = add i64 %a, 5
 ret i64 %b
}

define i64 @sh2add_imm(i64 %0) {
; CHECK-LABEL: sh2add_imm:
; CHECK:
  # %bb.0:
; CHECK-NEXT:    slli a0, a0, 2
; CHECK-NEXT:    addi a0, a0, -6
; CHECK-NEXT:    ret
 %a = shl i64 %0, 2
 %b = add i64 %a, -6
 ret i64 %b
}

define i64 @sh3add_imm(i64 %0) {
; CHECK-LABEL: sh3add_imm:
; CHECK:       # %bb.0:
; CHECK-NEXT:    slli a0, a0, 3
; CHECK-NEXT:    addi a0, a0, 7
; CHECK-NEXT:    ret
 %a = shl i64 %0, 3
 %b = add i64 %a, 7
 ret i64 %b
}

define i64 @sh1adduw_imm(i32 signext %0) {
; RV64I-LABEL: sh1adduw_imm:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 31
; RV64I-NEXT:    addi a0, a0, 11
; RV64I-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: sh1adduw_imm:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 1
; RV64ZBA-NEXT:    addi a0, a0, 11
; RV64ZBA-NEXT:    ret
 %a = zext i32 %0 to i64
 %b = shl i64 %a, 1
 %c = add i64 %b, 11
 ret i64 %c
}

define i64 @sh2adduw_imm(i32 signext %0) {
; RV64I-LABEL: sh2adduw_imm:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 30
;
 RV64I-NEXT:    addi a0, a0, -12
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh2adduw_imm:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 2
; RV64ZBA-NEXT:    addi a0, a0, -12
; RV64ZBA-NEXT:    ret
 %a = zext i32 %0 to i64
 %b = shl i64 %a, 2
 %c = add i64 %b, -12
 ret i64 %c
}

define i64 @sh3adduw_imm(i32 signext %0) {
; RV64I-LABEL: sh3adduw_imm:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 29
; RV64I-NEXT:    addi a0, a0, 13
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh3adduw_imm:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli.uw a0, a0, 3
; RV64ZBA-NEXT:    addi a0, a0, 13
; RV64ZBA-NEXT:    ret
 %a = zext i32 %0 to i64
 %b = shl i64 %a, 3
 %c = add i64 %b, 13
```

```
 ret i64 %c
}

define i64 @adduw_imm(i32 signext %0) nounwind {
; RV64I-LABEL: adduw_imm:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    addi a0, a0, 5
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: adduw_imm:
;
 RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    addi a0, a0, 5
; RV64ZBA-NEXT:    ret
 %a = zext i32 %0 to i64
 %b = add i64 %a, 5
 ret i64 %b
}

define i64 @mul258(i64 %a) {
; RV64I-LABEL: mul258:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 258
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul258:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a0, 8
; RV64ZBA-NEXT:    sh1add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 258
 ret i64 %c
}

define i64 @mul260(i64 %a) {
; RV64I-LABEL: mul260:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 260
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul260:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a0, 8
```

```
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 260
 ret i64 %c
}

define i64 @mul264(i64 %a) {
; RV64I-LABEL: mul264:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 264
; RV64I-NEXT:    mul a0, a0, a1
;
 RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul264:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a0, 8
; RV64ZBA-NEXT:    sh3add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 264
 ret i64 %c
}

define i64 @imm_zextw() nounwind {
; RV64I-LABEL: imm_zextw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    addi a0, a0, -2
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: imm_zextw:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    li a0, -2
; RV64ZBA-NEXT:    zext.w a0, a0
; RV64ZBA-NEXT:    ret
 ret i64 4294967294 ; -2 in 32 bits.
}

define i64 @mul11(i64 %a) {
; RV64I-LABEL: mul11:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 11
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul11:
; RV64ZBA:       # %bb.0:
```

```
; RV64ZBA-NEXT:    sh2add a1, a0, a0
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 11
 ret i64 %c
}

define i64 @mul19(i64 %a) {
; RV64I-LABEL: mul19:
; RV64I:       # %bb.0:
;
 RV64I-NEXT:    li a1, 19
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul19:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a1, a0, a0
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 19
 ret i64 %c
}

define i64 @mul13(i64 %a) {
; RV64I-LABEL: mul13:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 13
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul13:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a1, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 13
 ret i64 %c
}

define i64 @mul21(i64 %a) {
; RV64I-LABEL: mul21:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 21
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul21:
```

```
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a1, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 21
 ret i64 %c
}

define i64 @mul37(i64 %a) {
; RV64I-LABEL: mul37:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 37
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul37:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a1, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 37
 ret i64 %c
}

define i64 @mul25(i64 %a) {
; RV64I-LABEL: mul25:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 25
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul25:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 25
 ret i64 %c
}

define i64 @mul41(i64 %a) {
; RV64I-LABEL: mul41:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 41
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
```

```
; RV64ZBA-LABEL: mul41:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a1, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 41
 ret i64 %c
}

define i64 @mul73(i64 %a) {
;
 RV64I-LABEL: mul73:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 73
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul73:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a1, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 73
 ret i64 %c
}

define i64 @mul27(i64 %a) {
; RV64I-LABEL: mul27:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 27
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul27:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 27
 ret i64 %c
}

define i64 @mul45(i64 %a) {
; RV64I-LABEL: mul45:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 45
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
```

```
;
; RV64ZBA-LABEL: mul45:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 45
 ret i64 %c
}

define
 i64 @mul81(i64 %a) {
; RV64I-LABEL: mul81:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 81
; RV64I-NEXT:    mul a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul81:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    sh3add a0, a0, a0
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 81
 ret i64 %c
}

define i64 @mul4098(i64 %a) {
; RV64I-LABEL: mul4098:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 1
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul4098:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a0, 12
; RV64ZBA-NEXT:    sh1add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 4098
 ret i64 %c
}

define i64 @mul4100(i64 %a) {
; RV64I-LABEL: mul4100:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 2
```

```
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul4100:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1,
 a0, 12
; RV64ZBA-NEXT:    sh2add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 4100
 ret i64 %c
}

define i64 @mul4104(i64 %a) {
; RV64I-LABEL: mul4104:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 3
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: mul4104:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a0, 12
; RV64ZBA-NEXT:    sh3add a0, a0, a1
; RV64ZBA-NEXT:    ret
 %c = mul i64 %a, 4104
 ret i64 %c
}

define signext i32 @mulw192(i32 signext %a) {
; CHECK-LABEL: mulw192:
; CHECK:       # %bb.0:
; CHECK-NEXT:    li a1, 192
; CHECK-NEXT:    mulw a0, a0, a1
; CHECK-NEXT:    ret
 %c = mul i32 %a, 192
 ret i32 %c
}

define signext i32 @mulw320(i32 signext %a) {
; CHECK-LABEL: mulw320:
; CHECK:       # %bb.0:
; CHECK-NEXT:    li a1, 320
; CHECK-NEXT:    mulw a0, a0, a1
; CHECK-NEXT:    ret
 %c = mul i32 %a, 320
```

```
 ret i32 %c
}

define signext i32 @mulw576(i32 signext %a) {
; CHECK-LABEL: mulw576:
; CHECK:
      # %bb.0:
; CHECK-NEXT:    li a1, 576
; CHECK-NEXT:    mulw a0, a0, a1
; CHECK-NEXT:    ret
 %c = mul i32 %a, 576
 ret i32 %c
}

define i64 @add4104(i64 %a) {
; RV64I-LABEL: add4104:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 1
; RV64I-NEXT:    addiw a1, a1, 8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: add4104:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    li a1, 1026
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = add i64 %a, 4104
 ret i64 %c
}

define i64 @add8208(i64 %a) {
; RV64I-LABEL: add8208:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 2
; RV64I-NEXT:    addiw a1, a1, 16
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: add8208:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    li a1, 1026
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ret
 %c = add i64 %a, 8208
 ret i64 %c
}
```

```
; Make sure we prefer LUI for the 8192 instead of using sh3add.
define signext i32 @add8192_i32(i32 signext %a)
 {
; CHECK-LABEL: add8192_i32:
; CHECK:       # %bb.0:
; CHECK-NEXT:    lui a1, 2
; CHECK-NEXT:    addw a0, a0, a1
; CHECK-NEXT:    ret
 %c = add i32 %a, 8192
 ret i32 %c
}

; Make sure we prefer LUI for the 8192 instead of using sh3add.
define i64 @add8192(i64 %a) {
; CHECK-LABEL: add8192:
; CHECK:       # %bb.0:
; CHECK-NEXT:    lui a1, 2
; CHECK-NEXT:    add a0, a0, a1
; CHECK-NEXT:    ret
 %c = add i64 %a, 8192
 ret i64 %c
}

define signext i32 @addshl32_5_6(i32 signext %a, i32 signext %b) {
; RV64I-LABEL: addshl32_5_6:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    slli a1, a1, 6
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addshl32_5_6:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    slliw a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = shl i32 %a, 5
 %d = shl i32 %b, 6
 %e = add i32 %c, %d
 ret i32 %e
}

define i64 @addshl64_5_6(i64 %a, i64 %b) {
; RV64I-LABEL: addshl64_5_6:
; RV64I:       #
 %bb.0:
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    slli a1, a1, 6
```

```
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addshl64_5_6:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    slli a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = shl i64 %a, 5
 %d = shl i64 %b, 6
 %e = add i64 %c, %d
 ret i64 %e
}

define signext i32 @addshl32_5_7(i32 signext %a, i32 signext %b) {
; RV64I-LABEL: addshl32_5_7:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    slli a1, a1, 7
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addshl32_5_7:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    slliw a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = shl i32 %a, 5
 %d = shl i32 %b, 7
 %e = add i32 %c, %d
 ret i32 %e
}

define i64 @addshl64_5_7(i64 %a, i64 %b) {
; RV64I-LABEL: addshl64_5_7:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    slli a1, a1, 7
; RV64I-NEXT:
    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addshl64_5_7:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    slli a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = shl i64 %a, 5
```

```
 %d = shl i64 %b, 7
 %e = add i64 %c, %d
 ret i64 %e
}

define signext i32 @addshl32_5_8(i32 signext %a, i32 signext %b) {
; RV64I-LABEL: addshl32_5_8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    slli a1, a1, 8
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addshl32_5_8:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    slliw a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = shl i32 %a, 5
 %d = shl i32 %b, 8
 %e = add i32 %c, %d
 ret i32 %e
}

define i64 @addshl64_5_8(i64 %a, i64 %b) {
; RV64I-LABEL: addshl64_5_8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 5
; RV64I-NEXT:    slli a1, a1, 8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: addshl64_5_8:
; RV64ZBA:
      # %bb.0:
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    slli a0, a0, 5
; RV64ZBA-NEXT:    ret
 %c = shl i64 %a, 5
 %d = shl i64 %b, 8
 %e = add i64 %c, %d
 ret i64 %e
}

; Make sure we use sext.h+slli+srli for Zba+Zbb.
; FIXME: The RV64I and Zba only cases can be done with only 3 shifts.
define zeroext i32 @sext_ashr_zext_i8(i8 %a) nounwind {
; RV64I-LABEL: sext_ashr_zext_i8:
```

```
; RV64I:      # %bb.0:
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a0, a0, 31
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBANOZBB-LABEL: sext_ashr_zext_i8:
; RV64ZBANOZBB:      # %bb.0:
; RV64ZBANOZBB-NEXT:    slli a0, a0, 24
; RV64ZBANOZBB-NEXT:    sraiw a0, a0, 31
; RV64ZBANOZBB-NEXT:    zext.w a0, a0
; RV64ZBANOZBB-NEXT:    ret
;
; RV64ZBAZBB-LABEL: sext_ashr_zext_i8:
; RV64ZBAZBB:      # %bb.0:
; RV64ZBAZBB-NEXT:    sext.b a0, a0
; RV64ZBAZBB-NEXT:    sraiw a0, a0, 9
; RV64ZBAZBB-NEXT:    zext.w a0, a0
; RV64ZBAZBB-NEXT:
   ret
 %ext = sext i8 %a to i32
 %1 = ashr i32 %ext, 9
 ret i32 %1
}

; Make sure we use sext.h+slli+srli for Zba+Zbb.
; FIXME: The RV64I and Zba only cases can be done with only 3 shifts.
define zeroext i32 @sext_ashr_zext_i16(i16 %a) nounwind {
; RV64I-LABEL: sext_ashr_zext_i16:
; RV64I:      # %bb.0:
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 25
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64ZBANOZBB-LABEL: sext_ashr_zext_i16:
; RV64ZBANOZBB:      # %bb.0:
; RV64ZBANOZBB-NEXT:    slli a0, a0, 16
; RV64ZBANOZBB-NEXT:    sraiw a0, a0, 25
; RV64ZBANOZBB-NEXT:    zext.w a0, a0
; RV64ZBANOZBB-NEXT:    ret
;
; RV64ZBAZBB-LABEL: sext_ashr_zext_i16:
; RV64ZBAZBB:      # %bb.0:
; RV64ZBAZBB-NEXT:    slli a0, a0, 48
; RV64ZBAZBB-NEXT:    srai a0, a0, 57
```

```
; RV64ZBAZBB-NEXT:    zext.w a0, a0
; RV64ZBAZBB-NEXT:    ret
 %ext = sext i16 %a to i32
 %1 = ashr i32 %ext, 9
 ret i32 %1
}

; This the IR you get from InstCombine
 if take the difference of 2 pointers and
; cast is to unsigned before using as an index.
define signext i16 @sh1adduw_ptrdiff(i64 %diff, ptr %baseptr) {
; RV64I-LABEL: sh1adduw_ptrdiff:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    slli a2, a2, 33
; RV64I-NEXT:    addi a2, a2, -2
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh1adduw_ptrdiff:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a0, a0, 1
; RV64ZBA-NEXT:    sh1add.uw a0, a0, a1
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %ptrdiff = lshr exact i64 %diff, 1
 %cast = and i64 %ptrdiff, 4294967295
 %ptr = getelementptr inbounds i16, ptr %baseptr, i64 %cast
 %res = load i16, ptr %ptr
 ret i16 %res
}

define signext i32 @sh2adduw_ptrdiff(i64 %diff, ptr %baseptr) {
; RV64I-LABEL: sh2adduw_ptrdiff:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    slli a2, a2, 34
; RV64I-NEXT:    addi a2,
 a2, -4
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh2adduw_ptrdiff:
; RV64ZBA:       # %bb.0:
```

```
; RV64ZBA-NEXT:    srli a0, a0, 2
; RV64ZBA-NEXT:    sh2add.uw a0, a0, a1
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %ptrdiff = lshr exact i64 %diff, 2
 %cast = and i64 %ptrdiff, 4294967295
 %ptr = getelementptr inbounds i32, ptr %baseptr, i64 %cast
 %res = load i32, ptr %ptr
 ret i32 %res
}

define i64 @sh3adduw_ptrdiff(i64 %diff, ptr %baseptr) {
; RV64I-LABEL: sh3adduw_ptrdiff:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    slli a2, a2, 35
; RV64I-NEXT:    addi a2, a2, -8
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: sh3adduw_ptrdiff:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a0, a0, 3
; RV64ZBA-NEXT:    sh3add.uw a0, a0, a1
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:
   ret
 %ptrdiff = lshr exact i64 %diff, 3
 %cast = and i64 %ptrdiff, 4294967295
 %ptr = getelementptr inbounds i64, ptr %baseptr, i64 %cast
 %res = load i64, ptr %ptr
 ret i64 %res
}

define signext i16 @srliw_1_sh1add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_1_sh1add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 1
; RV64I-NEXT:    slli a1, a1, 1
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_1_sh1add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 1
```

```
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 1
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i16, ptr %0, i64 %4
 %6 = load i16, ptr %5, align 2
 ret i16 %6
}

define i128 @slliuw_ptrdiff(i64 %diff, ptr %baseptr) {
; RV64I-LABEL: slliuw_ptrdiff:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a2, 1
; RV64I-NEXT:    slli a2, a2, 36
; RV64I-NEXT:    addi a2, a2, -16
; RV64I-NEXT:
   and a0, a0, a2
; RV64I-NEXT:    add a1, a1, a0
; RV64I-NEXT:    ld a0, 0(a1)
; RV64I-NEXT:    ld a1, 8(a1)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: slliuw_ptrdiff:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a0, a0, 4
; RV64ZBA-NEXT:    slli.uw a0, a0, 4
; RV64ZBA-NEXT:    add a1, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a1)
; RV64ZBA-NEXT:    ld a1, 8(a1)
; RV64ZBA-NEXT:    ret
 %ptrdiff = lshr exact i64 %diff, 4
 %cast = and i64 %ptrdiff, 4294967295
 %ptr = getelementptr inbounds i128, ptr %baseptr, i64 %cast
 %res = load i128, ptr %ptr
 ret i128 %res
}

define signext i32 @srliw_2_sh2add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_2_sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 2
; RV64I-NEXT:    slli a1, a1, 2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_2_sh2add:
```

```
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 2
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    lw a0,
 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 2
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i32, ptr %0, i64 %4
 %6 = load i32, ptr %5, align 4
 ret i32 %6
}

define i64 @srliw_3_sh3add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_3_sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 3
; RV64I-NEXT:    slli a1, a1, 3
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_3_sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 3
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 3
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i64, ptr %0, i64 %4
 %6 = load i64, ptr %5, align 8
 ret i64 %6
}

define signext i32 @srliw_1_sh2add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_1_sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 1
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 30
; RV64I-NEXT:
 add a0, a0, a1
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_1_sh2add:
; RV64ZBA:       # %bb.0:
```

```
; RV64ZBA-NEXT:    srliw a1, a1, 1
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 1
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i32, ptr %0, i64 %4
 %6 = load i32, ptr %5, align 4
 ret i32 %6
}

define i64 @srliw_1_sh3add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_1_sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 1
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 29
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_1_sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 1
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 1
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i64, ptr %0, i64 %4
 %6 = load i64, ptr %5,
 align 8
 ret i64 %6
}

define i64 @srliw_2_sh3add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_2_sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 2
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 29
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_2_sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 2
```

```
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 2
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i64, ptr %0, i64 %4
 %6 = load i64, ptr %5, align 8
 ret i64 %6
}

define signext i16 @srliw_2_sh1add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_2_sh1add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 2
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 31
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_2_sh1add:
; RV64ZBA:
  # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 2
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 2
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i16, ptr %0, i64 %4
 %6 = load i16, ptr %5, align 2
 ret i16 %6
}


define signext i32 @srliw_3_sh2add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_3_sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 3
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 30
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_3_sh2add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 3
```

```
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 3
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i32, ptr %0, i64 %4
 %6 = load i32, ptr %5, align 4
 ret i32 %6
}

define i64 @srliw_4_sh3add(ptr %0, i32 signext %1) {
; RV64I-LABEL: srliw_4_sh3add:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a1, a1, 4
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 29
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srliw_4_sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srliw a1, a1, 4
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i32 %1, 4
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i64, ptr %0, i64 %4
 %6 = load i64, ptr %5, align 8
 ret i64 %6
}

define signext i32 @srli_1_sh2add(ptr %0, i64 %1) {
; RV64I-LABEL: srli_1_sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 1
; RV64I-NEXT:    andi a1, a1, -4
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srli_1_sh2add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a1, a1, 1
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    lw a0, 0(a0)
```

```
; RV64ZBA-NEXT:    ret
 %3 = lshr i64 %1,
 1
 %4 = getelementptr inbounds i32, ptr %0, i64 %3
 %5 = load i32, ptr %4, align 4
 ret i32 %5
}

define i64 @srli_2_sh3add(ptr %0, i64 %1) {
; RV64I-LABEL: srli_2_sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 1
; RV64I-NEXT:    andi a1, a1, -8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srli_2_sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a1, a1, 2
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i64 %1, 2
 %4 = getelementptr inbounds i64, ptr %0, i64 %3
 %5 = load i64, ptr %4, align 8
 ret i64 %5
}

define signext i16 @srli_2_sh1add(ptr %0, i64 %1) {
; RV64I-LABEL: srli_2_sh1add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a1, 1
; RV64I-NEXT:    andi a1, a1, -2
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srli_2_sh1add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a1, a1,
 2
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i64 %1, 2
 %4 = getelementptr inbounds i16, ptr %0, i64 %3
 %5 = load i16, ptr %4, align 2
```

```
 ret i16 %5
}

define signext i32 @srli_3_sh2add(ptr %0, i64 %1) {
; RV64I-LABEL: srli_3_sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a1, 1
; RV64I-NEXT:    andi a1, a1, -4
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srli_3_sh2add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a1, a1, 3
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i64 %1, 3
 %4 = getelementptr inbounds i32, ptr %0, i64 %3
 %5 = load i32, ptr %4, align 4
 ret i32 %5
}

define i64 @srli_4_sh3add(ptr %0, i64 %1) {
; RV64I-LABEL: srli_4_sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srli a1, a1, 1
; RV64I-NEXT:    andi a1, a1, -8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
;
 RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: srli_4_sh3add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    srli a1, a1, 4
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = lshr i64 %1, 4
 %4 = getelementptr inbounds i64, ptr %0, i64 %3
 %5 = load i64, ptr %4, align 8
 ret i64 %5
}

define signext i16 @shl_2_sh1add(ptr %0, i32 signext %1) {
; RV64I-LABEL: shl_2_sh1add:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 2
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 31
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lh a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: shl_2_sh1add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a1, 2
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    sh1add a0, a1, a0
; RV64ZBA-NEXT:    lh a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = shl i32 %1, 2
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i16, ptr %0, i64 %4
 %6 = load i16, ptr %5, align 2
 ret i16 %6
}

define signext
 i32 @shl_16_sh2add(ptr %0, i32 signext %1) {
; RV64I-LABEL: shl_16_sh2add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 16
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 30
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    lw a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: shl_16_sh2add:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a1, 16
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    sh2add a0, a1, a0
; RV64ZBA-NEXT:    lw a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = shl i32 %1, 16
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i32, ptr %0, i64 %4
 %6 = load i32, ptr %5, align 4
 ret i32 %6
}

define i64 @shl_31_sh3add(ptr %0, i32 signext %1) {
```

```
; RV64I-LABEL: shl_31_sh3add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 31
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 29
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: shl_31_sh3add:
; RV64ZBA:       # %bb.0:
;
 RV64ZBA-NEXT:    slli a1, a1, 31
; RV64ZBA-NEXT:    zext.w a1, a1
; RV64ZBA-NEXT:    sh3add a0, a1, a0
; RV64ZBA-NEXT:    ld a0, 0(a0)
; RV64ZBA-NEXT:    ret
 %3 = shl i32 %1, 31
 %4 = zext i32 %3 to i64
 %5 = getelementptr inbounds i64, ptr %0, i64 %4
 %6 = load i64, ptr %5, align 8
 ret i64 %6
}

define i64 @pack_i64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: pack_i64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: pack_i64:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a1, 32
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %shl = and i64 %a, 4294967295
 %shl1 = shl i64 %b, 32
 %or = or i64 %shl1, %shl
 ret i64 %or
}

define i64 @pack_i64_2(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: pack_i64_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
```

```
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:
   slli a1, a1, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: pack_i64_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    slli a1, a1, 32
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %zexta = zext i32 %a to i64
 %zextb = zext i32 %b to i64
 %shl1 = shl i64 %zextb, 32
 %or = or i64 %shl1, %zexta
 ret i64 %or
}

define i64 @pack_i64_3(i32 signext %a, i32 signext %b) nounwind {
; RV64I-LABEL: pack_i64_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    addi a1, a1, 1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: pack_i64_3:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    addi a0, a0, 1
; RV64ZBA-NEXT:    addi a1, a1, 1
; RV64ZBA-NEXT:    slli a1, a1, 32
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %adda = add i32 %a, 1
 %addb = add i32 %b, 1
 %zexta = zext i32 %adda to i64
 %zextb
 = zext i32 %addb to i64
 %shl1 = shl i64 %zextb, 32
 %or = or i64 %shl1, %zexta
 ret i64 %or
}

define i64 @pack_i64_disjoint(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: pack_i64_disjoint:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: pack_i64_disjoint:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %shl = and i64 %a, 4294967295
 %or = or disjoint i64 %b, %shl
 ret i64 %or
}

define i64 @pack_i64_disjoint_2(i32 signext %a, i64 %b) nounwind {
; RV64I-LABEL: pack_i64_disjoint_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64ZBA-LABEL: pack_i64_disjoint_2:
; RV64ZBA:       # %bb.0:
; RV64ZBA-NEXT:    add.uw a0, a0, a1
; RV64ZBA-NEXT:    ret
 %zexta = zext i32 %a to i64
 %or = or disjoint i64 %b, %zexta
 ret i64 %or
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=RV64I %s
; RUN: llc -mtriple=riscv64 -mattr=+m -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=RV64IM %s

define i32 @urem(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: urem:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 32
; RV64I-NEXT:    call __umoddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
```

```
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: urem:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    remuw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = urem i32 %a, %b
 ret i32 %1
}

define i32 @urem_constant_lhs(i32 %a) nounwind {
;
 RV64I-LABEL: urem_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a1, a0, 32
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    call __umoddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: urem_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    remuw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = urem i32 10, %a
 ret i32 %1
}

define i32 @srem(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: srem:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sext.w a1, a1
; RV64I-NEXT:    call __moddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem:
; RV64IM:       # %bb.0:
```

```
; RV64IM-NEXT:
   remw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = srem i32 %a, %b
 ret i32 %1
}

define i32 @srem_pow2(i32 %a) nounwind {
; RV64I-LABEL: srem_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraiw a1, a0, 31
; RV64I-NEXT:    srliw a1, a1, 29
; RV64I-NEXT:    add a1, a0, a1
; RV64I-NEXT:    andi a1, a1, -8
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    sraiw a1, a0, 31
; RV64IM-NEXT:    srliw a1, a1, 29
; RV64IM-NEXT:    add a1, a0, a1
; RV64IM-NEXT:    andi a1, a1, -8
; RV64IM-NEXT:    subw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = srem i32 %a, 8
 ret i32 %1
}

define i32 @srem_pow2_2(i32 %a) nounwind {
; RV64I-LABEL: srem_pow2_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraiw a1, a0, 31
; RV64I-NEXT:    srliw a1, a1, 16
; RV64I-NEXT:    add a1, a0, a1
; RV64I-NEXT:    lui a2, 1048560
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem_pow2_2:
; RV64IM:
      # %bb.0:
; RV64IM-NEXT:    sraiw a1, a0, 31
; RV64IM-NEXT:    srliw a1, a1, 16
; RV64IM-NEXT:    add a1, a0, a1
; RV64IM-NEXT:    lui a2, 1048560
; RV64IM-NEXT:    and a1, a1, a2
```

```
; RV64IM-NEXT:    subw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = srem i32 %a, 65536
 ret i32 %1
}

define i32 @srem_constant_lhs(i32 %a) nounwind {
; RV64I-LABEL: srem_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sext.w a1, a0
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    call __moddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    remw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = srem i32 -10, %a
 ret i32 %1
}

define i64 @urem64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: urem64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    tail __umoddi3
;
; RV64IM-LABEL:
 urem64:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    remu a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = urem i64 %a, %b
 ret i64 %1
}

define i64 @urem64_constant_lhs(i64 %a) nounwind {
; RV64I-LABEL: urem64_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mv a1, a0
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    tail __umoddi3
;
```

```
; RV64IM-LABEL: urem64_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    remu a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = urem i64 10, %a
 ret i64 %1
}

define i64 @srem64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: srem64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    tail __moddi3
;
; RV64IM-LABEL: srem64:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    rem a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = srem i64 %a, %b
 ret i64 %1
}

define i64 @srem64_constant_lhs(i64 %a) nounwind {
; RV64I-LABEL: srem64_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mv a1, a0
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    tail __moddi3
;
; RV64IM-LABEL: srem64_constant_lhs:
;
 RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    rem a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = srem i64 -10, %a
 ret i64 %1
}

define i8 @urem8(i8 %a, i8 %b) nounwind {
; RV64I-LABEL: urem8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    andi a1, a1, 255
; RV64I-NEXT:    call __umoddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
```

```
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: urem8:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    andi a1, a1, 255
; RV64IM-NEXT:    andi a0, a0, 255
; RV64IM-NEXT:    remuw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = urem i8 %a, %b
 ret i8 %1
}

define i8 @urem8_constant_lhs(i8 %a) nounwind {
; RV64I-LABEL: urem8_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    andi a1, a0, 255
; RV64I-NEXT:
   li a0, 10
; RV64I-NEXT:    call __umoddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: urem8_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    andi a0, a0, 255
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    remuw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = urem i8 10, %a
 ret i8 %1
}


define i8 @srem8(i8 %a, i8 %b) nounwind {
; RV64I-LABEL: srem8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a1, a1, 24
; RV64I-NEXT:    sraiw a1, a1, 24
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a0, a0, 24
; RV64I-NEXT:    call __moddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
```

```
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem8:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a1, a1, 24
; RV64IM-NEXT:    sraiw a1, a1, 24
; RV64IM-NEXT:    slli a0, a0, 24
; RV64IM-NEXT:    sraiw
 a0, a0, 24
; RV64IM-NEXT:    remw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = srem i8 %a, %b
 ret i8 %1
}

define i8 @srem8_constant_lhs(i8 %a) nounwind {
; RV64I-LABEL: srem8_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a1, a0, 24
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    call __moddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem8_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 24
; RV64IM-NEXT:    sraiw a0, a0, 24
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    remw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = srem i8 -10, %a
 ret i8 %1
}


define i16 @urem16(i16 %a, i16 %b) nounwind {
; RV64I-LABEL: urem16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    lui a2, 16
; RV64I-NEXT:
```

```
    addiw a2, a2, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    call __umoddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: urem16:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    lui a2, 16
; RV64IM-NEXT:    addi a2, a2, -1
; RV64IM-NEXT:    and a1, a1, a2
; RV64IM-NEXT:    and a0, a0, a2
; RV64IM-NEXT:    remuw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = urem i16 %a, %b
 ret i16 %1
}

define i16 @urem16_constant_lhs(i16 %a) nounwind {
; RV64I-LABEL: urem16_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a1, a0, 48
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    call __umoddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: urem16_constant_lhs:
; RV64IM:       # %bb.0:
;
 RV64IM-NEXT:    slli a0, a0, 48
; RV64IM-NEXT:    srli a0, a0, 48
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    remuw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = urem i16 10, %a
 ret i16 %1
}

define i16 @srem16(i16 %a, i16 %b) nounwind {
; RV64I-LABEL: srem16:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a1, a1, 16
; RV64I-NEXT:    sraiw a1, a1, 16
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    call __moddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem16:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a1, a1, 16
; RV64IM-NEXT:    sraiw a1, a1, 16
; RV64IM-NEXT:    slli a0, a0, 16
; RV64IM-NEXT:    sraiw a0, a0, 16
; RV64IM-NEXT:    remw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = srem i16 %a, %b
 ret i16 %1
}

define i16 @srem16_constant_lhs(i16 %a) nounwind {
; RV64I-LABEL: srem16_constant_lhs:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a1, a0, 16
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    call __moddi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: srem16_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 16
; RV64IM-NEXT:    sraiw a0, a0, 16
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    remw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = srem i16 -10, %a
 ret i16 %1
}
```

```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64I

; These tests are each targeted at a particular RISC-V ALU instruction. Most
; other files in this folder exercise LLVM IR instructions that don't directly
; match a RISC-V instruction.

; Register-immediate instructions.

define i32 @addi(i32 %a) nounwind {
; RV64I-LABEL: addi:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addiw a0, a0, 1
; RV64I-NEXT:    ret
 %1 = add i32 %a, 1
 ret i32 %1
}

define i32 @slti(i32 %a) nounwind {
; RV64I-LABEL: slti:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    slti a0, a0, 2
; RV64I-NEXT:    ret
 %1 = icmp slt i32 %a, 2
 %2 = zext i1 %1 to i32
 ret i32 %2
}

define i32 @sltiu(i32 %a) nounwind {
; RV64I-LABEL: sltiu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sltiu a0, a0, 3
; RV64I-NEXT:
   ret
 %1 = icmp ult i32 %a, 3
 %2 = zext i1 %1 to i32
 ret i32 %2
}

define i32 @xori(i32 %a) nounwind {
; RV64I-LABEL: xori:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xori a0, a0, 4
; RV64I-NEXT:    ret
 %1 = xor i32 %a, 4
```

```
 ret i32 %1
}

define i32 @ori(i32 %a) nounwind {
; RV64I-LABEL: ori:
; RV64I:       # %bb.0:
; RV64I-NEXT:    ori a0, a0, 5
; RV64I-NEXT:    ret
 %1 = or i32 %a, 5
 ret i32 %1
}

define i32 @andi(i32 %a) nounwind {
; RV64I-LABEL: andi:
; RV64I:       # %bb.0:
; RV64I-NEXT:    andi a0, a0, 6
; RV64I-NEXT:    ret
 %1 = and i32 %a, 6
 ret i32 %1
}

define i32 @slli(i32 %a) nounwind {
; RV64I-LABEL: slli:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slliw a0, a0, 7
; RV64I-NEXT:    ret
 %1 = shl i32 %a, 7
 ret i32 %1
}

define i32 @srli(i32 %a) nounwind {
; RV64I-LABEL: srli:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a0, a0, 8
; RV64I-NEXT:    ret
 %1 = lshr i32 %a, 8
 ret i32 %1
}

define i32 @srai(i32 %a) nounwind {
; RV64I-LABEL: srai:
; RV64I:
   # %bb.0:
; RV64I-NEXT:    sraiw a0, a0, 9
; RV64I-NEXT:    ret
 %1 = ashr i32 %a, 9
 ret i32 %1
}
```

```
; Register-register instructions

define i32 @add(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: add:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    ret
 %1 = add i32 %a, %b
 ret i32 %1
}

define i32 @sub(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: sub:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    ret
 %1 = sub i32 %a, %b
 ret i32 %1
}

define i32 @sub_negative_constant_lhs(i32 %a) nounwind {
; RV64I-LABEL: sub_negative_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -2
; RV64I-NEXT:    subw a0, a1, a0
; RV64I-NEXT:    ret
 %1 = sub i32 -2, %a
 ret i32 %1
}

define i32 @sll(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: sll:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sllw a0, a0, a1
; RV64I-NEXT:    ret
 %1 = shl i32 %a, %b
 ret i32 %1
}

; Make sure we don't emit instructions to zero extend the shift amount to i64.
define
 i32 @sll_shamt_zext(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: sll_shamt_zext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a1, a1, 1
; RV64I-NEXT:    sllw a0, a0, a1
; RV64I-NEXT:    ret
```

```
 %shamt = add i32 %b, 1
 %1 = shl i32 %a, %shamt
 ret i32 %1
}

define i32 @sll_negative_constant_lhs(i32 %a) nounwind {
; RV64I-LABEL: sll_negative_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    sllw a0, a1, a0
; RV64I-NEXT:    ret
 %1 = shl i32 -1, %a
 ret i32 %1
}

define i32 @slt(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: slt:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sext.w a1, a1
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    slt a0, a0, a1
; RV64I-NEXT:    ret
 %1 = icmp slt i32 %a, %b
 %2 = zext i1 %1 to i32
 ret i32 %2
}

define i32 @sltu(i32 %a, i32 %b) nounwind {
;
; RV64I-LABEL: sltu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sext.w a1, a1
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sltu a0, a0, a1
; RV64I-NEXT:    ret
 %1 = icmp ult i32 %a,
 %b
 %2 = zext i1 %1 to i32
 ret i32 %2
}

define i32 @xor(i32 %a, i32 %b) nounwind {
;
; RV64I-LABEL: xor:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    ret
 %1 = xor i32 %a, %b
```

```
 ret i32 %1
}

define i32 @srl(i32 %a, i32 %b) nounwind {
;
; RV64I-LABEL: srl:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srlw a0, a0, a1
; RV64I-NEXT:    ret
 %1 = lshr i32 %a, %b
 ret i32 %1
}

; Make sure we don't emit instructions to zero extend the shift amount to i64.
define i32 @srl_shamt_zext(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: srl_shamt_zext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a1, a1, 1
; RV64I-NEXT:    srlw a0, a0, a1
; RV64I-NEXT:    ret
 %shamt = add i32 %b, 1
 %1 = lshr i32 %a, %shamt
 ret i32 %1
}

define i32 @srl_negative_constant_lhs(i32 %a) nounwind {
;
; RV64I-LABEL: srl_negative_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    srlw a0, a1, a0
; RV64I-NEXT:    ret
 %1 = lshr i32 -1, %a
 ret i32 %1
}

define
 i32 @sra(i32 %a, i32 %b) nounwind {
;
; RV64I-LABEL: sra:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraw a0, a0, a1
; RV64I-NEXT:    ret
 %1 = ashr i32 %a, %b
 ret i32 %1
}

; Make sure we don't emit instructions to zero extend the shift amount to i64.
```

```
define i32 @sra_shamt_zext(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: sra_shamt_zext:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a1, a1, 1
; RV64I-NEXT:    sraw a0, a0, a1
; RV64I-NEXT:    ret
 %shamt = add i32 %b, 1
 %1 = ashr i32 %a, %shamt
 ret i32 %1
}

define i32 @sra_negative_constant_lhs(i32 %a) nounwind {
;
; RV64I-LABEL: sra_negative_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    sraw a0, a1, a0
; RV64I-NEXT:    ret
 %1 = ashr i32 2147483648, %a
 ret i32 %1
}

define i32 @or(i32 %a, i32 %b) nounwind {
;
; RV64I-LABEL: or:
; RV64I:       # %bb.0:
; RV64I-NEXT:    or a0, a0, a1
; RV64I-NEXT:    ret
 %1 = or i32 %a, %b
 ret i32 %1
}

define i32 @and(i32 %a, i32 %b) nounwind {
;
; RV64I-LABEL:
 and:
; RV64I:       # %bb.0:
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
 %1 = and i32 %a, %b
 ret i32 %1
}
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --include-generated-
funcs
; RUN: opt -S -passes=verify,iroutliner -ir-outlining-no-cost < %s | FileCheck %s

; This test checks that we do outline indirect calls when it is not specified
; that we should not.
```

```
declare void @f1(ptr, ptr);
declare void @f2(ptr, ptr);

define void @function1(ptr %func) {
entry:
 %a = alloca i32, align 4
 %b = alloca i32, align 4
 %c = alloca i32, align 4
 store i32 2, ptr %a, align 4
 store i32 3, ptr %b, align 4
 store i32 4, ptr %c, align 4
 call void %func()
 %al = load i32, ptr %a
 %bl = load i32, ptr %b
 %cl = load i32, ptr %c
 ret void
}

define void @function2(ptr %func) {
entry:
 %a = alloca i32, align 4
 %b = alloca i32, align 4
 %c = alloca i32, align 4
 store i32 2, ptr %a, align 4
 store i32 3, ptr %b, align 4
 store i32 4, ptr %c, align 4
 call void %func()
 %al = load i32, ptr %a
 %bl = load i32, ptr %b
 %cl = load i32, ptr %c
 ret void
}
;
 CHECK-LABEL: @function1(
; CHECK-NEXT:  entry:
; CHECK-NEXT:    [[A:%.*]] = alloca i32, align 4
; CHECK-NEXT:    [[B:%.*]] = alloca i32, align 4
; CHECK-NEXT:    [[C:%.*]] = alloca i32, align 4
; CHECK-NEXT:    call void @outlined_ir_func_0(ptr [[A]], ptr [[B]], ptr [[C]], ptr [[FUNC:%.*]])
; CHECK-NEXT:    ret void
;
;
; CHECK-LABEL: @function2(
; CHECK-NEXT:  entry:
; CHECK-NEXT:    [[A:%.*]] = alloca i32, align 4
; CHECK-NEXT:    [[B:%.*]] = alloca i32, align 4
; CHECK-NEXT:    [[C:%.*]] = alloca i32, align 4
```

```
; CHECK-NEXT:    call void @outlined_ir_func_0(ptr [[A]], ptr [[B]], ptr [[C]], ptr [[FUNC:%.*]])
; CHECK-NEXT:    ret void
;
;
; CHECK-LABEL: define internal void  @outlined_ir_func_0(
; CHECK-NEXT:  newFuncRoot:
; CHECK-NEXT:    br label [[ENTRY_TO_OUTLINE:%.*]]
; CHECK:       entry_to_outline:
; CHECK-NEXT:    store i32 2, ptr [[TMP0:%.*]], align 4
; CHECK-NEXT:    store i32 3, ptr [[TMP1:%.*]], align 4
; CHECK-NEXT:    store i32 4, ptr [[TMP2:%.*]], align 4
; CHECK-NEXT:
   call void [[TMP3:%.*]]()
; CHECK-NEXT:    [[AL:%.*]] = load i32, ptr [[TMP0]], align 4
; CHECK-NEXT:    [[BL:%.*]] = load i32, ptr [[TMP1]], align 4
; CHECK-NEXT:    [[CL:%.*]] = load i32, ptr [[TMP2]], align 4
; CHECK-NEXT:    br label [[ENTRY_AFTER_OUTLINE_EXITSTUB:%.*]]
; CHECK:       entry_after_outline.exitStub:
; CHECK-NEXT:    ret void
;
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -riscv-disable-using-constant-pool-for-large-ints -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=RV64I,RV64-NOPOOL
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefixes=RV64I,RV64I-POOL
; RUN: llc -mtriple=riscv64 -riscv-disable-using-constant-pool-for-large-ints -mattr=+zba \
; RUN:   -riscv-experimental-rv64-legal-i32 -verify-machineinstrs < %s | FileCheck %s -check-prefix=RV64IZBA
; RUN: llc -mtriple=riscv64 -riscv-disable-using-constant-pool-for-large-ints -mattr=+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 -verify-machineinstrs < %s | FileCheck %s -check-prefix=RV64IZBB
; RUN: llc -mtriple=riscv64 -riscv-disable-using-constant-pool-for-large-ints -mattr=+zbs \
; RUN:   -riscv-experimental-rv64-legal-i32 -verify-machineinstrs < %s | FileCheck
 %s -check-prefix=RV64IZBS
; RUN: llc -mtriple=riscv64 -riscv-disable-using-constant-pool-for-large-ints -mattr=+xtheadbb \
; RUN:   -riscv-experimental-rv64-legal-i32 -verify-machineinstrs < %s | FileCheck %s -check-
prefix=RV64IXTHEADBB

; Materializing constants

; TODO: It would be preferable if anyext constant returns were sign rather
; than zero extended. See PR39092. For now, mark returns as explicitly signext
; (this matches what Clang would generate for equivalent C/C++ anyway).

define signext i32 @zero() nounwind {
; RV64I-LABEL: zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 0
; RV64I-NEXT:    ret
;
```

```
; RV64IZBA-LABEL: zero:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: zero:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, 0
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: zero:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, 0
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: zero:
; RV64IXTHEADBB:       # %bb.0:
;
 RV64IXTHEADBB-NEXT:    li a0, 0
; RV64IXTHEADBB-NEXT:    ret
 ret i32 0
}

define signext i32 @pos_small() nounwind {
; RV64I-LABEL: pos_small:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 2047
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: pos_small:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 2047
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: pos_small:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, 2047
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: pos_small:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, 2047
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: pos_small:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, 2047
; RV64IXTHEADBB-NEXT:    ret
```

```
 ret i32 2047
}

define signext i32 @neg_small() nounwind {
; RV64I-LABEL: neg_small:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -2048
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: neg_small:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -2048
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL:
 neg_small:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -2048
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: neg_small:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -2048
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: neg_small:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -2048
; RV64IXTHEADBB-NEXT:    ret
 ret i32 -2048
}

define signext i32 @pos_i32() nounwind {
; RV64I-LABEL: pos_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 423811
; RV64I-NEXT:    addiw a0, a0, -1297
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: pos_i32:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 423811
; RV64IZBA-NEXT:    addiw a0, a0, -1297
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: pos_i32:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 423811
```

```
; RV64IZBB-NEXT:    addiw a0, a0, -1297
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: pos_i32:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 423811
; RV64IZBS-NEXT:    addiw a0, a0, -1297
; RV64IZBS-NEXT:
   ret
;
; RV64IXTHEADBB-LABEL: pos_i32:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 423811
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1297
; RV64IXTHEADBB-NEXT:    ret
 ret i32 1735928559
}

define signext i32 @neg_i32() nounwind {
; RV64I-LABEL: neg_i32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 912092
; RV64I-NEXT:    addiw a0, a0, -273
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: neg_i32:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 912092
; RV64IZBA-NEXT:    addiw a0, a0, -273
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: neg_i32:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 912092
; RV64IZBB-NEXT:    addiw a0, a0, -273
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: neg_i32:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 912092
; RV64IZBS-NEXT:    addiw a0, a0, -273
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: neg_i32:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 912092
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -273
;
```

```
 RV64IXTHEADBB-NEXT:    ret
 ret i32 -559038737
}

define signext i32 @pos_i32_hi20_only() nounwind {
; RV64I-LABEL: pos_i32_hi20_only:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 16
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: pos_i32_hi20_only:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 16
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: pos_i32_hi20_only:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 16
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: pos_i32_hi20_only:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 16
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: pos_i32_hi20_only:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 16
; RV64IXTHEADBB-NEXT:    ret
 ret i32 65536 ; 0x10000
}

define signext i32 @neg_i32_hi20_only() nounwind {
; RV64I-LABEL: neg_i32_hi20_only:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1048560
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: neg_i32_hi20_only:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0,
 1048560
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: neg_i32_hi20_only:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1048560
; RV64IZBB-NEXT:    ret
```

```
;
; RV64IZBS-LABEL: neg_i32_hi20_only:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 1048560
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: neg_i32_hi20_only:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1048560
; RV64IXTHEADBB-NEXT:    ret
 ret i32 -65536 ; -0x10000
}

; This can be materialized with ADDI+SLLI, improving compressibility.

define signext i32 @imm_left_shifted_addi() nounwind {
; RV64I-LABEL: imm_left_shifted_addi:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 32
; RV64I-NEXT:    addiw a0, a0, -64
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_left_shifted_addi:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 32
; RV64IZBA-NEXT:    addiw a0, a0, -64
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_left_shifted_addi:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 32
;
 RV64IZBB-NEXT:    addiw a0, a0, -64
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_left_shifted_addi:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 32
; RV64IZBS-NEXT:    addiw a0, a0, -64
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_left_shifted_addi:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 32
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -64
; RV64IXTHEADBB-NEXT:    ret
 ret i32 131008 ; 0x1FFC0
}
```

```
; This can be materialized with ADDI+SRLI, improving compressibility.

define signext i32 @imm_right_shifted_addi() nounwind {
; RV64I-LABEL: imm_right_shifted_addi:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 524288
; RV64I-NEXT:    addiw a0, a0, -1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_right_shifted_addi:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 524288
; RV64IZBA-NEXT:    addiw a0, a0, -1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_right_shifted_addi:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 524288
; RV64IZBB-NEXT:
   addiw a0, a0, -1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_right_shifted_addi:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 524288
; RV64IZBS-NEXT:    addiw a0, a0, -1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_right_shifted_addi:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 524288
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1
; RV64IXTHEADBB-NEXT:    ret
 ret i32 2147483647 ; 0x7FFFFFFF
}

; This can be materialized with LUI+SRLI, improving compressibility.

define signext i32 @imm_right_shifted_lui() nounwind {
; RV64I-LABEL: imm_right_shifted_lui:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 56
; RV64I-NEXT:    addiw a0, a0, 580
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_right_shifted_lui:
; RV64IZBA:       # %bb.0:
```

```
; RV64IZBA-NEXT:    lui a0, 56
; RV64IZBA-NEXT:    addiw a0, a0, 580
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_right_shifted_lui:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 56
; RV64IZBB-NEXT:
   addiw a0, a0, 580
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_right_shifted_lui:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 56
; RV64IZBS-NEXT:    addiw a0, a0, 580
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_right_shifted_lui:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 56
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 580
; RV64IXTHEADBB-NEXT:    ret
 ret i32 229956 ; 0x38244
}

define i64 @imm64_1() nounwind {
; RV64I-LABEL: imm64_1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 1
; RV64I-NEXT:    slli a0, a0, 31
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 1
; RV64IZBA-NEXT:    slli a0, a0, 31
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, 1
; RV64IZBB-NEXT:    slli a0, a0, 31
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    bseti a0, zero, 31
; RV64IZBS-NEXT:    ret
```

```
;
;
 RV64IXTHEADBB-LABEL: imm64_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, 1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 31
; RV64IXTHEADBB-NEXT:    ret
 ret i64 2147483648 ; 0x8000_0000
}

define i64 @imm64_2() nounwind {
; RV64I-LABEL: imm64_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -1
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_2:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -1
; RV64IZBA-NEXT:    srli a0, a0, 32
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_2:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -1
; RV64IZBB-NEXT:    srli a0, a0, 32
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_2:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -1
; RV64IZBS-NEXT:    srli a0, a0, 32
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_2:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -1
; RV64IXTHEADBB-NEXT:    srli a0, a0, 32
; RV64IXTHEADBB-NEXT:    ret
 ret i64 4294967295 ;
 0xFFFF_FFFF
}

define i64 @imm64_3() nounwind {
; RV64I-LABEL: imm64_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 1
```

```
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_3:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 1
; RV64IZBA-NEXT:    slli a0, a0, 32
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_3:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, 1
; RV64IZBB-NEXT:    slli a0, a0, 32
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_3:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    bseti a0, zero, 32
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_3:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, 1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 32
; RV64IXTHEADBB-NEXT:    ret
 ret i64 4294967296 ; 0x1_0000_0000
}

define i64 @imm64_4() nounwind {
; RV64I-LABEL: imm64_4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -1
; RV64I-NEXT:    slli a0, a0, 63
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_4:
; RV64IZBA:
    # %bb.0:
; RV64IZBA-NEXT:    li a0, -1
; RV64IZBA-NEXT:    slli a0, a0, 63
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_4:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -1
; RV64IZBB-NEXT:    slli a0, a0, 63
; RV64IZBB-NEXT:    ret
;
```

```
; RV64IZBS-LABEL: imm64_4:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    bseti a0, zero, 63
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_4:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 63
; RV64IXTHEADBB-NEXT:    ret
 ret i64 9223372036854775808 ; 0x8000_0000_0000_0000
}

define i64 @imm64_5() nounwind {
; RV64I-LABEL: imm64_5:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -1
; RV64I-NEXT:    slli a0, a0, 63
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_5:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -1
; RV64IZBA-NEXT:    slli a0, a0, 63
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_5:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -1
; RV64IZBB-NEXT:
   slli a0, a0, 63
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_5:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    bseti a0, zero, 63
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_5:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 63
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -9223372036854775808 ; 0x8000_0000_0000_0000
}

define i64 @imm64_6() nounwind {
; RV64I-LABEL: imm64_6:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 9321
; RV64I-NEXT:    addi a0, a0, -1329
; RV64I-NEXT:    slli a0, a0, 35
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_6:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 9321
; RV64IZBA-NEXT:    addi a0, a0, -1329
; RV64IZBA-NEXT:    slli a0, a0, 35
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_6:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 9321
; RV64IZBB-NEXT:    addi a0, a0, -1329
; RV64IZBB-NEXT:    slli a0, a0, 35
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_6:
;
 RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 9321
; RV64IZBS-NEXT:    addi a0, a0, -1329
; RV64IZBS-NEXT:    slli a0, a0, 35
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_6:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 9321
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1329
; RV64IXTHEADBB-NEXT:    slli a0, a0, 35
; RV64IXTHEADBB-NEXT:    ret
 ret i64 1311768464867721216 ; 0x1234_5678_0000_0000
}

define i64 @imm64_7() nounwind {
; RV64I-LABEL: imm64_7:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 7
; RV64I-NEXT:    slli a0, a0, 36
; RV64I-NEXT:    addi a0, a0, 11
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    addi a0, a0, 15
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_7:
```

```
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 7
; RV64IZBA-NEXT:    slli a0, a0, 36
; RV64IZBA-NEXT:    addi a0, a0, 11
; RV64IZBA-NEXT:    slli a0, a0, 24
; RV64IZBA-NEXT:    addi a0, a0, 15
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_7:
; RV64IZBB:       #
 %bb.0:
; RV64IZBB-NEXT:    li a0, 7
; RV64IZBB-NEXT:    slli a0, a0, 36
; RV64IZBB-NEXT:    addi a0, a0, 11
; RV64IZBB-NEXT:    slli a0, a0, 24
; RV64IZBB-NEXT:    addi a0, a0, 15
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_7:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, 7
; RV64IZBS-NEXT:    slli a0, a0, 36
; RV64IZBS-NEXT:    addi a0, a0, 11
; RV64IZBS-NEXT:    slli a0, a0, 24
; RV64IZBS-NEXT:    addi a0, a0, 15
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_7:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, 7
; RV64IXTHEADBB-NEXT:    slli a0, a0, 36
; RV64IXTHEADBB-NEXT:    addi a0, a0, 11
; RV64IXTHEADBB-NEXT:    slli a0, a0, 24
; RV64IXTHEADBB-NEXT:    addi a0, a0, 15
; RV64IXTHEADBB-NEXT:    ret
 ret i64 8070450532432478223 ; 0x7000_0000_0B00_000F
}

; TODO: it can be preferable to put constants that are expensive to materialise
; into the constant pool, especially for -Os.
define i64 @imm64_8() nounwind {
; RV64-NOPOOL-LABEL: imm64_8:
; RV64-NOPOOL:
      # %bb.0:
; RV64-NOPOOL-NEXT:    lui a0, 583
; RV64-NOPOOL-NEXT:    addiw a0, a0, -1875
; RV64-NOPOOL-NEXT:    slli a0, a0, 14
; RV64-NOPOOL-NEXT:    addi a0, a0, -947
```

```
; RV64-NOPOOL-NEXT:    slli a0, a0, 12
; RV64-NOPOOL-NEXT:    addi a0, a0, 1511
; RV64-NOPOOL-NEXT:    slli a0, a0, 13
; RV64-NOPOOL-NEXT:    addi a0, a0, -272
; RV64-NOPOOL-NEXT:    ret
;
; RV64I-POOL-LABEL: imm64_8:
; RV64I-POOL:       # %bb.0:
; RV64I-POOL-NEXT:    lui a0, %hi(.LCPI17_0)
; RV64I-POOL-NEXT:    ld a0, %lo(.LCPI17_0)(a0)
; RV64I-POOL-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_8:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 596523
; RV64IZBA-NEXT:    addi a0, a0, 965
; RV64IZBA-NEXT:    slli.uw a0, a0, 13
; RV64IZBA-NEXT:    addi a0, a0, -1347
; RV64IZBA-NEXT:    slli a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, -529
; RV64IZBA-NEXT:    slli a0, a0, 4
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_8:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 583
; RV64IZBB-NEXT:    addiw
 a0, a0, -1875
; RV64IZBB-NEXT:    slli a0, a0, 14
; RV64IZBB-NEXT:    addi a0, a0, -947
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1511
; RV64IZBB-NEXT:    slli a0, a0, 13
; RV64IZBB-NEXT:    addi a0, a0, -272
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_8:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 583
; RV64IZBS-NEXT:    addiw a0, a0, -1875
; RV64IZBS-NEXT:    slli a0, a0, 14
; RV64IZBS-NEXT:    addi a0, a0, -947
; RV64IZBS-NEXT:    slli a0, a0, 12
; RV64IZBS-NEXT:    addi a0, a0, 1511
; RV64IZBS-NEXT:    slli a0, a0, 13
; RV64IZBS-NEXT:    addi a0, a0, -272
; RV64IZBS-NEXT:    ret
;
```

```
; RV64IXTHEADBB-LABEL: imm64_8:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 583
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1875
; RV64IXTHEADBB-NEXT:    slli a0, a0, 14
; RV64IXTHEADBB-NEXT:    addi a0, a0, -947
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1511
; RV64IXTHEADBB-NEXT:    slli a0, a0, 13
; RV64IXTHEADBB-NEXT:
   addi a0, a0, -272
; RV64IXTHEADBB-NEXT:    ret
 ret i64 1311768467463790320 ; 0x1234_5678_9ABC_DEF0
}

define i64 @imm64_9() nounwind {
; RV64I-LABEL: imm64_9:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_9:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_9:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_9:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_9:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -1
}

; Various cases where extraneous ADDIs can be inserted where a (left shifted)
; LUI suffices.

define i64 @imm_left_shifted_lui_1() nounwind {
; RV64I-LABEL: imm_left_shifted_lui_1:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 262145
; RV64I-NEXT:    slli a0, a0, 1
;
 RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_left_shifted_lui_1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 262145
; RV64IZBA-NEXT:    slli a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_left_shifted_lui_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 262145
; RV64IZBB-NEXT:    slli a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_left_shifted_lui_1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 262145
; RV64IZBS-NEXT:    slli a0, a0, 1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_left_shifted_lui_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 262145
; RV64IXTHEADBB-NEXT:    slli a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 2147491840 ; 0x8000_2000
}

define i64 @imm_left_shifted_lui_2() nounwind {
; RV64I-LABEL: imm_left_shifted_lui_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 262145
; RV64I-NEXT:    slli a0, a0, 2
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_left_shifted_lui_2:
; RV64IZBA:
 # %bb.0:
; RV64IZBA-NEXT:    lui a0, 262145
; RV64IZBA-NEXT:    slli a0, a0, 2
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_left_shifted_lui_2:
; RV64IZBB:       # %bb.0:
```

```
; RV64IZBB-NEXT:    lui a0, 262145
; RV64IZBB-NEXT:    slli a0, a0, 2
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_left_shifted_lui_2:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 262145
; RV64IZBS-NEXT:    slli a0, a0, 2
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_left_shifted_lui_2:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 262145
; RV64IXTHEADBB-NEXT:    slli a0, a0, 2
; RV64IXTHEADBB-NEXT:    ret
 ret i64 4294983680 ; 0x1_0000_4000
}

define i64 @imm_left_shifted_lui_3() nounwind {
; RV64I-LABEL: imm_left_shifted_lui_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 4097
; RV64I-NEXT:    slli a0, a0, 20
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_left_shifted_lui_3:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 4097
; RV64IZBA-NEXT:    slli a0, a0, 20
;
 RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_left_shifted_lui_3:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 4097
; RV64IZBB-NEXT:    slli a0, a0, 20
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_left_shifted_lui_3:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 4097
; RV64IZBS-NEXT:    slli a0, a0, 20
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_left_shifted_lui_3:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 4097
; RV64IXTHEADBB-NEXT:    slli a0, a0, 20
```

```
; RV64IXTHEADBB-NEXT:    ret
 ret i64 17596481011712 ; 0x1001_0000_0000
}

; Various cases where extraneous ADDIs can be inserted where a (right shifted)
; LUI suffices, or where multiple ADDIs can be used instead of a single LUI.

define i64 @imm_right_shifted_lui_1() nounwind {
; RV64I-LABEL: imm_right_shifted_lui_1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 983056
; RV64I-NEXT:    srli a0, a0, 16
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_right_shifted_lui_1:
; RV64IZBA:
     # %bb.0:
; RV64IZBA-NEXT:    lui a0, 983056
; RV64IZBA-NEXT:    srli a0, a0, 16
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_right_shifted_lui_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 983056
; RV64IZBB-NEXT:    srli a0, a0, 16
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_right_shifted_lui_1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 983056
; RV64IZBS-NEXT:    srli a0, a0, 16
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_right_shifted_lui_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 983056
; RV64IXTHEADBB-NEXT:    srli a0, a0, 16
; RV64IXTHEADBB-NEXT:    ret
 ret i64 281474976706561 ; 0xFFFF_FFFF_F001
}

define i64 @imm_right_shifted_lui_2() nounwind {
; RV64I-LABEL: imm_right_shifted_lui_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1044481
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    srli a0, a0, 24
; RV64I-NEXT:    ret
```

```
;
; RV64IZBA-LABEL: imm_right_shifted_lui_2:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:
   lui a0, 1044481
; RV64IZBA-NEXT:    slli a0, a0, 12
; RV64IZBA-NEXT:    srli a0, a0, 24
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_right_shifted_lui_2:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1044481
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    srli a0, a0, 24
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_right_shifted_lui_2:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 1044481
; RV64IZBS-NEXT:    slli a0, a0, 12
; RV64IZBS-NEXT:    srli a0, a0, 24
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_right_shifted_lui_2:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1044481
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    srli a0, a0, 24
; RV64IXTHEADBB-NEXT:    ret
 ret i64 1099511623681 ; 0xFF_FFFF_F001
}

; We can materialize the upper bits with a single (shifted) LUI, but that option
; can be missed due to the lower bits, which aren't just 1s or just 0s.

define i64 @imm_decoupled_lui_addi() nounwind
 {
; RV64I-LABEL: imm_decoupled_lui_addi:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 4097
; RV64I-NEXT:    slli a0, a0, 20
; RV64I-NEXT:    addi a0, a0, -3
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_decoupled_lui_addi:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 4097
; RV64IZBA-NEXT:    slli a0, a0, 20
```

```
; RV64IZBA-NEXT:    addi a0, a0, -3
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_decoupled_lui_addi:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 4097
; RV64IZBB-NEXT:    slli a0, a0, 20
; RV64IZBB-NEXT:    addi a0, a0, -3
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_decoupled_lui_addi:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 4097
; RV64IZBS-NEXT:    slli a0, a0, 20
; RV64IZBS-NEXT:    addi a0, a0, -3
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_decoupled_lui_addi:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 4097
; RV64IXTHEADBB-NEXT:    slli a0, a0, 20
; RV64IXTHEADBB-NEXT:    addi a0, a0, -3
; RV64IXTHEADBB-NEXT:
   ret
 ret i64 17596481011709 ; 0x1000_FFFF_FFFD
}

; This constant can be materialized for RV64 with LUI+SRLI+XORI.

define i64 @imm_end_xori_1() nounwind {
; RV64I-LABEL: imm_end_xori_1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 983040
; RV64I-NEXT:    srli a0, a0, 3
; RV64I-NEXT:    not a0, a0
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_end_xori_1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 983040
; RV64IZBA-NEXT:    srli a0, a0, 3
; RV64IZBA-NEXT:    not a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_end_xori_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 983040
; RV64IZBB-NEXT:    srli a0, a0, 3
```

```
; RV64IZBB-NEXT:    not a0, a0
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_end_xori_1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 983040
; RV64IZBS-NEXT:    srli a0, a0, 3
; RV64IZBS-NEXT:    not a0, a0
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_end_xori_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:
  lui a0, 983040
; RV64IXTHEADBB-NEXT:    srli a0, a0, 3
; RV64IXTHEADBB-NEXT:    not a0, a0
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -2305843009180139521 ; 0xE000_0000_01FF_FFFF
}

; This constant can be materialized for RV64 with ADDI+SLLI+ADDI+ADDI.

define i64 @imm_end_2addi_1() nounwind {
; RV64I-LABEL: imm_end_2addi_1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -2047
; RV64I-NEXT:    slli a0, a0, 39
; RV64I-NEXT:    addi a0, a0, -2048
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_end_2addi_1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -2047
; RV64IZBA-NEXT:    slli a0, a0, 39
; RV64IZBA-NEXT:    addi a0, a0, -2048
; RV64IZBA-NEXT:    addi a0, a0, -1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_end_2addi_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -2047
; RV64IZBB-NEXT:    slli a0, a0, 39
; RV64IZBB-NEXT:    addi a0, a0, -2048
; RV64IZBB-NEXT:    addi a0, a0, -1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_end_2addi_1:
```

```
;
 RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -2047
; RV64IZBS-NEXT:    slli a0, a0, 39
; RV64IZBS-NEXT:    addi a0, a0, -2048
; RV64IZBS-NEXT:    addi a0, a0, -1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_end_2addi_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -2047
; RV64IXTHEADBB-NEXT:    slli a0, a0, 39
; RV64IXTHEADBB-NEXT:    addi a0, a0, -2048
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -1125350151030785 ; 0xFFFC_007F_FFFF_F7FF
}

; This constant can be more efficiently materialized for RV64 if we use two
; registers instead of one.

define i64 @imm_2reg_1() nounwind {
; RV64I-LABEL: imm_2reg_1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 74565
; RV64I-NEXT:    addiw a0, a0, 1656
; RV64I-NEXT:    slli a1, a0, 57
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_2reg_1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 74565
; RV64IZBA-NEXT:    addiw a0, a0, 1656
; RV64IZBA-NEXT:
   slli a1, a0, 57
; RV64IZBA-NEXT:    add a0, a0, a1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_2reg_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 74565
; RV64IZBB-NEXT:    addiw a0, a0, 1656
; RV64IZBB-NEXT:    slli a1, a0, 57
; RV64IZBB-NEXT:    add a0, a0, a1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_2reg_1:
```

```
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 74565
; RV64IZBS-NEXT:    addiw a0, a0, 1656
; RV64IZBS-NEXT:    slli a1, a0, 57
; RV64IZBS-NEXT:    add a0, a0, a1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_2reg_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 74565
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 1656
; RV64IXTHEADBB-NEXT:    slli a1, a0, 57
; RV64IXTHEADBB-NEXT:    add a0, a0, a1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -1152921504301427080 ; 0xF000_0000_1234_5678
}

; FIXME: This should use a single ADDI for the immediate.
define void @imm_store_i16_neg1(ptr %p) nounwind {
; RV64I-LABEL: imm_store_i16_neg1:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:    sh a1, 0(a0)
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_store_i16_neg1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a1, -1
; RV64IZBA-NEXT:    sh a1, 0(a0)
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_store_i16_neg1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a1, -1
; RV64IZBB-NEXT:    sh a1, 0(a0)
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_store_i16_neg1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a1, -1
; RV64IZBS-NEXT:    sh a1, 0(a0)
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_store_i16_neg1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a1, -1
; RV64IXTHEADBB-NEXT:    sh a1, 0(a0)
```

```
; RV64IXTHEADBB-NEXT:    ret
 store i16 -1, ptr %p
 ret void
}

; FIXME: This should use a single ADDI for the immediate.
define void @imm_store_i32_neg1(ptr %p) nounwind {
; RV64I-LABEL: imm_store_i32_neg1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, -1
; RV64I-NEXT:
  sw a1, 0(a0)
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_store_i32_neg1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a1, -1
; RV64IZBA-NEXT:    sw a1, 0(a0)
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_store_i32_neg1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a1, -1
; RV64IZBB-NEXT:    sw a1, 0(a0)
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_store_i32_neg1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a1, -1
; RV64IZBS-NEXT:    sw a1, 0(a0)
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_store_i32_neg1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a1, -1
; RV64IXTHEADBB-NEXT:    sw a1, 0(a0)
; RV64IXTHEADBB-NEXT:    ret
 store i32 -1, ptr %p
 ret void
}

define i64 @imm_5372288229() {
; RV64I-LABEL: imm_5372288229:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 160
; RV64I-NEXT:    addiw a0, a0, 437
; RV64I-NEXT:    slli a0, a0, 13
; RV64I-NEXT:    addi a0, a0, -795
```

```
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_5372288229:
; RV64IZBA:
      # %bb.0:
; RV64IZBA-NEXT:    lui a0, 655797
; RV64IZBA-NEXT:    slli.uw a0, a0, 1
; RV64IZBA-NEXT:    addi a0, a0, -795
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_5372288229:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 160
; RV64IZBB-NEXT:    addiw a0, a0, 437
; RV64IZBB-NEXT:    slli a0, a0, 13
; RV64IZBB-NEXT:    addi a0, a0, -795
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_5372288229:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 263018
; RV64IZBS-NEXT:    addiw a0, a0, -795
; RV64IZBS-NEXT:    bseti a0, a0, 32
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_5372288229:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 160
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 437
; RV64IXTHEADBB-NEXT:    slli a0, a0, 13
; RV64IXTHEADBB-NEXT:    addi a0, a0, -795
; RV64IXTHEADBB-NEXT:    ret
 ret i64 5372288229
}

define i64 @imm_neg_5372288229() {
; RV64I-LABEL: imm_neg_5372288229:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1048416
; RV64I-NEXT:
    addiw a0, a0, -437
; RV64I-NEXT:    slli a0, a0, 13
; RV64I-NEXT:    addi a0, a0, 795
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_5372288229:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 611378
```

```
; RV64IZBA-NEXT:    addiw a0, a0, 265
; RV64IZBA-NEXT:    sh1add a0, a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_5372288229:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1048416
; RV64IZBB-NEXT:    addiw a0, a0, -437
; RV64IZBB-NEXT:    slli a0, a0, 13
; RV64IZBB-NEXT:    addi a0, a0, 795
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_5372288229:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 785558
; RV64IZBS-NEXT:    addiw a0, a0, 795
; RV64IZBS-NEXT:    bclri a0, a0, 32
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_5372288229:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1048416
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -437
; RV64IXTHEADBB-NEXT:    slli a0, a0, 13
; RV64IXTHEADBB-NEXT:    addi a0, a0,
 795
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -5372288229
}

define i64 @imm_8953813715() {
; RV64I-LABEL: imm_8953813715:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 267
; RV64I-NEXT:    addiw a0, a0, -637
; RV64I-NEXT:    slli a0, a0, 13
; RV64I-NEXT:    addi a0, a0, -1325
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_8953813715:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 437198
; RV64IZBA-NEXT:    addiw a0, a0, -265
; RV64IZBA-NEXT:    sh2add a0, a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_8953813715:
; RV64IZBB:       # %bb.0:
```

```
; RV64IZBB-NEXT:    lui a0, 267
; RV64IZBB-NEXT:    addiw a0, a0, -637
; RV64IZBB-NEXT:    slli a0, a0, 13
; RV64IZBB-NEXT:    addi a0, a0, -1325
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_8953813715:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 88838
; RV64IZBS-NEXT:    addiw a0, a0, -1325
; RV64IZBS-NEXT:    bseti a0, a0, 33
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_8953813715:
; RV64IXTHEADBB:       # %bb.0:
;
 RV64IXTHEADBB-NEXT:    lui a0, 267
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -637
; RV64IXTHEADBB-NEXT:    slli a0, a0, 13
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1325
; RV64IXTHEADBB-NEXT:    ret
 ret i64 8953813715
}

define i64 @imm_neg_8953813715() {
; RV64I-LABEL: imm_neg_8953813715:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1048309
; RV64I-NEXT:    addiw a0, a0, 637
; RV64I-NEXT:    slli a0, a0, 13
; RV64I-NEXT:    addi a0, a0, 1325
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_8953813715:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 611378
; RV64IZBA-NEXT:    addiw a0, a0, 265
; RV64IZBA-NEXT:    sh2add a0, a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_8953813715:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1048309
; RV64IZBB-NEXT:    addiw a0, a0, 637
; RV64IZBB-NEXT:    slli a0, a0, 13
; RV64IZBB-NEXT:    addi a0, a0, 1325
; RV64IZBB-NEXT:    ret
;
```

```
; RV64IZBS-LABEL: imm_neg_8953813715:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:
   lui a0, 959738
; RV64IZBS-NEXT:    addiw a0, a0, 1325
; RV64IZBS-NEXT:    bclri a0, a0, 33
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_8953813715:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1048309
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 637
; RV64IXTHEADBB-NEXT:    slli a0, a0, 13
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1325
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -8953813715
}

define i64 @imm_16116864687() {
; RV64I-LABEL: imm_16116864687:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 961
; RV64I-NEXT:    addiw a0, a0, -1475
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1711
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_16116864687:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 437198
; RV64IZBA-NEXT:    addiw a0, a0, -265
; RV64IZBA-NEXT:    sh3add a0, a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_16116864687:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 961
; RV64IZBB-NEXT:    addiw a0, a0, -1475
; RV64IZBB-NEXT:
   slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1711
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_16116864687:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 961
; RV64IZBS-NEXT:    addiw a0, a0, -1475
; RV64IZBS-NEXT:    slli a0, a0, 12
```

```
; RV64IZBS-NEXT:    addi a0, a0, 1711
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_16116864687:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 961
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1475
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1711
; RV64IXTHEADBB-NEXT:    ret
 ret i64 16116864687
}

define i64 @imm_neg_16116864687() {
; RV64I-LABEL: imm_neg_16116864687:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1047615
; RV64I-NEXT:    addiw a0, a0, 1475
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, -1711
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_16116864687:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 611378
; RV64IZBA-NEXT:
 addiw a0, a0, 265
; RV64IZBA-NEXT:    sh3add a0, a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_16116864687:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1047615
; RV64IZBB-NEXT:    addiw a0, a0, 1475
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, -1711
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_16116864687:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 1047615
; RV64IZBS-NEXT:    addiw a0, a0, 1475
; RV64IZBS-NEXT:    slli a0, a0, 12
; RV64IZBS-NEXT:    addi a0, a0, -1711
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_16116864687:
; RV64IXTHEADBB:       # %bb.0:
```

```
; RV64IXTHEADBB-NEXT:    lui a0, 1047615
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 1475
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1711
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -16116864687
}

define i64 @imm_2344336315() {
; RV64I-LABEL: imm_2344336315:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 143087
; RV64I-NEXT:
   slli a0, a0, 2
; RV64I-NEXT:    addi a0, a0, -1093
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_2344336315:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 143087
; RV64IZBA-NEXT:    slli a0, a0, 2
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_2344336315:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 143087
; RV64IZBB-NEXT:    slli a0, a0, 2
; RV64IZBB-NEXT:    addi a0, a0, -1093
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_2344336315:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 143087
; RV64IZBS-NEXT:    slli a0, a0, 2
; RV64IZBS-NEXT:    addi a0, a0, -1093
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_2344336315:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 143087
; RV64IXTHEADBB-NEXT:    slli a0, a0, 2
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    ret
 ret i64 2344336315 ; 0x8bbbbbbb
}

define i64 @imm_70370820078523() {
```

```
; RV64-NOPOOL-LABEL: imm_70370820078523:
;
 RV64-NOPOOL:       # %bb.0:
; RV64-NOPOOL-NEXT:    lui a0, 256
; RV64-NOPOOL-NEXT:    addiw a0, a0, 31
; RV64-NOPOOL-NEXT:    slli a0, a0, 12
; RV64-NOPOOL-NEXT:    addi a0, a0, -273
; RV64-NOPOOL-NEXT:    slli a0, a0, 14
; RV64-NOPOOL-NEXT:    addi a0, a0, -1093
; RV64-NOPOOL-NEXT:    ret
;
; RV64I-POOL-LABEL: imm_70370820078523:
; RV64I-POOL:       # %bb.0:
; RV64I-POOL-NEXT:    lui a0, %hi(.LCPI37_0)
; RV64I-POOL-NEXT:    ld a0, %lo(.LCPI37_0)(a0)
; RV64I-POOL-NEXT:    ret
;
; RV64IZBA-LABEL: imm_70370820078523:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 256
; RV64IZBA-NEXT:    addiw a0, a0, 31
; RV64IZBA-NEXT:    slli a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, -273
; RV64IZBA-NEXT:    slli a0, a0, 14
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_70370820078523:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 256
; RV64IZBB-NEXT:    addiw a0, a0, 31
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, -273
;
 RV64IZBB-NEXT:    slli a0, a0, 14
; RV64IZBB-NEXT:    addi a0, a0, -1093
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_70370820078523:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 506812
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    bseti a0, a0, 46
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_70370820078523:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 256
```

```
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 31
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, -273
; RV64IXTHEADBB-NEXT:    slli a0, a0, 14
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    ret
 ret i64 70370820078523 ; 0x40007bbbbbbb
}

define i64 @imm_neg_9223372034778874949() {
; RV64I-LABEL: imm_neg_9223372034778874949:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 506812
; RV64I-NEXT:    addiw a0, a0, -1093
; RV64I-NEXT:    slli a1, a0, 63
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_9223372034778874949:
;
 RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 506812
; RV64IZBA-NEXT:    addiw a0, a0, -1093
; RV64IZBA-NEXT:    slli a1, a0, 63
; RV64IZBA-NEXT:    add a0, a0, a1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_9223372034778874949:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 506812
; RV64IZBB-NEXT:    addiw a0, a0, -1093
; RV64IZBB-NEXT:    slli a1, a0, 63
; RV64IZBB-NEXT:    add a0, a0, a1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_9223372034778874949:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 506812
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    bseti a0, a0, 63
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_9223372034778874949:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 506812
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1093
; RV64IXTHEADBB-NEXT:    slli a1, a0, 63
; RV64IXTHEADBB-NEXT:    add a0, a0, a1
```

```
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -9223372034778874949 ; 0x800000007bbbbbbb
}

define
 i64 @imm_neg_9223301666034697285() {
; RV64-NOPOOL-LABEL: imm_neg_9223301666034697285:
; RV64-NOPOOL:       # %bb.0:
; RV64-NOPOOL-NEXT:    lui a0, 917505
; RV64-NOPOOL-NEXT:    slli a0, a0, 8
; RV64-NOPOOL-NEXT:    addi a0, a0, 31
; RV64-NOPOOL-NEXT:    slli a0, a0, 12
; RV64-NOPOOL-NEXT:    addi a0, a0, -273
; RV64-NOPOOL-NEXT:    slli a0, a0, 14
; RV64-NOPOOL-NEXT:    addi a0, a0, -1093
; RV64-NOPOOL-NEXT:    ret
;
; RV64I-POOL-LABEL: imm_neg_9223301666034697285:
; RV64I-POOL:       # %bb.0:
; RV64I-POOL-NEXT:    lui a0, %hi(.LCPI39_0)
; RV64I-POOL-NEXT:    ld a0, %lo(.LCPI39_0)(a0)
; RV64I-POOL-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_9223301666034697285:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 917505
; RV64IZBA-NEXT:    slli a0, a0, 8
; RV64IZBA-NEXT:    addi a0, a0, 31
; RV64IZBA-NEXT:    slli a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, -273
; RV64IZBA-NEXT:    slli a0, a0, 14
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL:
 imm_neg_9223301666034697285:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 917505
; RV64IZBB-NEXT:    slli a0, a0, 8
; RV64IZBB-NEXT:    addi a0, a0, 31
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, -273
; RV64IZBB-NEXT:    slli a0, a0, 14
; RV64IZBB-NEXT:    addi a0, a0, -1093
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_9223301666034697285:
; RV64IZBS:       # %bb.0:
```

```
; RV64IZBS-NEXT:    lui a0, 506812
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    bseti a0, a0, 46
; RV64IZBS-NEXT:    bseti a0, a0, 63
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_9223301666034697285:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 917505
; RV64IXTHEADBB-NEXT:    slli a0, a0, 8
; RV64IXTHEADBB-NEXT:    addi a0, a0, 31
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, -273
; RV64IXTHEADBB-NEXT:    slli a0, a0, 14
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    ret
  ret i64 -9223301666034697285 ; 0x800040007bbbbbbb
}

define i64 @imm_neg_2219066437() {
; RV64I-LABEL: imm_neg_2219066437:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 913135
; RV64I-NEXT:    slli a0, a0, 2
; RV64I-NEXT:    addi a0, a0, -1093
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_2219066437:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 913135
; RV64IZBA-NEXT:    slli a0, a0, 2
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_2219066437:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 913135
; RV64IZBB-NEXT:    slli a0, a0, 2
; RV64IZBB-NEXT:    addi a0, a0, -1093
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_2219066437:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 913135
; RV64IZBS-NEXT:    slli a0, a0, 2
; RV64IZBS-NEXT:    addi a0, a0, -1093
; RV64IZBS-NEXT:    ret
;
```

```
; RV64IXTHEADBB-LABEL: imm_neg_2219066437:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 913135
; RV64IXTHEADBB-NEXT:
   slli a0, a0, 2
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -2219066437 ; 0xffffffff7bbbbbbb
}

define i64 @imm_neg_8798043653189() {
; RV64I-LABEL: imm_neg_8798043653189:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 917475
; RV64I-NEXT:    addiw a0, a0, -273
; RV64I-NEXT:    slli a0, a0, 14
; RV64I-NEXT:    addi a0, a0, -1093
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_8798043653189:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 917475
; RV64IZBA-NEXT:    addiw a0, a0, -273
; RV64IZBA-NEXT:    slli a0, a0, 14
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_8798043653189:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 917475
; RV64IZBB-NEXT:    addiw a0, a0, -273
; RV64IZBB-NEXT:    slli a0, a0, 14
; RV64IZBB-NEXT:    addi a0, a0, -1093
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_8798043653189:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 572348
; RV64IZBS-NEXT:
   addiw a0, a0, -1093
; RV64IZBS-NEXT:    bclri a0, a0, 43
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_8798043653189:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 917475
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -273
; RV64IXTHEADBB-NEXT:    slli a0, a0, 14
```

```
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -8798043653189 ; 0xfffff7ff8bbbbbbb
}

define i64 @imm_9223372034904144827() {
; RV64I-LABEL: imm_9223372034904144827:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 572348
; RV64I-NEXT:    addiw a0, a0, -1093
; RV64I-NEXT:    slli a1, a0, 63
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_9223372034904144827:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 572348
; RV64IZBA-NEXT:    addiw a0, a0, -1093
; RV64IZBA-NEXT:    slli a1, a0, 63
; RV64IZBA-NEXT:    add a0, a0, a1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_9223372034904144827:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:
   lui a0, 572348
; RV64IZBB-NEXT:    addiw a0, a0, -1093
; RV64IZBB-NEXT:    slli a1, a0, 63
; RV64IZBB-NEXT:    add a0, a0, a1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_9223372034904144827:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 572348
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    bclri a0, a0, 63
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_9223372034904144827:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 572348
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1093
; RV64IXTHEADBB-NEXT:    slli a1, a0, 63
; RV64IXTHEADBB-NEXT:    add a0, a0, a1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 9223372034904144827 ; 0x7fffffff8bbbbbbb
}
```

```
define i64 @imm_neg_9223354442718100411() {
; RV64-NOPOOL-LABEL: imm_neg_9223354442718100411:
; RV64-NOPOOL:       # %bb.0:
; RV64-NOPOOL-NEXT:    lui a0, 524287
; RV64-NOPOOL-NEXT:    slli a0, a0, 6
; RV64-NOPOOL-NEXT:    addi a0, a0, -29
; RV64-NOPOOL-NEXT:    slli a0, a0, 12
; RV64-NOPOOL-NEXT:
   addi a0, a0, -273
; RV64-NOPOOL-NEXT:    slli a0, a0, 14
; RV64-NOPOOL-NEXT:    addi a0, a0, -1093
; RV64-NOPOOL-NEXT:    ret
;
; RV64I-POOL-LABEL: imm_neg_9223354442718100411:
; RV64I-POOL:       # %bb.0:
; RV64I-POOL-NEXT:    lui a0, %hi(.LCPI43_0)
; RV64I-POOL-NEXT:    ld a0, %lo(.LCPI43_0)(a0)
; RV64I-POOL-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_9223354442718100411:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 524287
; RV64IZBA-NEXT:    slli a0, a0, 6
; RV64IZBA-NEXT:    addi a0, a0, -29
; RV64IZBA-NEXT:    slli a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, -273
; RV64IZBA-NEXT:    slli a0, a0, 14
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_9223354442718100411:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 524287
; RV64IZBB-NEXT:    slli a0, a0, 6
; RV64IZBB-NEXT:    addi a0, a0, -29
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, -273
; RV64IZBB-NEXT:    slli a0, a0, 14
; RV64IZBB-NEXT:
  addi a0, a0, -1093
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_9223354442718100411:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 572348
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    bclri a0, a0, 44
; RV64IZBS-NEXT:    bclri a0, a0, 63
```

```
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_9223354442718100411:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 524287
; RV64IXTHEADBB-NEXT:    slli a0, a0, 6
; RV64IXTHEADBB-NEXT:    addi a0, a0, -29
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, -273
; RV64IXTHEADBB-NEXT:    slli a0, a0, 14
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    ret
 ret i64 9223354442718100411 ; 0x7fffefff8bbbbbbb
}

define i64 @imm_2863311530() {
; RV64I-LABEL: imm_2863311530:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 349525
; RV64I-NEXT:    addiw a0, a0, 1365
; RV64I-NEXT:    slli a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_2863311530:
; RV64IZBA:
      # %bb.0:
; RV64IZBA-NEXT:    lui a0, 349525
; RV64IZBA-NEXT:    addiw a0, a0, 1365
; RV64IZBA-NEXT:    slli a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_2863311530:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 349525
; RV64IZBB-NEXT:    addiw a0, a0, 1365
; RV64IZBB-NEXT:    slli a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_2863311530:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 349525
; RV64IZBS-NEXT:    addiw a0, a0, 1365
; RV64IZBS-NEXT:    slli a0, a0, 1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_2863311530:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 349525
```

```
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 1365
; RV64IXTHEADBB-NEXT:    slli a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 2863311530 ; #0xaaaaaaaa
}

define i64 @imm_neg_2863311530() {
; RV64I-LABEL: imm_neg_2863311530:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 699051
; RV64I-NEXT:    addiw a0, a0, -1365
; RV64I-NEXT:    slli a0,
 a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_2863311530:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 699051
; RV64IZBA-NEXT:    addiw a0, a0, -1365
; RV64IZBA-NEXT:    slli a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_2863311530:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 699051
; RV64IZBB-NEXT:    addiw a0, a0, -1365
; RV64IZBB-NEXT:    slli a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_2863311530:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 699051
; RV64IZBS-NEXT:    addiw a0, a0, -1365
; RV64IZBS-NEXT:    slli a0, a0, 1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_2863311530:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 699051
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1365
; RV64IXTHEADBB-NEXT:    slli a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -2863311530 ; #0xffffffff55555556
}

define i64 @imm_2147486378() {
; RV64I-LABEL: imm_2147486378:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:
   li a0, 1
; RV64I-NEXT:    slli a0, a0, 31
; RV64I-NEXT:    addi a0, a0, 1365
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_2147486378:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 1
; RV64IZBA-NEXT:    slli a0, a0, 31
; RV64IZBA-NEXT:    addi a0, a0, 1365
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_2147486378:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, 1
; RV64IZBB-NEXT:    slli a0, a0, 31
; RV64IZBB-NEXT:    addi a0, a0, 1365
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_2147486378:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, 1365
; RV64IZBS-NEXT:    bseti a0, a0, 31
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_2147486378:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, 1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 31
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1365
; RV64IXTHEADBB-NEXT:    ret
 ret i64 2147485013
}

define i64 @imm_neg_2147485013() {
; RV64I-LABEL: imm_neg_2147485013:
; RV64I:       # %bb.0:
; RV64I-NEXT:
 lui a0, 524288
; RV64I-NEXT:    addi a0, a0, -1365
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_2147485013:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 524288
; RV64IZBA-NEXT:    addi a0, a0, -1365
; RV64IZBA-NEXT:    ret
```

```
;
; RV64IZBB-LABEL: imm_neg_2147485013:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 524288
; RV64IZBB-NEXT:    addi a0, a0, -1365
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_2147485013:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 524288
; RV64IZBS-NEXT:    addi a0, a0, -1365
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_2147485013:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 524288
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1365
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -2147485013
}

define i64 @imm_12900924131259() {
; RV64I-LABEL: imm_12900924131259:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 188
; RV64I-NEXT:    addiw a0, a0, -1093
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    addi a0,
 a0, 1979
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_12900924131259:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768955
; RV64IZBA-NEXT:    slli.uw a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, 1979
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_12900924131259:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 188
; RV64IZBB-NEXT:    addiw a0, a0, -1093
; RV64IZBB-NEXT:    slli a0, a0, 24
; RV64IZBB-NEXT:    addi a0, a0, 1979
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_12900924131259:
; RV64IZBS:       # %bb.0:
```

```
; RV64IZBS-NEXT:    lui a0, 188
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    slli a0, a0, 24
; RV64IZBS-NEXT:    addi a0, a0, 1979
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_12900924131259:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 188
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1093
; RV64IXTHEADBB-NEXT:    slli a0, a0, 24
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1979
; RV64IXTHEADBB-NEXT:    ret
 ret i64 12900924131259
}

define
 i64 @imm_50394234880() {
; RV64I-LABEL: imm_50394234880:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 188
; RV64I-NEXT:    addiw a0, a0, -1093
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_50394234880:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768955
; RV64IZBA-NEXT:    slli.uw a0, a0, 4
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_50394234880:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 188
; RV64IZBB-NEXT:    addiw a0, a0, -1093
; RV64IZBB-NEXT:    slli a0, a0, 16
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_50394234880:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 188
; RV64IZBS-NEXT:    addiw a0, a0, -1093
; RV64IZBS-NEXT:    slli a0, a0, 16
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_50394234880:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 188
```

```
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1093
; RV64IXTHEADBB-NEXT:    slli a0, a0, 16
; RV64IXTHEADBB-NEXT:    ret
 ret
 i64 50394234880
}

define i64 @imm_12900936431479() {
; RV64I-LABEL: imm_12900936431479:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 192239
; RV64I-NEXT:    slli a0, a0, 2
; RV64I-NEXT:    addi a0, a0, -1093
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1911
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_12900936431479:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768956
; RV64IZBA-NEXT:    addi a0, a0, -1093
; RV64IZBA-NEXT:    slli.uw a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, 1911
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_12900936431479:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 192239
; RV64IZBB-NEXT:    slli a0, a0, 2
; RV64IZBB-NEXT:    addi a0, a0, -1093
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1911
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_12900936431479:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 192239
; RV64IZBS-NEXT:    slli a0, a0, 2
; RV64IZBS-NEXT:    addi a0, a0, -1093
; RV64IZBS-NEXT:
   slli a0, a0, 12
; RV64IZBS-NEXT:    addi a0, a0, 1911
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_12900936431479:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 192239
; RV64IXTHEADBB-NEXT:    slli a0, a0, 2
```

```
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1093
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1911
; RV64IXTHEADBB-NEXT:    ret
 ret i64 12900936431479
}

define i64 @imm_12900918536874() {
; RV64I-LABEL: imm_12900918536874:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 384477
; RV64I-NEXT:    addiw a0, a0, 1365
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1365
; RV64I-NEXT:    slli a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_12900918536874:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768955
; RV64IZBA-NEXT:    addi a0, a0, -1365
; RV64IZBA-NEXT:    slli.uw a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, -1366
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_12900918536874:
;
 RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 384477
; RV64IZBB-NEXT:    addiw a0, a0, 1365
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1365
; RV64IZBB-NEXT:    slli a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_12900918536874:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 384477
; RV64IZBS-NEXT:    addiw a0, a0, 1365
; RV64IZBS-NEXT:    slli a0, a0, 12
; RV64IZBS-NEXT:    addi a0, a0, 1365
; RV64IZBS-NEXT:    slli a0, a0, 1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_12900918536874:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 384477
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 1365
```

```
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1365
; RV64IXTHEADBB-NEXT:    slli a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 12900918536874
}

define i64 @imm_12900925247761() {
; RV64I-LABEL: imm_12900925247761:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 384478
; RV64I-NEXT:    addiw
 a0, a0, -1911
; RV64I-NEXT:    slli a0, a0, 13
; RV64I-NEXT:    addi a0, a0, -2048
; RV64I-NEXT:    addi a0, a0, -1775
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_12900925247761:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768955
; RV64IZBA-NEXT:    addi a0, a0, 273
; RV64IZBA-NEXT:    slli.uw a0, a0, 12
; RV64IZBA-NEXT:    addi a0, a0, 273
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_12900925247761:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 384478
; RV64IZBB-NEXT:    addiw a0, a0, -1911
; RV64IZBB-NEXT:    slli a0, a0, 13
; RV64IZBB-NEXT:    addi a0, a0, -2048
; RV64IZBB-NEXT:    addi a0, a0, -1775
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_12900925247761:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 384478
; RV64IZBS-NEXT:    addiw a0, a0, -1911
; RV64IZBS-NEXT:    slli a0, a0, 13
; RV64IZBS-NEXT:    addi a0, a0, -2048
; RV64IZBS-NEXT:    addi a0, a0, -1775
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_12900925247761:
; RV64IXTHEADBB:
      # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 384478
```

```
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1911
; RV64IXTHEADBB-NEXT:    slli a0, a0, 13
; RV64IXTHEADBB-NEXT:    addi a0, a0, -2048
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1775
; RV64IXTHEADBB-NEXT:    ret
 ret i64 12900925247761
}

define i64 @imm_7158272001() {
; RV64I-LABEL: imm_7158272001:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 427
; RV64I-NEXT:    addiw a0, a0, -1367
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_7158272001:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 349525
; RV64IZBA-NEXT:    sh2add a0, a0, a0
; RV64IZBA-NEXT:    addi a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_7158272001:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 427
; RV64IZBB-NEXT:    addiw a0, a0, -1367
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_7158272001:
; RV64IZBS:
    # %bb.0:
; RV64IZBS-NEXT:    lui a0, 427
; RV64IZBS-NEXT:    addiw a0, a0, -1367
; RV64IZBS-NEXT:    slli a0, a0, 12
; RV64IZBS-NEXT:    addi a0, a0, 1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_7158272001:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 427
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1367
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
```

```
 ret i64 7158272001 ; 0x0000_0001_aaaa_9001
}

define i64 @imm_12884889601() {
; RV64I-LABEL: imm_12884889601:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 768
; RV64I-NEXT:    addiw a0, a0, -3
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_12884889601:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 349525
; RV64IZBA-NEXT:    sh3add a0, a0, a0
; RV64IZBA-NEXT:    addi a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_12884889601:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:
   lui a0, 768
; RV64IZBB-NEXT:    addiw a0, a0, -3
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_12884889601:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 768
; RV64IZBS-NEXT:    addiw a0, a0, -3
; RV64IZBS-NEXT:    slli a0, a0, 12
; RV64IZBS-NEXT:    addi a0, a0, 1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_12884889601:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 768
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -3
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 12884889601 ; 0x0000_0002_ffff_d001
}

define i64 @imm_neg_3435982847() {
; RV64I-LABEL: imm_neg_3435982847:
```

```
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1048371
; RV64I-NEXT:    addiw a0, a0, 817
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_3435982847:
; RV64IZBA:
 # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768955
; RV64IZBA-NEXT:    sh1add a0, a0, a0
; RV64IZBA-NEXT:    addi a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_3435982847:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1048371
; RV64IZBB-NEXT:    addiw a0, a0, 817
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_3435982847:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 734001
; RV64IZBS-NEXT:    addiw a0, a0, 1
; RV64IZBS-NEXT:    bclri a0, a0, 31
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_3435982847:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1048371
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 817
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -3435982847 ; 0xffff_ffff_3333_1001
}

define i64 @imm_neg_5726842879() {
; RV64I-LABEL: imm_neg_5726842879:
; RV64I:       # %bb.0:
; RV64I-NEXT:
   lui a0, 1048235
; RV64I-NEXT:    addiw a0, a0, -1419
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1
```

```
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_5726842879:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768945
; RV64IZBA-NEXT:    sh2add a0, a0, a0
; RV64IZBA-NEXT:    addi a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_5726842879:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1048235
; RV64IZBB-NEXT:    addiw a0, a0, -1419
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_5726842879:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 698997
; RV64IZBS-NEXT:    addiw a0, a0, 1
; RV64IZBS-NEXT:    bclri a0, a0, 32
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm_neg_5726842879:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1048235
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1419
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:
   addi a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -5726842879 ; 0xffff_fffe_aaa7_5001
}

define i64 @imm_neg_10307948543() {
; RV64I-LABEL: imm_neg_10307948543:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 1047962
; RV64I-NEXT:    addiw a0, a0, -1645
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    addi a0, a0, 1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm_neg_10307948543:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 768955
; RV64IZBA-NEXT:    sh3add a0, a0, a0
```

```
; RV64IZBA-NEXT:    addi a0, a0, 1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm_neg_10307948543:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1047962
; RV64IZBB-NEXT:    addiw a0, a0, -1645
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    addi a0, a0, 1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm_neg_10307948543:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 629139
; RV64IZBS-NEXT:    addiw a0, a0, 1
; RV64IZBS-NEXT:    bclri a0, a0, 33
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL:
 imm_neg_10307948543:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1047962
; RV64IXTHEADBB-NEXT:    addiw a0, a0, -1645
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -10307948543 ; 0xffff_fffd_9999_3001
}

define i64 @li_rori_1() {
; RV64I-LABEL: li_rori_1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -17
; RV64I-NEXT:    slli a0, a0, 43
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: li_rori_1:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -17
; RV64IZBA-NEXT:    slli a0, a0, 43
; RV64IZBA-NEXT:    addi a0, a0, -1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: li_rori_1:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -18
; RV64IZBB-NEXT:    rori a0, a0, 21
```

```
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: li_rori_1:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -17
; RV64IZBS-NEXT:    slli a0, a0, 43
; RV64IZBS-NEXT:    addi a0, a0,
 -1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: li_rori_1:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -18
; RV64IXTHEADBB-NEXT:    th.srri a0, a0, 21
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -149533581377537
}

define i64 @li_rori_2() {
; RV64I-LABEL: li_rori_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -5
; RV64I-NEXT:    slli a0, a0, 60
; RV64I-NEXT:    addi a0, a0, -6
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: li_rori_2:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -5
; RV64IZBA-NEXT:    slli a0, a0, 60
; RV64IZBA-NEXT:    addi a0, a0, -6
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: li_rori_2:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -86
; RV64IZBB-NEXT:    rori a0, a0, 4
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: li_rori_2:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -5
; RV64IZBS-NEXT:    slli a0, a0, 60
; RV64IZBS-NEXT:    addi a0, a0, -6
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: li_rori_2:
; RV64IXTHEADBB:
```

```
  # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -86
; RV64IXTHEADBB-NEXT:    th.srri a0, a0, 4
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -5764607523034234886
}

define i64 @li_rori_3() {
; RV64I-LABEL: li_rori_3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, -17
; RV64I-NEXT:    slli a0, a0, 27
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: li_rori_3:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, -17
; RV64IZBA-NEXT:    slli a0, a0, 27
; RV64IZBA-NEXT:    addi a0, a0, -1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: li_rori_3:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, -18
; RV64IZBB-NEXT:    rori a0, a0, 37
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: li_rori_3:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -17
; RV64IZBS-NEXT:    slli a0, a0, 27
; RV64IZBS-NEXT:    addi a0, a0, -1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: li_rori_3:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, -18
; RV64IXTHEADBB-NEXT:    th.srri a0,
 a0, 37
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -2281701377
}

; This used to assert when compiled with Zba.
define i64 @PR54812() {
; RV64I-LABEL: PR54812:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    lui a0, 1048447
; RV64I-NEXT:    addiw a0, a0, 1407
; RV64I-NEXT:    slli a0, a0, 12
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: PR54812:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 872917
; RV64IZBA-NEXT:    sh1add a0, a0, a0
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: PR54812:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1048447
; RV64IZBB-NEXT:    addiw a0, a0, 1407
; RV64IZBB-NEXT:    slli a0, a0, 12
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: PR54812:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 1045887
; RV64IZBS-NEXT:    bclri a0, a0, 31
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: PR54812:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 1048447
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 1407
; RV64IXTHEADBB-NEXT:    slli a0, a0, 12
; RV64IXTHEADBB-NEXT:
   ret
 ret i64 -2158497792;
}

define signext i32 @pos_2048() nounwind {
; RV64I-LABEL: pos_2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a0, 1
; RV64I-NEXT:    slli a0, a0, 11
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: pos_2048:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    li a0, 1
; RV64IZBA-NEXT:    slli a0, a0, 11
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: pos_2048:
```

```
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    li a0, 1
; RV64IZBB-NEXT:    slli a0, a0, 11
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: pos_2048:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    bseti a0, zero, 11
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: pos_2048:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    li a0, 1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 11
; RV64IXTHEADBB-NEXT:    ret
 ret i32 2048
}

define i64 @imm64_same_lo_hi() nounwind {
; RV64I-LABEL: imm64_same_lo_hi:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 65793
; RV64I-NEXT:    addiw a0, a0, 16
; RV64I-NEXT:
   slli a1, a0, 32
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_same_lo_hi:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 65793
; RV64IZBA-NEXT:    addiw a0, a0, 16
; RV64IZBA-NEXT:    slli a1, a0, 32
; RV64IZBA-NEXT:    add a0, a0, a1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_same_lo_hi:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 65793
; RV64IZBB-NEXT:    addiw a0, a0, 16
; RV64IZBB-NEXT:    slli a1, a0, 32
; RV64IZBB-NEXT:    add a0, a0, a1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_same_lo_hi:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 65793
; RV64IZBS-NEXT:    addiw a0, a0, 16
```

```
; RV64IZBS-NEXT:    slli a1, a0, 32
; RV64IZBS-NEXT:    add a0, a0, a1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_same_lo_hi:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 65793
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 16
; RV64IXTHEADBB-NEXT:    slli a1, a0, 32
; RV64IXTHEADBB-NEXT:    add a0, a0, a1
;
 RV64IXTHEADBB-NEXT:    ret
 ret i64 1157442765409226768 ; 0x0101010101010101
}

; Same as above with optsize. Make sure we use constant pool on RV64
define i64 @imm64_same_lo_hi_optsize() nounwind optsize {
; RV64-NOPOOL-LABEL: imm64_same_lo_hi_optsize:
; RV64-NOPOOL:       # %bb.0:
; RV64-NOPOOL-NEXT:    lui a0, 65793
; RV64-NOPOOL-NEXT:    addiw a0, a0, 16
; RV64-NOPOOL-NEXT:    slli a1, a0, 32
; RV64-NOPOOL-NEXT:    add a0, a0, a1
; RV64-NOPOOL-NEXT:    ret
;
; RV64I-POOL-LABEL: imm64_same_lo_hi_optsize:
; RV64I-POOL:       # %bb.0:
; RV64I-POOL-NEXT:    lui a0, %hi(.LCPI64_0)
; RV64I-POOL-NEXT:    ld a0, %lo(.LCPI64_0)(a0)
; RV64I-POOL-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_same_lo_hi_optsize:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 65793
; RV64IZBA-NEXT:    addiw a0, a0, 16
; RV64IZBA-NEXT:    slli a1, a0, 32
; RV64IZBA-NEXT:    add a0, a0, a1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_same_lo_hi_optsize:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui
 a0, 65793
; RV64IZBB-NEXT:    addiw a0, a0, 16
; RV64IZBB-NEXT:    slli a1, a0, 32
; RV64IZBB-NEXT:    add a0, a0, a1
; RV64IZBB-NEXT:    ret
;
```

```
; RV64IZBS-LABEL: imm64_same_lo_hi_optsize:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 65793
; RV64IZBS-NEXT:    addiw a0, a0, 16
; RV64IZBS-NEXT:    slli a1, a0, 32
; RV64IZBS-NEXT:    add a0, a0, a1
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_same_lo_hi_optsize:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 65793
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 16
; RV64IXTHEADBB-NEXT:    slli a1, a0, 32
; RV64IXTHEADBB-NEXT:    add a0, a0, a1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 1157442765409226768 ; 0x0101010101010101
}
; Hi and lo are the same and also negative.
define i64 @imm64_same_lo_hi_negative() nounwind {
; RV64-NOPOOL-LABEL: imm64_same_lo_hi_negative:
; RV64-NOPOOL:       # %bb.0:
; RV64-NOPOOL-NEXT:    lui a0, 983297
; RV64-NOPOOL-NEXT:    slli a0, a0, 4
; RV64-NOPOOL-NEXT:    addi a0, a0, 257
;
 RV64-NOPOOL-NEXT:    slli a0, a0, 16
; RV64-NOPOOL-NEXT:    addi a0, a0, 257
; RV64-NOPOOL-NEXT:    slli a0, a0, 15
; RV64-NOPOOL-NEXT:    addi a0, a0, 128
; RV64-NOPOOL-NEXT:    ret
;
; RV64I-POOL-LABEL: imm64_same_lo_hi_negative:
; RV64I-POOL:       # %bb.0:
; RV64I-POOL-NEXT:    lui a0, %hi(.LCPI65_0)
; RV64I-POOL-NEXT:    ld a0, %lo(.LCPI65_0)(a0)
; RV64I-POOL-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_same_lo_hi_negative:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 526344
; RV64IZBA-NEXT:    addi a0, a0, 128
; RV64IZBA-NEXT:    slli a1, a0, 32
; RV64IZBA-NEXT:    add.uw a0, a0, a1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_same_lo_hi_negative:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 983297
```

```
; RV64IZBB-NEXT:    slli a0, a0, 4
; RV64IZBB-NEXT:    addi a0, a0, 257
; RV64IZBB-NEXT:    slli a0, a0, 16
; RV64IZBB-NEXT:    addi a0, a0, 257
; RV64IZBB-NEXT:    slli a0, a0, 15
; RV64IZBB-NEXT:    addi a0, a0, 128
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_same_lo_hi_negative:
;
 RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    lui a0, 983297
; RV64IZBS-NEXT:    slli a0, a0, 4
; RV64IZBS-NEXT:    addi a0, a0, 257
; RV64IZBS-NEXT:    slli a0, a0, 16
; RV64IZBS-NEXT:    addi a0, a0, 257
; RV64IZBS-NEXT:    slli a0, a0, 15
; RV64IZBS-NEXT:    addi a0, a0, 128
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_same_lo_hi_negative:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 983297
; RV64IXTHEADBB-NEXT:    slli a0, a0, 4
; RV64IXTHEADBB-NEXT:    addi a0, a0, 257
; RV64IXTHEADBB-NEXT:    slli a0, a0, 16
; RV64IXTHEADBB-NEXT:    addi a0, a0, 257
; RV64IXTHEADBB-NEXT:    slli a0, a0, 15
; RV64IXTHEADBB-NEXT:    addi a0, a0, 128
; RV64IXTHEADBB-NEXT:    ret
 ret i64 9259542123273814144 ; 0x8080808080808080
}

define i64 @imm64_0x8000080000000() {
; RV64I-LABEL: imm64_0x8000080000000:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 256
; RV64I-NEXT:    addiw a0, a0, 1
; RV64I-NEXT:    slli a0, a0, 31
; RV64I-NEXT:    ret
;
;
 RV64IZBA-LABEL: imm64_0x8000080000000:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 256
; RV64IZBA-NEXT:    addiw a0, a0, 1
; RV64IZBA-NEXT:    slli a0, a0, 31
; RV64IZBA-NEXT:    ret
```

```
;
; RV64IZBB-LABEL: imm64_0x8000080000000:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 256
; RV64IZBB-NEXT:    addiw a0, a0, 1
; RV64IZBB-NEXT:    slli a0, a0, 31
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_0x8000080000000:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    bseti a0, zero, 31
; RV64IZBS-NEXT:    bseti a0, a0, 51
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_0x8000080000000:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 256
; RV64IXTHEADBB-NEXT:    addiw a0, a0, 1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 31
; RV64IXTHEADBB-NEXT:    ret
 ret i64 2251801961168896 ; 0x8000080000000
}

define i64 @imm64_0x10000100000000() {
; RV64I-LABEL: imm64_0x10000100000000:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a0, 256
; RV64I-NEXT:    addi a0, a0,
 1
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_0x10000100000000:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 256
; RV64IZBA-NEXT:    addi a0, a0, 1
; RV64IZBA-NEXT:    slli a0, a0, 32
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_0x10000100000000:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 256
; RV64IZBB-NEXT:    addi a0, a0, 1
; RV64IZBB-NEXT:    slli a0, a0, 32
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_0x10000100000000:
; RV64IZBS:       # %bb.0:
```

```
; RV64IZBS-NEXT:    bseti a0, zero, 32
; RV64IZBS-NEXT:    bseti a0, a0, 52
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_0x10000100000000:
; RV64IXTHEADBB:       # %bb.0:
; RV64IXTHEADBB-NEXT:    lui a0, 256
; RV64IXTHEADBB-NEXT:    addi a0, a0, 1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 32
; RV64IXTHEADBB-NEXT:    ret
 ret i64 4503603922337792 ; 0x10000100000000
}

define i64 @imm64_0xFF7FFFFF7FFFFFFE() {
; RV64I-LABEL: imm64_0xFF7FFFFF7FFFFFFE:
; RV64I:       # %bb.0:
;
 RV64I-NEXT:    lui a0, 1044480
; RV64I-NEXT:    addiw a0, a0, -1
; RV64I-NEXT:    slli a0, a0, 31
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    ret
;
; RV64IZBA-LABEL: imm64_0xFF7FFFFF7FFFFFFE:
; RV64IZBA:       # %bb.0:
; RV64IZBA-NEXT:    lui a0, 1044480
; RV64IZBA-NEXT:    addiw a0, a0, -1
; RV64IZBA-NEXT:    slli a0, a0, 31
; RV64IZBA-NEXT:    addi a0, a0, -1
; RV64IZBA-NEXT:    ret
;
; RV64IZBB-LABEL: imm64_0xFF7FFFFF7FFFFFFE:
; RV64IZBB:       # %bb.0:
; RV64IZBB-NEXT:    lui a0, 1044480
; RV64IZBB-NEXT:    addiw a0, a0, -1
; RV64IZBB-NEXT:    slli a0, a0, 31
; RV64IZBB-NEXT:    addi a0, a0, -1
; RV64IZBB-NEXT:    ret
;
; RV64IZBS-LABEL: imm64_0xFF7FFFFF7FFFFFFE:
; RV64IZBS:       # %bb.0:
; RV64IZBS-NEXT:    li a0, -1
; RV64IZBS-NEXT:    bclri a0, a0, 31
; RV64IZBS-NEXT:    bclri a0, a0, 55
; RV64IZBS-NEXT:    ret
;
; RV64IXTHEADBB-LABEL: imm64_0xFF7FFFFF7FFFFFFE:
; RV64IXTHEADBB:       # %bb.0:
```

```
; RV64IXTHEADBB-NEXT:    lui a0, 1044480
; RV64IXTHEADBB-NEXT:    addiw
 a0, a0, -1
; RV64IXTHEADBB-NEXT:    slli a0, a0, 31
; RV64IXTHEADBB-NEXT:    addi a0, a0, -1
; RV64IXTHEADBB-NEXT:    ret
 ret i64 -36028799166447617 ; 0xFF7FFFFF7FFFFFFE
}
; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right
; register classes.

define void @autogen_SD4739(ptr) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
 %L34 = load i8, ptr %0
 %Cmp56 = icmp sgt i8 undef, %L34
 br label %CF246

CF246:                                            ; preds = %CF246, %BB
 %Sl163 = select i1 %Cmp56, i8 %L34, i8 undef
 br i1 undef, label %CF246, label %CF248

CF248:                                            ; preds = %CF248, %CF246
 store i8 %Sl163, ptr %0
 br label %CF248
}
; RUN: llc -march=hexagon -hexagon-hvx-widen=32 < %s | FileCheck %s

; Truncating a type-to-be-widenened to a legal type (v8i8).
; Check that this compiles successfully.
; CHECK-LABEL: f0:
; CHECK: dealloc_return

target datalayout = "e-m:e-p:32:32:32-a:0-n16:32-i64:64:64-i32:32:32-i16:16:16-i1:8:8-f32:32:32-f64:64:64-
v32:32:32-v64:64:64-v512:512:512-v1024:1024:1024-v2048:2048:2048"
target triple = "hexagon"

define dllexport void @f0(ptr %a0) local_unnamed_addr #0 {
b0:
 %v0 = load i8, ptr undef, align 1
 %v1 = zext i8 %v0 to i16
 %v2 = add i16 0, %v1
 %v3 = icmp sgt i16 %v2, 1
 %v4 = select i1 %v3, i16 %v2, i16 1
 %v5 = udiv i16 -32768, %v4
 %v6 = zext i16 %v5 to i32
```

```
 %v7 = insertelement <8 x i32> undef, i32 %v6, i32 0
 %v8 = shufflevector <8 x i32> %v7, <8 x i32> undef, <8 x i32> zeroinitializer
 %v9 = load <8 x i16>, ptr undef, align 2
 %v10 = sext <8 x i16> %v9 to <8 x i32>
 %v11 = mul nsw <8 x i32> %v8, %v10
 %v12 = add nsw <8 x i32> %v11, <i32 16384, i32 16384, i32 16384,
 i32 16384, i32 16384, i32 16384, i32 16384, i32 16384>
 %v13 = lshr <8 x i32> %v12, <i32 15, i32 15, i32 15, i32 15, i32 15, i32 15, i32 15, i32 15>
 %v14 = trunc <8 x i32> %v13 to <8 x i8>
 %v15 = getelementptr inbounds i8, ptr %a0, i32 undef
 store <8 x i8> %v14, ptr %v15, align 1
 ret void
}

attributes #0 = { "target-features"="+hvx,+hvx-length128b" }
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --
check-attributes --check-globals
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal  -attributor-annotate-decl-cs -S <
%s | FileCheck %s --check-prefixes=CHECK,TUNIT
; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgscc -attributor-manifest-internal  -attributor-annotate-decl-cs -
S < %s | FileCheck %s --check-prefixes=CHECK,CGSCC
; Test that we only promote arguments when the caller/callee have compatible
; function attrubtes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr %arg, ptr
readonly %arg1) #0 {
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; CHECK-SAME:
 (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>
[[TMP0:%.*]]) #[[ATTR0:[0-9]+]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; CHECK-NEXT:    store <8 x i64> [[TMP0]], ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}
```

```
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr %arg) #0 {
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR0]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align
 32
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5:[0-9]+]]
; TUNIT-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; TUNIT-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6:[0-9]+]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull
 writeonly align 2 dereferenceable(64) [[ARG:%.*]]) #[[ATTR0]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5:[0-9]+]]
; CGSCC-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; CGSCC-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6:[0-9]+]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp,
 i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
```

```
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr %arg, ptr readonly %arg1) #1 {
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]]) #[[ATTR1:[0-9]+]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; CHECK-NEXT:    store <8 x i64> [[TMP0]], ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:
   store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr %arg) #1 {
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR1]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; TUNIT-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr
 noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
```

```
#[[ATTR1]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
;
 CGSCC-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; CGSCC-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr %arg, ptr readonly %arg1) #1 {
;
; CHECK: Function Attrs:
 inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]]) #[[ATTR1]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; CHECK-NEXT:    store <8 x i64> [[TMP0]], ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr %arg) #0 {
```

```
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
;
 TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR0]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; TUNIT-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind
 willreturn memory(argmem: readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]]) #[[ATTR0]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; CGSCC-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; CGSCC-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr
 [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}
```

```
; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr %arg, ptr readonly %arg1) #0 {
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]]) #[[ATTR0]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>,
 align 64
; CHECK-NEXT:    store <8 x i64> [[TMP0]], ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr %arg) #1 {
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR1]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32 dereferenceable(64) [[TMP]], i8 noundef 0,
 i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; TUNIT-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]]) #[[ATTR1]] {
```

```
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr
 noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP]], i8 noundef 0, i64
noundef 32, i1 noundef false) #[[ATTR5]]
; CGSCC-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; CGSCC-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr
noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
#[[ATTR6]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should
 not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %arg, ptr
readonly %arg1) #1 {
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], ptr noalias nocapture nofree noundef nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
#[[ATTR1]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %arg) #2 {
;
```

```
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse
 nosync nounwind willreturn memory(argmem: readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR2:[0-9]+]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32
dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr
noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], ptr noalias nocapture
nofree noundef nonnull readonly align 64 dereferenceable(64) [[TMP]]) #[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align
 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
#[[ATTR2:[0-9]+]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64
dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; CGSCC-NEXT:    call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr
noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], ptr noalias nocapture
nofree noundef nonnull readonly align 64 dereferenceable(64) [[TMP]]) #[[ATTR6]]
;
 CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %arg, ptr
readonly %arg1) #2 {
```

```
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], ptr noalias nocapture nofree noundef nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]]) #[[ATTR2:[0-9]+]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %arg) #1 {
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR1]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], ptr noalias nocapture nofree noundef nonnull readonly align 64 dereferenceable(64) [[TMP]]) #[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]]) #[[ATTR1]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]]
 = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; CGSCC-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr
```

```
noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], ptr noalias nocapture
nofree noundef nonnull readonly align 64 dereferenceable(64) [[TMP]]) #[[ATTR6]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store
 <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr %arg, ptr readonly
%arg1) #3 {
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]]) #[[ATTR3:[0-9]+]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; CHECK-NEXT:    store <8 x i64> [[TMP0]], ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr
 %arg) #4 {
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR3]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
```

```
; TUNIT-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; TUNIT-NEXT:    call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]]
 = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]]) #[[ATTR3]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; CGSCC-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; CGSCC-NEXT:    call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr noalias nocapture nofree noundef
 nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]]) #[[ATTR6]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr %arg, ptr readonly %arg1) #4 {
;
; CHECK: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem: readwrite) uwtable
; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (ptr noalias
```

```
 nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
#[[ATTR3]] {
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; CHECK-NEXT:    store <8 x i64> [[TMP0]], ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1_PRIV]], align 64
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]], align 64
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr %arg) #3 {
;
; TUNIT: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; TUNIT-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; TUNIT-SAME: (ptr nocapture nofree writeonly [[ARG:%.*]]) #[[ATTR3]] {
; TUNIT-NEXT:  bb:
; TUNIT-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; TUNIT-NEXT:    [[TMP2:%.*]]
 = alloca <8 x i64>, align 32
; TUNIT-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 32
dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; TUNIT-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; TUNIT-NEXT:    call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr noalias
nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
#[[ATTR6]]
; TUNIT-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; TUNIT-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; TUNIT-NEXT:    ret void
;
; CGSCC: Function Attrs: inlinehint mustprogress nofree norecurse nosync nounwind willreturn memory(argmem:
readwrite) uwtable
; CGSCC-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CGSCC-SAME: (ptr nocapture nofree noundef nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
 #[[ATTR3]] {
; CGSCC-NEXT:  bb:
; CGSCC-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CGSCC-NEXT:    call void @llvm.memset.p0.i64(ptr noalias nocapture nofree noundef nonnull writeonly align 64
dereferenceable(64) [[TMP]], i8 noundef 0, i64 noundef 32, i1 noundef false) #[[ATTR5]]
; CGSCC-NEXT:    [[TMP0:%.*]] = load <8 x i64>, ptr [[TMP]], align 64
; CGSCC-NEXT:    call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr noalias
nocapture nofree noundef nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
```

```
#[[ATTR6]]
; CGSCC-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 64
; CGSCC-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CGSCC-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr
 %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0.i64(ptr nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512"
"prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256"
"prefer-vector-width"="256" }
attributes
 #5 = { argmemonly nounwind }
;.
; TUNIT: attributes #[[ATTR0]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="512" "prefer-vector-width"="512" "target-
features"="+avx512vl" }
; TUNIT: attributes #[[ATTR1]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="512" "prefer-vector-width"="256" "target-
features"="+avx512vl" }
; TUNIT: attributes #[[ATTR2]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="256" "prefer-vector-width"="256" "target-
features"="+avx512vl" }
; TUNIT: attributes #[[ATTR3]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="512" "prefer-vector-width"="256" "target-
features"="+avx2" }
; TUNIT: attributes #[[ATTR4:[0-9]+]] = { nocallback
 nofree nounwind willreturn memory(argmem: write) }
; TUNIT: attributes #[[ATTR5]] = { nofree willreturn memory(write) }
; TUNIT: attributes #[[ATTR6]] = { nofree nosync nounwind willreturn }
```

```
;.
; CGSCC: attributes #[[ATTR0]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="512" "prefer-vector-width"="512" "target-
features"="+avx512vl" }
; CGSCC: attributes #[[ATTR1]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="512" "prefer-vector-width"="256" "target-
features"="+avx512vl" }
; CGSCC: attributes #[[ATTR2]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem: readwrite) uwtable "min-legal-vector-width"="256" "prefer-vector-width"="256" "target-
features"="+avx512vl" }
; CGSCC: attributes #[[ATTR3]] = { inlinehint mustprogress nofree norecurse nosync nounwind willreturn
memory(argmem:
 readwrite) uwtable "min-legal-vector-width"="512" "prefer-vector-width"="256" "target-features"="+avx2" }
; CGSCC: attributes #[[ATTR4:[0-9]+]] = { nocallback nofree nounwind willreturn memory(argmem: write) }
; CGSCC: attributes #[[ATTR5]] = { nofree willreturn memory(write) }
; CGSCC: attributes #[[ATTR6]] = { nofree nounwind willreturn }
;.
UNRECOGNIZED LICENSE; MD5 sum: fbd1651ce79891908cc5e8bac385fff7
; RUN: llc -O3 -mtriple=powerpc-unknown-linux-gnu -mcpu=e500 -mattr=spe < %s | FileCheck %s

; PowerPC SPE is a rare in-tree target that has the FP_TO_SINT node marked
; as Legal.

; Verify that fptosi(42.1) isn't simplified when the rounding mode is
; unknown.
; Verify that no gross errors happen.
; CHECK-LABEL: @f20
; COMMON: cfdctsiz
define i32 @f20(double %a) strictfp {
entry:
 %result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1,
                                        metadata !"fpexcept.strict")
                                        strictfp
 ret i32 %result
}

@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)
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```

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THIS SOFTWARE IS PROVIDED BY THE COPYRIGHT HOLDERS AND CONTRIBUTORS "AS IS" AND ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE DISCLAIMED. IN NO EVENT SHALL THE COPYRIGHT
HOLDER OR CONTRIBUTORS BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s -passes="print<cost-model>" 2>&1 -disable-output -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=VEC256,AVX
; RUN: opt < %s -passes="print<cost-model>" 2>&1 -disable-output -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+prefer-256-bit | FileCheck %s --check-prefixes=VEC256,AVX512VL256
; RUN: opt < %s -passes="print<cost-model>" 2>&1 -disable-output -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,-prefer-256-bit | FileCheck %s --check-prefixes=AVX512VL512
; RUN: opt < %s -passes="print<cost-model>" 2>&1 -disable-output -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=VEC256,SKX256
; RUN: opt < %s -passes="print<cost-model>" 2>&1 -disable-output -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=SKX512

define
 void @zext256() "min-legal-vector-width"="256" {
; AVX-LABEL: 'zext256'
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'zext256'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX512VL256-NEXT: Cost
 Model: Found an estimated cost of 2 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;

```
; AVX512VL512-LABEL: 'zext256'
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction:
 %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext256'
; SKX256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext256'
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT:  Cost Model: Found an estimated
 cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
 %A = zext <8 x i16> undef to <8 x i64>
 %B = zext <8 x i32> undef to <8 x i64>
 %C = zext <16 x i8> undef to <16 x i32>
 %D = zext <16 x i16> undef to <16 x i32>
 %E = zext <32 x i8> undef to <32 x i16>
 ret void
}

define void @zext512() "min-legal-vector-width"="512" {
```

; AVX-LABEL: 'zext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %B = zext <8 x i32>
 undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'zext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost
 of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'zext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8
 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

```
;
; SKX512-LABEL: 'zext512'
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16
 x i16> undef to <16 x i32>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
 %A = zext <8 x i16> undef to <8 x i64>
 %B = zext <8 x i32> undef to <8 x i64>
 %C = zext <16 x i8> undef to <16 x i32>
 %D = zext <16 x i16> undef to <16 x i32>
 %E = zext <32 x i8> undef to <32 x i16>
 ret void
}

define void @sext256() "min-legal-vector-width"="256" {
; AVX-LABEL: 'sext256'
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 5 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT:  Cost Model: Found
 an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT:  Cost Model: Found an estimated cost of 5 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'sext256'
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef
 to <32 x i16>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
```

; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext256'
; SKX256-NEXT: Cost Model: Found an
 estimated cost of 2 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost
 Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
 %A = sext <8 x i8> undef to <8 x i64>
 %B = sext <8 x i16> undef to <8 x i64>
 %C = sext <8 x i32> undef to <8 x i64>
 %D = sext <16 x i8> undef to <16 x i32>

```
 %E = sext <16 x i16> undef to <16 x i32>
 %F = sext <32 x i8> undef to <32 x i16>
 ret void
}

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT:  Cost Model: Found
 an estimated cost of 4 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 5 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT:  Cost Model: Found an estimated cost of 5 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'sext512'
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL256-NEXT:
  Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to
<16 x i32>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL256-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext512'
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8
x i64>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to
<8 x i64>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to
<8 x i64>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to
<16 x i32>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost
 of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to
<32 x i16>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
```

```
; SKX256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX256-NEXT:  Cost
 Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext512'
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
i64>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x
i64>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x
i32>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x
i16>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
 %A = sext <8 x i8> undef to <8 x i64>
 %B = sext <8 x i16> undef to <8 x i64>
 %C = sext <8 x i32> undef to <8 x i64>
 %D = sext <16 x i8>
 undef to <16 x i32>
 %E = sext <16 x i16> undef to <16 x i32>
 %F = sext <32 x i8> undef to <32 x i16>
 ret void
}

define void @trunc256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'trunc256'
; VEC256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x
i32>
; VEC256-NEXT:  Cost Model: Found an estimated cost of 10 for instruction: %B = trunc <8 x i64> undef to <8 x
i16>
; VEC256-NEXT:  Cost Model: Found an estimated cost of 8 for instruction: %C = trunc <8 x i64> undef to <8 x
i8>
; VEC256-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; VEC256-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %E = trunc <16 x i32> undef to <16 x
```

```
i8>
; VEC256-NEXT:  Cost Model: Found an estimated cost of 5 for instruction: %F = trunc <32 x i16> undef to <32 x
i8>
; VEC256-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'trunc256'
;
 AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to
<8 x i32>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to
<8 x i16>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to
<8 x i8>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef
to <16 x i16>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to
<16 x i8>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 8 for instruction: %F = trunc <32 x i16> undef to
<32 x i8>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'trunc256'
; SKX512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x
i32>
; SKX512-NEXT:  Cost Model: Found an estimated
 cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x
i8>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x
i16>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x
i8>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %F = trunc <32 x i16> undef to <32 x
i8>
; SKX512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
;
 %A = trunc <8 x i64> undef to <8 x i32>
 %B = trunc <8 x i64> undef to <8 x i16>
 %C = trunc <8 x i64> undef to <8 x i8>
 %D = trunc <16 x i32> undef to <16 x i16>
 %E = trunc <16 x i32> undef to <16 x i8>
 %F = trunc <32 x i16> undef to <32 x i8>
 ret void
}

define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'zext256_vXi1'
; AVX-NEXT:  Cost Model: Found
```

an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX-NEXT:
  Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef
 to <64 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL256-LABEL: 'zext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i32 = zext <16 x i1>

undef to <16 x i32>
; AVX512VL256-NEXT:
Cost Model: Found an estimated cost of 5 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated
cost of 12 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 25 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 50 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'zext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of
2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'zext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
;
SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost
 Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext
 <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated
 cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
 %V2i64 = zext <2 x i1> undef to <2 x i64>

```
 %V4i64 = zext <4 x i1> undef to <4 x i64>
 %V8i64 = zext <8 x i1> undef to <8 x i64>

 %V2i32 = zext <2 x i1> undef to <2 x i32>
 %V4i32 = zext <4 x i1> undef to <4 x i32>
 %V8i32 = zext <8 x i1> undef to <8 x i32>
 %V16i32 = zext <16 x i1> undef to <16 x i32>

 %V2i16 = zext <2 x i1> undef to <2 x i16>
 %V4i16 = zext <4 x i1> undef to <4 x i16>
 %V8i16 = zext <8 x i1> undef to <8 x i16>
 %V16i16 = zext <16 x i1> undef to <16 x i16>
 %V32i16 = zext <32 x i1> undef to <32 x i16>

 %V2i8 = zext <2 x i1> undef to <2 x i8>
 %V4i8 = zext <4 x i1> undef to <4 x i8>
 %V8i8 = zext <8 x i1> undef to <8 x i8>
 %V16i8 = zext <16 x i1> undef to <16 x i8>
 %V32i8 = zext <32 x i1> undef to <32 x i8>
 %V64i8 = zext <64 x i1> undef to <64 x i8>

 ret i32 undef
}

define i32 @sext256_vXi1()
 "min-legal-vector-width"="256" {
; AVX-LABEL: 'sext256_vXi1'
; AVX-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x
i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x
i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %V8i64 = sext <8 x i1> undef to <8 x
i64>
; AVX-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x
i32>
; AVX-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = sext <4 x i1> undef to <4 x
i32>
; AVX-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = sext <8 x i1> undef to <8 x
i32>
; AVX-NEXT:  Cost Model: Found an estimated cost
 of 4 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x
i16>
; AVX-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = sext <4 x i1> undef to <4 x
i16>
```

; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for
 instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL256-LABEL: 'sext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL256-NEXT:
  Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction:
 %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
;
 AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 42 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'sext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL512-NEXT: Cost Model: Found an estimated
 cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 =
 sext <16 x i1> undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT:
  Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'sext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef
 to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8
 = sext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>

; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'sext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated
 cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model:
 Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
;
 SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>

```
; SKX512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
 %I64 = sext i1 undef to i64
 %V2i64 = sext <2 x i1> undef to <2 x i64>
 %V4i64 = sext <4 x i1> undef to <4 x i64>
 %V8i64 = sext <8 x i1> undef to <8 x i64>

 %I32 = sext i1 undef to i32
 %V2i32 = sext <2 x i1> undef to <2 x i32>
 %V4i32 = sext <4 x i1> undef to <4 x i32>
 %V8i32 = sext <8 x i1> undef to <8 x i32>
 %V16i32 = sext <16 x i1> undef to <16 x i32>

 %I16 = sext i1 undef to i16
 %V2i16 = sext <2 x i1> undef to <2 x i16>
 %V4i16 = sext <4 x i1> undef to <4 x i16>
 %V8i16 = sext <8 x i1> undef to <8 x i16>
 %V16i16 = sext <16 x i1> undef to <16 x i16>
 %V32i16 = sext <32 x i1> undef to <32 x i16>

 %I8 = sext i1 undef to i8
 %V2i8 = sext <2
 x i1> undef to <2 x i8>
 %V4i8 = sext <4 x i1> undef to <4 x i8>
 %V8i8 = sext <8 x i1> undef to <8 x i8>
 %V16i8 = sext <16 x i1> undef to <16 x i8>
 %V32i8 = sext <32 x i1> undef to <32 x i8>
 %V64i8 = sext <64 x i1> undef to <64 x i8>

 ret i32 undef
}

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc_vXi1'
; AVX-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x
i1>
; AVX-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x
i1>
; AVX-NEXT:  Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x
i1>
; AVX-NEXT:  Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to
<16 x i1>
; AVX-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: %V32i64 = trunc <32 x i64> undef to <32
x i1>
; AVX-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: %V64i64
 = trunc <64 x i64> undef to <64 x i1>
; AVX-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x
i1>
```

; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated
 cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX-NEXT:
  Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL256-LABEL: 'trunc_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model:

Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 16 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an
estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 16 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 32 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of
0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'trunc_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction:
 %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction:
 %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = trunc <16 x
 i8> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'trunc_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to

<16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX256-NEXT:
  Cost Model: Found an estimated cost of 0 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction:
 %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX256-NEXT:
  Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'trunc_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>

; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction:
 %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
;
 SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for
 instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
 %V2i64 = trunc <2 x i64> undef to <2 x i1>

```
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>

%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>

%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to
<16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>

%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>

ret i32 undef
}
```

==========

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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp

LINK_LIBS
lldbCore
lldbHost
lldbSymbol
LINK_COMPONENTS
Support
)
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN: -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN: -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64IZbb

declare i4 @llvm.sadd.sat.i4(i4, i4)
declare i8 @llvm.sadd.sat.i8(i8, i8)
declare i16 @llvm.sadd.sat.i16(i16, i16)
declare i32 @llvm.sadd.sat.i32(i32, i32)
declare i64 @llvm.sadd.sat.i64(i64, i64)

define i32 @func32(i32 %x, i32 %y, i32 %z) nounwind {
; RV64I-LABEL: func32:
; RV64I: # %bb.0:

```
; RV64I-NEXT:    mulw a1, a1, a2
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    addw a2, a0, a1
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    beq a0, a2, .LBB0_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    sraiw a0, a0, 31
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:  .LBB0_2:
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL:
 func32:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    mulw a1, a1, a2
; RV64IZbb-NEXT:    sext.w a0, a0
; RV64IZbb-NEXT:    add a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 524288
; RV64IZbb-NEXT:    addiw a2, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a2
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i32 %y, %z
 %tmp = call i32 @llvm.sadd.sat.i32(i32 %x, i32 %a)
 ret i32 %tmp
}

define i64 @func64(i64 %x, i64 %y, i64 %z) nounwind {
; RV64-LABEL: func64:
; RV64:       # %bb.0:
; RV64-NEXT:    mv a1, a0
; RV64-NEXT:    add a0, a0, a2
; RV64-NEXT:    slt a1, a0, a1
; RV64-NEXT:    slti a2, a2, 0
; RV64-NEXT:    beq a2, a1, .LBB1_2
; RV64-NEXT:  # %bb.1:
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    li a1, -1
; RV64-NEXT:    slli a1, a1, 63
; RV64-NEXT:    xor a0, a0, a1
; RV64-NEXT:  .LBB1_2:
; RV64-NEXT:    ret
 %a = mul i64 %y, %z
 %tmp = call i64 @llvm.sadd.sat.i64(i64 %x, i64 %z)
 ret i64 %tmp
}
```

```
define i16 @func16(i16 %x, i16 %y, i16 %z) nounwind {
; RV64I-LABEL:
 func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    slli a1, a1, 16
; RV64I-NEXT:    sraiw a1, a1, 16
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    lui a1, 8
; RV64I-NEXT:    addiw a1, a1, -1
; RV64I-NEXT:    bge a0, a1, .LBB2_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    bge a1, a0, .LBB2_4
; RV64I-NEXT:  .LBB2_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB2_3:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    blt a1, a0, .LBB2_2
; RV64I-NEXT:  .LBB2_4:
; RV64I-NEXT:    lui a0, 1048568
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    sext.h a0, a0
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    sext.h a1, a1
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 8
; RV64IZbb-NEXT:    addiw a1, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 1048568
;
 RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i16 %y, %z
 %tmp = call i16 @llvm.sadd.sat.i16(i16 %x, i16 %a)
 ret i16 %tmp
}

define i8 @func8(i8 %x, i8 %y, i8 %z) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 24
```

```
; RV64I-NEXT:    sraiw a0, a0, 24
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    slli a1, a1, 24
; RV64I-NEXT:    sraiw a1, a1, 24
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 127
; RV64I-NEXT:    bge a0, a1, .LBB3_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -127
; RV64I-NEXT:    blt a0, a1, .LBB3_4
; RV64I-NEXT:  .LBB3_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB3_3:
; RV64I-NEXT:    li a0, 127
; RV64I-NEXT:    li a1, -127
; RV64I-NEXT:    bge a0, a1, .LBB3_2
; RV64I-NEXT:  .LBB3_4:
; RV64I-NEXT:    li a0, -128
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    sext.b a0, a0
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    sext.b a1, a1
;
 RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 127
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -128
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i8 %y, %z
 %tmp = call i8 @llvm.sadd.sat.i8(i8 %x, i8 %a)
 ret i8 %tmp
}

define i4 @func4(i4 %x, i4 %y, i4 %z) nounwind {
; RV64I-LABEL: func4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 28
; RV64I-NEXT:    sraiw a0, a0, 28
; RV64I-NEXT:    mul a1, a1, a2
; RV64I-NEXT:    slli a1, a1, 28
; RV64I-NEXT:    sraiw a1, a1, 28
; RV64I-NEXT:    addw a0, a0, a1
; RV64I-NEXT:    li a1, 7
; RV64I-NEXT:    bge a0, a1, .LBB4_3
```

```
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -7
; RV64I-NEXT:    blt a0, a1, .LBB4_4
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB4_3:
; RV64I-NEXT:    li a0, 7
; RV64I-NEXT:    li a1, -7
; RV64I-NEXT:    bge a0, a1, .LBB4_2
; RV64I-NEXT:  .LBB4_4:
; RV64I-NEXT:    li a0, -8
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func4:
; RV64IZbb:       #
 %bb.0:
; RV64IZbb-NEXT:    slli a0, a0, 28
; RV64IZbb-NEXT:    sraiw a0, a0, 28
; RV64IZbb-NEXT:    mul a1, a1, a2
; RV64IZbb-NEXT:    slli a1, a1, 28
; RV64IZbb-NEXT:    sraiw a1, a1, 28
; RV64IZbb-NEXT:    addw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 7
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -8
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %a = mul i4 %y, %z
 %tmp = call i4 @llvm.sadd.sat.i4(i4 %x, i4 %a)
 ret i4 %tmp
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=RV64I %s
; RUN: llc -mtriple=riscv64 -mattr=+m -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=RV64IM %s

define i32 @udiv(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: udiv:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    slli a1, a1, 32
; RV64I-NEXT:    srli a1, a1, 32
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
```

```
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    divuw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = udiv i32 %a, %b
 ret i32 %1
}

define i32 @udiv_constant(i32 %a) nounwind {
; RV64I-LABEL:
 udiv_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a0, a0, 32
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 32
; RV64IM-NEXT:    lui a1, 838861
; RV64IM-NEXT:    addi a1, a1, -819
; RV64IM-NEXT:    slli a1, a1, 32
; RV64IM-NEXT:    mulhu a0, a0, a1
; RV64IM-NEXT:    srli a0, a0, 34
; RV64IM-NEXT:    ret
 %1 = udiv i32 %a, 5
 ret i32 %1
}

define i32 @udiv_pow2(i32 %a) nounwind {
; RV64I-LABEL: udiv_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    srliw a0, a0, 3
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    srliw a0, a0, 3
; RV64IM-NEXT:    ret
```

```
 %1 = udiv i32 %a, 8
 ret i32 %1
}

define i32 @udiv_constant_lhs(i32
 %a) nounwind {
; RV64I-LABEL: udiv_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 32
; RV64I-NEXT:    srli a1, a0, 32
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    divuw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = udiv i32 10, %a
 ret i32 %1
}

define i64 @udiv64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: udiv64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    tail __udivdi3
;
; RV64IM-LABEL: udiv64:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    divu a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = udiv i64 %a, %b
 ret i64 %1
}

define i64 @udiv64_constant(i64 %a) nounwind {
; RV64I-LABEL: udiv64_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:
    tail __udivdi3
;
; RV64IM-LABEL: udiv64_constant:
```

```
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    lui a1, 838861
; RV64IM-NEXT:    addiw a1, a1, -819
; RV64IM-NEXT:    slli a2, a1, 32
; RV64IM-NEXT:    add a1, a1, a2
; RV64IM-NEXT:    mulhu a0, a0, a1
; RV64IM-NEXT:    srli a0, a0, 2
; RV64IM-NEXT:    ret
 %1 = udiv i64 %a, 5
 ret i64 %1
}

define i64 @udiv64_constant_lhs(i64 %a) nounwind {
; RV64I-LABEL: udiv64_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mv a1, a0
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    tail __udivdi3
;
; RV64IM-LABEL: udiv64_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    divu a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = udiv i64 10, %a
 ret i64 %1
}

define i8 @udiv8(i8 %a, i8 %b) nounwind {
; RV64I-LABEL: udiv8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    andi a1, a1, 255
; RV64I-NEXT:    call
 __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv8:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    andi a1, a1, 255
; RV64IM-NEXT:    andi a0, a0, 255
; RV64IM-NEXT:    divuw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = udiv i8 %a, %b
```

```
 ret i8 %1
}

define i8 @udiv8_constant(i8 %a) nounwind {
; RV64I-LABEL: udiv8_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    andi a0, a0, 255
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv8_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    andi a0, a0, 255
; RV64IM-NEXT:    li a1, 205
; RV64IM-NEXT:    mul a0, a0, a1
; RV64IM-NEXT:    srliw a0, a0, 10
; RV64IM-NEXT:    ret
 %1 = udiv i8 %a, 5
 ret i8 %1
}

define i8 @udiv8_pow2(i8 %a) nounwind
 {
; RV64I-LABEL: udiv8_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 56
; RV64I-NEXT:    srli a0, a0, 59
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv8_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 56
; RV64IM-NEXT:    srli a0, a0, 59
; RV64IM-NEXT:    ret
 %1 = udiv i8 %a, 8
 ret i8 %1
}

define i8 @udiv8_constant_lhs(i8 %a) nounwind {
; RV64I-LABEL: udiv8_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
```

```
; RV64I-NEXT:    andi a1, a0, 255
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv8_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    andi a0, a0, 255
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    divuw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = udiv i8 10, %a
 ret i8 %1
}

define i16 @udiv16(i16 %a, i16 %b) nounwind {
; RV64I-LABEL: udiv16:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    lui a2, 16
; RV64I-NEXT:    addiw a2, a2, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv16:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    lui a2, 16
; RV64IM-NEXT:    addi a2, a2, -1
; RV64IM-NEXT:    and a1, a1, a2
; RV64IM-NEXT:    and a0, a0, a2
; RV64IM-NEXT:    divuw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = udiv i16 %a, %b
 ret i16 %1
}

define i16 @udiv16_constant(i16 %a) nounwind {
; RV64I-LABEL: udiv16_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
```

```
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 48
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    call __udivdi3
; RV64I-NEXT:    ld ra, 8(sp)
 # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv16_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    lui a1, 52429
; RV64IM-NEXT:    slli a1, a1, 4
; RV64IM-NEXT:    slli a0, a0, 48
; RV64IM-NEXT:    mulhu a0, a0, a1
; RV64IM-NEXT:    srliw a0, a0, 18
; RV64IM-NEXT:    ret
 %1 = udiv i16 %a, 5
 ret i16 %1
}

define i16 @udiv16_pow2(i16 %a) nounwind {
; RV64I-LABEL: udiv16_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a0, a0, 51
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv16_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 48
; RV64IM-NEXT:    srli a0, a0, 51
; RV64IM-NEXT:    ret
 %1 = udiv i16 %a, 8
 ret i16 %1
}

define i16 @udiv16_constant_lhs(i16 %a) nounwind {
; RV64I-LABEL: udiv16_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 48
; RV64I-NEXT:    srli a1, a0, 48
; RV64I-NEXT:
    li a0, 10
; RV64I-NEXT:    call __udivdi3
```

```
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: udiv16_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 48
; RV64IM-NEXT:    srli a0, a0, 48
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    divuw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = udiv i16 10, %a
 ret i16 %1
}

define i32 @sdiv(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: sdiv:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sext.w a1, a1
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    divw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i32 %a, %b
 ret i32 %1
}

define i32 @sdiv_constant(i32 %a) nounwind {
; RV64I-LABEL: sdiv_constant:
;
 RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
```

```
; RV64IM-LABEL: sdiv_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    sext.w a0, a0
; RV64IM-NEXT:    lui a1, 419430
; RV64IM-NEXT:    addiw a1, a1, 1639
; RV64IM-NEXT:    mul a0, a0, a1
; RV64IM-NEXT:    srli a1, a0, 63
; RV64IM-NEXT:    srai a0, a0, 33
; RV64IM-NEXT:    addw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i32 %a, 5
 ret i32 %1
}

define i32 @sdiv_pow2(i32 %a) nounwind {
; RV64I-LABEL: sdiv_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraiw a1, a0, 31
; RV64I-NEXT:    srliw a1, a1, 29
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    sraiw a0, a0, 3
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    sraiw a1, a0,
 31
; RV64IM-NEXT:    srliw a1, a1, 29
; RV64IM-NEXT:    add a0, a0, a1
; RV64IM-NEXT:    sraiw a0, a0, 3
; RV64IM-NEXT:    ret
 %1 = sdiv i32 %a, 8
 ret i32 %1
}

define i32 @sdiv_pow2_2(i32 %a) nounwind {
; RV64I-LABEL: sdiv_pow2_2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sraiw a1, a0, 31
; RV64I-NEXT:    srliw a1, a1, 16
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv_pow2_2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    sraiw a1, a0, 31
; RV64IM-NEXT:    srliw a1, a1, 16
```

```
; RV64IM-NEXT:    add a0, a0, a1
; RV64IM-NEXT:    sraiw a0, a0, 16
; RV64IM-NEXT:    ret
 %1 = sdiv i32 %a, 65536
 ret i32 %1
}

define i32 @sdiv_constant_lhs(i32 %a) nounwind {
; RV64I-LABEL: sdiv_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sext.w a1, a0
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded
 Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    divw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = sdiv i32 -10, %a
 ret i32 %1
}

define i64 @sdiv64(i64 %a, i64 %b) nounwind {
; RV64I-LABEL: sdiv64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    tail __divdi3
;
; RV64IM-LABEL: sdiv64:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    div a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i64 %a, %b
 ret i64 %1
}

define i64 @sdiv64_constant(i64 %a) nounwind {
; RV64I-LABEL: sdiv64_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    tail __divdi3
;
```

```
; RV64IM-LABEL: sdiv64_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    lui a1, %hi(.LCPI21_0)
; RV64IM-NEXT:    ld a1, %lo(.LCPI21_0)(a1)
; RV64IM-NEXT:    mulh a0, a0, a1
; RV64IM-NEXT:    srli a1, a0, 63
; RV64IM-NEXT:    srai a0, a0, 1
; RV64IM-NEXT:    add a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i64 %a,
 5
 ret i64 %1
}

define i64 @sdiv64_constant_lhs(i64 %a) nounwind {
; RV64I-LABEL: sdiv64_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    mv a1, a0
; RV64I-NEXT:    li a0, 10
; RV64I-NEXT:    tail __divdi3
;
; RV64IM-LABEL: sdiv64_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    li a1, 10
; RV64IM-NEXT:    div a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = sdiv i64 10, %a
 ret i64 %1
}

; Although this sdiv has two sexti32 operands, it shouldn't compile to divw on
; RV64M as that wouldn't produce the correct result for e.g. INT_MIN/-1.

define i64 @sdiv64_sext_operands(i32 %a, i32 %b) nounwind {
; RV64I-LABEL: sdiv64_sext_operands:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    sext.w a1, a1
; RV64I-NEXT:    tail __divdi3
;
; RV64IM-LABEL: sdiv64_sext_operands:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    sext.w a0, a0
; RV64IM-NEXT:    sext.w a1, a1
; RV64IM-NEXT:    div a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sext i32 %a to i64
 %2 = sext i32 %b to
```

```
 i64
 %3 = sdiv i64 %1, %2
 ret i64 %3
}

define i8 @sdiv8(i8 %a, i8 %b) nounwind {
; RV64I-LABEL: sdiv8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a1, a1, 24
; RV64I-NEXT:    sraiw a1, a1, 24
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a0, a0, 24
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv8:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a1, a1, 24
; RV64IM-NEXT:    sraiw a1, a1, 24
; RV64IM-NEXT:    slli a0, a0, 24
; RV64IM-NEXT:    sraiw a0, a0, 24
; RV64IM-NEXT:    divw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i8 %a, %b
 ret i8 %1
}

define i8 @sdiv8_constant(i8 %a) nounwind {
; RV64I-LABEL: sdiv8_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:
    sraiw a0, a0, 24
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv8_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 24
```

```
; RV64IM-NEXT:    sraiw a0, a0, 24
; RV64IM-NEXT:    li a1, 103
; RV64IM-NEXT:    mul a0, a0, a1
; RV64IM-NEXT:    sraiw a1, a0, 9
; RV64IM-NEXT:    slli a0, a0, 48
; RV64IM-NEXT:    srli a0, a0, 63
; RV64IM-NEXT:    addw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = sdiv i8 %a, 5
 ret i8 %1
}

define i8 @sdiv8_pow2(i8 %a) nounwind {
; RV64I-LABEL: sdiv8_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 24
; RV64I-NEXT:    sraiw a1, a1, 24
; RV64I-NEXT:    slli a1, a1, 49
; RV64I-NEXT:    srli a1, a1, 61
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a0, a0, 27
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv8_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a1, a0,
 24
; RV64IM-NEXT:    sraiw a1, a1, 24
; RV64IM-NEXT:    slli a1, a1, 49
; RV64IM-NEXT:    srli a1, a1, 61
; RV64IM-NEXT:    add a0, a0, a1
; RV64IM-NEXT:    slli a0, a0, 24
; RV64IM-NEXT:    sraiw a0, a0, 27
; RV64IM-NEXT:    ret
 %1 = sdiv i8 %a, 8
 ret i8 %1
}

define i8 @sdiv8_constant_lhs(i8 %a) nounwind {
; RV64I-LABEL: sdiv8_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 24
; RV64I-NEXT:    sraiw a1, a0, 24
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    call __divdi3
```

```
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv8_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 24
; RV64IM-NEXT:    sraiw a0, a0, 24
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    divw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = sdiv i8 -10, %a
 ret i8 %1
}

define i16 @sdiv16(i16 %a, i16 %b) nounwind {
; RV64I-LABEL:
 sdiv16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a1, a1, 16
; RV64I-NEXT:    sraiw a1, a1, 16
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv16:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a1, a1, 16
; RV64IM-NEXT:    sraiw a1, a1, 16
; RV64IM-NEXT:    slli a0, a0, 16
; RV64IM-NEXT:    sraiw a0, a0, 16
; RV64IM-NEXT:    divw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i16 %a, %b
 ret i16 %1
}

define i16 @sdiv16_constant(i16 %a) nounwind {
; RV64I-LABEL: sdiv16_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 16
```

```
; RV64I-NEXT:    sraiw a0, a0, 16
; RV64I-NEXT:    li a1, 5
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:
   ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv16_constant:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 16
; RV64IM-NEXT:    sraiw a0, a0, 16
; RV64IM-NEXT:    lui a1, 6
; RV64IM-NEXT:    addi a1, a1, 1639
; RV64IM-NEXT:    mul a0, a0, a1
; RV64IM-NEXT:    srliw a1, a0, 31
; RV64IM-NEXT:    sraiw a0, a0, 17
; RV64IM-NEXT:    addw a0, a0, a1
; RV64IM-NEXT:    ret
 %1 = sdiv i16 %a, 5
 ret i16 %1
}

define i16 @sdiv16_pow2(i16 %a) nounwind {
; RV64I-LABEL: sdiv16_pow2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a0, 16
; RV64I-NEXT:    sraiw a1, a1, 16
; RV64I-NEXT:    slli a1, a1, 33
; RV64I-NEXT:    srli a1, a1, 61
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a0, a0, 19
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv16_pow2:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a1, a0, 16
; RV64IM-NEXT:    sraiw a1, a1, 16
; RV64IM-NEXT:    slli a1, a1, 33
;
 RV64IM-NEXT:    srli a1, a1, 61
; RV64IM-NEXT:    add a0, a0, a1
; RV64IM-NEXT:    slli a0, a0, 16
; RV64IM-NEXT:    sraiw a0, a0, 19
; RV64IM-NEXT:    ret
 %1 = sdiv i16 %a, 8
 ret i16 %1
```

```
}

define i16 @sdiv16_constant_lhs(i16 %a) nounwind {
; RV64I-LABEL: sdiv16_constant_lhs:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    slli a0, a0, 16
; RV64I-NEXT:    sraiw a1, a0, 16
; RV64I-NEXT:    li a0, -10
; RV64I-NEXT:    call __divdi3
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64IM-LABEL: sdiv16_constant_lhs:
; RV64IM:       # %bb.0:
; RV64IM-NEXT:    slli a0, a0, 16
; RV64IM-NEXT:    sraiw a0, a0, 16
; RV64IM-NEXT:    li a1, -10
; RV64IM-NEXT:    divw a0, a1, a0
; RV64IM-NEXT:    ret
 %1 = sdiv i16 -10, %a
 ret i16 %1
}
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comment syntax for the file format. We also recommend that a
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```
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py UTC_ARGS: --version 3
; RUN: llc -mtriple=riscv64 -target-abi=lp64f -mattr=+f,+zbs \
; RUN:   -riscv-experimental-rv64-legal-i32 < %s | FileCheck %s -check-prefix=RV64I
; RUN: llc -mtriple=riscv64 -target-abi=lp64f -mattr=+f,+zbs,+xventanacondops \
; RUN:   -riscv-experimental-rv64-legal-i32 < %s | FileCheck %s -check-prefix=RV64XVENTANACONDOPS
; RUN: llc -mtriple=riscv64 -target-abi=lp64f -mattr=+f,+zbs,+xtheadcondmov \
; RUN:   -riscv-experimental-rv64-legal-i32 < %s | FileCheck %s -check-prefix=RV64XTHEADCONDMOV
; RUN: llc -mtriple=riscv64 -target-abi=lp64f -mattr=+f,+zbs,+zicond \
; RUN:   -riscv-experimental-rv64-legal-i32 < %s | FileCheck %s -check-prefix=RV64ZICOND

define i64 @zero1(i64 %rs1, i1 zeroext %rc) {
; RV64I-LABEL: zero1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a1, a1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1:
; RV64XVENTANACONDOPS:       #
 %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a0, zero, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2(i64 %rs1, i1 zeroext %rc) {
; RV64I-LABEL: zero2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a1, a1, -1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
```

```
; RV64XTHEADCONDMOV-LABEL: zero2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a0, zero, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:
   czero.nez a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero_singlebit1(i64 %rs1, i64 %rs2) {
; RV64I-LABEL: zero_singlebit1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bexti a1, a1, 12
; RV64I-NEXT:    addi a1, a1, -1
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero_singlebit1:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    bexti a1, a1, 12
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero_singlebit1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    lui a2, 1
; RV64XTHEADCONDMOV-NEXT:    and a1, a1, a2
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a0, zero, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero_singlebit1:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    bexti a1, a1, 12
; RV64ZICOND-NEXT:    czero.nez a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %and = and
 i64 %rs2, 4096
 %rc = icmp eq i64 %and, 0
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero_singlebit2(i64 %rs1, i64 %rs2) {
```

```
; RV64I-LABEL: zero_singlebit2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    slli a1, a1, 51
; RV64I-NEXT:    srai a1, a1, 63
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero_singlebit2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    bexti a1, a1, 12
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero_singlebit2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    slli a1, a1, 51
; RV64XTHEADCONDMOV-NEXT:    srai a1, a1, 63
; RV64XTHEADCONDMOV-NEXT:    and a0, a1, a0
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero_singlebit2:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    bexti a1, a1, 12
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %and = and i64 %rs2, 4096
  %rc = icmp eq i64 %and, 0
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @add1(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: add1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: add1:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    add a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: add1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, zero, a0
```

```
; RV64XTHEADCONDMOV-NEXT:    add a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: add1:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    add a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %add = add i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %add, i64 %rs1
 ret i64 %sel
}

define i64 @add2(i1 zeroext %rc, i64 %rs1,
 i64 %rs2) {
; RV64I-LABEL: add2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: add2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    add a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: add2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    add a0, a2, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: add2:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    add a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %add = add i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %add, i64 %rs2
 ret i64 %sel
}

define i64 @add3(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: add3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
```

```
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:
   add a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: add3:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    add a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: add3:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    add a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: add3:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    add a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %add = add i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs1, i64 %add
 ret i64 %sel
}

define i64 @add4(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: add4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    add a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: add4:
; RV64XVENTANACONDOPS:       # %bb.0:
;
 RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    add a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: add4:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    add a0, a2, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
```

```
; RV64ZICOND-LABEL: add4:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    add a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %add = add i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs2, i64 %add
 ret i64 %sel
}

define i64 @sub1(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: sub1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    sub a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: sub1:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    sub a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
;
 RV64XTHEADCONDMOV-LABEL: sub1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    sub a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: sub1:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    sub a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %sub = sub i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %sub, i64 %rs1
 ret i64 %sel
}

define i64 @sub2(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: sub2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    sub a0, a1, a0
; RV64I-NEXT:    ret
```

```
;
; RV64XVENTANACONDOPS-LABEL: sub2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    sub a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: sub2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, zero, a0
;
 RV64XTHEADCONDMOV-NEXT:    sub a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: sub2:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    sub a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %sub = sub i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs1, i64 %sub
 ret i64 %sel
}

define i64 @or1(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: or1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: or1:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: or1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    or a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: or1:
; RV64ZICOND:       # %bb.0:
;
```

```
 RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %or = or i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %or, i64 %rs1
 ret i64 %sel
}

define i64 @or2(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: or2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: or2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: or2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    or a0, a2, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: or2:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %or = or i64 %rs1, %rs2
 %sel = select
 i1 %rc, i64 %or, i64 %rs2
 ret i64 %sel
}

define i64 @or3(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: or3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    or a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: or3:
```

```
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: or3:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    or a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: or3:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %or = or i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs1, i64 %or
 ret i64 %sel
}

define i64 @or4(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: or4:
; RV64I:
   # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    or a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: or4:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: or4:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    or a0, a2, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: or4:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %or = or i64 %rs1, %rs2
```

```
 %sel = select i1 %rc, i64 %rs2, i64 %or
 ret i64 %sel
}

define i64 @xor1(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: xor1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL:
 xor1:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    xor a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: xor1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    xor a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: xor1:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    xor a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %xor = xor i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %xor, i64 %rs1
 ret i64 %sel
}

define i64 @xor2(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: xor2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    neg a0, a0
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    xor a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: xor2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:
```

```
    xor a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: xor2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    xor a0, a2, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: xor2:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    xor a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %xor = xor i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %xor, i64 %rs2
 ret i64 %sel
}

define i64 @xor3(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: xor3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    xor a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: xor3:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    xor a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: xor3:
; RV64XTHEADCONDMOV:
      # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    xor a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: xor3:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    xor a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %xor = xor i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs1, i64 %xor
 ret i64 %sel
```

```
}

define i64 @xor4(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: xor4:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    xor a0, a2, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: xor4:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    xor a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: xor4:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    xor a0, a2, a1
; RV64XTHEADCONDMOV-NEXT:
   ret
;
; RV64ZICOND-LABEL: xor4:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    xor a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %xor = xor i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs2, i64 %xor
 ret i64 %sel
}

define i64 @and1(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: and1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB18_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:  .LBB18_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: and1:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    and a2, a1, a2
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a2, a0
```

```
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: and1:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    and a2, a1, a2
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a1, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:
   ret
;
; RV64ZICOND-LABEL: and1:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    and a2, a1, a2
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %and = and i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %and, i64 %rs1
 ret i64 %sel
}

define i64 @and2(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: and2:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB19_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    and a2, a1, a2
; RV64I-NEXT:  .LBB19_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: and2:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    and a1, a1, a2
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: and2:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    and a1, a1, a2
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:
   mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: and2:
; RV64ZICOND:       # %bb.0:
```

```
; RV64ZICOND-NEXT:    and a1, a1, a2
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %and = and i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %and, i64 %rs2
 ret i64 %sel
}

define i64 @and3(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: and3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bnez a0, .LBB20_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    and a1, a1, a2
; RV64I-NEXT:  .LBB20_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: and3:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    and a2, a1, a2
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: and3:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    and a2, a1, a2
; RV64XTHEADCONDMOV-NEXT:
   th.mvnez a2, a1, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: and3:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    and a2, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %and = and i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs1, i64 %and
 ret i64 %sel
}

define i64 @and4(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: and4:
; RV64I:       # %bb.0:
```

```
; RV64I-NEXT:    bnez a0, .LBB21_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    and a2, a1, a2
; RV64I-NEXT:  .LBB21_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: and4:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    and a1, a1, a2
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: and4:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:
   and a1, a1, a2
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: and4:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    and a1, a1, a2
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %and = and i64 %rs1, %rs2
 %sel = select i1 %rc, i64 %rs2, i64 %and
 ret i64 %sel
}

define i64 @basic(i1 zeroext %rc, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: basic:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bnez a0, .LBB22_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB22_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: basic:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
```

```
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: basic:
; RV64XTHEADCONDMOV:
      # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: basic:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a2, a2, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @seteq(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: seteq:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beq a0, a1, .LBB23_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB23_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: seteq:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xor a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL:
 seteq:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xor a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: seteq:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xor a0, a0, a1
```

```
; RV64ZICOND-NEXT:    czero.eqz a1, a3, a0
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setne(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setne:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bne a0, a1, .LBB24_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB24_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setne:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xor a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:
   vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setne:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xor a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setne:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xor a0, a0, a1
; RV64ZICOND-NEXT:    czero.nez a1, a3, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setgt(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
```

```
; RV64I-LABEL: setgt:
; RV64I:       # %bb.0:
; RV64I-NEXT:    blt a1, a0, .LBB25_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB25_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setgt:
; RV64XVENTANACONDOPS:
      # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    slt a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setgt:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    slt a0, a1, a0
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setgt:
; RV64ZICOND:      # %bb.0:
; RV64ZICOND-NEXT:    slt a0, a1, a0
; RV64ZICOND-NEXT:    czero.nez a1, a3, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp sgt i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setge(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setge:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bge a0, a1, .LBB26_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:
   mv a2, a3
; RV64I-NEXT:  .LBB26_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
```

```
; RV64XVENTANACONDOPS-LABEL: setge:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    slt a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setge:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    slt a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setge:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    slt a0, a0, a1
; RV64ZICOND-NEXT:    czero.eqz a1, a3, a0
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp sge i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setlt(i64 %a, i64 %b,
 i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setlt:
; RV64I:       # %bb.0:
; RV64I-NEXT:    blt a0, a1, .LBB27_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB27_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setlt:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    slt a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setlt:
; RV64XTHEADCONDMOV:       # %bb.0:
```

```
; RV64XTHEADCONDMOV-NEXT:    slt a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setlt:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    slt a0, a0, a1
; RV64ZICOND-NEXT:    czero.nez a1, a3, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:
   ret
 %rc = icmp slt i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setle(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setle:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bge a1, a0, .LBB28_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB28_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setle:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    slt a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setle:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    slt a0, a1, a0
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setle:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    slt a0, a1,
 a0
; RV64ZICOND-NEXT:    czero.eqz a1, a3, a0
```

```
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp sle i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setugt(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setugt:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bltu a1, a0, .LBB29_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB29_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setugt:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    sltu a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setugt:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    sltu a0, a1, a0
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:
   mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setugt:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    sltu a0, a1, a0
; RV64ZICOND-NEXT:    czero.nez a1, a3, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp ugt i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setuge(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setuge:
```

```
; RV64I:      # %bb.0:
; RV64I-NEXT:    bgeu a0, a1, .LBB30_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB30_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setuge:
; RV64XVENTANACONDOPS:      # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    sltu a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setuge:
;
 RV64XTHEADCONDMOV:      # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    sltu a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setuge:
; RV64ZICOND:      # %bb.0:
; RV64ZICOND-NEXT:    sltu a0, a0, a1
; RV64ZICOND-NEXT:    czero.eqz a1, a3, a0
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp uge i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setult(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setult:
; RV64I:      # %bb.0:
; RV64I-NEXT:    bltu a0, a1, .LBB31_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB31_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setult:
; RV64XVENTANACONDOPS:      # %bb.0:
```

```
; RV64XVENTANACONDOPS-NEXT:    sltu a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:
   vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setult:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    sltu a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setult:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    sltu a0, a0, a1
; RV64ZICOND-NEXT:    czero.nez a1, a3, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp ult i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setule(i64 %a, i64 %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setule:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bgeu a1, a0, .LBB32_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a2, a3
; RV64I-NEXT:  .LBB32_2:
; RV64I-NEXT:    mv a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setule:
; RV64XVENTANACONDOPS:
      # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    sltu a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a3, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setule:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    sltu a0, a1, a0
```

```
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setule:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    sltu a0, a1, a0
; RV64ZICOND-NEXT:    czero.eqz a1, a3, a0
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = icmp ule i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @seteq_zero(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: seteq_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    beqz a0, .LBB33_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:
   mv a1, a2
; RV64I-NEXT:  .LBB33_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: seteq_zero:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: seteq_zero:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: seteq_zero:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, 0
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
```

```
 ret i64 %sel
}

define i64 @setne_zero(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setne_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bnez a0, .LBB34_2
; RV64I-NEXT:
  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB34_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setne_zero:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setne_zero:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setne_zero:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a2, a2, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, 0
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @seteq_constant(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: seteq_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:
    li a3, 123
; RV64I-NEXT:    beq a0, a3, .LBB35_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB35_2:
; RV64I-NEXT:    mv a0, a1
```

```
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: seteq_constant:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, -123
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: seteq_constant:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi a0, a0, -123
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: seteq_constant:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, -123
; RV64ZICOND-NEXT:    czero.eqz a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
  %rc = icmp eq i64 %a, 123
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @setne_constant(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setne_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a3, 456
; RV64I-NEXT:    bne a0, a3, .LBB36_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB36_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setne_constant:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, -456
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
```

```
; RV64XTHEADCONDMOV-LABEL: setne_constant:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi a0, a0, -456
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setne_constant:
; RV64ZICOND:
      # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, -456
; RV64ZICOND-NEXT:    czero.nez a2, a2, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, 456
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @seteq_2048(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: seteq_2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    bseti a3, zero, 11
; RV64I-NEXT:    beq a0, a3, .LBB37_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB37_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: seteq_2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, -2048
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: seteq_2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:
   addi a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: seteq_2048:
```

```
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, -2048
; RV64ZICOND-NEXT:    czero.eqz a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, 2048
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @seteq_neg2048(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: seteq_neg2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a3, -2048
; RV64I-NEXT:    beq a0, a3, .LBB38_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB38_2:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: seteq_neg2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xori a0, a0, -2048
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:
   vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: seteq_neg2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xori a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: seteq_neg2048:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xori a0, a0, -2048
; RV64ZICOND-NEXT:    czero.eqz a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, -2048
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
```

```
}

define i64 @setne_neg2048(i64 %a, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setne_neg2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    li a3, -2048
; RV64I-NEXT:    bne a0, a3, .LBB39_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a1, a2
; RV64I-NEXT:  .LBB39_2:
; RV64I-NEXT:    mv a0, a1
;
 RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setne_neg2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xori a0, a0, -2048
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setne_neg2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xori a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, a2, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setne_neg2048:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xori a0, a0, -2048
; RV64ZICOND-NEXT:    czero.nez a2, a2, a0
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    or a0, a0, a2
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, -2048
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

define i64 @zero1_seteq(i64 %a, i64 %b, i64 %rs1) {
; RV64I-LABEL:
 zero1_seteq:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
```

```
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_seteq:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xor a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_seteq:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xor a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_seteq:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xor a0, a0, a1
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_seteq(i64 %a, i64 %b, i64 %rs1) {
; RV64I-LABEL: zero2_seteq:
; RV64I:       # %bb.0:
;
 RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_seteq:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xor a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_seteq:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xor a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
```

```
;
; RV64ZICOND-LABEL: zero2_seteq:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xor a0, a0, a1
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, %b
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero1_setne(i64 %a, i64 %b, i64 %rs1) {
; RV64I-LABEL: zero1_setne:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xor a0, a0, a1
;
 RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_setne:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xor a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_setne:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xor a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_setne:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xor a0, a0, a1
; RV64ZICOND-NEXT:    czero.eqz a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_setne(i64 %a, i64 %b, i64 %rs1) {
; RV64I-LABEL: zero2_setne:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xor a0, a0, a1
```

```
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:
   addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a2
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_setne:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xor a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a2, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_setne:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xor a0, a0, a1
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a2, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_setne:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xor a0, a0, a1
; RV64ZICOND-NEXT:    czero.nez a0, a2, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, %b
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero1_seteq_zero(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero1_seteq_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:
   ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_seteq_zero:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_seteq_zero:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
```

```
;
; RV64ZICOND-LABEL: zero1_seteq_zero:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, 0
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_seteq_zero(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero2_seteq_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_seteq_zero:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:
   ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_seteq_zero:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_seteq_zero:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, 0
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero1_setne_zero(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero1_setne_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_setne_zero:
```

```
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_setne_zero:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv
 a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_setne_zero:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, 0
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_setne_zero(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero2_setne_zero:
; RV64I:       # %bb.0:
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_setne_zero:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_setne_zero:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_setne_zero:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    ret
  %rc = icmp ne i64 %a, 0
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}
```

```
define i64 @zero1_seteq_constant(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero1_seteq_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 231
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_seteq_constant:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, 231
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_seteq_constant:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi a0, a0, 231
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_seteq_constant:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, 231
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:
   ret
 %rc = icmp eq i64 %a, -231
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_seteq_constant(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero2_seteq_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -546
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_seteq_constant:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, -546
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
```

```
; RV64XTHEADCONDMOV-LABEL: zero2_seteq_constant:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi a0, a0, -546
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_seteq_constant:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, -546
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
;
 RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, 546
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero1_setne_constant(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero1_setne_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, -321
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_setne_constant:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, -321
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_setne_constant:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi a0, a0, -321
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_setne_constant:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, -321
; RV64ZICOND-NEXT:    czero.eqz
 a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, 321
 %sel = select i1 %rc, i64 %rs1, i64 0
```

```
 ret i64 %sel
}

define i64 @zero2_setne_constant(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero2_setne_constant:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 654
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_setne_constant:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi a0, a0, 654
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_setne_constant:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi a0, a0, 654
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_setne_constant:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    addi a0, a0, 654
; RV64ZICOND-NEXT:
   czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, -654
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero1_seteq_neg2048(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero1_seteq_neg2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xori a0, a0, -2048
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_seteq_neg2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xori a0, a0, -2048
```

```
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_seteq_neg2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xori a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_seteq_neg2048:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xori a0, a0, -2048
;
 RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, -2048
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_seteq_neg2048(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero2_seteq_neg2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xori a0, a0, -2048
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero2_seteq_neg2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xori a0, a0, -2048
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_seteq_neg2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xori a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_seteq_neg2048:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    xori
 a0, a0, -2048
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
```

```
; RV64ZICOND-NEXT:    ret
 %rc = icmp eq i64 %a, -2048
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define i64 @zero1_setne_neg2048(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero1_setne_neg2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xori a0, a0, -2048
; RV64I-NEXT:    seqz a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: zero1_setne_neg2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xori a0, a0, -2048
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero1_setne_neg2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xori a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero1_setne_neg2048:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:
    xori a0, a0, -2048
; RV64ZICOND-NEXT:    czero.eqz a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, -2048
 %sel = select i1 %rc, i64 %rs1, i64 0
 ret i64 %sel
}

define i64 @zero2_setne_neg2048(i64 %a, i64 %rs1) {
; RV64I-LABEL: zero2_setne_neg2048:
; RV64I:       # %bb.0:
; RV64I-NEXT:    xori a0, a0, -2048
; RV64I-NEXT:    snez a0, a0
; RV64I-NEXT:    addi a0, a0, -1
; RV64I-NEXT:    and a0, a0, a1
; RV64I-NEXT:    ret
;
```

```
; RV64XVENTANACONDOPS-LABEL: zero2_setne_neg2048:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    xori a0, a0, -2048
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a1, a0
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: zero2_setne_neg2048:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    xori a0, a0, -2048
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a1, zero, a0
; RV64XTHEADCONDMOV-NEXT:    mv a0, a1
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: zero2_setne_neg2048:
; RV64ZICOND:
 # %bb.0:
; RV64ZICOND-NEXT:    xori a0, a0, -2048
; RV64ZICOND-NEXT:    czero.nez a0, a1, a0
; RV64ZICOND-NEXT:    ret
 %rc = icmp ne i64 %a, -2048
 %sel = select i1 %rc, i64 0, i64 %rs1
 ret i64 %sel
}

define void @sextw_removal_maskc(i1 %c, i32 signext %arg, i32 signext %arg1) nounwind {
; RV64I-LABEL: sextw_removal_maskc:
; RV64I:       # %bb.0: # %bb
; RV64I-NEXT:    addi sp, sp, -32
; RV64I-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a2
; RV64I-NEXT:    slli a0, a0, 63
; RV64I-NEXT:    srai a0, a0, 63
; RV64I-NEXT:    and s1, a0, a1
; RV64I-NEXT:  .LBB56_1: # %bb2
; RV64I-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64I-NEXT:    mv a0, s1
; RV64I-NEXT:    call bar
; RV64I-NEXT:    sllw s1, s1, s0
; RV64I-NEXT:    bnez a0, .LBB56_1
; RV64I-NEXT:  # %bb.2: # %bb7
; RV64I-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 16(sp)
 # 8-byte Folded Reload
; RV64I-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 32
; RV64I-NEXT:    ret
```

```
;
; RV64XVENTANACONDOPS-LABEL: sextw_removal_maskc:
; RV64XVENTANACONDOPS:       # %bb.0: # %bb
; RV64XVENTANACONDOPS-NEXT:    addi sp, sp, -32
; RV64XVENTANACONDOPS-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    mv s0, a2
; RV64XVENTANACONDOPS-NEXT:    andi a0, a0, 1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc s1, a1, a0
; RV64XVENTANACONDOPS-NEXT:  .LBB56_1: # %bb2
; RV64XVENTANACONDOPS-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64XVENTANACONDOPS-NEXT:    mv a0, s1
; RV64XVENTANACONDOPS-NEXT:    call bar
; RV64XVENTANACONDOPS-NEXT:    sllw s1, s1, s0
; RV64XVENTANACONDOPS-NEXT:    bnez a0, .LBB56_1
; RV64XVENTANACONDOPS-NEXT:  # %bb.2: # %bb7
; RV64XVENTANACONDOPS-NEXT:    ld ra, 24(sp) # 8-byte
 Folded Reload
; RV64XVENTANACONDOPS-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    addi sp, sp, 32
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: sextw_removal_maskc:
; RV64XTHEADCONDMOV:       # %bb.0: # %bb
; RV64XTHEADCONDMOV-NEXT:    addi sp, sp, -32
; RV64XTHEADCONDMOV-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    mv s0, a2
; RV64XTHEADCONDMOV-NEXT:    mv s1, a1
; RV64XTHEADCONDMOV-NEXT:    andi a0, a0, 1
; RV64XTHEADCONDMOV-NEXT:    th.mveqz s1, zero, a0
; RV64XTHEADCONDMOV-NEXT:  .LBB56_1: # %bb2
; RV64XTHEADCONDMOV-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64XTHEADCONDMOV-NEXT:    sext.w a0, s1
; RV64XTHEADCONDMOV-NEXT:    call bar
; RV64XTHEADCONDMOV-NEXT:    sllw s1, s1, s0
; RV64XTHEADCONDMOV-NEXT:
  bnez a0, .LBB56_1
; RV64XTHEADCONDMOV-NEXT:  # %bb.2: # %bb7
; RV64XTHEADCONDMOV-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    addi sp, sp, 32
; RV64XTHEADCONDMOV-NEXT:    ret
;
```

```
; RV64ZICOND-LABEL: sextw_removal_maskc:
; RV64ZICOND:       # %bb.0: # %bb
; RV64ZICOND-NEXT:    addi sp, sp, -32
; RV64ZICOND-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    mv s0, a2
; RV64ZICOND-NEXT:    andi a0, a0, 1
; RV64ZICOND-NEXT:    czero.eqz s1, a1, a0
; RV64ZICOND-NEXT:  .LBB56_1: # %bb2
; RV64ZICOND-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64ZICOND-NEXT:    mv a0, s1
; RV64ZICOND-NEXT:    call bar
; RV64ZICOND-NEXT:    sllw s1, s1, s0
; RV64ZICOND-NEXT:    bnez a0, .LBB56_1
; RV64ZICOND-NEXT:  # %bb.2:
 # %bb7
; RV64ZICOND-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    addi sp, sp, 32
; RV64ZICOND-NEXT:    ret
bb:
 %i = select i1 %c, i32 %arg, i32 0
 br label %bb2

bb2:                                              ; preds = %bb2, %bb
 %i3 = phi i32 [ %i, %bb ], [ %i5, %bb2 ]
 %i4 = tail call signext i32 @bar(i32 signext %i3)
 %i5 = shl i32 %i3, %arg1
 %i6 = icmp eq i32 %i4, 0
 br i1 %i6, label %bb7, label %bb2

bb7:                                              ; preds = %bb2
 ret void
}
declare signext i32 @bar(i32 signext)

define void @sextw_removal_maskcn(i1 %c, i32 signext %arg, i32 signext %arg1) nounwind {
; RV64I-LABEL: sextw_removal_maskcn:
; RV64I:       # %bb.0: # %bb
; RV64I-NEXT:    addi sp, sp, -32
; RV64I-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s1,
 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a2
; RV64I-NEXT:    andi a0, a0, 1
```

```
; RV64I-NEXT:    addiw a0, a0, -1
; RV64I-NEXT:    and s1, a0, a1
; RV64I-NEXT:  .LBB57_1: # %bb2
; RV64I-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64I-NEXT:    mv a0, s1
; RV64I-NEXT:    call bar
; RV64I-NEXT:    sllw s1, s1, s0
; RV64I-NEXT:    bnez a0, .LBB57_1
; RV64I-NEXT:  # %bb.2: # %bb7
; RV64I-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 32
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: sextw_removal_maskcn:
; RV64XVENTANACONDOPS:       # %bb.0: # %bb
; RV64XVENTANACONDOPS-NEXT:    addi sp, sp, -32
; RV64XVENTANACONDOPS-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    mv s0, a2
; RV64XVENTANACONDOPS-NEXT:
   andi a0, a0, 1
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn s1, a1, a0
; RV64XVENTANACONDOPS-NEXT:  .LBB57_1: # %bb2
; RV64XVENTANACONDOPS-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64XVENTANACONDOPS-NEXT:    mv a0, s1
; RV64XVENTANACONDOPS-NEXT:    call bar
; RV64XVENTANACONDOPS-NEXT:    sllw s1, s1, s0
; RV64XVENTANACONDOPS-NEXT:    bnez a0, .LBB57_1
; RV64XVENTANACONDOPS-NEXT:  # %bb.2: # %bb7
; RV64XVENTANACONDOPS-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    addi sp, sp, 32
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: sextw_removal_maskcn:
; RV64XTHEADCONDMOV:       # %bb.0: # %bb
; RV64XTHEADCONDMOV-NEXT:    addi sp, sp, -32
; RV64XTHEADCONDMOV-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    sd s0, 16(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:
   sd s1, 8(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    mv s0, a2
; RV64XTHEADCONDMOV-NEXT:    mv s1, a1
; RV64XTHEADCONDMOV-NEXT:    andi a0, a0, 1
```

```
; RV64XTHEADCONDMOV-NEXT:    th.mvnez s1, zero, a0
; RV64XTHEADCONDMOV-NEXT:  .LBB57_1: # %bb2
; RV64XTHEADCONDMOV-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64XTHEADCONDMOV-NEXT:    sext.w a0, s1
; RV64XTHEADCONDMOV-NEXT:    call bar
; RV64XTHEADCONDMOV-NEXT:    sllw s1, s1, s0
; RV64XTHEADCONDMOV-NEXT:    bnez a0, .LBB57_1
; RV64XTHEADCONDMOV-NEXT:  # %bb.2: # %bb7
; RV64XTHEADCONDMOV-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    addi sp, sp, 32
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: sextw_removal_maskcn:
; RV64ZICOND:       # %bb.0: # %bb
; RV64ZICOND-NEXT:    addi sp, sp, -32
; RV64ZICOND-NEXT:    sd ra, 24(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:
   sd s0, 16(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    sd s1, 8(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    mv s0, a2
; RV64ZICOND-NEXT:    andi a0, a0, 1
; RV64ZICOND-NEXT:    czero.nez s1, a1, a0
; RV64ZICOND-NEXT:  .LBB57_1: # %bb2
; RV64ZICOND-NEXT:    # =>This Inner Loop Header: Depth=1
; RV64ZICOND-NEXT:    mv a0, s1
; RV64ZICOND-NEXT:    call bar
; RV64ZICOND-NEXT:    sllw s1, s1, s0
; RV64ZICOND-NEXT:    bnez a0, .LBB57_1
; RV64ZICOND-NEXT:  # %bb.2: # %bb7
; RV64ZICOND-NEXT:    ld ra, 24(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    ld s0, 16(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    ld s1, 8(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    addi sp, sp, 32
; RV64ZICOND-NEXT:    ret
bb:
 %i = select i1 %c, i32 0, i32 %arg
 br label %bb2

bb2:                                              ; preds = %bb2, %bb
 %i3 = phi i32 [ %i, %bb ], [ %i5, %bb2 ]
 %i4 = tail call signext i32 @bar(i32 signext %i3)
 %i5 = shl i32 %i3, %arg1
 %i6 = icmp eq i32
 %i4, 0
 br i1 %i6, label %bb7, label %bb2
```

```
bb7:                                              ; preds = %bb2
 ret void
}

define i32 @setune_32(float %a, float %b, i32 %rs1, i32 %rs2) {
; RV64I-LABEL: setune_32:
; RV64I:       # %bb.0:
; RV64I-NEXT:    feq.s a2, fa0, fa1
; RV64I-NEXT:    beqz a2, .LBB58_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:  .LBB58_2:
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setune_32:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    feq.s a2, fa0, fa1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a1, a2
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setune_32:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    feq.s a2, fa0, fa1
; RV64XTHEADCONDMOV-NEXT:    th.mvnez a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setune_32:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:
   feq.s a2, fa0, fa1
; RV64ZICOND-NEXT:    czero.eqz a1, a1, a2
; RV64ZICOND-NEXT:    czero.nez a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = fcmp une float %a, %b
 %sel = select i1 %rc, i32 %rs1, i32 %rs2
 ret i32 %sel
}

define i64 @setune_64(float %a, float %b, i64 %rs1, i64 %rs2) {
; RV64I-LABEL: setune_64:
; RV64I:       # %bb.0:
; RV64I-NEXT:    feq.s a2, fa0, fa1
; RV64I-NEXT:    beqz a2, .LBB59_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv a0, a1
```

```
; RV64I-NEXT:  .LBB59_2:
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: setune_64:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    feq.s a2, fa0, fa1
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a1, a1, a2
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn a0, a0, a2
; RV64XVENTANACONDOPS-NEXT:    or a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: setune_64:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    feq.s a2, fa0, fa1
; RV64XTHEADCONDMOV-NEXT:
   th.mvnez a0, a1, a2
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: setune_64:
; RV64ZICOND:       # %bb.0:
; RV64ZICOND-NEXT:    feq.s a2, fa0, fa1
; RV64ZICOND-NEXT:    czero.eqz a1, a1, a2
; RV64ZICOND-NEXT:    czero.nez a0, a0, a2
; RV64ZICOND-NEXT:    or a0, a0, a1
; RV64ZICOND-NEXT:    ret
 %rc = fcmp une float %a, %b
 %sel = select i1 %rc, i64 %rs1, i64 %rs2
 ret i64 %sel
}

; Test that we can ComputeNumSignBits across basic blocks when the live out is
; RISCVISD::SELECT_CC. There should be no slli+srai or sext.h in the output.
define signext i16 @numsignbits(i16 signext %0, i16 signext %1, i16 signext %2, i16 signext %3) nounwind {
; RV64I-LABEL: numsignbits:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64I-NEXT:    mv s0, a3
; RV64I-NEXT:    beqz a0, .LBB60_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    mv s0, a2
; RV64I-NEXT:  .LBB60_2:
;
 RV64I-NEXT:    beqz a1, .LBB60_4
; RV64I-NEXT:  # %bb.3:
; RV64I-NEXT:    mv a0, s0
; RV64I-NEXT:    call bat
```

```
; RV64I-NEXT:  .LBB60_4:
; RV64I-NEXT:    mv a0, s0
; RV64I-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: numsignbits:
; RV64XVENTANACONDOPS:       # %bb.0:
; RV64XVENTANACONDOPS-NEXT:    addi sp, sp, -16
; RV64XVENTANACONDOPS-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a2, a2, a0
; RV64XVENTANACONDOPS-NEXT:    vt.maskcn s0, a3, a0
; RV64XVENTANACONDOPS-NEXT:    or s0, s0, a2
; RV64XVENTANACONDOPS-NEXT:    beqz a1, .LBB60_2
; RV64XVENTANACONDOPS-NEXT:  # %bb.1:
; RV64XVENTANACONDOPS-NEXT:    mv a0, s0
; RV64XVENTANACONDOPS-NEXT:    call bat
; RV64XVENTANACONDOPS-NEXT:  .LBB60_2:
; RV64XVENTANACONDOPS-NEXT:    mv a0, s0
; RV64XVENTANACONDOPS-NEXT:
   ld ra, 8(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64XVENTANACONDOPS-NEXT:    addi sp, sp, 16
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: numsignbits:
; RV64XTHEADCONDMOV:       # %bb.0:
; RV64XTHEADCONDMOV-NEXT:    addi sp, sp, -16
; RV64XTHEADCONDMOV-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64XTHEADCONDMOV-NEXT:    th.mveqz a2, a3, a0
; RV64XTHEADCONDMOV-NEXT:    sext.w s0, a2
; RV64XTHEADCONDMOV-NEXT:    beqz a1, .LBB60_2
; RV64XTHEADCONDMOV-NEXT:  # %bb.1:
; RV64XTHEADCONDMOV-NEXT:    mv a0, s0
; RV64XTHEADCONDMOV-NEXT:    call bat
; RV64XTHEADCONDMOV-NEXT:  .LBB60_2:
; RV64XTHEADCONDMOV-NEXT:    mv a0, s0
; RV64XTHEADCONDMOV-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64XTHEADCONDMOV-NEXT:    addi sp, sp, 16
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL:
 numsignbits:
; RV64ZICOND:       # %bb.0:
```

```
; RV64ZICOND-NEXT:    addi sp, sp, -16
; RV64ZICOND-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    sd s0, 0(sp) # 8-byte Folded Spill
; RV64ZICOND-NEXT:    czero.eqz a2, a2, a0
; RV64ZICOND-NEXT:    czero.nez s0, a3, a0
; RV64ZICOND-NEXT:    or s0, s0, a2
; RV64ZICOND-NEXT:    beqz a1, .LBB60_2
; RV64ZICOND-NEXT:  # %bb.1:
; RV64ZICOND-NEXT:    mv a0, s0
; RV64ZICOND-NEXT:    call bat
; RV64ZICOND-NEXT:  .LBB60_2:
; RV64ZICOND-NEXT:    mv a0, s0
; RV64ZICOND-NEXT:    ld ra, 8(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    ld s0, 0(sp) # 8-byte Folded Reload
; RV64ZICOND-NEXT:    addi sp, sp, 16
; RV64ZICOND-NEXT:    ret
 %5 = icmp eq i16 %0, 0
 %6 = select i1 %5, i16 %3, i16 %2
 %7 = icmp eq i16 %1, 0
 br i1 %7, label %9, label %8

8:                                                ; preds = %4
 tail call void @bat(i16 signext %6)
 br label %9

9:                                                ; preds = %8, %4
  ret i16 %6
}

declare void @bat(i16 signext)

define i64 @single_bit(i64 %x) {
; RV64I-LABEL: single_bit:
; RV64I:       # %bb.0: # %entry
; RV64I-NEXT:    slli a1, a0, 53
; RV64I-NEXT:    srai a1, a1, 63
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: single_bit:
; RV64XVENTANACONDOPS:       # %bb.0: # %entry
; RV64XVENTANACONDOPS-NEXT:    andi a1, a0, 1024
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: single_bit:
; RV64XTHEADCONDMOV:       # %bb.0: # %entry
; RV64XTHEADCONDMOV-NEXT:    slli a1, a0, 53
```

```
; RV64XTHEADCONDMOV-NEXT:    srai a1, a1, 63
; RV64XTHEADCONDMOV-NEXT:    and a0, a1, a0
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: single_bit:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    andi a1, a0, 1024
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
 %and = and i64 %x, 1024
 %tobool.not = icmp eq i64 %and, 0
 %cond =
 select i1 %tobool.not, i64 0, i64 %x
 ret i64 %cond
}

; Test to fold select with single bit check to (and (sra (shl x))).
define i64 @single_bit2(i64 %x) {
; RV64I-LABEL: single_bit2:
; RV64I:       # %bb.0: # %entry
; RV64I-NEXT:    slli a1, a0, 52
; RV64I-NEXT:    srai a1, a1, 63
; RV64I-NEXT:    and a0, a1, a0
; RV64I-NEXT:    ret
;
; RV64XVENTANACONDOPS-LABEL: single_bit2:
; RV64XVENTANACONDOPS:       # %bb.0: # %entry
; RV64XVENTANACONDOPS-NEXT:    bexti a1, a0, 11
; RV64XVENTANACONDOPS-NEXT:    vt.maskc a0, a0, a1
; RV64XVENTANACONDOPS-NEXT:    ret
;
; RV64XTHEADCONDMOV-LABEL: single_bit2:
; RV64XTHEADCONDMOV:       # %bb.0: # %entry
; RV64XTHEADCONDMOV-NEXT:    slli a1, a0, 52
; RV64XTHEADCONDMOV-NEXT:    srai a1, a1, 63
; RV64XTHEADCONDMOV-NEXT:    and a0, a1, a0
; RV64XTHEADCONDMOV-NEXT:    ret
;
; RV64ZICOND-LABEL: single_bit2:
; RV64ZICOND:       # %bb.0: # %entry
; RV64ZICOND-NEXT:    bexti a1, a0, 11
; RV64ZICOND-NEXT:    czero.eqz a0, a0, a1
; RV64ZICOND-NEXT:    ret
entry:
  %and = and i64 %x, 2048
 %tobool.not = icmp eq i64 %and, 0
 %cond = select i1 %tobool.not, i64 0, i64 %x
```

```
 ret i64 %cond
}
```

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -verify-machineinstrs < %s \
; RUN: -riscv-experimental-rv64-legal-i32 | FileCheck -check-prefix=RV64I %s

; Check indexed and unindexed, sext, zext and anyext loads

define dso_local i64 @lb(ptr %a) nounwind {
; RV64I-LABEL: lb:
; RV64I: # %bb.0:
; RV64I-NEXT: lb a1, 1(a0)
; RV64I-NEXT: lbu zero, 0(a0)

```
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i32 1
 %2 = load i8, ptr %1
 %3 = sext i8 %2 to i64
 ; the unused load will produce an anyext for selection
 %4 = load volatile i8, ptr %a
 ret i64 %3
}

define dso_local i64 @lh(ptr %a) nounwind {
; RV64I-LABEL: lh:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lh a1, 4(a0)
; RV64I-NEXT:    lh zero, 0(a0)
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i32 2
 %2 = load i16, ptr %1
 %3 = sext i16 %2 to i64
 ; the unused load will produce an anyext for selection
  %4 = load volatile i16, ptr %a
 ret i64 %3
}

define dso_local i64 @lw(ptr %a) nounwind {
; RV64I-LABEL: lw:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lw a1, 12(a0)
; RV64I-NEXT:    lw zero, 0(a0)
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i32 3
 %2 = load i32, ptr %1
 %3 = sext i32 %2 to i64
 ; the unused load will produce an anyext for selection
 %4 = load volatile i32, ptr %a
 ret i64 %3
}

define dso_local i64 @lbu(ptr %a) nounwind {
; RV64I-LABEL: lbu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lbu a1, 4(a0)
; RV64I-NEXT:    lbu a0, 0(a0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ret
 %1 = getelementptr i8, ptr %a, i32 4
```

```
 %2 = load i8, ptr %1
 %3 = zext i8 %2 to i64
 %4 = load volatile i8, ptr %a
 %5 = zext i8 %4 to i64
 %6 = add i64 %3, %5
 ret i64 %6
}

define dso_local i64 @lhu(ptr %a) nounwind {
; RV64I-LABEL: lhu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lhu a1, 10(a0)
; RV64I-NEXT:    lhu a0, 0(a0)
; RV64I-NEXT:
   add a0, a1, a0
; RV64I-NEXT:    ret
 %1 = getelementptr i16, ptr %a, i32 5
 %2 = load i16, ptr %1
 %3 = zext i16 %2 to i64
 %4 = load volatile i16, ptr %a
 %5 = zext i16 %4 to i64
 %6 = add i64 %3, %5
 ret i64 %6
}

define dso_local i64 @lwu(ptr %a) nounwind {
; RV64I-LABEL: lwu:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lwu a1, 24(a0)
; RV64I-NEXT:    lwu a0, 0(a0)
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    ret
 %1 = getelementptr i32, ptr %a, i32 6
 %2 = load i32, ptr %1
 %3 = zext i32 %2 to i64
 %4 = load volatile i32, ptr %a
 %5 = zext i32 %4 to i64
 %6 = add i64 %3, %5
 ret i64 %6
}

; 64-bit loads and stores

define dso_local i64 @ld(ptr %a) nounwind {
; RV64I-LABEL: ld:
; RV64I:       # %bb.0:
; RV64I-NEXT:    ld a1, 80(a0)
; RV64I-NEXT:    ld zero, 0(a0)
```

```
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
 %1 = getelementptr i64, ptr %a, i32 10
 %2 = load i64, ptr %1
 %3 = load volatile i64, ptr %a
 ret i64 %2
}

define dso_local void @sd(ptr %a, i64 %b)
 nounwind {
; RV64I-LABEL: sd:
; RV64I:       # %bb.0:
; RV64I-NEXT:    sd a1, 0(a0)
; RV64I-NEXT:    sd a1, 88(a0)
; RV64I-NEXT:    ret
 store i64 %b, ptr %a
 %1 = getelementptr i64, ptr %a, i32 11
 store i64 %b, ptr %1
 ret void
}

; Check load and store to an i1 location
define dso_local i64 @load_sext_zext_anyext_i1(ptr %a) nounwind {
; RV64I-LABEL: load_sext_zext_anyext_i1:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lbu a1, 1(a0)
; RV64I-NEXT:    lbu a2, 2(a0)
; RV64I-NEXT:    lbu zero, 0(a0)
; RV64I-NEXT:    sub a0, a2, a1
; RV64I-NEXT:    ret
 ; sextload i1
 %1 = getelementptr i1, ptr %a, i32 1
 %2 = load i1, ptr %1
 %3 = sext i1 %2 to i64
 ; zextload i1
 %4 = getelementptr i1, ptr %a, i32 2
 %5 = load i1, ptr %4
 %6 = zext i1 %5 to i64
 %7 = add i64 %3, %6
 ; extload i1 (anyext). Produced as the load is unused.
 %8 = load volatile i1, ptr %a
 ret i64 %7
}

define dso_local i16 @load_sext_zext_anyext_i1_i16(ptr %a) nounwind {
; RV64I-LABEL: load_sext_zext_anyext_i1_i16:
;
```

```
 RV64I:       # %bb.0:
; RV64I-NEXT:    lbu a1, 1(a0)
; RV64I-NEXT:    lbu a2, 2(a0)
; RV64I-NEXT:    lbu zero, 0(a0)
; RV64I-NEXT:    subw a0, a2, a1
; RV64I-NEXT:    ret
 ; sextload i1
 %1 = getelementptr i1, ptr %a, i32 1
 %2 = load i1, ptr %1
 %3 = sext i1 %2 to i16
 ; zextload i1
 %4 = getelementptr i1, ptr %a, i32 2
 %5 = load i1, ptr %4
 %6 = zext i1 %5 to i16
 %7 = add i16 %3, %6
 ; extload i1 (anyext). Produced as the load is unused.
 %8 = load volatile i1, ptr %a
 ret i16 %7
}

; Check load and store to a global
@G = dso_local global i64 0

define dso_local i64 @ld_sd_global(i64 %a) nounwind {
; RV64I-LABEL: ld_sd_global:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a2, %hi(G)
; RV64I-NEXT:    ld a1, %lo(G)(a2)
; RV64I-NEXT:    addi a3, a2, %lo(G)
; RV64I-NEXT:    sd a0, %lo(G)(a2)
; RV64I-NEXT:    ld zero, 72(a3)
; RV64I-NEXT:    sd a0, 72(a3)
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    ret
 %1 = load volatile i64, ptr @G
 store i64 %a, ptr @G
 %2 = getelementptr
 i64, ptr @G, i64 9
 %3 = load volatile i64, ptr %2
 store i64 %a, ptr %2
 ret i64 %1
}

define i64 @lw_near_local(ptr %a)  {
; RV64I-LABEL: lw_near_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 2047
; RV64I-NEXT:    ld a0, 9(a0)
```

```
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 257
 %2 = load volatile i64, ptr %1
 ret i64 %2
}

define void @st_near_local(ptr %a, i64 %b)  {
; RV64I-LABEL: st_near_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a0, a0, 2047
; RV64I-NEXT:    sd a1, 9(a0)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 257
 store i64 %b, ptr %1
 ret void
}

define i64 @lw_sw_near_local(ptr %a, i64 %b)  {
; RV64I-LABEL: lw_sw_near_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    addi a2, a0, 2047
; RV64I-NEXT:    ld a0, 9(a2)
; RV64I-NEXT:    sd a1, 9(a2)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 257
 %2 = load volatile i64, ptr %1
 store i64 %b, ptr %1
 ret i64 %2
}

define
 i64 @lw_far_local(ptr %a)  {
; RV64I-LABEL: lw_far_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 8
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, -8(a0)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 4095
 %2 = load volatile i64, ptr %1
 ret i64 %2
}

define void @st_far_local(ptr %a, i64 %b)  {
; RV64I-LABEL: st_far_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a2, 8
; RV64I-NEXT:    add a0, a0, a2
```

```
; RV64I-NEXT:    sd a1, -8(a0)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 4095
 store i64 %b, ptr %1
 ret void
}

define i64 @lw_sw_far_local(ptr %a, i64 %b)  {
; RV64I-LABEL: lw_sw_far_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a2, 8
; RV64I-NEXT:    add a2, a0, a2
; RV64I-NEXT:    ld a0, -8(a2)
; RV64I-NEXT:    sd a1, -8(a2)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 4095
 %2 = load volatile i64, ptr %1
 store i64 %b, ptr %1
 ret i64 %2
}

; Make sure we don't fold the addiw
 into the load offset. The sign extend of the
; addiw is required.
define i64 @lw_really_far_local(ptr %a)  {
; RV64I-LABEL: lw_really_far_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    addiw a1, a1, -2048
; RV64I-NEXT:    add a0, a0, a1
; RV64I-NEXT:    ld a0, 0(a0)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 268435200
 %2 = load volatile i64, ptr %1
 ret i64 %2
}

; Make sure we don't fold the addiw into the store offset. The sign extend of
; the addiw is required.
define void @st_really_far_local(ptr %a, i64 %b)  {
; RV64I-LABEL: st_really_far_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a2, 524288
; RV64I-NEXT:    addiw a2, a2, -2048
; RV64I-NEXT:    add a0, a0, a2
; RV64I-NEXT:    sd a1, 0(a0)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 268435200
```

```
 store i64 %b, ptr %1
 ret void
}

; Make sure we don't fold the addiw into the load/store offset. The sign extend
; of the addiw is required.
define
 i64 @lw_sw_really_far_local(ptr %a, i64 %b)  {
; RV64I-LABEL: lw_sw_really_far_local:
; RV64I:       # %bb.0:
; RV64I-NEXT:    lui a2, 524288
; RV64I-NEXT:    addiw a2, a2, -2048
; RV64I-NEXT:    add a2, a0, a2
; RV64I-NEXT:    ld a0, 0(a2)
; RV64I-NEXT:    sd a1, 0(a2)
; RV64I-NEXT:    ret
 %1 = getelementptr inbounds i64, ptr %a, i64 268435200
 %2 = load volatile i64, ptr %1
 store i64 %b, ptr %1
 ret i64 %2
}

%struct.quux = type { i32, [0 x i8] }

; Make sure we don't remove the addi and fold the C from
; (add (addi FrameIndex, C), X) into the store address.
; FrameIndex cannot be the operand of an ADD. We must keep the ADDI.
define void @addi_fold_crash(i64 %arg) nounwind {
; RV64I-LABEL: addi_fold_crash:
; RV64I:       # %bb.0: # %bb
; RV64I-NEXT:    addi sp, sp, -16
; RV64I-NEXT:    sd ra, 8(sp) # 8-byte Folded Spill
; RV64I-NEXT:    addi a1, sp, 4
; RV64I-NEXT:    add a0, a1, a0
; RV64I-NEXT:    sb zero, 0(a0)
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    call snork
; RV64I-NEXT:
    ld ra, 8(sp) # 8-byte Folded Reload
; RV64I-NEXT:    addi sp, sp, 16
; RV64I-NEXT:    ret
bb:
 %tmp = alloca %struct.quux, align 8
 %tmp1 = getelementptr inbounds %struct.quux, ptr %tmp, i64 0, i32 1
 %tmp2 = getelementptr inbounds %struct.quux, ptr %tmp, i64 0, i32 1, i64 %arg
 store i8 0, ptr %tmp2, align 1
 call void @snork(ptr %tmp1)
 ret void
```

```
}

declare void @snork(ptr)
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible
; function attrubtes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr %arg, ptr readonly %arg1) #0 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; CHECK-SAME: (ptr [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr %arg) #0 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; CHECK-SAME:
 (ptr [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, ptr [[TMP]]
; CHECK-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
```

```
 ret void
}

; This should promote
define
 internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr %arg, ptr readonly
%arg1) #1 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr %arg) #1 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, ptr [[TMP]]
; CHECK-NEXT:    call fastcc
 void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr [[TMP2]], <8 x i64>
[[TMP_VAL]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr %arg, ptr
readonly %arg1) #1 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
```

```
; CHECK-SAME: (ptr [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]],
 ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr %arg) #0 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, ptr [[TMP]]
; CHECK-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align
 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr %arg, ptr
readonly %arg1) #0 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (ptr [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
```

```
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr %arg) #1 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (ptr [[ARG:%.*]])
; CHECK-NEXT:
  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, ptr [[TMP]]
; CHECK-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr
 %arg, ptr readonly %arg1) #1 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]], ptr readonly [[ARG1:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1]]
; CHECK-NEXT:    store <8 x i64> [[TMP]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %arg) #2 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]])
```

```
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr
 [[TMP2]], ptr [[TMP]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %arg, ptr
readonly %arg1) #2 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]], ptr readonly [[ARG1:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = load <8 x i64>, ptr [[ARG1]]
; CHECK-NEXT:    store <8 x i64>
 [[TMP]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %arg) #1 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr
[[TMP2]], ptr [[TMP]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
```

```
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64
 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr %arg, ptr readonly
%arg1) #3 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr %arg) #4 {
; CHECK-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:
   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, ptr [[TMP]]
; CHECK-NEXT:    call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(ptr %tmp2, ptr %tmp)
```

```
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr %arg, ptr readonly
%arg1) #4 {
;
 CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], ptr [[ARG]]
; CHECK-NEXT:    ret void
;
bb:
 %tmp = load <8 x i64>, ptr %arg1
 store <8 x i64> %tmp, ptr %arg
 ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr %arg) #3 {
; CHECK-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; CHECK-SAME: (ptr [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:    call void @llvm.memset.p0.i64(ptr align 32 [[TMP]], i8 0, i64 32, i1 false)
; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, ptr [[TMP]]
; CHECK-NEXT:    call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr
[[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT:    [[TMP4:%.*]] = load
 <8 x i64>, ptr [[TMP2]], align 32
; CHECK-NEXT:    store <8 x i64> [[TMP4]], ptr [[ARG]], align 2
; CHECK-NEXT:    ret void
;
bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 call void @llvm.memset.p0.i64(ptr align 32 %tmp, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(ptr %tmp2, ptr %tmp)
 %tmp4 = load <8 x i64>, ptr %tmp2, align 32
 store <8 x i64> %tmp4, ptr %arg, align 2
 ret void
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %X, ptr %Y)
```

```
#2 {
; CHECK-LABEL: define
{{[^@]+}}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT:    [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT:    ret i32 [[C]]
;
 %A = load i32, ptr %X
 %B = load i32, ptr %Y
 %C = add i32 %A, %B
 ret i32 %C
}

define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr
 %B) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (ptr [[B:%.*]])
; CHECK-NEXT:    [[A:%.*]] = alloca i32
; CHECK-NEXT:    store i32 1, ptr [[A]]
; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, ptr [[A]]
; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, ptr [[B]]
; CHECK-NEXT:    [[C:%.*]] = call i32
@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:    ret i32 [[C]]
;
 %A = alloca i32
 store i32 1, ptr %A
 %C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(ptr %A, ptr %B)
 ret i32 %C
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %X, ptr %Y)
#2 {
; CHECK-LABEL: define
{{[^@]+}}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]],
 i32 [[Y_VAL:%.*]])
; CHECK-NEXT:    [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT:    ret i32 [[C]]
;
 %A = load i32, ptr %X
 %B = load i32, ptr %Y
 %C = add i32 %A, %B
 ret i32 %C
}

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %B) #2 {
```

```
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (ptr [[B:%.*]])
; CHECK-NEXT:    [[A:%.*]] = alloca i32
; CHECK-NEXT:    store i32 1, ptr [[A]]
; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, ptr [[A]]
; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, ptr [[B]]
; CHECK-NEXT:    [[C:%.*]] = call i32
@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:    ret i32 [[C]]
;
 %A = alloca i32
 store i32 1, ptr %A
 %C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(ptr %A, ptr %B)
 ret i32 %C
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0.i64(ptr
 nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512"
"prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256"
"prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=riscv64 -mattr=+zbc -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64ZBC-ZBKC
; RUN: llc -mtriple=riscv64 -mattr=+zbkc -verify-machineinstrs < %s \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s -check-prefix=RV64ZBC-ZBKC

declare i64 @llvm.riscv.clmul.i64(i64 %a, i64 %b)

define i64 @clmul64(i64 %a, i64 %b) nounwind {
; RV64ZBC-ZBKC-LABEL: clmul64:
; RV64ZBC-ZBKC:       # %bb.0:
; RV64ZBC-ZBKC-NEXT:    clmul a0, a0, a1
; RV64ZBC-ZBKC-NEXT:    ret
 %tmp = call i64 @llvm.riscv.clmul.i64(i64 %a, i64 %b)
 ret i64 %tmp
}
```

```
declare i64 @llvm.riscv.clmulh.i64(i64 %a, i64 %b)

define i64 @clmul64h(i64 %a, i64 %b) nounwind {
; RV64ZBC-ZBKC-LABEL: clmul64h:
; RV64ZBC-ZBKC:       # %bb.0:
; RV64ZBC-ZBKC-NEXT:    clmulh a0, a0, a1
; RV64ZBC-ZBKC-NEXT:    ret
 %tmp = call i64 @llvm.riscv.clmulh.i64(i64 %a, i64 %b)
 ret i64 %tmp
}

declare
 i32 @llvm.riscv.clmul.i32(i32 %a, i32 %b)

define signext i32 @clmul32(i32 signext %a, i32 signext %b) nounwind {
; RV64ZBC-ZBKC-LABEL: clmul32:
; RV64ZBC-ZBKC:       # %bb.0:
; RV64ZBC-ZBKC-NEXT:    clmul a0, a0, a1
; RV64ZBC-ZBKC-NEXT:    sext.w a0, a0
; RV64ZBC-ZBKC-NEXT:    ret
 %tmp = call i32 @llvm.riscv.clmul.i32(i32 %a, i32 %b)
 ret i32 %tmp
}

declare i32 @llvm.riscv.clmulh.i32(i32 %a, i32 %b)

define signext i32 @clmul32h(i32 signext %a, i32 signext %b) nounwind {
; RV64ZBC-ZBKC-LABEL: clmul32h:
; RV64ZBC-ZBKC:       # %bb.0:
; RV64ZBC-ZBKC-NEXT:    slli a1, a1, 32
; RV64ZBC-ZBKC-NEXT:    slli a0, a0, 32
; RV64ZBC-ZBKC-NEXT:    clmulh a0, a0, a1
; RV64ZBC-ZBKC-NEXT:    srai a0, a0, 32
; RV64ZBC-ZBKC-NEXT:    ret
 %tmp = call i32 @llvm.riscv.clmulh.i32(i32 %a, i32 %b)
 ret i32 %tmp
}

; FIXME: We could avoid the slli instructions by using clmul+srai since the
; inputs are zero extended.
define signext i32 @clmul32h_zext(i32 zeroext %a, i32 zeroext %b) nounwind {
;
 RV64ZBC-ZBKC-LABEL: clmul32h_zext:
; RV64ZBC-ZBKC:       # %bb.0:
; RV64ZBC-ZBKC-NEXT:    slli a1, a1, 32
; RV64ZBC-ZBKC-NEXT:    slli a0, a0, 32
; RV64ZBC-ZBKC-NEXT:    clmulh a0, a0, a1
; RV64ZBC-ZBKC-NEXT:    srai a0, a0, 32
```

```
; RV64ZBC-ZBKC-NEXT:    ret
 %tmp = call i32 @llvm.riscv.clmulh.i32(i32 %a, i32 %b)
 ret i32 %tmp
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=riscv64 -mattr=+m \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64I
; RUN: llc < %s -mtriple=riscv64 -mattr=+m,+zbb \
; RUN:   -riscv-experimental-rv64-legal-i32 | FileCheck %s --check-prefixes=RV64,RV64IZbb

declare i4 @llvm.ssub.sat.i4(i4, i4)
declare i8 @llvm.ssub.sat.i8(i8, i8)
declare i16 @llvm.ssub.sat.i16(i16, i16)
declare i32 @llvm.ssub.sat.i32(i32, i32)
declare i64 @llvm.ssub.sat.i64(i64, i64)

define signext i32 @func(i32 signext %x, i32 signext %y) nounwind {
; RV64I-LABEL: func:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a2, a0, a1
; RV64I-NEXT:    sub a0, a0, a1
; RV64I-NEXT:    beq a0, a2, .LBB0_2
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    sraiw a0, a0, 31
; RV64I-NEXT:    lui a1, 524288
; RV64I-NEXT:    xor a0, a0, a1
; RV64I-NEXT:  .LBB0_2:
; RV64I-NEXT:    sext.w a0, a0
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func:
; RV64IZbb:
     # %bb.0:
; RV64IZbb-NEXT:    sub a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 524288
; RV64IZbb-NEXT:    addiw a2, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a2
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i32 @llvm.ssub.sat.i32(i32 %x, i32 %y);
 ret i32 %tmp;
}

define i64 @func2(i64 %x, i64 %y) nounwind {
; RV64-LABEL: func2:
; RV64:       # %bb.0:
; RV64-NEXT:    mv a2, a0
; RV64-NEXT:    sgtz a3, a1
```

```
; RV64-NEXT:    sub a0, a0, a1
; RV64-NEXT:    slt a1, a0, a2
; RV64-NEXT:    beq a3, a1, .LBB1_2
; RV64-NEXT:  # %bb.1:
; RV64-NEXT:    srai a0, a0, 63
; RV64-NEXT:    li a1, -1
; RV64-NEXT:    slli a1, a1, 63
; RV64-NEXT:    xor a0, a0, a1
; RV64-NEXT:  .LBB1_2:
; RV64-NEXT:    ret
 %tmp = call i64 @llvm.ssub.sat.i64(i64 %x, i64 %y);
 ret i64 %tmp;
}

define signext i16 @func16(i16 signext %x, i16 signext %y) nounwind {
; RV64I-LABEL: func16:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    lui a1, 8
; RV64I-NEXT:    addiw a1, a1, -1
;
 RV64I-NEXT:    bge a0, a1, .LBB2_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    bge a1, a0, .LBB2_4
; RV64I-NEXT:  .LBB2_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB2_3:
; RV64I-NEXT:    mv a0, a1
; RV64I-NEXT:    lui a1, 1048568
; RV64I-NEXT:    blt a1, a0, .LBB2_2
; RV64I-NEXT:  .LBB2_4:
; RV64I-NEXT:    lui a0, 1048568
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func16:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 8
; RV64IZbb-NEXT:    addiw a1, a1, -1
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    lui a1, 1048568
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i16 @llvm.ssub.sat.i16(i16 %x, i16 %y);
 ret i16 %tmp;
}
```

```
define signext i8 @func8(i8 signext %x, i8 signext %y) nounwind {
; RV64I-LABEL: func8:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    li a1, 127
; RV64I-NEXT:    bge a0, a1, .LBB3_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -127
;
 RV64I-NEXT:    blt a0, a1, .LBB3_4
; RV64I-NEXT:  .LBB3_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB3_3:
; RV64I-NEXT:    li a0, 127
; RV64I-NEXT:    li a1, -127
; RV64I-NEXT:    bge a0, a1, .LBB3_2
; RV64I-NEXT:  .LBB3_4:
; RV64I-NEXT:    li a0, -128
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func8:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 127
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -128
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i8 @llvm.ssub.sat.i8(i8 %x, i8 %y);
 ret i8 %tmp;
}

define signext i4 @func3(i4 signext %x, i4 signext %y) nounwind {
; RV64I-LABEL: func3:
; RV64I:       # %bb.0:
; RV64I-NEXT:    subw a0, a0, a1
; RV64I-NEXT:    li a1, 7
; RV64I-NEXT:    bge a0, a1, .LBB4_3
; RV64I-NEXT:  # %bb.1:
; RV64I-NEXT:    li a1, -7
; RV64I-NEXT:    blt a0, a1, .LBB4_4
; RV64I-NEXT:  .LBB4_2:
; RV64I-NEXT:    ret
; RV64I-NEXT:  .LBB4_3:
; RV64I-NEXT:    li a0, 7
; RV64I-NEXT:    li a1,
 -7
; RV64I-NEXT:    bge a0, a1, .LBB4_2
```

```
; RV64I-NEXT:  .LBB4_4:
; RV64I-NEXT:    li a0, -8
; RV64I-NEXT:    ret
;
; RV64IZbb-LABEL: func3:
; RV64IZbb:       # %bb.0:
; RV64IZbb-NEXT:    subw a0, a0, a1
; RV64IZbb-NEXT:    li a1, 7
; RV64IZbb-NEXT:    min a0, a0, a1
; RV64IZbb-NEXT:    li a1, -8
; RV64IZbb-NEXT:    max a0, a0, a1
; RV64IZbb-NEXT:    ret
 %tmp = call i4 @llvm.ssub.sat.i4(i4 %x, i4 %y);
 ret i4 %tmp;
}
```

1.150 thrift 0.13.0

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1.151 commons-lang3 3.2.1

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1.152 time-out 0.1.1

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1.153 oniguruma 6.9.6-1.el9.6

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1.154 digest 3.1.0

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1.158 kerberos 1.21.1-8.el9_6

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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
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lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
uts/common/gssapi/include/gssapi_err_generic.h

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lib/gssapi/mechglue/g_accept_sec_context.c
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lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c

lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c
kprop/kproplog.c

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Schemas for Java EE Deployment Descriptors:
- javaee_5.xsd
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- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
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- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd

- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
- javaee_8.xsd
- web-app_4_0.xsd
- web-common_4_0.xsd
- web-fragment_4_0.xsd

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- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
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- web-app_4_0.xsd
- web-common_4_0.xsd
- web-fragment_4_0.xsd

may be obtained from:
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1.163 humanizer (net6.0) 2.14.1

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1.164 tomcat-catalina-ha 9.0.108

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1.166 racc 1.5.2

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1.167 blas 0.3.26-2.el9

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 * [2013-04-27] Optimize AVX and FMA4 TRSM on AMD Bulldozer
 * [2013-06-09] Optimize AVX and FMA4 SGEMM on AMD Bulldozer
 * [2013-06-11] Optimize AVX and FMA4 ZGEMM on AMD Bulldozer
 * [2013-06-12] Optimize AVX and FMA4 CGEMM on AMD Bulldozer
 * [2013-06-16] Optimize dgemv_n kernel on AMD Bulldozer
 * [2013-06-20] Optimize ddot, daxpy kernel on AMD Bulldozer
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* Stefan Karpinski <stefan@karpinski.org>
 * [2011-12-28] Fix a bug about SystemStubs on Mac OS X.

* Alexander Eberspcher <https://github.com/aeberspaecher>
 * [2012-05-02] Add note on patch for segfaults on Linux kernel 2.6.32.

* Mike Nolta <mike@nolta.net>
 * [2012-05-19] Fix building bug on FreeBSD and NetBSD.

* Sylvestre Ledru <https://github.com/sylvestre>
 * [2012-07-01] Improve the detection of sparc. Fix building bug under

Hurd and kfreebsd.

* Jameson Nash <https://github.com/vtjnash>
 * [2012-08-20] Provide support for
 passing CFLAGS, FFLAGS, PFLAGS, FPFLAGS to
 make on the command line.

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 * [2012-11-10] Fix NetBSD build.

* Sbastien Villemot <sebastien@debian.org>
 * [2012-11-14] Fix compilation with TARGET=GENERIC. Patch applied to Debian package.
 * [2013-08-28] Avoid failure on qemu guests declaring an Athlon CPU without 3dnow!

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 * [2013-05-17] Fix typo in the document. Re-order the architecture list in getarch.c.

* Kenneth Hoste <kenneth.hoste@gmail.com>
 * [2013-05-22] Adjust Makefile about downloading LAPACK source files.

* Lei WANG <https://github.com/wlbksy>
 * [2013-05-22] Fix a bug about wget.

* Dan Luu <http://www.linkedin.com/in/danluu>
 * [2013-06-30] Add Intel Haswell support (using sandybridge optimizations).

* grisuthedragon <https://github.com/grisuthedragon>
 * [2013-07-11] create openblas_get_parallel to retrieve information which parallelization
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 * [2013-07-22] Add in return value for `interface/trtri.c`

* Sbastien Fabbro <bicatali@gentoo.org>
 * [2013-07-24] Modify makefile to respect user's LDFLAGS
 * [2013-07-24] Add stack markings for GNU as arch-independent for assembler files

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 * [2013-08-21] Patch LAPACK XLASD4.f as discussed in JuliaLang/julia#2340

* Lars Buitinck <https://github.com/larsmans>
 * [2013-08-28] get rid of the generated cblas_noconst.h file
 * [2013-08-28] Missing threshold in gemm.c
 * [2013-08-28] fix default prefix handling in makefiles

* yieldthought <https://github.com/yieldthought>
 * [2013-10-08] Remove -Wl,--retain-symbols-file from dynamic link line to fix tool support

* Keno Fischer <https://github.com/loladiro>
 * [2013-10-23] Use FC instead of CC to link the dynamic library on OS X

* Christopher Meng <cickumqt@gmail.com>
 * [2013-12-09] Add DESTDIR support for easier building on RPM based distros.
 Use
 install command instead of cp to install files with permissions control.

* Lucas Beyer <lucasb.eyer.be@gmail.com>
 * [2013-12-10] Added support for NO_SHARED in make install.

* carlkl <https://github.com/carlkl>
 * [2013-12-13] Fixed LAPACKE building bug on Windows

* Isaac Dunham <https://github.com/idunham>
 * [2014-08-03] Fixed link error on Linux/musl

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 * [2014-10-10] trmm and sgemm kernels (optimized for APM's X-Gene 1).
 ARMv8 support.

* Jerome Robert <jeromerobert@gmx.com>
 * [2015-01-01] Speed-up small `ger` and `gemv` using stack allocation (bug #478)
 * [2015-12-23] `stack_check` in `gemv.c` (bug #722)
 * [2015-12-28] Allow to force the number of parallel make job
 * [2015-12-28] Fix detection of AMD E2-3200 detection
 * [2015-12-31] Let `make MAX_STACK_ALLOC=0` do what expected
 * [2016-01-19] Disable multi-threading in `ger` and `swap` for small matrices (bug #731)
 * [2016-01-24] Use `GEMM_MULTITHREAD_THRESHOLD` as a number
 of ops (bug #742)
 * [2016-01-26] Let `openblas_get_num_threads` return the number of active threads (bug #760)
 * [2016-01-30] Speed-up small `zger`, `zgemv`, `ztrmv` using stack allocation (bug #727)

* Dan Kortschak
 * [2015-01-07] Added test for drotmg bug #484.

* Ton van den Heuvel <https://github.com/ton>
 * [2015-03-18] Fix race condition during shutdown causing a crash in gotoblas_set_affinity().

* Martin Koehler <https://github.com/grisuthedragon/>
 * [2015-09-07] Improved imatcopy

* Ashwin Sekhar T K <https://github.com/ashwinyes/>
 * [2015-11-09] Assembly kernels for Cortex-A57 (ARMv8)
 * [2015-11-20] lapack-test fixes for Cortex-A57
 * [2016-03-14] Additional functional Assembly Kernels for Cortex-A57
 * [2016-03-14] Optimize Dgemm 4x4 for Cortex-A57

* theoractice <https://github.com/theoractice/>
 * [2016-03-20] Fix compiler error in VisualStudio with CMake
 * [2016-03-22] Fix access violation on Windows while static linking

* Paul Mustire <https://github.com/buffer51/>
  * [2016-02-04] Fix Android build on ARMV7
 * [2016-04-26] Android build with LAPACK for ARMV7 & ARMV8

* Shivraj Patil <https://github.com/sva-img/>
 * [2016-05-03] DGEMM optimization for MIPS P5600 and I6400 using MSA

* Kaustubh Raste <https://github.com/ksraste/>
 * [2016-05-09] DTRSM optimization for MIPS P5600 and I6400 using MSA
 * [2016-05-20] STRSM optimization for MIPS P5600 and I6400 using MSA

* Abdelrauf <https://github.com/quickwritereader>
 * [2017-01-01] dgemm and dtrmm kernels for IBM z13
 * [2017-02-26] ztrmm kernel for IBM z13
 * [2017-03-13] strmm and ctrmm kernel for IBM z13
 * [2017-09-01] initial Blas Level-1,2 (double precision) for IBM z13
 * [2018-03-07] added missing Blas Level 1-2 (double precision) simd codes
 * [2019-02-01] added missing Blas Level-1,2 (single precision) simd codes
 * [2019-03-14] power9 dgemm/dtrmm kernel
 * [2019-04-29] power9 sgemm/strmm kernel

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 * [2019-07-29]
 optimize AVX2 DGEMM
 * [2019-10-20] AVX512 DGEMM kernel (4x8)
 * [2019-11-06] optimize AVX512 SGEMM
 * [2019-11-12] AVX512 CGEMM & ZGEMM kernels
 * [2019-12-23] optimize AVX2 CGEMM and ZGEMM
 * [2019-12-30] AVX2 CGEMM3M & ZGEMM3M kernels
 * [2020-01-07] optimize AVX2 SGEMM and STRMM

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 * [2020-04-15] Half-precision GEMM for bfloat16

* Marius Hillenbrand <https://github.com/mhillenibm>
 * [2020-05-12] Revise dynamic architecture detection for IBM z
 * [2020-05-12] Add new sgemm and strmm kernel for IBM z14
 * [2020-09-07] Fix builds with clang on IBM z, including dynamic architecture support

* Danfeng Zhang <https://github.com/craft-zhang>
 * [2020-05-20] Improve performance of SGEMM and STRMM on Arm Cortex-A53

* PingTouGe Semiconductor Co., Ltd.

* [2020-10] Add RISC-V Vector (0.7.1) support. Optimize BLAS kernels for Xuantie C910

* River Dillon <oss@outerpassage.net>
* [2021-07-10] fix compilation with
musl libc

* Bine Brank <https://github.com/binebrank>
* [2021-10-27] Add vector-length-agnostic DGEMM kernels for Arm SVE
* [2021-11-20] Vector-length-agnostic Arm SVE copy routines for DGEMM, DTRMM, DSYMM
* [2021-11-12] SVE kernels for SGEMM, STRMM and corresponding SVE copy functions
* [2022-01-06] SVE kernels for CGEMM, ZGEMM, CTRMM, ZTRMM and corresponding SVE copy functions
* [2022-01-18] SVE kernels and copy functions for TRSM

* Ilya Kurdyukov <https://github.com/ilyakurdyukov>
* [2021-02-21] Add basic support for the Elbrus E2000 architecture

* PLCT Lab, Institute of Software Chinese Academy of Sciences
* [2022-03] Support RISC-V Vector Intrinisc 1.0 version.

* Pablo Romero <https://github.com/pablorcum>
* [2022-08] Fix building from sources for QNX

* Mark Seminatore <https://github.com/mseminatore>
* [2023-11-09] Improve Windows threading performance scaling
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1.171 libvmtools 12.5.0-1.el9.alma.1

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$OpenLDAP: pkg/ldap/libraries/liblutil/base64.c,v 1.15 2006/01/03 22:12:11 kurt Exp $ /
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To apply the Apache License to your work, attach the following
boilerplate notice, with the fields enclosed by brackets "[]"
replaced with your own identifying information. (Don't include
the brackets!) The text should be enclosed in the appropriate
comment syntax for the file format. We also recommend that a
file or class name and description of purpose be included on the
same "printed page" as the copyright notice for easier
identification within third-party archives.

Copyright [yyyy] [name of copyright owner]

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Apache Commons DBCP
Copyright 2001-2015 The Apache Software Foundation

This product includes software developed at
The Apache Software Foundation (http://www.apache.org/).

1.179 bcprov-jdk18on 1.74

1.179.1 Available under license :

No license file was found, but licenses were detected in source scan.

/**
* Contains the polynomial classes used in the NTRU round 3 algorithm.
*
*
* This implementation is based on the C reference implementation submitted for the round 3 NIST PQC competition,
* released under CC0-1.0 license.
*
*
* NIST submission files
* License of reference implementation

*/

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/pqc/math/ntru/package-info.java
No license file was found, but licenses were detected in source scan.

/**
* Poly1305 message authentication code, designed by D. J. Bernstein.
*
* Poly1305 computes a 128-bit (16 bytes) authenticator, using a 128 bit nonce and a 256 bit key
* consisting of a 128 bit key applied to an underlying cipher, and a 128 bit key (with 106
* effective key bits) used in the authenticator.
*
* The polynomial calculation in this implementation is adapted from the public domain <a
* href="https://github.com/floodyberry/poly1305-donna">poly1305-donna-unrolled C implementation
* by Andrew M (@floodyberry).
* @see Poly1305KeyGenerator
*/

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/crypto/macs/Poly1305.java
No license file was found, but licenses were detected in source scan.

/**
   * Magic value for proprietary OpenSSH private key.
   **/

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/crypto/util/OpenSSHPrivateKeyUtil.java
No license file was found, but licenses were detected in source scan.

/**
* SPHINCS-256 signer.
*
* This implementation is heavily based on the reference implementation in SUPERCOP, the main difference being the digests used
* for message hashing and tree construction are now configurable (within limits...) and that the implementation produces
* detached signatures.
*
*
* The SPHINCS reference implementation is public domain, as per the statement in the second last paragraph of
* section 1 in https://eprint.iacr.org/2014/795.pdf
*
*/

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/pqc/crypto/sphincs/SPHINCS256Signer.java

No license file was found, but licenses were detected in source scan.

```
/**
* The NTRU algorithm based on the round 3 submission of the NIST post-quantum cryptography. For the old NTRU, see
* {@link org.bouncycastle.pqc.legacy.crypto.ntru}.
*
* <p>
* This implementation is based on the C reference implementation submitted for the round 3 NIST PQC competition,
* released under CC0-1.0 license.
* </p>
*
* <p><a href="https://csrc.nist.gov/CSRC/media/Projects/post-quantum-cryptography/documents/round-3/submissions/NTRU-Round3.zip">NIST submission files</a></p>
* <p><a href="https://github.com/jschanck/ntru/blob/master/LICENSE">License of reference implementation</a></p>
*
* @see <a href="https://csrc.nist.gov/Projects/post-quantum-cryptography/round-3-submissions">NIST round 3 PQC submissions page</a>
* @see <a href="https://ntru.org/">NTRU website</a>
* @see <a href="https://ntru.org/f/ntru-20190330.pdf">NTRU specification</a>
*/
```

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/pqc/crypto/ntru/package-info.java
No license file was found, but licenses were detected in source scan.

```
// now keep space is free to use
```

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/pqc/legacy/crypto/gmss/GMSSPrivateKeyParameters.java
No license file was found, but licenses were detected in source scan.

```
/**
* The Bouncy Castle License
*
* Copyright (c) 2000-2023 The Legion Of The Bouncy Castle Inc. (https://www.bouncycastle.org)
* <p>
* Permission is hereby granted, free of charge, to any person obtaining a copy of this software
* and associated documentation files (the "Software"), to deal in the Software without restriction,
* including without limitation the rights to use, copy, modify, merge, publish, distribute, sublicense,
* and/or sell copies of the Software, and to permit persons to whom the Software is furnished to do so,
* subject to the following conditions:
* <p>
* The above copyright notice and this permission notice shall be included in all copies or substantial
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* <p>
```

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* OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
* DEALINGS IN THE SOFTWARE.
*/

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/LICENSE.java
No license file was found, but licenses were detected in source scan.

/**
* An implementation of RFC 7253 on The OCB
* Authenticated-Encryption Algorithm. For those still concerned about the original patents
* around this, please see:
*
* https://mailarchive.ietf.org/arch/msg/cfrg/qLTveWOdTJcLn4HP3ev-vrj05Vg/
*
* Text reproduced below.
* <blockquote>
* Phillip Rogaway >rogaway@cs.ucdavis.edu< Sat, 27 February 2021 02:46 UTCShow header
*
* I can confirm that I have abandoned all OCB patents
* and placed into the public domain all OCB-related IP of mine.
* While I have been telling people this for quite some time, I don't
* think I ever made a proper announcement to the CFRG or on the
* OCB webpage. Consider that done.
* </blockquote>
*/

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/crypto/modes/OCBBlockCipher.java
No license file was found, but licenses were detected in source scan.

public DomainParameters(BigInteger p, BigInteger g, BigInteger q, BigInteger j,

Found in path(s):
* /bcprov-jdk18on-1-74-java-jar/org/bouncycastle/asn1/x9/DomainParameters.java

1.180 uri 0.10.3

1.180.1 Available under license :

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SUCH DAMAGE.

1.181 xerces-j 2.8.1

1.181.1 Available under license :

No license file was found, but licenses were detected in source scan.

cos-nonambig = cos-nonambig: {0} and {1} (or elements from their substitution group) violate \"Unique Particle Attribution\". During validation against this schema, ambiguity would be created for those two particles.

Found in path(s):
* /xercesimpl-2-8-1-jar/org/apache/xerces/impl/msg/XMLSchemaMessages.properties

1.182 microsoft® windows® operating system 6.1.7600

1.182.1 Available under license :

Instructions for Finding Linux Legal Metadata

(Licenses, Copyright and Source Code)

Linux based container images typically contain multiple software components, each built from different source and with different licensing. It is important that users of these images can determine the license information for these images and have a link back to the source for copyright and pedigree information.

This document is intended to be informative, primarily defining patterns for discovering license and pedigree information for certain Linux based containers and describing expectations (whether the desired information can be found within the images). We currently only provide information for Ubuntu, Debian and Alpine Linux distributions and their official packages installed with an official package manager for that Linux distribution. We plan to update this document with information for other major Linux builds like CentOS. This information can change at any time, which also demonstrates why
using the documented patterns is important.

This document focuses solely on official Linux distros and packages pulled in by their official package managers. Windows based containers are out of scope. For licensing information about any other component (whether open source or closed source) in a Microsoft container, other than the specific base images and official packages covered in this document, please see the "Licensing" section in the container's Docker Hub landing page.

This document answers two basic questions:

* How can I interrogate a Linux image to determine licensing and version information for the Linux base image and packages pulled in by the Linux distribution's official package manager?
* Where can I look to learn about source pedigree information for a Linux image and its official packages from external sources (since it is difficult/impossible to learn everything from an image)?

This document uses .NET Core as an example throughout, to demonstrate how to find
license information for a variety of Linux distros. These distros are not uniform, which is why looking at multiple examples is useful. You can find the artifacts that will be used as examples at:[.NET Core Docker files](https://github.com/dotnet/dotnet-docker) and [.NET Core container images](https://hub.docker.com/_/microsoft-dotnet/).

This document describes the licensing and source pedigree of the following components:

* [Base images](https://github.com/microsoft/oss-law-central/blob/master/image-artifact-details.md#Base-Images) (from Docker Hub)
* [Additional official packages](https://github.com/microsoft/oss-law-central/blob/master/image-artifact-details.md#Additional-Packages) (from Linux package managers)

Base Images

Images are provided for a set of Linux distros, delivered via [MCR](https://azure.microsoft.com/blog/microsoft-syndicates-container-catalog/) (and advertised on Docker Hub). The Linux distros are acquired from Docker Hub as part of building the images.

The following base images are delivered as components of the .NET Core images:

* [alpine](https://hub.docker.com/_/alpine)
* [debian](https://hub.docker.com/_/debian)
* [ubuntu](https://hub.docker.com/_/ubuntu)
* [buildpack-deps](https://hub.docker.com/_/buildpack-deps)

.NET Core is dependent on those images, and the individuals/organizations that build them, to satisfactorily describe the licensing and pedigree of these images.

Retrieving Package Information From Base Images

The practices to retrieve package information vary across the dependent distro types. This is demonstrated via the following introspection technique, for each of the supported distros.

Note: The output of the commands is intentionally cut-off for purposes of brevity. The output you see below is intended to demonstrate the pattern, and you can repeat it to view all current license information for the base image of your choosing.

alpine

You can retrieve the full list of packages installed within
Alpine images, as demonstrated below.

```
$ docker run --rm alpine:3.11 apk list -I
musl-1.1.24-r0 x86_64 {musl} (MIT) [installed]
zlib-1.2.11-r3 x86_64 {zlib} (Zlib) [installed]
apk-tools-2.10.4-r3 x86_64 {apk-tools} (GPL2) [installed]
musl-utils-1.1.24-r0 x86_64 {musl} (MIT BSD GPL2+) [installed]
libssl1.1-1.1.1d-r2 x86_64 {openssl} (OpenSSL) [installed]
alpine-baselayout-3.2.0-r3 x86_64 {alpine-baselayout} (GPL-2.0-only) [installed]
alpine-keys-2.1-r2 x86_64 {alpine-keys} (MIT) [installed]
busybox-1.31.1-r8 x86_64 {busybox} (GPL-2.0-only) [installed]
scanelf-1.2.4-r0 x86_64 {pax-utils} (GPL-2.0-only) [installed]
libc-utils-0.7.2-r0 x86_64 {libc-dev} (BSD) [installed]
libtls-standalone-2.9.1-r0 x86_64 {libtls-standalone} (ISC) [installed]
ssl_client-1.31.1-r8 x86_64 {busybox} (GPL-2.0-only) [installed]
ca-certificates-cacert-20191127-r0 x86_64 {ca-certificates} (MPL-2.0 GPL-2.0-or-later) [installed]
libcrypto1.1-1.1.1d-r2 x86_64 {openssl} (OpenSSL) [installed]
```

The package
listing above includes the license in the fourth cell. You can also retrieve the license for any single package, as demonstrated below. This example retrieves the musl license from the alpine:3.11 image.

```
$ docker run --rm alpine:3.11 apk info --license musl
```

musl-1.1.24-r0 license:
MIT
You can retrieve the source for any package by looking up the package on the [alpine package website](https://pkgs.alpinelinux.org/packages). You must enter the package name, branch (which corresponds to the Alpine version), and architecture. Finding the [information for the zlib package](https://pkgs.alpinelinux.org/packages?name=zlib&branch=v3.11&repo=main&arch=x86) is demonstrated below.
```

Navigating the [Package link](https://pkgs.alpinelinux.org/package/v3.11/main/x86/zlib) will show detailed package information including the originating source amongst other information.

#### debian

You can retrieve the full list of packages installed within Debian images, as demonstrated below.

```
$ docker
 run --rm debian:buster-slim dpkg-query -l
Desired=Unknown/Install/Remove/Purge/Hold
| Status=Not/Inst/Conf-files/Unpacked/halF-conf/Half-inst/trig-aWait/Trig-pend
|/ Err?=(none)/Reinst-required (Status,Err: uppercase=bad)
||/ Name Version Architecture Description
+++-======================-======================-============-
================================================================================
ii adduser 3.118 all add and remove users and groups
ii apt 1.8.2 amd64 commandline package manager
ii base-files 10.3+deb10u2 amd64 Debian base system miscellaneous files
ii base-passwd 3.5.46 amd64 Debian base system master password and group files
ii bash 5.0-4 amd64 GNU Bourne Again SHell
ii bsdutils 1:2.33.1-0.1 amd64 basic utilities from 4.4BSD-Lite
ii coreutils 8.30-3 amd64 GNU core utilities
ii dash 0.5.10.2-5 amd64 POSIX-compliant shell
ii debconf 1.5.71 all Debian configuration management system
ii debian-archive-keyring 2019.1 all GnuPG archive keys of the Debian archive
...
```

You
 can retrieve the license for any package, as demonstrated below. This example retrieves the apt license from the debian:buster-slim image by performing the following steps with a single command:

1. List out the package contents
2. Find the copyright file
3. Print out the copyright file contents

```
$ docker run --rm debian:buster-slim sh -c 'cat `dpkg -L apt | grep copyright`'
```
```

Apt is copyright 1997, 1998, 1999 Jason Gunthorpe and others.

Apt is currently developed by APT Development Team <deity@lists.debian.org>.
License: GPLv2+

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the Free Software Foundation; either version 2 of the License, or
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You should have received a copy of the GNU General Public License
along with this program; if not, write to the Free Software
Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301, USA.

See /usr/share/common-licenses/GPL-2, or
<http://www.gnu.org/copyleft/gpl.txt> for the terms of the latest version
of the GNU General Public License.
```

You can retrieve the source for package, as demonstrated below. This example retrieves the apt source from the debian:buster-slim image by performing the following steps with a single command:

1. Add the source feeds for the existing package feeds.
2. Utilize the [apt-get](https://manpages.debian.org/buster/apt/apt-get.8.en.html) tool to print out the URIs of archive files that contain the package source. See the [apt-get source](https://manpages.debian.org/buster/apt/apt-get.8.en.html) documentation for additional options.

```
$ docker run --rm debian:buster-slim sh -c "find /etc/apt/sources.list* -type f -exec
 sed -i -e 'p; s/^deb /deb-src /' '{}' + && apt-get update -qq && apt-get source -qq --print-uris apt=1.8.2"

'http://deb.debian.org/debian/pool/main/a/apt/apt_1.8.2.dsc' apt_1.8.2.dsc 2766
SHA256:891cc952f028b79e2eace3db6c19d55dee247ac19d934bbe43c3921104b01c3b

'http://deb.debian.org/debian/pool/main/a/apt/apt_1.8.2.tar.xz' apt_1.8.2.tar.xz 2188344
SHA256:7f9a91c26624bc85733683ee239b0c0d971a593d670855cf7bcf693b08a37734
```

ubuntu

You can retrieve the full list of packages installed within Ubuntu images, as demonstrated below.
```

```
$ docker run --rm ubuntu:bionic dpkg-query -l
Desired=Unknown/Install/Remove/Purge/Hold
| Status=Not/Inst/Conf-files/Unpacked/halF-conf/Half-inst/trig-aWait/Trig-pend
|/ Err?=(none)/Reinst-required (Status,Err: uppercase=bad)
||/ Name Version Architecture Description
+++-==============================-====================-====================-
================================================================
ii adduser 3.116ubuntu1 all add
 and remove users and groups
ii apt 1.6.12 amd64 commandline package manager
ii base-files 10.1ubuntu2.7 amd64 Debian base system miscellaneous files
ii base-passwd 3.5.44 amd64 Debian base system master password and group files
ii bash 4.4.18-2ubuntu1.2 amd64 GNU Bourne Again SHell
ii bsdutils 1:2.31.1-0.4ubuntu3.4 amd64 basic utilities from 4.4BSD-Lite
ii bzip2 1.0.6-8.1ubuntu0.2 amd64 high-quality block-sorting file compressor - utilities
ii coreutils 8.28-1ubuntu1 amd64 GNU core utilities
ii dash 0.5.8-2.10 amd64 POSIX-compliant shell
ii debconf 1.5.66ubuntu1 all Debian configuration management system
...
```

You can retrieve the license for any package, as demonstrated below. This example retrieves the apt license from the ubuntu:bionic image by performing the following steps with a single command:

1. List out the package contents
2. Find the copyright file
3. Print out the copyright file contents

```
$ docker run --rm ubuntu:bionic sh -c 'cat `dpkg -L apt | grep copyright`'

Apt
 is copyright 1997, 1998, 1999 Jason Gunthorpe and others.

Apt is currently developed by APT Development Team <deity@lists.debian.org>.
License: GPLv2+

This program is free software; you can redistribute it and/or modify
it under the terms of the GNU General Public License as published by
the Free Software Foundation; either version 2 of the License, or
(at your option) any later version.
This program is distributed in the hope that it will be useful,
but WITHOUT ANY WARRANTY; without even the implied warranty of
MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
GNU General Public License for more details.
```

You should have received a copy of the GNU General Public License
along with this program; if not, write to the Free Software
Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301, USA.

See /usr/share/common-licenses/GPL-2, or
<http://www.gnu.org/copyleft/gpl.txt> for the terms of the latest version
of the GNU General Public License.
```

You can
 retrieve the source for package, as demonstrated below. This example retrieves the apt source from the ubuntu:bionic image by performing the following steps with a single command:

1. Add the source feeds for the existing package feeds.
2. Utilize the [apt-get](https://manpages.debian.org/buster/apt/apt-get.8.en.html) tool to print out the URIs of archive files that contain the package source. See the [apt-get source](https://manpages.debian.org/buster/apt/apt-get.8.en.html) documentation for additional options.

```
$ docker run --rm ubuntu:bionic sh -c "find /etc/apt/sources.list* -type f -exec sed -i -e 'p; s/^deb /deb-src /' '{}' +
&& apt-get update -qq && apt-get source -qq --print-uris apt=1.6.12"

'http://archive.ubuntu.com/ubuntu/pool/main/a/apt/apt_1.6.12.dsc' apt_1.6.12.dsc 2796
SHA256:061d7de8e9faea48b69b37f01a54b2eac04d39eaab143ec4ffb7cd534fa01cfd

'http://archive.ubuntu.com/ubuntu/pool/main/a/apt/apt_1.6.12.tar.xz' apt_1.6.12.tar.xz 2171176
SHA256:d4e2ae405a1ff12bd108a5a530a24d52afde9cde40065ed508fdea33302c8a35
```

####
 buildpack-deps

The Debian and Ubuntu variants of [buildpack-deps](https://hub.docker.com/_/buildpack-deps) are used as base images, enabling you to use the same patterns described above to retrieve the licensing information for these images.

Retrieving Pedigree Information for Base Images**

The base images are retrieved from official Docker image repositories which provide detailed information about the images. This information includes layer information, source code, license information, etc. This information is stored in the [Official Images "Extended Information" repository](https://github.com/docker-library/repo-info) and is split into two types:

* remote
  * gathered from the Docker Hub/Registry API
  * image digests/blobs, transfer sizes, image metadata, etc.
* local
  * inspected from the image on-disk after it is pulled
  * installed packages, creation date, architecture, environment variables, detected
```

licenses, etc.

An abbreviated copy of the "extended information" for the debian:buster-slim image is included below, first the [remote](https://github.com/docker-library/repo-info/blob/master/repos/debian/remote/buster-slim.md) and then the [local](https://github.com/docker-library/repo-info/blob/master/repos/debian/local/buster-slim.md) information.

debian:buster-slim (remote)

```
$ docker pull debian@sha256:e4c1417236abc57971755ca2bfccd546cbca45b33daf66001a5addae4bf78517

*   Manifest MIME: application/vnd.docker.distribution.manifest.list.v2+json
*   Platforms:
  *   linux; amd64
  *   linux; arm variant v5
  *   linux; arm variant v7
  *   linux; arm64 variant v8
  *   linux; 386
  *   linux; ppc64le
  *   linux; s390x
```

debian:buster-slim - linux; amd64

```
$ docker pull debian@sha256:0c679627b3a61b2e3ee902ec224b0505839bc2ad76d99530e5f0566e47ac8400

*   Docker Version: 18.06.1-ce
*   Manifest MIME: application/vnd.docker.distribution.manifest.v2+json
*
  Total Size: **27.1 MB (27092274 bytes)**
*   (compressed transfer size, not on-disk size)
*   Image ID: sha256:e1af56d072b8d93fce4b566f4bf76311108dbbbe952b12a85418bd32c2fcdda7
*   Default Command: ["bash"]

# Sat, 28 Dec 2019 04:21:22 GMT

ADD file:04caaf303199c81ff1a94e2e39d5096f9d02b73294b82758e5bc6e23aff94272 in /

# Sat, 28 Dec 2019 04:21:23 GMT

CMD ["bash"]

*   Layers:
  *   sha256:8ec398bc03560e0fa56440e96da307cdf0b1ad153f459b52bca53ae7ddb8236d
  *   Last Modified: Sat, 28 Dec 2019 04:25:53 GMT
  *   Size: 27.1 MB (27092274 bytes)
  *   MIME: application/vnd.docker.image.rootfs.diff.tar.gzip
```

...
```

#### debian:buster-slim (local)

**Docker Metadata**

```
*   Image ID: sha256:e1af56d072b8d93fce4b566f4bf76311108dbbbe952b12a85418bd32c2fcdda7
*   Created: 2019-12-28T04:21:23.037912523Z
*   Virtual Size: ~ 69.21 Mb
  (total size of all layers on-disk)
*   Arch: linux/amd64
*   Command: ["bash"]
*   Environment:
  *   PATH=/usr/local/sbin:/usr/local/bin:/usr/sbin:/usr/bin:/sbin:/bin
```

**dpkg
(.deb-based packages)**

dpkg source package: acl=2.2.53-4

Binary Packages:

```
*   libacl1:amd64=2.2.53-4
```

Licenses: (parsed from: /usr/share/doc/libacl1/copyright)

```
*   GPL-2
*   GPL-2+
*   LGPL-2+
*   LGPL-2.1
```

Source:

```
$ apt-get source -qq --print-uris acl=2.2.53-4

'http://deb.debian.org/debian/pool/main/a/acl/acl_2.2.53-4.dsc' acl_2.2.53-4.dsc 2330
SHA256:532eb4029659db74e6625adc2bd277144f33c92cb0603272d61693b069896a85

'http://deb.debian.org/debian/pool/main/a/acl/acl_2.2.53.orig.tar.gz' acl_2.2.53.orig.tar.gz 524300
SHA256:06be9865c6f418d851ff4494e12406568353b891ffe1f596b34693c387af26c7

'http://deb.debian.org/debian/pool/main/a/acl/acl_2.2.53.orig.tar.gz.asc' acl_2.2.53.orig.tar.gz.asc 833
SHA256:06849bece0b56a6a7269173abe101cff223bb9346d74027a3cd5ff80914abf4b
```
```

'http://deb.debian.org/debian/pool/main/a/acl/acl_2.2.53-4.debian.tar.gz.asc' acl_2.2.53-4.debian.tar.xz 18572 SHA256:3e6571adea4886a9549bdc2323d5c55ee8f7dafb6a204513111d5943d2776dd8
```

Other
potentially useful URLs:

* [https://sources.debian.net/src/acl/2.2.53-4/](https://sources.debian.net/src/acl/2.2.53-4/) (for browsing the source)
* [https://sources.debian.net/src/acl/2.2.53-4/debian/copyright/](https://sources.debian.net/src/acl/2.2.53-4/debian/copyright/) (for direct copyright/license information)
* [http://snapshot.debian.org/package/acl/2.2.53-4/](http://snapshot.debian.org/package/acl/2.2.53-4/) (for access to the source package after it no longer exists in the archive)

**dpkg source package: adduser=3.118**

...

## Pedigree Information for Base Images

The following links provide the pedigree information for the Linux base images.

* [alpine](https://hub.docker.com/_/alpine)
  * [local](https://github.com/docker-library/repo-info/blob/master/repos/alpine/local/)
  * [remote](https://github.com/docker-library/repo-info/blob/master/repos/alpine/remote/)
* [debian](https://hub.docker.com/_/debian)
  * [local](https://github.com/docker-library/repo-info/blob/master/repos/debian/local/)
  * [remote](https://github.com/docker-library/repo-info/blob/master/repos/debian/remote/)
* [ubuntu](https://hub.docker.com/_/ubuntu)
  * [local](https://github.com/docker-library/repo-info/blob/master/repos/ubuntu/local/)
  * [remote](https://github.com/docker-library/repo-info/blob/master/repos/ubuntu/remote/)
* [buildpack-deps](https://hub.docker.com/_/buildpack-deps)
  * [local](https://github.com/docker-library/repo-info/blob/master/repos/buildpack-deps/local/)
  * [remote](https://github.com/docker-library/repo-info/blob/master/repos/buildpack-deps/remote/)

### Additional Packages

Images often install a set of packages (from the associated package manager for the distro) in addition the the package provided by the base image. You can inspect the Dockerfiles to discover these additional packages.

You can see an example of the additional packages the .NET Core images install in this [runtime-deps](https://hub.docker.com/_/microsoft-dotnet-runtime-deps)
Dockerfile, which is copied below (see RUN apt-get instruction).

```
FROM debian:buster-slim
RUN apt-get update \
```
```

&& apt-get install -y --no-install-recommends \
ca-certificates \
\

.NET Core dependencies

libc6 \
libgcc1 \
libgssapi-krb5-2 \
libicu63 \
libssl1.1 \
libstdc++6 \
zlib1g \
&& rm -rf /var/lib/apt/lists/*

Configure web servers to bind to port 80 when present

ENV ASPNETCORE_URLS=http://+:80 \

Enable detection of running in a container

DOTNET_RUNNING_IN_CONTAINER=true
```

There is no guarantee that these additional packages are not present in the base image, but typically they will not be (hence why they are explicitly installed), as is demonstrated below.

```
$ docker run --rm debian:buster-slim apt list ca-certificates

Listing...

$ docker run --rm mcr.microsoft.com/dotnet/core/runtime-deps:3.1-buster-slim apt list ca-certificates

Listing...

ca-certificates/now 20190110 all [installed,local]
```

These
 patterns can be repeated for packages that are added in [.NET Core images (above and beyond the base image)](https://github.com/dotnet/dotnet-docker/blob/master/documentation/image-artifact-details.md).
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1.183 vapi-runtime 1.0.0

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1.184 commons-collections 3.2.2

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1.185 sqlite 3.34.1-8.el9_6

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1.186 jclouds-jsch 1.0-beta-7

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* ====================================================================
```

*/

Found in path(s):
* /jclouds-jsch-1-0-beta-7-sources-jar/org/jclouds/ssh/jsch/config/JschSshClientModule.java
* /jclouds-jsch-1-0-beta-7-sources-jar/org/jclouds/ssh/jsch/JschSshClient.java
*
/jclouds-jsch-1-0-beta-7-sources-jar/org/jclouds/ssh/jsch/predicates/InetSocketAddressConnect.java

1.187 vim 8.2.2637-22.el9_6

1.187.1 Available under license :

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License:

Found in path(s):
* /vim-r9-zip/vim-r9/SOURCES/spec-template.new
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cp runtime/doc/uganda.txt LICENSE

Found in path(s):
* /vim-r9-zip/vim-r9/SPECS/vim.spec

1.188 jruby 9.4.6.0

1.188.1 Available under license :

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1.193 go-rootcerts 1.0.2

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1.195 english 0.7.1

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1.196 openssh 8.7p1-45.el9

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*
* Created: Sat Sep 02 12:17:00 2000 cv
*
* This file contains functions for forcing opened file descriptors to
* binary mode on Windows systems.
*/

#ifndef _BSD_CYGWIN_UTIL_H
#define _BSD_CYGWIN_UTIL_H

#ifdef HAVE_CYGWIN

#undef ERROR
#define is_winnt       (GetVersion() < 0x80000000)

#include <windows.h>
#include <sys/cygwin.h>
#include <io.h>

int binary_open(const char *, int , ...);
int binary_pipe(int fd[2]);
int check_nt_auth(int, struct passwd *);
int check_ntsec(const char *);
void register_9x_service(void);
char **fetch_windows_environment(void);
void free_windows_environment(char **);

#define
```

```
 open binary_open
#define pipe binary_pipe

#endif /* HAVE_CYGWIN */

#endif /* _BSD_CYGWIN_UTIL_H */
```
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```

[Tatu continues]
```
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- IDEA is no longer included, its use is deprecated
- DES is now external, in the OpenSSL library
- GMP is no longer used, and instead we call BN code from OpenSSL
- Zlib is now external, in a library
- The make-ssh-known-hosts script is no longer included
- TSS has been removed
- MD5 is now external, in the OpenSSL library
- RC4 support has been replaced with ARC4 support from OpenSSL
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[The licence continues]

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```
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*
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*
* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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```

```
*/

/* OPENBSD ORIGINAL: sys/sys/poll.h */

#if !defined(HAVE_POLL) && !defined(HAVE_POLL_H)
#ifndef _COMPAT_POLL_H_
#define _COMPAT_POLL_H_

typedef struct pollfd {
 int fd;
 short events;
 short revents;
} pollfd_t;

typedef unsigned int nfds_t;

#define POLLIN  0x0001
#define POLLOUT  0x0004
#define POLLERR  0x0008
#define POLLHUP  0x0010
#define POLLNVAL 0x0020
#if 0
/* the following are currently not implemented */
#define POLLPRI  0x0002
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLWRNORM      POLLOUT
#define POLLRDBAND 0x0080
#define POLLWRBAND 0x0100
#endif

#define INFTIM  (-1) /* not
 standard */

int   poll(struct pollfd *, nfds_t, int);
#endif /* !_COMPAT_POLL_H_ */
#endif /* !HAVE_POLL_H */
/* $OpenBSD: poll.h,v 1.11 2003/12/10 23:10:08 millert Exp $ */

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#ifndef _COMPAT_POLL_H_
#define _COMPAT_POLL_H_

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 int  fd;
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} pollfd_t;

typedef unsigned int nfds_t;

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/* the following are currently not implemented */
#define POLLPRI  0x0002
#define POLLHUP  0x0010
#define POLLNVAL 0x0020
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLWRNORM      POLLOUT
#define POLLRDBAND 0x0080
#define POLLWRBAND 0x0100
#endif

#define INFTIM  (-1) /* not
```

```
 standard */

int   poll(struct pollfd *, nfds_t, int);
#endif /* !_COMPAT_POLL_H_ */
#endif /* !HAVE_POLL_H */
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*/

#include "includes.h"

#if !defined(HAVE_STATVFS) || !defined(HAVE_FSTATVFS)

#include <sys/types.h>

#ifdef HAVE_SYS_MOUNT_H
#include <sys/mount.h>
#endif
#ifdef HAVE_SYS_STATFS_H
#include <sys/statfs.h>
#endif
#ifdef
 HAVE_SYS_VFS_H
#include <sys/vfs.h>
#endif

#ifndef HAVE_FSBLKCNT_T
typedef unsigned long fsblkcnt_t;
#endif
#ifndef HAVE_FSFILCNT_T
typedef unsigned long fsfilcnt_t;
#endif

#ifndef ST_RDONLY
#define ST_RDONLY 1
#endif
```

```
#ifndef ST_NOSUID
#define ST_NOSUID 2
#endif

 /* as defined in IEEE Std 1003.1, 2004 Edition */
struct statvfs {
 unsigned long f_bsize; /* File system block size. */
 unsigned long f_frsize; /* Fundamental file system block size. */
 fsblkcnt_t f_blocks; /* Total number of blocks on file system in */
   /* units of f_frsize. */
 fsblkcnt_t    f_bfree; /* Total number of free blocks. */
 fsblkcnt_t    f_bavail; /* Number of free blocks available to  */
   /* non-privileged process.  */
 fsfilcnt_t    f_files; /* Total number of file serial numbers. */
 fsfilcnt_t    f_ffree; /* Total number of free file serial numbers. */
 fsfilcnt_t    f_favail; /* Number of file serial numbers available to */
   /* non-privileged process. */
 unsigned long f_fsid; /*
 File system ID. */
 unsigned long f_flag; /* BBit mask of f_flag values. */
 unsigned long f_namemax;/*  Maximum filename length. */
};
#endif

#ifndef HAVE_STATVFS
int statvfs(const char *, struct statvfs *);
#endif

#ifndef HAVE_FSTATVFS
int fstatvfs(int, struct statvfs *);
#endif
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*/

#ifndef _BSD_MISC_H
```

```
#define _BSD_MISC_H

#include "includes.h"

char *ssh_get_progname(char *);
int seed_from_prngd(unsigned char *, size_t);

#ifndef HAVE_SETSID
#define setsid() setpgrp(0, getpid())
#endif
 /* !HAVE_SETSID */

#ifndef HAVE_SETENV
int setenv(const char *, const char *, int);
#endif /* !HAVE_SETENV */

#ifndef HAVE_SETLOGIN
int setlogin(const char *);
#endif /* !HAVE_SETLOGIN */

#ifndef HAVE_INNETGR
int innetgr(const char *, const char *, const char *, const char *);
#endif /* HAVE_INNETGR */

#if !defined(HAVE_SETEUID) && defined(HAVE_SETREUID)
int seteuid(uid_t);
#endif /* !defined(HAVE_SETEUID) && defined(HAVE_SETREUID) */

#if !defined(HAVE_SETEGID) && defined(HAVE_SETRESGID)
int setegid(uid_t);
#endif /* !defined(HAVE_SETEGID) && defined(HAVE_SETRESGID) */

#if !defined(HAVE_STRERROR) && defined(HAVE_SYS_ERRLIST) && defined(HAVE_SYS_NERR)
const char *strerror(int);
#endif

#if !defined(HAVE_SETLINEBUF)
#define setlinebuf(a) (setvbuf((a), NULL, _IOLBF, 0))
#endif

#ifndef HAVE_UTIMES
#ifndef HAVE_STRUCT_TIMEVAL
struct timeval {
 long tv_sec;
 long tv_usec;
}
#endif /* HAVE_STRUCT_TIMEVAL */
```

```
int utimes(char *, struct timeval *);
#endif /* HAVE_UTIMES */

#ifndef AT_FDCWD
#
 define AT_FDCWD (-2)
#endif

#ifndef HAVE_FCHMODAT
int fchmodat(int, const char *, mode_t, int);
#endif

#ifndef HAVE_FCHOWNAT
int fchownat(int, const char *, uid_t, gid_t, int);
#endif

#ifndef HAVE_TRUNCATE
int truncate (const char *, off_t);
#endif /* HAVE_TRUNCATE */

#ifndef HAVE_STRUCT_TIMESPEC
struct timespec {
 time_t tv_sec;
 long tv_nsec;
};
#endif /* !HAVE_STRUCT_TIMESPEC */

#if !defined(HAVE_NANOSLEEP) && !defined(HAVE_NSLEEP)
# include <time.h>
int nanosleep(const struct timespec *, struct timespec *);
#endif

#ifndef HAVE_UTIMENSAT
# include <time.h>
/* start with the high bits and work down to minimise risk of overlap */
# ifndef AT_SYMLINK_NOFOLLOW
#  define AT_SYMLINK_NOFOLLOW 0x80000000
# endif
int utimensat(int, const char *, const struct timespec[2], int);
#endif /* !HAVE_UTIMENSAT */

#ifndef HAVE_USLEEP
int usleep(unsigned int useconds);
#endif

#ifndef HAVE_TCGETPGRP
pid_t tcgetpgrp(int);
#endif
```

```
#ifndef HAVE_TCSENDBREAK
int tcsendbreak(int, int);
#endif

#ifndef
 HAVE_UNSETENV
int unsetenv(const char *);
#endif

#ifndef HAVE_ISBLANK
int isblank(int);
#endif

#ifndef HAVE_GETPGID
pid_t getpgid(pid_t);
#endif

#ifndef HAVE_PSELECT
int pselect(int, fd_set *, fd_set *, fd_set *, const struct timespec *,
   const sigset_t *);
#endif

#ifndef HAVE_ENDGRENT
# define endgrent() do { } while(0)
#endif

#ifndef HAVE_KRB5_GET_ERROR_MESSAGE
# define krb5_get_error_message krb5_get_err_text
#endif

#ifndef HAVE_KRB5_FREE_ERROR_MESSAGE
# define krb5_free_error_message(a,b) do { } while(0)
#endif

#ifndef HAVE_PLEDGE
int pledge(const char *promises, const char *paths[]);
#endif

/* bsd-err.h */
#ifndef HAVE_ERR
void err(int, const char *, ...) __attribute__((format(printf, 2, 3)));
#endif
#ifndef HAVE_ERRX
void errx(int, const char *, ...) __attribute__((format(printf, 2, 3)));
#endif
#ifndef HAVE_WARN
void warn(const char *, ...) __attribute__((format(printf, 1, 2)));
```

```
#endif

#ifndef HAVE_LLABS
long long llabs(long long);
#endif

#if defined(HAVE_DECL_BZERO)
 && HAVE_DECL_BZERO == 0
void bzero(void *, size_t);
#endif

#ifndef HAVE_RAISE
int raise(int);
#endif

#ifndef HAVE_GETSID
pid_t getsid(pid_t);
#endif

#ifndef HAVE_FLOCK
# define LOCK_SH  0x01
# define LOCK_EX  0x02
# define LOCK_NB  0x04
# define LOCK_UN  0x08
int flock(int, int);
#endif

#ifdef FFLUSH_NULL_BUG
# define fflush(x) (_ssh_compat_fflush(x))
#endif

#ifndef HAVE_LOCALTIME_R
struct tm *localtime_r(const time_t *, struct tm *);
#endif

#ifndef HAVE_REALPATH
#define realpath(x, y) (sftp_realpath((x), (y)))
#endif

#endif /* _BSD_MISC_H */
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*/

#ifndef _BSD_SIGNAL_H
#define _BSD_SIGNAL_H

#include "includes.h"

#include <signal.h>

#ifndef _NSIG
# ifdef NSIG
#  define _NSIG NSIG
# else
#  define _NSIG 128
# endif
#endif

#if !defined(HAVE_STRSIGNAL)
char
 *strsignal(int);
#endif

#endif /* _BSD_SIGNAL_H */
/* $Id: bsd-cray.h,v 1.12 2005/02/02 06:10:11 dtucker Exp $ */

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*
* Created: Apr 22 16.34:00 2002 wp
*
* This file contains functions required for proper execution
* on UNICOS systems.
*
*/

#ifndef _BSD_CRAY_H
#define _BSD_CRAY_H

#ifdef _UNICOS

void cray_init_job(struct passwd *);
void cray_job_termination_handler(int);
void cray_login_failure(char *, int );
int cray_access_denied(char *);
extern char cray_tmpdir[];

#define CUSTOM_FAILED_LOGIN 1

#ifndef IA_SSHD
# define IA_SSHD IA_LOGIN
#endif
#ifndef MAXHOSTNAMELEN
#
 define MAXHOSTNAMELEN  64
#endif
#ifndef _CRAYT3E
# define TIOCGPGRP (tIOC|20)
#endif

#endif /* UNICOS */

#endif /* _BSD_CRAY_H */
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*
*/

#ifndef _NEXT_POSIX_H
#define _NEXT_POSIX_H

#ifdef HAVE_NEXT
#include <sys/dir.h>

/* NGROUPS_MAX is behind -lposix.  Use the BSD version which is NGROUPS */
#undef NGROUPS_MAX
#define NGROUPS_MAX NGROUPS

/* NeXT's readdir() is BSD (struct direct) not POSIX (struct dirent) */
#define dirent direct

/* Swap out NeXT's BSD wait() for a more POSIX compliant one */
pid_t posix_wait(int *);
#define wait(a) posix_wait(a)

/* #ifdef wrapped functions that need defining for clean compiling */
pid_t getppid(void);
void vhangup(void);
int innetgr(const char *, const char *, const char *, const char *);

/* TERMCAP */
int tcgetattr(int, struct termios *);
int tcsetattr(int, int,
```

```
 const struct termios *);
int tcsetpgrp(int, pid_t);
speed_t cfgetospeed(const struct termios *);
speed_t cfgetispeed(const struct termios *);
int cfsetospeed(struct termios *, int);
int cfsetispeed(struct termios *, int);
#endif /* HAVE_NEXT */
#endif /* _NEXT_POSIX_H */
* Foremost, OpenSSH from which this project is derived.
* Jamie Beverly
* Rafael D'Halleweyn - 2011-06-05 18:56:24 EDT
* Jan-Pieter Cornet ( johnpc ) - 2012-03-23 03:25:52 PDT
* chrysn@fsfe.org
* dfberger@users.noreply.github.com
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*
*/

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potential issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w) (*(int*)&(w)) /* convert union wait to int */
#define WIFEXITED(w) (!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w) ((_W_INT(w)) & 0100)
#define WIFSIGNALED(w) (!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w) (int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1)
#define WTERMSIG(w) (int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1)
#define WCOREFLAG 0x80
#define WCOREDUMP(w) ((_W_INT(w)) & WCOREFLAG)

/* Prototype */
pid_t waitpid(int,
 int *, int);

#endif /* !HAVE_WAITPID */
#endif /* _BSD_WAITPID_H */
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*
* Created: Sat Sep 02 12:17:00 2000 cv
*
* This file contains functions for forcing opened file descriptors to
* binary mode on Windows systems.
*/

#ifndef _BSD_CYGWIN_UTIL_H
#define _BSD_CYGWIN_UTIL_H

#ifdef HAVE_CYGWIN

#undef ERROR

/* Avoid including windows headers. */
typedef void *HANDLE;
#define INVALID_HANDLE_VALUE ((HANDLE) -1)
#define DNLEN 16
#define UNLEN 256

/* Cygwin functions for which declarations are only available when including
  windows headers, so we have to define them here explicitly. */
extern HANDLE cygwin_logon_user (const struct passwd *, const char *);
extern void cygwin_set_impersonation_token (const HANDLE);

#include
 <sys/cygwin.h>
#include <io.h>

#define CYGWIN_SSH_PRIVSEP_USER (cygwin_ssh_privsep_user())
const char *cygwin_ssh_privsep_user();

int binary_open(const char *, int , ...);
```

```
int check_ntsec(const char *);
char **fetch_windows_environment(void);
void free_windows_environment(char **);
int cygwin_ug_match_pattern_list(const char *, const char *);

#ifndef NO_BINARY_OPEN
#define open binary_open
#endif

#endif /* HAVE_CYGWIN */

#endif /* _BSD_CYGWIN_UTIL_H */
```

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#ifndef _BSD_MISC_H
#define _BSD_MISC_H
```

```
#include "includes.h"

char *ssh_get_progname(char *);

#ifndef HAVE_SETSID
#define setsid() setpgrp(0,
 getpid())
#endif /* !HAVE_SETSID */

#ifndef HAVE_SETENV
int setenv(const char *, const char *, int);
#endif /* !HAVE_SETENV */

#ifndef HAVE_SETLOGIN
int setlogin(const char *);
#endif /* !HAVE_SETLOGIN */

#ifndef HAVE_INNETGR
int innetgr(const char *, const char *, const char *, const char *);
#endif /* HAVE_INNETGR */

#if !defined(HAVE_SETEUID) && defined(HAVE_SETREUID)
int seteuid(uid_t);
#endif /* !defined(HAVE_SETEUID) && defined(HAVE_SETREUID) */

#if !defined(HAVE_SETEGID) && defined(HAVE_SETRESGID)
int setegid(uid_t);
#endif /* !defined(HAVE_SETEGID) && defined(HAVE_SETRESGID) */

#if !defined(HAVE_STRERROR) && defined(HAVE_SYS_ERRLIST) && defined(HAVE_SYS_NERR)
const char *strerror(int);
#endif


#ifndef HAVE_UTIMES
#ifndef HAVE_STRUCT_TIMEVAL
struct timeval {
 long tv_sec;
 long tv_usec;
}
#endif /* HAVE_STRUCT_TIMEVAL */

int utimes(char *, struct timeval *);
#endif /* HAVE_UTIMES */

#ifndef HAVE_TRUNCATE
int truncate (const char *, off_t);
```

```
#endif /* HAVE_TRUNCATE */

#if
 !defined(HAVE_NANOSLEEP) && !defined(HAVE_NSLEEP)
#ifndef HAVE_STRUCT_TIMESPEC
struct timespec {
 time_t tv_sec;
 long tv_nsec;
};
#endif
int nanosleep(const struct timespec *, struct timespec *);
#endif

#ifndef HAVE_TCGETPGRP
pid_t tcgetpgrp(int);
#endif

#ifndef HAVE_TCSENDBREAK
int tcsendbreak(int, int);
#endif

#ifndef HAVE_UNSETENV
void unsetenv(const char *);
#endif

/* wrapper for signal interface */
typedef void (*mysig_t)(int);
mysig_t mysignal(int sig, mysig_t act);

#define signal(a,b) mysignal(a,b)

#endif /* _BSD_MISC_H */
```

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*/

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potental issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w) (*(int*)&(w)) /* convert union wait to int */
#define WIFEXITED(w) (!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w) ((_W_INT(w)) & 0100)
#define WIFSIGNALED(w) (!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w) (int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1)
#define WTERMSIG(w) (int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1)
#define WCOREFLAG 0x80
#define WCOREDUMP(w)
  ((_W_INT(w)) & WCOREFLAG)

/* Prototype */
pid_t waitpid(int, int *, int);

#endif /* !HAVE_WAITPID */
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#ifndef HAVE_SETRESGID
int setresgid(gid_t, gid_t, gid_t);
#endif
#ifndef HAVE_SETRESUID
int setresuid(uid_t, uid_t, uid_t);
#endif
/* $Id: bsd-nextstep.h,v 1.9 2003/08/29 16:59:52 mouring Exp $ */

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#define _NEXT_POSIX_H

#ifdef HAVE_NEXT
#include <sys/dir.h>

/* NGROUPS_MAX is behind -lposix.  Use the BSD version which is NGROUPS */
#undef NGROUPS_MAX
#define NGROUPS_MAX NGROUPS

/* NeXT's readdir() is BSD (struct direct) not POSIX (struct dirent) */
#define dirent direct

/* Swap out NeXT's BSD wait() for a more POSIX complient one */
pid_t posix_wait(int *);
#define wait(a) posix_wait(a)

/* #ifdef wrapped functions that need defining for clean compiling */
pid_t getppid(void);
void vhangup(void);
int innetgr(const char *, const char *, const char *, const char *);

/* TERMCAP
 */
int tcgetattr(int, struct termios *);
int tcsetattr(int, int, const struct termios *);
int tcsetpgrp(int, pid_t);
speed_t cfgetospeed(const struct termios *);
speed_t cfgetispeed(const struct termios *);
int cfsetospeed(struct termios *, int);
int cfsetispeed(struct termios *, int);
#endif /* HAVE_NEXT */
#endif /* _NEXT_POSIX_H */
```

1.197 httpcomponents-core 4.3

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1.199 erb 2.2.3

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1.200 vault-sdk 0.4.1

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layout: docs
page_title: Frequently Asked Questions (FAQ)
description: An overview of license.

License FAQ

This FAQ section is for license changes and updates introduced for Vault Enterprise.

- [Q: How do the license termination changes affect upgrades?](#q-how-do-the-license-termination-changes-affect-upgrades)
- [Q: What impact on upgrades do the license termination behavior changes pose?](#q-what-impact-on-upgrades-do-the-license-termination-behavior-changes-pose)
- [Q: Will these license changes impact HCP Vault?](#q-will-these-license-changes-impact-hcp-vault)
- [Q: Do these license changes impact all Vault customers/licenses?](#q-do-these-license-changes-impact-all-vault-customers-licenses)
- [Q: What is the product behavior change introduced by the licensing changes?](#q-what-is-the-product-behavior-change-introduced-by-the-licensing-changes)
- [Q: How will Vault behave at startup when a license expires or terminates?](#q-how-will-vault-behave-at-startup-when-a-license-expires-or-terminates)
-
[Q: What is the impact on evaluation licenses due to this change?](#q-what-is-the-impact-on-evaluation-licenses-due-to-this-change)
- [Q: Are there any changes to existing methods for manual license loading (API or CLI)?](#q-are-there-any-changes-to-existing-methods-for-manual-license-loading-api-or-cli)
- [Q: Is there a grace period when evaluation licenses expire?](#q-is-there-a-grace-period-when-evaluation-licenses-expire)
- [Q: Does this affect clients?](#q-does-this-affect-clients)
- [Q: Are the license files locked to a specific cluster?](#q-are-the-license-files-locked-to-a-specific-cluster)
- [Q: Will a EULA check happen every time a Vault restarts?](#q-will-a-eula-check-happen-every-time-a-vault-restarts)
- [Q: What scenarios should a customer plan for due to these license changes?](#q-what-scenarios-should-a-customer-plan-for-due-to-these-license-changes)
- [Q: What is the migration path for customers who want to migrate from their existing license-as-applied-via-the-CLI
flow to the license on disk flow?](#q-what-is-the-migration-path-for-customers-who-want-to-migrate-from-their-existing-license-as-applied-via-the-cli-flow-to-the-license-on-disk-flow)
- [Q: What is the path for customers who want to downgrade/rollback from Vault 1.11 or later (auto-loaded license mandatory) to a pre-Vault 1.11 (auto-loading not mandatory, stored license supported)?](#q-what-is-the-path-for-customers-who-want-to-downgrade-rollback-from-vault-1-11-or-later-auto-loaded-license-mandatory-to-a-pre-vault-1-11-auto-loading-not-mandatory-stored-license-supported)
- [Q: Is there a limited time for support of licenses that are in storage?](#q-is-there-a-limited-time-for-support-of-licenses-that-are-in-storage)
- [Q: What are the steps to upgrade from one autoloaded license to another autoloaded license?](#q-what-are-the-steps-to-upgrade-from-one-autoloaded-license-to-another-autoloaded-license)
- [Q: What are the Vault ADP module licensing changes introduced
in 1.8?](#q-what-are-the-vault-adp-module-licensing-changes-introduced-in-1-8)
- [Q: How can the new ADP modules be purchased and what features are customer entitled to as part of that purchase?](#q-how-can-the-new-adp-modules-be-purchased-and-what-features-are-customer-entitled-to-as-part-of-that-purchase)
- [Q: What is the impact to customers based on these ADP module licensing changes?](#q-what-is-the-impact-to-customers-based-on-these-adp-module-licensing-changes)

Q: What is the product behavior change introduced by the licensing changes?

Per the [feature deprecation plans](/docs/deprecation), Vault will no longer support licenses in storage. An [auto-loaded license](/docs/enterprise/license/autoloading) must be used instead. If you are using stored licenses, you must

migrate to auto-loaded licenses prior to upgrading to Vault 1.11

Vault 1.12 will also introduce different termination behavior for evaluation licenses versus non-evaluation licenses. An [evaluation](https://www.hashicorp.com/products/vault/trial)
 license will include a 30-day trial period after which a running Vault server will terminate. Vault servers using a non-evaluation license will not terminate.

Q: How do the license termination changes affect upgrades?
Vault 1.12 will introduce changes to the license termination behavior. Upgrades when using expired licenses will now be limited.
Vault will not startup if the build date of the binary is _after_ the expiration date of a license. License expiration date and binary build date compatibility can be verified using the [Check for Autoloaded License](/docs/commands/operator/diagnose#check-for-autoloaded-license) check performed by the `vault operator diagnose` command.
The build date of a binary can also be found using the [vault version](/docs/commands/version#version) command.

A user can expect the following to occur based on the following scenarios:

Evaluation or non-evaluation license is valid:
Vault will start normally

**Evaluation
 or non-evaluation license is expired, binary build date _before_ license expiry date:**
Vault will start normally

Evaluation or non-evaluation license is expired, binary build date _after_ license expiry date:
Vault will not start

Evaluation license is terminated:
Vault will not start independent of the binarys build date

Non-evaluation license is terminated, binary build date _before_ license expiry date:
Vault will start normally

Non-evaluation license is terminated, binary build date _after_ license expiry date:
Vault will not start

The Vault support team can issue you a temporary evaluation license to allow for security upgrades if your license has expired.

Q: Will these license changes impact HCP Vault?

No, these changes will not impact HCP Vault.

Q: Do these license changes impact all Vault customers/licenses?

| Customer/licenses | Impacted?

|
| --- | --- |
| ENT binaries (evaluation or non-evaluation downloaded from [releases.hashicorp.com](https://releases.hashicorp.com/vault/)) | Yes |
| Open-Source (OSS) | No |

Q: What is the product behavior change introduced by the licensing changes?

With Vault 1.11, the use of an [auto-loaded license](/docs/enterprise/license/autoloading) is required for Vault to start successfully.

Q: How will Vault behave at startup when a license expires or terminates?

When a license expires, Vault continues to function until the license terminates. This behavior exists today and remains unchanged in Vault 1.11. The grace period, defined as the time between license expiration and license termination, is one day for evaluation licenses
(as of 1.8), and ten years for non-evaluation licenses.
Customers must provide a valid license before the grace period expires. This license is required to be [auto-loaded](/docs/enterprise/license/autoloading). When license terminates (upon grace period expiry), Vault will seal itself and customers will need a valid license in order to successfully bring-up Vault. If a valid license was not installed after license expiry, customers will need to provide one, and this license will need to be auto-loaded.

Vault 1.12 changes the license expiration and termination behavior. Evaluation licenses include a 30-day trial period after which a running Vault server will terminate. Non-evaluation licenses, however, will no longer terminate. When a non-evaluation license expires, Vault will continue to function but upgrades will be limited. The build date of the upgrade binary must be before the expiration date of the license.
Vault will not start when attempting to use an expired license and binary
with a build date _after_ the license expiration date. Attempting to [reload](/api-docs/system/config-reload#reload-license-file) an expired license will result in an error if the build date of the running Vault server is _after_ the license expiration date.

License expiration date and binary build date compatibility can be verified using the [Check for Autoloaded License](/docs/commands/operator/diagnose#check-for-autoloaded-license) check performed by the `vault operator diagnose` command. The build date of a binary can also be found using the [vault version](/docs/commands/version#version) command.

Q: What is the impact on evaluation licenses due to this change?

As of Vault 1.8, any Vault cluster deployed must have a valid [auto-loaded](/docs/enterprise/license/autoloading) license.

Vault 1.12 introduces [expiration and termination behavior changes](#q-how-will-vault-behave-at-startup-when-a-license-expires-or-terminates) for non-evaluation licenses. Evaluation licenses will
continue to have a 1-day grace period upon license expiry after which they will terminate. Vault will seal itself and shutdown once an evaluation license terminates.

Q: Are there any changes to existing methods for manual license loading (API or CLI)?

The [`/sys/license`](/api-docs/v1.10.x/system/license#install-license) and [`/sys/license/signed`](/api-docs/v1.10.x/system/license#read-signed-license) endpoints have been removed as of Vault 1.11. With that said, it is no longer possible to provide a license via the `/sys/license` endpoint. License [auto-loading](/docs/enterprise/license/autoloading) must be used instead.
The [`/sys/config/reload/license`](/api-docs/system/config-reload#reload-license-file) endpoint can be used to reload an auto-loaded license provided as a path via an environment variable or configuration.

Q: Is there a grace period when evaluation licenses expire?

Evaluation licenses have a 1-day grace period. The grace period is the time until the license expires. Upon expiration, Vault will seal and will require a valid license to unseal and function properly.

Q: Are the license files locked to a specific cluster?

The changes to licenses apply to all nodes in a cluster. The license files are not locked to a cluster, but are independently applied to the appropriate clusters.

Q: Will a EULA check happen every time a Vault restarts?

Yes, starting with Vault 1.8, ENT binaries will be subjected to a EULA check. The release of Vault 1.8 introduces the EULA check for evaluation licenses (non-evaluation licenses are evaluated with a EULA check during contractual agreement) .
Although the agreement to a EULA occurs only once (when the user receives their license), Vault will check for the presence of a valid license every time a node is restarted.

Starting Vault 1.11, when customers deploy new Vault clusters, or upgrade existing Vault clusters, a valid [auto-loaded](/docs/enterprise/license/autoloading) license must exist for the upgrade to be successful.

Q: What scenarios should a customer plan for due to these license changes?

- **New Cluster Deployment**: When a customer deploys new clusters to Vault 1.11 or later, a valid license must exist to successfully deploy. This valid license must be on-disk ([auto-loaded](/docs/enterprise/license/autoloading)).

- **Eventual Migration**: Vault 1.11 removes support for in-storage licenses. Migrating to an auto-loaded license is required for Vault to start successfully using version 1.11 or greater. Pre-existing license storage entries will be automatically removed from storage upon startup.

Q: What is the migration path for customers who want to migrate from their existing license-as-applied-via-the-CLI flow to the license on disk flow?

If a Vault cluster using a stored license is planned to be upgraded to Vault 1.11 or greater, the operator must migrate to using an auto-loaded license. The [`vault license get -signed`](/docs/v1.10.x/commands/license/get) command (or underlying [`/sys/license/signed`](/api-docs/v1.10.x/system/license#read-signed-license) endpoint) can be used to retrieve the license from storage in a running cluster.
It is not necessary to remove the stored license entry. That will occur automatically upon startup in Vault 1.11 or greater. Prior to completing the [recommended upgrade steps](/docs/upgrading), perform the following to ensure

your license is properly configured:

1. Use the command `vault license get -signed` to retrieve the license from storage of your running cluster.
2. Put the license on disk
3. Configure license auto-loading by specifying the [`license_path`](/docs/configuration#license_path) config option or setting the [`VAULT_LICENSE`](/docs/commands#vault_license) or [`VAULT_LICENSE_PATH`](o/docs/commands#vault_license_path) environment variable.

Q: What is the path for customers who want to downgrade/rollback from Vault 1.11 or later (auto-loaded license mandatory) to a pre-Vault 1.11 (auto-loading
not mandatory, stored license supported)?

The downgrade procedure remains the same for Vault customers who are currently on Vault 1.11 or later, have a license installed via auto-loading, and would like to downgrade their cluster to a pre-1.11 version. Please refer to the [upgrade procedures](https://learn.hashicorp.com/tutorials/vault/sop-upgrade?in=vault/standard-procedures) that remind the customers that they must take a snapshot before the upgrade. Customers will need to restore their version from the snapshot, apply the pre-1.11 enterprise binary version they wish to roll back, and bring up the clusters.

Q: Is there a limited time for support of licenses that are in storage?

The support of licenses installed by alternative means often leads to difficulties providing the appropriate support. To provide the support expected by our customers, as we have announced in [Vault feature deprecations and plans](/docs/deprecation) we are removing support for licenses in storage
with Vault 1.11. This implies licensing endpoints that deal with licenses in storage will be removed, and Vault will no longer check for valid licenses in storage. This change requires that all customers have [auto-loaded](/docs/enterprise/license/autoloading) licenses to upgrade to 1.11(+) successfully.

Q: What are the steps to upgrade from one autoloaded license to another autoloaded license?

Follow these steps to migrate from one autoloaded license to another autoloaded license.

1. Use the [`vault license inspect`](/docs/commands/license/inspect) command to compare the new license against the output of the [`vault license get`](/docs/commands/license/get) command. This is to ensure that you have the correct license.
1. Backup the old license file in a safe location.
1. Replace the old license file on each Vault server with the new one.
1. Invoke the [reload command](/api-docs/system/config-reload#reload-license-file) on each individual Vault server, starting with the standbys
and doing the leader last. Invoking in this manner reduces possible disruptions if something was performed incorrectly with the above steps. You can either use the [reload command](/api-docs/system/config-reload#reload-license-file) or send a SIGHUP.
1. On each node, ensure that the new license is in use by using the [`vault license get`](/docs/commands/license/get) command and/or checking the logs.

ADP Licensing

This FAQ section is for the Advanced Data Protection (ADP) license changes introduced in Vault Enterprise 1.8.

Q: What are the Vault ADP module licensing changes introduced in 1.8?

As of Vault Enterprise 1.8, the functionality formerly sold as the Vault ADP module is now separated between two new modules:

ADP-KM includes:

- [Key Management Secrets Engine (KMSE)](/docs/secrets/key-management)
- [Key Management Interoperability (KMIP)](/docs/secrets/kmip)
- [MSSQL Transparent Data Encryption (TDE)](https://www.hashicorp.com/blog/enabling-transparent-data-encryption-for-microsoft-sql-with-vault)

ADP-Transform
includes:

- [Transform Secrets Engine (TSE)](/docs/secrets/transform)

Q: How can the new ADP modules be purchased and what features are customer entitled to as part of that purchase?

ADP-KM includes:

- This is the first Vault Enterprise module that can be purchased standalone. This means it can be purchased without the purchase of a Vault Enterprise Standard license.
- ADP-KM still requires a Vault Enterprise binary. The Vault Enterprise Standard license is automatically included with the ADP-KM module, but customers are contractually prohibited from using any features besides those in Vault OSS and ADP-KM (KMSE and KMIP).

ADP-Transform includes:

- This module cannot be purchased as a standalone. It requires a Vault Enterprise binary, and customers must purchase the base Vault Enterprise Standard license (at least) to use the corresponding Enterprise features.
- The ADP-Transform SKU can be applied as an add-on.
This workflow is similar to the consolidated ADP SKU.

Q: What is the impact to customers based on these ADP module licensing changes?

Customers need to be aware of the following as a result of these changes:

- **New customers** may choose to purchase either or both of these modules. The old (consolidated) module is not available to them as an option.
- **Existing customers** may continue with the consolidated Vault ADP module uninterrupted. They will only be converted to one or both new ADP modules the next time they make a change to their licensing details (i.e. contract change).
Enterprise Licenses

In versions 1.2.6, 1.3.9, 1.4.5, and 1.5.2, the enterprise licenses were not incorporated correctly
into the build and we have issued patch releases (x.y.z.1) for enterprise customers containing the

proper license. As the previous builds were both not working and causing confusion, we have removed the binaries.
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```
{{#if this.licenseExpired}}
 <div class="license-banner-wrapper" data-test-license-banner data-test-license-banner-expired>
  <AlertBanner
   @bannerType="license"
```

```
    @type="danger"
    @title="License expired"
    @message="Your Vault license expired on {{date-format
      @expiry
      'MMM d, yyyy'
    }}. Add a new license to your configuration and restart Vault."
    @marginless={{true}}
  >
    <a
      href="https://learn.hashicorp.com/tutorials/nomad/hashicorp-enterprise-license"
      target="_blank"
      rel="noreferrer noopener"
    >
      Read documentation
    </a>
  </AlertBanner>
 </div>
{{else if (lte this.licenseExpiringInDays 30)}}
 <div class="license-banner-wrapper" data-test-license-banner data-test-license-banner-warning>
  <AlertBanner
    @bannerType="license"
    @type="warning"
    @title="Vault license expiring"
    @message="Your Vault license will expire in {{this.licenseExpiringInDays}} days at {{date-format
       @expiry
      'hh:mm:ss a'
    }} on {{date-format @expiry 'MMM d, yyyy'}}. {{if
      @autoloaded
      'Add a new license to your configuration.'
      'Keep in mind that your next license will need to be autoloaded'
    }}"
    @marginless={{true}}
  >
    <a
      href="https://learn.hashicorp.com/tutorials/nomad/hashicorp-enterprise-license"
      target="_blank"
      rel="noreferrer noopener"
    >
      Read documentation
    </a>
  </AlertBanner>
 </div>
{{/if}}
---
layout: docs
page_title: License Autoloading
description: An overview of license autoloading.
---
```

License Autoloading

Prior to Vault 1.8, Vault Enterprise would be licensed using special binaries
that contained embedded licenses, or via a license written into Vault storage
using the [POST sys/license API](/api-docs/system/license#install-license).

As of Vault 1.8 those options still exist but are deprecated, and the recommended
mechanism for managing licenses is called License Autoloading. New clusters are
required to use the autoloading method.

License Autoloading can be done using one of these mechanisms, in decreasing order
of priority (i.e. `VAULT_LICENSE` takes precedence over `VAULT_LICENSE_PATH`, which
takes precedence over `license_path` in config.)

- [VAULT_LICENSE environment variable](/docs/commands#vault_license)
- [VAULT_LICENSE_PATH environment variable](/docs/commands#vault_license_path)
- [license_path in config](/docs/configuration#license_path)

Nodes within
 a cluster should be consistently licensed. All nodes in a cluster
should use autoloading, or none should. If autoloading is used, all nodes should
use the same license string. Warnings will be written to the log when there
are discrepancies.

If autoloading is used, any existing stored license will be ignored. The presence
of a stored license in conjunction with an autoloaded license will also result in
logged warnings, which may require a service restart to clear.
---
layout: docs
page_title: license - Command
description: |-
 The "license" command groups subcommands for interacting with Vault licenses.
---

license

The `license` command groups subcommands for interacting with Vault licenses

For more information, please see the [license documentation](/docs/enterprise/license)

Examples

Query current license in use by a node:

```shell-session
$ vault license get
Key                     Value
```

```
---                     -----
expiration_time         2022-05-17T23:59:59.999Z
features                [HSM Performance Replication DR Replication MFA Sentinel Seal Wrapping Control Groups
Performance Standby Namespaces KMIP Entropy Augmentation Transform Secrets Engine Lease Count Quotas
Key Management Secrets Engine Automated Snapshots]
license_id              060d7820-fa59-f95c-832b-395db0aeb9ba
performance_standby_count    9999
start_time              2021-05-17T00:00:00Z
```

Query signed license string (only when
 using a deprecated stored license)

```shell-session
$ vault license get -signed
02MV4UU43BK5H...
```

Inspect a signed a license string:

```shell-session
$ vault license inspect ~/vaults/license
Source: /Users/ncc/vaults/license
Product: vault
License ID: 060d7820-fa59-f95c-832b-395db0aeb9ba
Customer ID: 5401ae3b-136b-3e47-3b43-432927fa03e7
Installation ID: *
Issue Time: 2021-05-17 19:33:47.93981585 +0000 UTC
Start Time: 2021-05-17 00:00:00 +0000 UTC
Expiration Time: 2022-05-17 23:59:59.999 +0000 UTC
{"license_id":"060d7820-fa59-f95c-832b-395db0aeb9ba","customer_id":"5401ae3b-136b-3e47-3b43-
432927fa03e7","installation_id":"*","issue_time":"2021-05-17T19:33:47.93981585Z","start_time":"2021-05-
17T00:00:00Z","expiration_time":"2022-05-17T23:59:59.999Z","flags":{"modules":["multi-dc-scale","governance-
policy","advanced-data-protection"]},"features":["HSM","Performance Replication","DR
Replication","MFA","Sentinel","Seal Wrapping","Control Groups","Performance
Standby","Namespaces","KMIP","Entropy
 Augmentation","Transform Secrets Engine","Lease Count Quotas","Key Management Secrets Engine","Automated
Snapshots"],"performance_standby_count":9999}

License is valid

```

Usage

```text
Usage: vault license <subcommand> [options] [args]
```

```
 # ...

Subcommands:
   get      Query Vault's license
   inspect  View the contents of a license string
```

For more information, examples, and usage about a subcommand, click on the name
of the subcommand in the sidebar.

```
<LicenseInfo
 @startTime={{this.model.startTime}}
 @expirationTime={{this.model.expirationTime}}
 @licenseId={{this.model.licenseId}}
 @features={{this.model.features}}
 @autoloaded={{this.model.autoloaded}}
 @performanceStandbyCount={{this.model.performanceStandbyCount}}
/>
variable "file_name" {}

output "license" {
 value = file(var.file_name)
}
---
layout: docs
page_title: license inspect - Command
description: |-
 The "license inspect" command describes the contents of a signed license string.
---

# license inspect

The `license inspect` command describes the contents of a signed license string.

## Examples

Describe license in the environment (either `VAULT_LICENSE` for a raw value
or `VAULT_LICENSE_PATH` for the path to a file containing the value):

```shell-session
$ vault license inspect
Source: /Users/ncc/vaults/license
Product: vault
License ID: 060d7820-fa59-f95c-832b-395db0aeb9ba
Customer ID: 5401ae3b-136b-3e47-3b43-432927fa03e7
Installation ID: *
Issue Time: 2021-05-17 19:33:47.93981585 +0000 UTC
Start Time: 2021-05-17 00:00:00 +0000 UTC
Expiration Time: 2022-05-17 23:59:59.999 +0000 UTC
```
```

Termination Time: 2031-05-17 23:59:59.999 +0000 UTC
{"license_id":"060d7820-fa59-f95c-832b-395db0aeb9ba","customer_id":"5401ae3b-136b-3e47-3b43-432927fa03e7","installation_id":"*","issue_time":"2021-05-17T19:33:47.93981585Z","start_time":"2021-05-17T00:00:00Z","expiration_time":"2022-05-17T23:59:59.999Z","flags":{"modules":["multi-dc-scale","governance-policy","advanced-data-protection"]},"features":["HSM","Performance Replication","DR Replication","MFA","Sentinel","Seal Wrapping","Control Groups","Performance Standby","Namespaces","KMIP","Entropy Augmentation","Transform Secrets Engine","Lease Count Quotas","Key Management Secrets Engine","Automated Snapshots"],"performance_standby_count":9999}

License is valid
```

Describe license at given path:

```shell-session
$ vault license inspect ~/vaults/license
Source: /Users/ncc/vaults/license
Product: vault
License ID: 060d7820-fa59-f95c-832b-395db0aeb9ba
Customer ID: 5401ae3b-136b-3e47-3b43-432927fa03e7
Installation ID: *
Issue Time: 2021-05-17 19:33:47.93981585 +0000 UTC
Start Time: 2021-05-17 00:00:00 +0000 UTC
Expiration Time: 2022-05-17 23:59:59.999 +0000 UTC
Termination Time: 2031-05-17 23:59:59.999 +0000 UTC
{"license_id":"060d7820-fa59-f95c-832b-395db0aeb9ba","customer_id":"5401ae3b-136b-3e47-3b43-432927fa03e7","installation_id":"*","issue_time":"2021-05-17T19:33:47.93981585Z","start_time":"2021-05-17T00:00:00Z","expiration_time":"2022-05-17T23:59:59.999Z","flags":{"modules":["multi-dc-scale","governance-policy","advanced-data-protection"]},"features":["HSM","Performance Replication","DR Replication","MFA","Sentinel","Seal Wrapping","Control Groups","Performance Standby","Namespaces","KMIP","Entropy Augmentation","Transform Secrets Engine","Lease Count Quotas","Key Management Secrets Engine","Automated Snapshots"],"performance_standby_count":9999}

License is valid
```

Usage

The following flags are available in addition to the [standard set of
flags](/docs/commands) included on all commands.

Command Options

- `-installation-id` `(string: "")` - Installation ID to validate the license against
---
layout: api
page_title: /sys/license/status - HTTP API
description: |-
```

The `/sys/license/status` endpoint is used to view the license used in
Vault.

`/sys/license/status`

~> **Enterprise Only** These endpoints require Vault Enterprise.

The `/sys/license/status` endpoint is used to view update the license used in
Vault.

License Status

This endpoint returns information about licensing. See [license autoloading](/docs/enterprise/license/autoloading)
for additional background.

In the response:

- `autoloading_used` will be true if an autoloaded license was provided to the
 node, false otherwise. This should always be true from Vault 1.11.0 onward.
- `autoloaded` is the autoloaded license if autoloading_used is true.
- `persisted_autoload` is the autoloaded license the active node last wrote to
 storage; this is only used to detect inconsistently licensed nodes in the
 cluster. It cannot be used as a license itself.
- `stored` was used for the stored license prior
 to Vault 1.11.0. Stored licenses are no longer supported.

`autoloaded`, and `persisted_autoload` will both have the same structure.

Method	Path
`GET`	`/sys/license/status`

Sample Request

```shell-session
$ curl \
  --header "X-Vault-Token: ..." \
  http://127.0.0.1:8200/v1/sys/license/status
```

Sample Response

```json
{
 "data": {
   "autoloading_used": true,
   "autoloaded": {
```

```
  "expiration_time": "2022-05-17T23:59:59Z",
  "features": [
    "HSM",
    "Performance Replication",
    "DR Replication",
    "MFA",
    "Sentinel",
    "Seal Wrapping",
    "Control Groups",
    "Performance Standby",
    "Namespaces",
    "KMIP",
    "Entropy Augmentation",
    "Transform Secrets Engine",
    "Lease Count Quotas",
    "Key Management Secrets Engine",
    "Automated Snapshots"
  ],
  "license_id": "060d7820-fa59-f95c-832b-395db0aeb9ba",
   "performance_standby_count": 9999,
  "start_time": "2021-05-17T00:00:00Z",
  "termination_time": "2031-05-17T23:59:59Z"
 },
 "persisted_autoload": {
  "expiration_time": "2022-05-17T23:59:59Z",
  "features": [
    "HSM",
    "Performance Replication",
    "DR Replication",
    "MFA",
    "Sentinel",
    "Seal Wrapping",
    "Control Groups",
    "Performance Standby",
    "Namespaces",
    "KMIP",
    "Entropy Augmentation",
    "Transform Secrets Engine",
    "Lease Count Quotas",
    "Key Management Secrets Engine",
    "Automated Snapshots"
  ],
  "license_id": "060d7820-fa59-f95c-832b-395db0aeb9ba",
  "performance_standby_count": 9999,
  "start_time": "2021-05-17T00:00:00Z",
  "termination_time": "2031-05-17T23:59:59Z"
 }
}
```

```
}
```
```
<PageHeader as |p|>
 <p.levelLeft>
  <h1 class="title is-3">License</h1>
 </p.levelLeft>
</PageHeader>

<section class="box is-sideless is-marginless is-shadowless is-fullwidth">
 <span class="title is-5">Details</span>
 <div class="field box is-fullwidth is-shadowless is-paddingless is-marginless">
  <InfoTableRow @label="License ID" @value={{@licenseId}} data-test-detail-row />
  <InfoTableRow @label="Valid from" @value={{@startTime}} data-test-detail-row>
   {{date-format @startTime "MMM dd, yyyy hh:mm:ss a"}}
   to
   {{date-format @expirationTime "MMM dd, yyyy hh:mm:ss a"}}
  </InfoTableRow>
  <InfoTableRow @label="License state" @value={{"Autoloaded"}} data-test-detail-row />
 </div>
</section>

<section class="box is-sideless is-marginless is-shadowless is-fullwidth">
 <span class="title is-5">Features</span>
 <div class="field box is-fullwidth is-shadowless is-paddingless is-marginless">
  {{#each this.featuresInfo as |info|}}
   <InfoTableRow @label={{info.name}}
 @value={{if info.active "Active" "Not Active"}} data-test-feature-row>
    {{#if info.active}}
     <Icon @name="check-circle" class="icon-true" />
     <span data-test-feature-status>
      Active
      {{#if info.count}}&mdash; {{info.count}} standby nodes allotted{{/if}}
     </span>
    {{else}}
     <Icon @name="x-circle" class="icon-false" />
     <span data-test-feature-status>Not Active</span>
    {{/if}}
   </InfoTableRow>
  {{/each}}
 </div>
</section>
---
layout: docs
page_title: license get - Command
description: |-
 The "license get" command reports on the license in use.
---
```

license get

The `license get` command reports on the license in use. With the `-signed`
flag, it will return the actual license string, but only if the license
is stored, as opposed to [autoloaded](/docs/enterprise/license/autoloading).

Examples

Describe current license:

```shell-session
$ vault license get
Key                     Value
---                     -----
expiration_time         2022-05-17T23:59:59Z
features                [HSM Performance Replication DR Replication MFA Sentinel Seal Wrapping Control Groups Performance Standby Namespaces KMIP Entropy Augmentation Transform Secrets Engine Lease Count Quotas Key Management Secrets Engine Automated Snapshots]
license_id              060d7820-fa59-f95c-832b-395db0aeb9ba
performance_standby_count    9999
start_time              2021-05-17T00:00:00Z
termination_time
        2031-05-17T23:59:59Z
```

Get stored license string:

```shell-session
$ vault license get -signed
02MV4UU43BK5H...
```

Try to get stored license string when using autoloading:

```shell-session
$ vault license get -signed
License not found or using a temporary license.
```

Usage

The following flags are available in addition to the [standard set of
flags](/docs/commands) included on all commands.

Command Options

- `-signed` `(bool: false)` - Return the signed license string when using a
stored license.

---
layout: docs
page_title: Vault Enterprise License
description: An overview of license.

Vault License

Licenses and EULA enhancements have been introduced in Vault 1.8 release. Please refer to the [FAQ](/docs/enterprise/license/faq) for common questions concerning these changes.

The [Install a HashiCorp Enterprise License](https://learn.hashicorp.com/tutorials/nomad/hashicorp-enterprise-license?in=vault/enterprise) tutorial provides the instruction to load your Vault license.

```
.license-banner-wrapper {
 width: 100%;
 max-width: 1344px;
 margin: $spacing-l auto 0;
 padding: 0 1.5rem;
}
```

```
package license

// Features is a bitmask of feature flags
type Features uint

const FeatureNone Features = 0

func (f Features) HasFeature(flag Features) bool {
 return false
}
```

1.201 delegate 0.2.0

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1.202 gopsutil 3.23.9

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1.203 mxj 2.5.5

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1.204 axis-saaj 1.4

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1.205 util-linux 2.37.4-21.el9

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1 32 7679 7648 3.7M 8f8378c0-01
2 7680 16383 8704 4.3M 8f8378c0-02
5 7936 12799 4864 2.4M
6 12544 16127 3584 1.8M

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```
#ifndef UTIL_LINUX_PT_BSD_H
#define UTIL_LINUX_PT_BSD_H

#define BSD_MAXPARTITIONS 16
#define BSD_FS_UNUSED  0

#ifndef BSD_DISKMAGIC
# define BSD_DISKMAGIC     ((uint32_t) 0x82564557)
#endif

#define BSD_LINUX_BOOTDIR "/usr/ucb/mdec"

#if defined (__alpha__) || defined (__powerpc__) || \
   defined (__ia64__) || defined (__hppa__)
# define BSD_LABELSECTOR   0
# define BSD_LABELOFFSET   64
#else
# define BSD_LABELSECTOR   1
# define BSD_LABELOFFSET   0
#endif

#define BSD_BBSIZE        8192  /* size of boot area, with label */
#define BSD_SBSIZE        8192  /* max size of fs superblock */

struct bsd_disklabel {
 uint32_t d_magic;  /* the magic number */
 int16_t  d_type;   /* drive type */
 int16_t  d_subtype;  /* controller/d_type specific */
 char  d_typename[16];  /* type name, e.g. "eagle" */
 char  d_packname[16];  /* pack identifier */

   /* disk geometry: */
 uint32_t d_secsize;  /* # of bytes per sector */
 uint32_t d_nsectors;  /* # of data sectors per track */
 uint32_t d_ntracks;  /*
 # of tracks per cylinder */
 uint32_t d_ncylinders;  /* # of data cylinders per unit */
 uint32_t d_secpercyl;  /* # of data sectors per cylinder */
 uint32_t d_secperunit;  /* # of data sectors per unit */

 /*
  * Spares (bad sector replacements) below
```

```
 * are not counted in d_nsectors or d_secpercyl.
 * Spare sectors are assumed to be physical sectors
 * which occupy space at the end of each track and/or cylinder.
 */
uint16_t d_sparespertrack; /* # of spare sectors per track */
uint16_t d_sparespercyl;  /* # of spare sectors per cylinder */

/*
 * Alternate cylinders include maintenance, replacement,
 * configuration description areas, etc.
 */
uint32_t d_acylinders;  /* # of alt. cylinders per unit */

  /* hardware characteristics: */
/*
 * d_interleave, d_trackskew and d_cylskew describe perturbations
 * in the media format used to compensate for a slow controller.
 * Interleave is physical sector interleave, set up by the formatter
 * or controller
when formatting.  When interleaving is in use,
 * logically adjacent sectors are not physically contiguous,
 * but instead are separated by some number of sectors.
 * It is specified as the ratio of physical sectors traversed
 * per logical sector.  Thus an interleave of 1:1 implies contiguous
 * layout, while 2:1 implies that logical sector 0 is separated
 * by one sector from logical sector 1.
 * d_trackskew is the offset of sector 0 on track N
 * relative to sector 0 on track N-1 on the same cylinder.
 * Finally, d_cylskew is the offset of sector 0 on cylinder N
 * relative to sector 0 on cylinder N-1.
 */
uint16_t d_rpm;   /* rotational speed */
uint16_t d_interleave;  /* hardware sector interleave */
uint16_t d_trackskew;  /* sector 0 skew, per track */
uint16_t d_cylskew;  /* sector 0 skew, per cylinder */
uint32_t d_headswitch;  /* head switch time, usec */
uint32_t d_trkseek;  /* track-to-track seek, usec */
uint32_t d_flags;  /* generic flags */
uint32_t d_drivedata[5];  /*
drive-type specific information */
uint32_t d_spare[5];  /* reserved for future use */
uint32_t d_magic2;  /* the magic number (again) */
uint16_t d_checksum;  /* xor of data incl. partitions */

  /* filesystem and partition information: */
uint16_t d_npartitions;        /* number of partitions in following */
uint32_t d_bbsize;         /* size of boot area at sn0, bytes */
uint32_t d_sbsize;         /* max size of fs superblock, bytes */
```

```
 struct bsd_partition  {  /* the partition table */
  uint32_t p_size;        /* number of sectors in partition */
  uint32_t p_offset;      /* starting sector */
  uint32_t p_fsize;       /* filesystem basic fragment size */
  uint8_t  p_fstype; /* filesystem type, see below */
  uint8_t  p_frag;  /* filesystem fragments per block */
  uint16_t p_cpg;         /* filesystem cylinders per group */
 } __attribute__((packed)) d_partitions[BSD_MAXPARTITIONS]; /* actually may be more */
} __attribute__((packed));


/* d_type values:
 */
#define BSD_DTYPE_SMD  1  /* SMD, XSMD; VAX hp/up */
#define BSD_DTYPE_MSCP  2  /* MSCP */
#define BSD_DTYPE_DEC  3  /* other DEC (rk, rl) */
#define BSD_DTYPE_SCSI  4  /* SCSI */
#define BSD_DTYPE_ESDI  5  /* ESDI interface */
#define BSD_DTYPE_ST506  6  /* ST506 etc. */
#define BSD_DTYPE_HPIB  7  /* CS/80 on HP-IB */
#define BSD_DTYPE_HPFL  8  /* HP Fiber-link */
#define BSD_DTYPE_FLOPPY 10  /* floppy */

/* d_subtype values: */
#define BSD_DSTYPE_INDOSPART 0x8  /* is inside dos partition */
#define BSD_DSTYPE_DOSPART(s) ((s) & 3) /* dos partition number */
#define BSD_DSTYPE_GEOMETRY 0x10  /* drive params in label */

/*
* Filesystem type and version.
* Used to interpret other filesystem-specific
* per-partition information.
*/
#define BSD_FS_UNUSED 0  /* unused */
#define BSD_FS_SWAP     1  /* swap */
#define BSD_FS_V6       2  /* Sixth Edition */
#define BSD_FS_V7       3  /* Seventh Edition */
#define BSD_FS_SYSV     4  /* System V */
#define BSD_FS_V71K     5  /* V7
 with 1K blocks (4.1, 2.9) */
#define BSD_FS_V8       6  /* Eighth Edition, 4K blocks */
#define BSD_FS_BSDFFS 7  /* 4.2BSD fast file system */
#define BSD_FS_BSDLFS 9  /* 4.4BSD log-structured file system */
#define BSD_FS_OTHER 10  /* in use, but unknown/unsupported */
#define BSD_FS_HPFS 11  /* OS/2 high-performance file system */
#define BSD_FS_ISO9660 12  /* ISO-9660 filesystem (cdrom) */
#define BSD_FS_ISOFS BSD_FS_ISO9660
```

```
#define BSD_FS_BOOT 13  /* partition contains bootstrap */
#define BSD_FS_ADOS 14  /* AmigaDOS fast file system */
#define BSD_FS_HFS 15  /* Macintosh HFS */
#define BSD_FS_ADVFS 16  /* Digital Unix AdvFS */

/* this is annoying, but it's also the way it is :-( */
#ifdef __alpha__
#define BSD_FS_EXT2 8  /* ext2 file system */
#else
#define BSD_FS_MSDOS 8  /* MS-DOS file system */
#endif

/*
* flags shared by various drives:
*/
#define BSD_D_REMOVABLE 0x01  /* removable media */
#define BSD_D_ECC       0x02  /* supports ECC */
#define BSD_D_BADSECT 0x04  /* supports
 bad sector forw. */
#define BSD_D_RAMDISK 0x08  /* disk emulator */
#define BSD_D_CHAIN     0x10  /* can do back-back transfers */
#define BSD_D_DOSPART 0x20  /* within MSDOS partition */

#endif /* UTIL_LINUX_PT_BSD_H */
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*/

1.206 io-wait 0.2.0

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1.207 unboundid-ldap-sdk 5.1.1

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operation requests should include the UnboundID-proprietary assured \ requests should include the UnboundID-proprietary get authorization \ UnboundID-proprietary get backend set ID request control to request that \ modify, and modify DN requests should include the UnboundID-proprietary \ bind requests should include the UnboundID-proprietary get user resource \ requests should include the UnboundID-proprietary hard delete request \ requests should include the UnboundID-proprietary ignore \ UnboundID-proprietary name with entryUUID request control in all add \ attribute should include the UnboundID-proprietary password validation \ should include the UnboundID-proprietary purge current password request \ operation requests should include the UnboundID-proprietary replication \ should include the UnboundID-proprietary retire current password \ requests should include the UnboundID-proprietary soft delete request \ tool should include the UnboundID-proprietary suppress referential \
operations should include the UnboundID-proprietary operation purpose \ all operations should include the UnboundID-proprietary suppress \ requests should include the UnboundID-proprietary account usable request \ bind requests should include the UnboundID-proprietary get user resource \ search requests should include the UnboundID-proprietary return conflict \ search requests should include the UnboundID-proprietary soft-deleted \ requests should include the UnboundID-proprietary real attributes only \ all operations should include the UnboundID-proprietary suppress \ requests should include the UnboundID-proprietary virtual attributes \ requests should include the UnboundID-proprietary exclude branch \ requests should include the UnboundID-proprietary get authorization \ search requests should include the UnboundID-proprietary get backend set \ requests should include the UnboundID-proprietary get server ID request \ search requests should include the UnboundID-proprietary get effective
\ all search requests should include the UnboundID-proprietary get \ all search requests should include the UnboundID-proprietary matching \ requests should include the UnboundID-proprietary operation purpose \ search requests should include the UnboundID-proprietary reject unindexed \ search requests should include the UnboundID-proprietary permit unindexed \ attempt to use the Ping Identity-proprietary start administrative session \ include the Ping Identity-proprietary soft delete request control to \ include the Ping Identity-proprietary hard delete request control to \ Identity-proprietary get backend set ID request control to indicate that \ delete requests should include the Ping Identity-proprietary route to \ include the Ping Identity-proprietary get server ID request control to \ requests should include the Ping Identity-proprietary route to server \ requests should include the Ping Identity-proprietary assured replication \ should include the Ping Identity-proprietary replication
repair request \ requests should include the Ping Identity-proprietary suppress referential \ the Ping Identity-proprietary operation purpose request control to indicate \ should include the Ping Identity-proprietary get authorization entry \ requests should include the Ping Identity-proprietary get user resource \ UnboundID-proprietary get authorization entry request control in the bind \ UnboundID-proprietary get user resource limits request control in the bind \ UnboundID-proprietary password update behavior request control in the \ UnboundID-proprietary get password validation details request control in \
INFO_PWMOD_ARG_DESC_RETIRE_CURRENT_PASSWORD=Include the UnboundID-proprietary \
INFO_PWMOD_ARG_DESC_PURGE_CURRENT_PASSWORD=Indicate the UnboundID-proprietary \
INFO_PWMOD_ARG_DESC_ASSURED_REPLICATION=Include the UnboundID-proprietary \
INFO_PWMOD_ARG_DESC_OPERATION_PURPOSE=Include the UnboundID-proprietary \ UnboundID-proprietary get authorization entry request control
in all bind \ UnboundID-proprietary get user resource limits request control in all \ UnboundID-proprietary operation purpose request control in all compare \'
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 * The quality of protection that should be used for any communication that occurs after the authentication has completed. Allowed values are "auth" (for just authentication with no communication protection), "auth-int" (for integrity protection for communication, which does not encrypt but ensures that the communication cannot be imperceptibly altered by a man in the middle attack), and "auth-conf" (for confidentiality protection for communication, which encrypts the communication so that it cannot be deciphered by a third-party observer). If no value is specified, then a default of "auth" will be assumed. If any of multiple qualities of protection will be considered acceptable then the permissible QoP values may be separated by commas and listed in order from most desirable to least desirable. INFO_SASL_DIGEST_MD5_OPTION_QOP("The quality of protection that should be used for any communication that occurs after the authentication has completed. Allowed values are "auth"

(for just authentication with no communication protection), "auth-int" (for integrity protection for communication, which does not encrypt but ensures that the communication cannot be imperceptibly altered by a man in the middle attack), and "auth-conf" (for confidentiality protection for communication, which encrypts the communication so that it cannot be deciphered by a third-party observer). If no value is specified, then a default of "auth" will be assumed. If any of multiple qualities of protection will be considered acceptable then the permissible QoP values may be separated by commas and listed in order from most desirable to least desirable."), * The quality of protection that should be used for any communication that occurs after the authentication has completed. Allowed values are "auth" (for just authentication with no communication protection), "auth-int" (for integrity protection for communication, which does not encrypt but ensures that the communication
cannot be imperceptibly altered by a man in the middle attack), and "auth-conf" (for confidentiality protection for communication, which encrypts the communication so that it cannot be deciphered by a third-party observer). If no value is specified, then a default of "auth" will be assumed. If any of multiple qualities of protection will be considered acceptable then the permissible QoP values may be separated by commas and listed in order from most desirable to least desirable. INFO_SASL_GSSAPI_OPTION_QOP("The quality of protection that should be used for any communication that occurs after the authentication has completed. Allowed values are "auth" (for just authentication with no communication protection), "auth-int" (for integrity protection for communication, which does not encrypt but ensures that the communication cannot be imperceptibly altered by a man in the middle attack), and "auth-conf" (for confidentiality protection for communication, which encrypts the communication
so that it cannot be deciphered by a third-party observer). If no value is specified, then a default of "auth" will be assumed. If any of multiple qualities of protection will be considered acceptable then the permissible QoP values may be separated by commas and listed in order from most desirable to least desirable."),'
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* Include the UnboundID-proprietary get authorization entry request control in all bind requests to indicate that the bind result should include the specified attributes from the entries for the authentication and authorization identities. This argument may be provided multiple times to specify multiple attributes to request.
INFO_LDAPCOMPARE_ARG_DESCRIPTION_GET_AUTHZ_ENTRY_ATTR("Include the UnboundID-proprietary get authorization entry request control in all bind requests to indicate that the bind result should include the specified attributes from the entries for the authentication and authorization identities. This argument may be provided multiple times to specify multiple attributes to request."), * Include the UnboundID-proprietary operation purpose request control in all compare requests to provide information about the purpose for the operation.
INFO_LDAPCOMPARE_ARG_DESCRIPTION_OPERATION_PURPOSE("Include the UnboundID-proprietary operation purpose request control in all
compare requests to provide information about the purpose for the operation."), * Include the UnboundID-proprietary get user resource limits request control in all bind requests to indicate that a successful bind result should include information about resource limits (e.g., size limit, time limit, idle time limit, etc.) that the server may impose for the user. INFO_LDAPCOMPARE_ARG_PLACEHOLDER_GET_USER_RESOURCE_LIMITS("Include the UnboundID-proprietary get user resource limits request control in all bind requests to indicate that a successful bind result should include information about resource limits (e.g., size limit, time limit, idle time limit, etc.) that the server may impose for the user."), * Indicates that bind requests should include the Ping Identity-proprietary get authorization entry request control to retrieve the specified attribute from the authenticated user"s entry. This argument may be provided multiple times to request that multiple attributes be returned.
INFO_LDAPDELETE_ARG_DESC_GET_AUTHZ_ENTRY_ATTR("Indicates
that bind requests should include the Ping Identity-proprietary get authorization entry request control to retrieve the specified attribute from the authenticated user"s entry. This argument may be provided multiple times to request that multiple attributes be returned."), * Indicates that delete requests sent through a Directory Proxy Server should include the Ping Identity-proprietary get backend set ID request control to indicate that the response should include a control with the ID of the entry-balancing backend set in which the delete was processed.
INFO_LDAPDELETE_ARG_DESC_GET_BACKEND_SET_ID("Indicates that delete requests sent through a Directory Proxy Server should include the Ping Identity-proprietary get backend set ID request control to indicate that the response should include a control with the ID of the entry-balancing backend set in which the delete was processed."), * Indicates that delete requests should include the
Ping Identity-proprietary get server ID request control to indicate that the response should include a control with the server ID of the Directory Server instance in which the delete was processed.
INFO_LDAPDELETE_ARG_DESC_GET_SERVER_ID("Indicates that delete requests should include the Ping Identity-proprietary get server ID request control to indicate that the response should include a control with the server ID of the Directory Server instance in which the delete was processed."), * Indicates that bind requests should include the Ping Identity-proprietary get user resource limits request control to retrieve information about the resource limits that the server will impose for the authenticated connection.
INFO_LDAPDELETE_ARG_DESC_GET_USER_RESOURCE_LIMITS("Indicates that bind requests should include the Ping Identity-proprietary get user resource limits request control to retrieve information about the resource limits that the server will impose for the authenticated connection."),
* Indicates that delete requests should include the Ping Identity-proprietary hard delete request control to indicate that the target entries should be completely removed, even if they would have otherwise been processed as soft deletes. INFO_LDAPDELETE_ARG_DESC_HARD_DELETE("Indicates that delete requests should include the Ping Identity-proprietary hard delete request control to indicate that the target entries should be completely removed, even if they would have otherwise been processed as soft deletes."), * Indicates that requests should include the Ping Identity-proprietary operation purpose request control to indicate the intended purpose for the operations.
INFO_LDAPDELETE_ARG_DESC_OP_PURPOSE("Indicates that requests should include the Ping Identity-

proprietary operation purpose request control to indicate the intended purpose for the operations."), * Indicates that delete requests should include the Ping Identity-proprietary replication repair request control to indicate that the delete operation should not be replicated.
INFO_LDAPDELETE_ARG_DESC_REPLICATION_REPAIR("Indicates that delete requests should include the Ping Identity-proprietary replication repair request control to indicate that the delete operation should not be replicated."), * Indicates that search and delete requests should include the Ping Identity-proprietary route to backend set request control to indicate that the Directory Proxy Server should forward those requests to servers in the specified entry-balancing backend set. This control may be provided multiple times to specify multiple backend sets for the same or different entry-balancing request processors.
INFO_LDAPDELETE_ARG_DESC_ROUTE_TO_BACKEND_SET("Indicates that search and delete requests should include the Ping Identity-proprietary route to backend set request control to indicate that the Directory Proxy Server should forward those requests to servers in the specified entry-balancing backend set. This control may be provided multiple
times to specify multiple backend sets for the same or different entry-balancing request processors."), * Indicates that search and delete requests should include the Ping Identity-proprietary route to server request control to indicate that the Directory Proxy Server should forward those requests to the specified backend server.
INFO_LDAPDELETE_ARG_DESC_ROUTE_TO_SERVER("Indicates that search and delete requests should include the Ping Identity-proprietary route to server request control to indicate that the Directory Proxy Server should forward those requests to the specified backend server."), * Indicates that delete requests should include the Ping Identity-proprietary soft delete request control to indicate that the server should hide the entries rather than deleting them immediately. Soft-deleted may or may not be completely removed after a period of time, based on the server configuration. INFO_LDAPDELETE_ARG_DESC_SOFT_DELETE("Indicates that delete requests should include
the Ping Identity-proprietary soft delete request control to indicate that the server should hide the entries rather than deleting them immediately. Soft-deleted may or may not be completely removed after a period of time, based on the server configuration."), * Indicates that delete requests should include the Ping Identity-proprietary suppress referential integrity updates request control so that the server will not perform any referential integrity processing for the delete operation. INFO_LDAPDELETE_ARG_DESC_SUPPRESS_REFINT_UPDATES("Indicates that delete requests should include the Ping Identity-proprietary suppress referential integrity updates request control so that the server will not perform any referential integrity processing for the delete operation."), * Indicates that the tool should attempt to use the Ping Identity-proprietary start administrative session extended operation to create an administrative session that will cause all requests to be processed in a dedicated
pool of worker threads. This may be useful when trying to diagnose or resolve an issue when all regular worker threads are busy processing other requests
INFO_LDAPDELETE_ARG_DESC_USE_ADMIN_SESSION("Indicates that the tool should attempt to use the Ping Identity-proprietary start administrative session extended operation to create an administrative session that will cause all requests to be processed in a dedicated pool of worker threads. This may be useful when trying to diagnose or resolve an issue when all regular worker threads are busy processing other requests"), * Indicates that delete requests should include the Ping Identity-proprietary assured replication request control to delay the response from the server until the change has been replicated to other servers. The --assuredReplicationLocalLevel, --assuredReplicationRemoteLevel, and --assuredReplicationTimeout arguments may also be used to customize the content of the request control. INFO_LDAPDELETE_ARG_DESC_USE_ASSURED_REPLICATION("Indicates that delete requests should include the Ping Identity-proprietary assured replication request control to delay the response from the server until the change has been replicated to other servers. The --assuredReplicationLocalLevel, --assuredReplicationRemoteLevel, and --assuredReplicationTimeout arguments may also be used to customize the content of the request control."), * Indicates that all operation requests should include the UnboundID-proprietary assured replication request control to indicate that the server should delay returning a response to the client until a minimum amount of replication processing has been performed for the operation. The "--{0}", "--{1}", and "--{2}"

arguments may be used to configure the settings to use for the assured replication control, but the server will automatically determine an appropriate value for any argument that is not provided.
INFO_LDAPMODIFY_ARG_DESCRIPTION_ASSURED_REPLICATION("Indicates that all operation requests should include the UnboundID-proprietary assured replication request control to indicate that the server should delay returning a response to the client until a minimum amount of replication processing has been performed for the operation. The "--{0}", "--{1}", and "--{2}" arguments may be used to configure the settings to use for the assured replication control, but the server will automatically determine an appropriate value for any argument that is not provided."), * Indicates that all bind requests should include the UnboundID-proprietary get authorization entry request control to request that the server return the specified attribute (or collection of attributes, in the case of a special identifier like "*" to indicate all user attributes or "+" to indicate all operational attributes) from the authenticated user"s entry. This argument may be provided multiple times to specify multiple attributes to request.
INFO_LDAPMODIFY_ARG_DESCRIPTION_GET_AUTHZ_ENTRY_ATTR("Indicates that all bind requests should include the UnboundID-proprietary get authorization entry request control to request that the server return the specified attribute (or collection of attributes, in the case of a special identifier like "*" to indicate all user attributes or "+" to indicate all operational attributes) from the authenticated user"s entry. This argument may be provided multiple times to specify multiple attributes to request."), * Indicates that all add, delete, modify, and modify DN requests should include the UnboundID-proprietary get backend set ID request control to request that the Directory Proxy Server include a corresponding get backend set ID response control in each operation response, indicating the entry-balancing backend set in which the write was processed.
INFO_LDAPMODIFY_ARG_DESCRIPTION_GET_BACKEND_SET_ID("Indicates that all add, delete, modify, and modify DN requests should include the UnboundID-proprietary get backend set ID request control to request that the Directory Proxy Server include a corresponding get backend set ID response control in each operation response, indicating the entry-balancing backend set in which the write was processed."), * Indicates that all add, delete modify, and modify DN requests should include the UnboundID-proprietary get server ID request control to request that server include a corresponding get server ID response control in each operation response, indicating the server in which the write was processed.
INFO_LDAPMODIFY_ARG_DESCRIPTION_GET_SERVER_ID("Indicates that all add, delete modify, and modify DN requests should include the UnboundID-proprietary get server ID request control to request that server include a corresponding get server ID response control in each operation response, indicating the server in which the write was processed."), * Indicates that all bind requests should include the UnboundID-proprietary get user resource limits request control to request that the server return information about resource limits (e.g., size limit, time limit, idle time limit, etc.) imposed for the user.
INFO_LDAPMODIFY_ARG_DESCRIPTION_GET_USER_RESOURCE_LIMITS("Indicates that all bind requests should include the UnboundID-proprietary get user resource limits request control to request that the server return information about resource limits (e.g., size limit, time limit, idle time limit, etc.) imposed for the user."), * Indicates that all delete requests should include the UnboundID-proprietary hard delete request control, which will permanently delete the target entry even if the server would have otherwise performed a soft delete operation to hide the entry for a period of time before deleting it.
INFO_LDAPMODIFY_ARG_DESCRIPTION_HARD_DELETE("Indicates that all delete requests should include the UnboundID-proprietary hard delete request control, which will permanently delete the target entry even if the server would have otherwise performed a soft delete operation to hide the entry for a period of time before deleting it."), * Indicates that all add requests should include the UnboundID-proprietary ignore NO-USER-MODIFICATION request control to permit setting values for certain operational attributes not normally permitted to be provided by external clients.
INFO_LDAPMODIFY_ARG_DESCRIPTION_IGNORE_NO_USER_MOD("Indicates that all add requests should include the UnboundID-proprietary ignore NO-USER-MODIFICATION request control to permit setting values for certain operational attributes not normally permitted to be provided by external clients."), * Include the UnboundID-proprietary name with entryUUID request control in all add requests sent to the server to indicate that the server

should use the entryUUID operational attribute as the naming attribute for the resulting entry instead of the provided RDN. INFO_LDAPMODIFY_ARG_DESCRIPTION_NAME_WITH_ENTRY_UUID("Include the UnboundID-proprietary name with entryUUID request control in all add requests sent to the
server to indicate that the server should use the entryUUID operational attribute as the naming attribute for the resulting entry instead of the provided RDN."), * Indicates that all operations should include the UnboundID-proprietary operation purpose request control to provide the specified reason for the operation. INFO_LDAPMODIFY_ARG_DESCRIPTION_OPERATION_PURPOSE("Indicates that all operations should include the UnboundID-proprietary operation purpose request control to provide the specified reason for the operation."), * Indicates that all add and modify requests that target either the "{0}" or "{1}" attribute should include the UnboundID-proprietary password validation details request control to indicate that the response should include information about the password quality requirements that the server will impose for the target user"s password and whether the provided password satisfies each of those constraints.
INFO_LDAPMODIFY_ARG_DESCRIPTION_PASSWORD_VALIDATION_DETAILS("Indicates
that all add and modify requests that target either the "{0}" or "{1}" attribute should include the UnboundID-proprietary password validation details request control to indicate that the response should include information about the password quality requirements that the server will impose for the target user"s password and whether the provided password satisfies each of those constraints."), * Indicates that any modify operation that targets either the "{0}" or "{1}" attribute should include the UnboundID-proprietary purge current password request control. This will indicate that the server should could purge the current password from the entry (even if it would have otherwise been retired for a brief period of time).
INFO_LDAPMODIFY_ARG_DESCRIPTION_PURGE_CURRENT_PASSWORD("Indicates that any modify operation that targets either the "{0}" or "{1}" attribute should include the UnboundID-proprietary purge current password request control. This will indicate that
the server should could purge the current password from the entry (even if it would have otherwise been retired for a brief period of time)."), * Indicates that all operation requests should include the UnboundID-proprietary replication repair request control. This will cause the change to be applied only to the target directory server instance but not to any other server in the replication topology.
INFO_LDAPMODIFY_ARG_DESCRIPTION_REPLICATION_REPAIR("Indicates that all operation requests should include the UnboundID-proprietary replication repair request control. This will cause the change to be applied only to the target directory server instance but not to any other server in the replication topology."), * Indicates that any modify operation that targets either the "{0}" or "{1}" attribute should include the UnboundID-proprietary retire current password request control. This will indicate that the server should continue to allow the user to authenticate with their current password
(in addition to the new password) for a brief period of time.
INFO_LDAPMODIFY_ARG_DESCRIPTION_RETIRE_CURRENT_PASSWORD("Indicates that any modify operation that targets either the "{0}" or "{1}" attribute should include the UnboundID-proprietary retire current password request control. This will indicate that the server should continue to allow the user to authenticate with their current password (in addition to the new password) for a brief period of time."), * Indicates that all delete requests should include the UnboundID-proprietary soft delete request control, which indicates that the server should hide the entry for a period of time before deleting it so that it may be restored with an undelete operation if the delete should be reverted. INFO_LDAPMODIFY_ARG_DESCRIPTION_SOFT_DELETE("Indicates that all delete requests should include the UnboundID-proprietary soft delete request control, which indicates that the server should hide the entry for a period of time before deleting
it so that it may be restored with an undelete operation if the delete should be reverted."), * Indicates that all operations should include the UnboundID-proprietary suppress operational attribute updates request control to indicate that the server should not apply any updates to the specified operational attributes. The value may be one of "last-access-time", "last-login-time", "last-login-ip", or "lastmod".
INFO_LDAPMODIFY_ARG_DESCRIPTION_SUPPRESS_OP_ATTR_UPDATES("Indicates that all operations

should include the UnboundID-proprietary suppress operational attribute updates request control to indicate that the server should not apply any updates to the specified operational attributes. The value may be one of "last-access-time", "last-login-time", "last-login-ip", or "lastmod".'), * Indicates that the tool should include the UnboundID-proprietary suppress referential integrity updates request control in all delete and modify DN operations to indicate that the
server should not perform any referential integrity processing for those operations.
INFO_LDAPMODIFY_ARG_DESCRIPTION_SUPPRESS_REFERINT_UPDATES("Indicates that the tool should include the UnboundID-proprietary suppress referential integrity updates request control in all delete and modify DN operations to indicate that the server should not perform any referential integrity processing for those operations."), * Indicates that all search requests should include the UnboundID-proprietary account usable request control to request that each search result entry returned include a response control with information about the password policy usability state for the entry.
INFO_LDAPSEARCH_ARG_DESCRIPTION_ACCOUNT_USABLE("Indicates that all search requests should include the UnboundID-proprietary account usable request control to request that each search result entry returned include a response control with information about the password policy usability state for the entry."), * Indicates that all
search requests should include the UnboundID-proprietary exclude branch request control to indicate that matching entries below the specified base DN should be excluded from search results. This argument may be provided multiple times if multiple branches should be excluded.
INFO_LDAPSEARCH_ARG_DESCRIPTION_EXCLUDE_BRANCH("Indicates that all search requests should include the UnboundID-proprietary exclude branch request control to indicate that matching entries below the specified base DN should be excluded from search results. This argument may be provided multiple times if multiple branches should be excluded."), * Indicates that all bind requests should include the UnboundID-proprietary get authorization entry request control to request that the server return the specified attribute (or collection of attributes, in the case of a special identifier like "*" to indicate all user attributes or "+" to indicate all operational attributes) from the authenticated user"s entry. This
argument may be provided multiple times to specify multiple attributes to request.
INFO_LDAPSEARCH_ARG_DESCRIPTION_GET_AUTHZ_ENTRY_ATTR("Indicates that all bind requests should include the UnboundID-proprietary get authorization entry request control to request that the server return the specified attribute (or collection of attributes, in the case of a special identifier like "*" to indicate all user attributes or "+" to indicate all operational attributes) from the authenticated user"s entry. This argument may be provided multiple times to specify multiple attributes to request."), * Indicates that all search requests should include the UnboundID-proprietary get backend set ID request control to request that the Directory Proxy Server include a corresponding get backend set ID response control in each search result entry, indicating the entry-balancing backend set from which that entry was retrieved.
INFO_LDAPSEARCH_ARG_DESCRIPTION_GET_BACKEND_SET_ID("Indicates that all search
requests should include the UnboundID-proprietary get backend set ID request control to request that the Directory Proxy Server include a corresponding get backend set ID response control in each search result entry, indicating the entry-balancing backend set from which that entry was retrieved."), * Indicates that all search requests should include the UnboundID-proprietary get effective rights request control to return information about the access control rights that a user has when interacting with each matching entry. This argument may be provided multiple times to specify multiple attributes.
INFO_LDAPSEARCH_ARG_DESCRIPTION_GET_EFFECTIVE_RIGHTS_ATTR("Indicates that all search requests should include the UnboundID-proprietary get effective rights request control to return information about the access control rights that a user has when interacting with each matching entry. This argument may be provided multiple times to specify multiple attributes."), * Indicates that all search
requests should include the UnboundID-proprietary get effective rights request control to return information about the access control rights the specified user has when interacting with each matching entry.

INFO_LDAPSEARCH_ARG_DESCRIPTION_GET_EFFECTIVE_RIGHTS_AUTHZID("Indicates that all search requests should include the UnboundID-proprietary get effective rights request control to return information about the access control rights the specified user has when interacting with each matching entry."), * Indicates that all search requests should include the UnboundID-proprietary get server ID request control to request that server include a corresponding get server ID response control in each search result entry, indicating the server from which that entry was retrieved. INFO_LDAPSEARCH_ARG_DESCRIPTION_GET_SERVER_ID("Indicates that all search requests should include the UnboundID-proprietary get server ID request control to request that server include a corresponding get server ID response

control in each search result entry, indicating the server from which that entry was retrieved."), * Indicates that all bind requests should include the UnboundID-proprietary get user resource limits request control to request that the server return information about resource limits (e.g., size limit, time limit, idle time limit, etc.) imposed for the user. INFO_LDAPSEARCH_ARG_DESCRIPTION_GET_USER_RESOURCE_LIMITS("Indicates that all bind requests should include the UnboundID-proprietary get user resource limits request control to request that the server return information about resource limits (e.g., size limit, time limit, idle time limit, etc.) imposed for the user."), * Indicates that all search requests should include the UnboundID-proprietary return conflict entries request control to indicate that the server may return any replication conflict entries that match the search criteria. Replication conflict entries are normally excluded from search results.

INFO_LDAPSEARCH_ARG_DESCRIPTION_INCLUDE_REPL_CONFLICTS("Indicates

that all search requests should include the UnboundID-proprietary return conflict entries request control to indicate that the server may return any replication conflict entries that match the search criteria. Replication conflict entries are normally excluded from search results."), * Indicates that all search requests should include the UnboundID-proprietary soft-deleted entry access request control to indicate that the server may return any soft-deleted entries that match the search criteria. Soft-deleted entries are normally excluded from search results. The value for this argument must be one of: "with-non-deleted-entries" (indicates that both regular and soft-deleted entries should be returned), "without-non-deleted-entries" (indicates that only soft-deleted entries should be returned), or "deleted-entries-in-undeleted-form" (returns only soft-deleted entries in the form in the form the entries had before they were deleted).

INFO_LDAPSEARCH_ARG_DESCRIPTION_INCLUDE_SOFT_DELETED("Indicates that all search requests should include the UnboundID-proprietary soft-deleted entry access request control to indicate that the server may return any soft-deleted entries that match the search criteria. Soft-deleted entries are normally excluded from search results. The value for this argument must be one of: "with-non-deleted-entries" (indicates that both regular and soft-deleted entries should be returned), "without-non-deleted-entries" (indicates that only soft-deleted entries should be returned), or "deleted-entries-in-undeleted-form" (returns only soft-deleted entries in the form in the form the entries had before they were deleted)."), * Indicates that all search requests should include the UnboundID-proprietary matching entry count request control, which indicates that the server should return information about the number of entries that match the search criteria. The maximum number of entries to examine

must be specified, which helps indicate whether an exact count or an estimate will be returned. If alwaysExamine is specified and the number of candidates is less than the examine count, then each candidate will be examined to verify that it matches the criteria and would actually be returned to the client in a search. If allowUnindexed is specified, then the count will be allowed to be processed even if the search is unindexed (and may take a very long time to complete). If debug is specified, then additional debug information may be included in the output. INFO_LDAPSEARCH_ARG_DESCRIPTION_MATCHING_ENTRY_COUNT_CONTROL("Indicates that all search requests should include the UnboundID-proprietary matching entry count request control, which indicates that the server should return information about the number of entries that match the search criteria. The maximum number of entries to examine must be specified, which helps indicate whether an exact count or an estimate will be returned.

If alwaysExamine is specified and the number of candidates is less than the examine count, then each candidate will be examined to verify that it matches the criteria and would actually be returned to the client in a search. If

allowUnindexed is specified, then the count will be allowed to be processed even if the search is unindexed (and may take a very long time to complete). If debug is specified, then additional debug information may be included in the output."), * Indicates that all search requests should include the UnboundID-proprietary operation purpose request control to provide the specified reason for the operation.
INFO_LDAPSEARCH_ARG_DESCRIPTION_OPERATION_PURPOSE("Indicates that all search requests should include the UnboundID-proprietary operation purpose request control to provide the specified reason for the operation."), * Indicates that all search requests should include the UnboundID-proprietary permit unindexed search request control to indicate that the server should
process the search operation even if it cannot do so efficiently using server indexes. The requester must have either the unindexed-search or unindexed-search-with-control privilege.
INFO_LDAPSEARCH_ARG_DESCRIPTION_PERMIT_UNINDEXED_SEARCH("Indicates that all search requests should include the UnboundID-proprietary permit unindexed search request control to indicate that the server should process the search operation even if it cannot do so efficiently using server indexes. The requester must have either the unindexed-search or unindexed-search-with-control privilege."), * Indicates that all search requests should include the UnboundID-proprietary real attributes only request control to indicate that the server should not include any virtual attributes in entries that are returned.
INFO_LDAPSEARCH_ARG_DESCRIPTION_REAL_ATTRS_ONLY("Indicates that all search requests should include the UnboundID-proprietary real attributes only request control to indicate that the server should not
include any virtual attributes in entries that are returned."), * Indicates that all search requests should include the UnboundID-proprietary reject unindexed search request control to indicate that the server should not process the search operation if it cannot do so efficiently using server indexes, even if the requester has the unindexed-search privilege. INFO_LDAPSEARCH_ARG_DESCRIPTION_REJECT_UNINDEXED_SEARCH("Indicates that all search requests should include the UnboundID-proprietary reject unindexed search request control to indicate that the server should not process the search operation if it cannot do so efficiently using server indexes, even if the requester has the unindexed-search privilege."), * Indicates that all operations should include the UnboundID-proprietary suppress operational attribute updates request control to indicate that the server should not apply any updates to the specified operational attributes. The value may be one of "last-access-time", "last-login-time",
"last-login-ip", or "lastmod".
INFO_LDAPSEARCH_ARG_DESCRIPTION_SUPPRESS_OP_ATTR_UPDATES("Indicates that all operations should include the UnboundID-proprietary suppress operational attribute updates request control to indicate that the server should not apply any updates to the specified operational attributes. The value may be one of "last-access-time", "last-login-time", "last-login-ip", or "lastmod"."), * Indicates that all search requests should include the UnboundID-proprietary virtual attributes only request control to indicate that the server should only include virtual attributes in entries that are returned.
INFO_LDAPSEARCH_ARG_DESCRIPTION_VIRTUAL_ATTRS_ONLY("Indicates that all search requests should include the UnboundID-proprietary virtual attributes only request control to indicate that the server should only include virtual attributes in entries that are returned."), * Include the UnboundID-proprietary assured replication request control to indicate
that the update response should be delayed until the change has been sufficiently replicated to other servers.
INFO_PWMOD_ARG_DESC_ASSURED_REPLICATION("Include the UnboundID-proprietary assured replication request control to indicate that the update response should be delayed until the change has been sufficiently replicated to other servers."), * Include the UnboundID-proprietary get authorization entry request control in the bind request to indicate that the bind response should include the entry for the authenticated user with the specified attribute. This argument may be provided multiple times to specify that multiple attributes from the user entry should be included. INFO_PWMOD_ARG_DESC_GET_AUTHZ_ENTRY_ATTRIBUTE("Include the UnboundID-proprietary get authorization entry request control in the bind request to indicate that the bind response should include the entry for the authenticated user with the specified attribute. This argument may be provided multiple times to specify that

multiple attributes from the user entry should be included."), * Include the UnboundID-proprietary get password validation details request control in the request used to update the password. The corresponding response control will include information about the requirements that the server will impose for the target user and whether the provided new password satisfies each of those constraints.
INFO_PWMOD_ARG_DESC_GET_PW_VALIDATION_DETAILS("Include the UnboundID-proprietary get password validation details request control in the request used to update the password. The corresponding response control will include information about the requirements that the server will impose for the target user and whether the provided new password satisfies each of those constraints."), * Include the UnboundID-proprietary get user resource limits request control in the bind request to indicate that the server should return information about resource limits (e.g., size limit, time limit, idle time limit,
etc.) imposed for the user. INFO_PWMOD_ARG_DESC_GET_USER_RESOURCE_LIMITS("Include the UnboundID-proprietary get user resource limits request control in the bind request to indicate that the server should return information about resource limits (e.g., size limit, time limit, idle time limit, etc.) imposed for the user."), * Include the UnboundID-proprietary operation purpose request control in the request sent to update the target user"s password to provide additional information about the purpose for the request.
INFO_PWMOD_ARG_DESC_OPERATION_PURPOSE("Include the UnboundID-proprietary operation purpose request control in the request sent to update the target user"s password to provide additional information about the purpose for the request."), * Include the UnboundID-proprietary password update behavior request control in the request used to update the password to specify settings that the server should use when updating the password. Values of this argument must be in the form
"name=value", where the property name can be any one of the following: is-self-change, allow-pre-encoded-password, skip-password-validation, ignore-password-history, ignore-minimum-password-age, password-storage-scheme, and must-change-password. The value for each property should be either "true" or "false", with the exception of the storage-scheme property, whose value should be the name of the desired password storage scheme to use to encode the new password. This argument can be provided multiple times to specify multiple password update behaviors. INFO_PWMOD_ARG_DESC_PASSWORD_UPDATE_BEHAVIOR("Include the UnboundID-proprietary password update behavior request control in the request used to update the password to specify settings that the server should use when updating the password. Values of this argument must be in the form "name=value", where the property name can be any one of the following: is-self-change, allow-pre-encoded-password, skip-password-validation, ignore-password-history,
ignore-minimum-password-age, password-storage-scheme, and must-change-password. The value for each property should be either "true" or "false", with the exception of the storage-scheme property, whose value should be the name of the desired password storage scheme to use to encode the new password. This argument can be provided multiple times to specify multiple password update behaviors."), * Indicate the UnboundID-proprietary purge password request control in the request used to update the password. This will indicate that the server should completely remove the former password from the user"s entry, even if the server would have otherwise retired the former password for a brief period of time.
INFO_PWMOD_ARG_DESC_PURGE_CURRENT_PASSWORD("Indicate the UnboundID-proprietary purge password request control in the request used to update the password. This will indicate that the server should completely remove the former password from the user"s entry, even
if the server would have otherwise retired the former password for a brief period of time."), * Include the UnboundID-proprietary retire password request control in the request used to update the password. This will indicate that the server should continue to allow the user to authenticate with their former password (in addition to the new password) for a brief period of time.
INFO_PWMOD_ARG_DESC_RETIRE_CURRENT_PASSWORD("Include the UnboundID-proprietary retire password request control in the request used to update the password. This will indicate that the server should continue to allow the user to authenticate with their former password (in addition to the new password) for a brief period of time."),'

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* <TD ALIGN="LEFT">CONFIDENTIALITY_REQUIRED</TD> * The integer value (13) for the "CONFIDENTIALITY_REQUIRED" result code. public static final int CONFIDENTIALITY_REQUIRED_INT_VALUE = 13; @NotNull public static final ResultCode CONFIDENTIALITY_REQUIRED = new ResultCode(INFO_RC_CONFIDENTIALITY_REQUIRED.get(), CONFIDENTIALITY_REQUIRED_INT_VALUE); case CONFIDENTIALITY_REQUIRED_INT_VALUE: return CONFIDENTIALITY_REQUIRED; CONFIDENTIALITY_REQUIRED,'
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1.208 ostruct 0.5.5

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1.209 tomcat-el-api 9.0.108

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1.210 joda-time 2.12.5

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1.211 jaxb-api 2.0

1.212 idna 13.0.0

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*         bug 185682 - Increment/decrement operators mark local variables as read
*         bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*         bug 331649 - [compiler][null] consider null annotations for fields
*         bug 383368 - [compiler][null] syntactic null analysis for field references
*         bug 392384 - [1.8][compiler][null] Restore nullness
 info from type annotations in class files
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
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*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*       Bug 406973 - [compiler] Parse MethodParameters attribute
*        Andy Clement - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*       bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*       Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*     Stephan Herrmann - Contribution for
*       bug 401030 - [1.8][null] Null analysis support for lambda methods.
*       Bug 392099
 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 425142 - [1.8][compiler] NPE in ConstraintTypeFormula.reduceSubType
*       Bug 425153 - [1.8] Having wildcard allows incompatible types in a lambda expression
*       Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
*       Bug 425798 - [1.8][compiler] Another NPE in ConstraintTypeFormula.reduceSubType
*       Bug 425156 - [1.8] Lambda as an argument is flagged with incompatible error
*       Bug 424403 - [1.8][compiler] Generic method call with method reference argument fails to resolve properly.
*       Bug 426563 - [1.8] AIOOBE when method with error
 invoked with lambda expression as argument
*       Bug 420525 - [1.8] [compiler] Incorrect error "The type Integer does not define sum(Object, Object) that is
applicable here"
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 428294 - [1.8][compiler] Type mismatch: cannot convert from List<Object> to Collection<Object[]>
*       Bug 428786 - [1.8][compiler] Inference needs to compute the "ground target type" when reducing a lambda
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*       Bug 428980 - [1.8][null] simple expression as lambda body doesn't leverage null annotation on argument
*       Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException
instead of IOException)
*       Bug 432110 - [1.8][compiler] nested lambda type incorrectly inferred vs javac
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type
 variables
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
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*       Bug 463728 - [1.8][compiler][inference] Ternary operator in lambda derives wrong type
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
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*   bug 358827 - [1.7] exception analysis for t-w-r spoils null analysis
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*   bug 345305 - [compiler][null]
 Compiler misidentifies a case of "variable can only be null"
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check

```
*       bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 455723 - Nonnull argument not correctly inferred in loop
*       Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*       Bug 446691 - [1.8][null][compiler] NullPointerException in SingleNameReference.analyseCode
**********************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/flow/FlowContext.java
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*
* Contributors:
*     IBM Corporation - initial API and implementation
*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*       bug 328281 - visibility leaks not detected when analyzing unused field in private class
*       bug 382353 - [1.8][compiler] Implementation property modifiers should be accepted on default methods.
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ExtraCompilerModifiers.java
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* Contributors:
*    wharley@bea.com - initial API and implementation
*
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Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/dispatch/BaseAnnotationProcessorManager.java
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*
* Contributors:
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*         Bug 407191 - [1.8] Binary access support for type annotations
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/FieldInfoWithTypeAnnotation.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IBinaryTypeAnnotation.java
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* Contributors:
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*     Stephan Herrmann - Contribution for
*        bug 331649 - [compiler][null] consider null annotations for fields
*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 426996 - [1.8][inference] try to avoid method Expression.unresolve()?
*     Jesper S Moller - Contributions for
*       bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
 *******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/NameReference.java

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*
* Contributors:
*     IBM Corporation - initial API and implementation
*     Luiz-Otavio Zorzella <zorzella at gmail dot com> - Improve CamelCase algorithm
*     Gábor Kövesdán - Contribution for Bug 350000 - [content assist] Include non-prefix matches in auto-complete
suggestions
*     Stefan Xenos <sxenos@gmail.com> (Google) - Bug 501283 - Lots of hash collisions during indexing
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/core/compiler/CharOperation.java
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*
* Contributors:
*     IBM Corporation - initial API and implementation
*     Stephan Herrmann - Contribution for bug 349326 - [1.7] new warning for missing try-with-resources
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/EmptyStatement.java
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* Contributors:
*   IBM Corporation - initial API and implementation
*   Stephan Herrmann - Contribution for
*   Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*   Bug 426676 - [1.8][compiler] Wrong generic method type inferred from lambda expression
*   Bug 426542 - [1.8] Most specific method not picked when one method has intersection type as type parameter
*   Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*   Andy
 Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*   Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
*   Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
**/

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/IntersectionTypeBinding18.java
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* Contributors:
*   IBM Corporation - initial API and implementation
*   Stephan Herrmann - Contributions for
*   bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 358903 - Filter practically unimportant resource
 leak warnings
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*   bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking

* Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
* Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 424727 - [compiler][null] NullPointerException in nullAnnotationUnsupportedLocation(ProblemReporter.java:5708)
* Bug 424710
 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
* Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
* Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
* Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
* Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 426764 - [1.8] Presence of conditional expression as method argument confuses compiler
* Bug 424930 - [1.8][compiler] Regression: "Cannot infer type arguments" error from compiler.
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug 427438 - [1.8][compiler] NPE at org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 * Bug 426996 - [1.8][inference] try to avoid method Expression.unresolve()?
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 429203 - [1.8][compiler] NPE in AllocationExpression.binding
* Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException instead of IOException)
* Bug 434297 - [1.8] NPE in LamdaExpression.analyseCode with lamda expression nested in a conditional expression
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Bug 448709 - [1.8][null] ensure we don't infer types that violate null constraints on a type parameter's bound
* Jesper S Moller <jesper@selskabet.org> - Contributions for
* bug 378674 - "The method can be declared as static" is wrong
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 383624 - [1.8][compiler] Revive
 code generation support for type annotations (from Olivier's work)
* Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic bridge method
* Till Brychcy - Contributions for
* bug 413460 - NonNullByDefault is not inherited to Constructors when accessed via Class File
* Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
* Bug 473178
**********/

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/AllocationExpression.java
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* Contributors:
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*   Stephan Herrmann - Contribution for
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
**********/

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/CombinedBinaryExpression.java
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* Contributors:
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*   Stephan Herrmann - Contributions for
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 359362 - FUP of bug 349326: Resource leak on non-Closeable resource
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 358903 - Filter practically unimportant resource leak warnings
*   bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject
 into account
*   bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*   Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*   Jesper S Moller <jesper@selskabet.org> - Contributions for
*   Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*   Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
*   bug 386692 - Missing "unused" warning on "autowired" fields
*   Pierre-Yves B. <pyvesdev@gmail.com> - Contribution for
*   bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
**********/

Found in path(s):

* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/TypeIds.java
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*     IBM Corporation - bug fixes
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/TypeBound.java
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* Contributors:
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*     Stephan Herrmann - Contribution for
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*        bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
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Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/UnaryExpression.java
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*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*          Bug 407191 - [1.8] Binary access support for type annotations
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/TypeAnnotationInfo.java
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*        bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*        bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        Bug
 422796 - [compiler][null] boxed boolean reported as potentially null after null test in lazy disjunction
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/OR_OR_Expression.java
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*   Bug 407191 - [1.8] Binary access support for type annotations
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Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/FieldInfo.java
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* Contributors:
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*   Stephan Herrmann - Contributions for
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 265744 - Enum switch should warn about missing default
*   bug 374605 - Unreasonable warning for enum-based switch statements
*   bug 381443
 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*   Bug 441208 - [1.8][null]SuppressWarnings("null") does not suppress / marked Unnecessary
*   Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
**********/

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/IrritantSet.java
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*    IBM Corporation - added the following constants
*         NonStaticAccessToStaticField
*         NonStaticAccessToStaticMethod
*         Task
*         ExpressionShouldBeAVariable
*         AssignmentHasNoEffect
*    IBM Corporation - added the following constants
*         TooManySyntheticArgumentSlots
*         TooManyArrayDimensions
*         TooManyBytesForStringConstant
*         TooManyMethods
*         TooManyFields
 *         NonBlankFinalLocalAssignment
*         ObjectCannotHaveSuperTypes
*         MissingSemiColon
*         InvalidParenthesizedExpression
*         EnclosingInstanceInConstructorCall
*         BytecodeExceeds64KLimitForConstructor
*         IncompatibleReturnTypeForNonInheritedInterfaceMethod
*         UnusedPrivateMethod
*         UnusedPrivateConstructor
*         UnusedPrivateType
*         UnusedPrivateField
*         IncompatibleExceptionInThrowsClauseForNonInheritedInterfaceMethod
*         InvalidExplicitConstructorCall
*    IBM Corporation - added the following constants
*         PossibleAccidentalBooleanAssignment
*         SuperfluousSemicolon
*         IndirectAccessToStaticField
*         IndirectAccessToStaticMethod
*         IndirectAccessToStaticType
*         BooleanMethodThrowingException
*         UnnecessaryCast
*         UnnecessaryArgumentCast
*         UnnecessaryInstanceof
*
  FinallyMustCompleteNormally
*         UnusedMethodDeclaredThrownException
*         UnusedConstructorDeclaredThrownException
*         InvalidCatchBlockSequence
*         UnqualifiedFieldAccess
*    IBM Corporation - added the following constants
*         Javadoc
```

```
*         JavadocUnexpectedTag
*         JavadocMissingParamTag
*         JavadocMissingParamName
*         JavadocDuplicateParamName
*         JavadocInvalidParamName
*         JavadocMissingReturnTag
*         JavadocDuplicateReturnTag
*         JavadocMissingThrowsTag
*         JavadocMissingThrowsClassName
*         JavadocInvalidThrowsClass
*         JavadocDuplicateThrowsClassName
*         JavadocInvalidThrowsClassName
*         JavadocMissingSeeReference
*         JavadocInvalidSeeReference
*         JavadocInvalidSeeHref
*         JavadocInvalidSeeArgs
*         JavadocMissing
*         JavadocInvalidTag
*         JavadocMessagePrefix
 *         EmptyControlFlowStatement
*    IBM Corporation - added the following constants
*         IllegalUsageOfQualifiedTypeReference
*         InvalidDigit
*    IBM Corporation - added the following constants
*         ParameterAssignment
*         FallthroughCase
*    IBM Corporation - added the following constants
*                         UnusedLabel
*                         UnnecessaryNLSTag
*                         LocalVariableMayBeNull
*                         EnumConstantsCannotBeSurroundedByParenthesis
*                         JavadocMissingIdentifier
*                         JavadocNonStaticTypeFromStaticInvocation
*                         RawTypeReference
*                         NoAdditionalBoundAfterTypeVariable
*                         UnsafeGenericArrayForVarargs
*                         IllegalAccessFromTypeVariable
*
  AnnotationValueMustBeArrayInitializer
*                         InvalidEncoding
*                         CannotReadSource
*                         EnumStaticFieldInInInitializerContext
*                         ExternalProblemNotFixable
*                         ExternalProblemFixable
*    IBM Corporation - added the following constants
*                         AnnotationValueMustBeAnEnumConstant
*                         OverridingMethodWithoutSuperInvocation
*                         MethodMustOverrideOrImplement
```

```
*                       TypeHidingTypeParameterFromType
*                       TypeHidingTypeParameterFromMethod
*                       TypeHidingType
*    IBM Corporation - added the following constants
*        NullLocalVariableReference
*        PotentialNullLocalVariableReference
*        RedundantNullCheckOnNullLocalVariable
*         NullLocalVariableComparisonYieldsFalse
 *         RedundantLocalVariableNullAssignment
*         NullLocalVariableInstanceofYieldsFalse
*         RedundantNullCheckOnNonNullLocalVariable
*         NonNullLocalVariableComparisonYieldsFalse
*    IBM Corporation - added the following constants
*                       InvalidUsageOfTypeParametersForAnnotationDeclaration
*                       InvalidUsageOfTypeParametersForEnumDeclaration
*    IBM Corporation - added the following constants
*        RedundantSuperinterface
*  Benjamin Muskalla - added the following constants
*       MissingSynchronizedModifierInInheritedMethod
*  Stephan Herrmann  - added the following constants
*       UnusedObjectAllocation
*       PotentiallyUnclosedCloseable
*       PotentiallyUnclosedCloseableAtExit
*       UnclosedCloseable
*       UnclosedCloseableAtExit
*       ExplicitlyClosedAutoCloseable
*          RequiredNonNullButProvidedNull
 *        RequiredNonNullButProvidedPotentialNull
*        RequiredNonNullButProvidedUnknown
*        NullAnnotationNameMustBeQualified
*        IllegalReturnNullityRedefinition
*        IllegalRedefinitionToNonNullParameter
*        IllegalDefinitionToNonNullParameter
*        ParameterLackingNonNullAnnotation
*        ParameterLackingNullableAnnotation
*        PotentialNullMessageSendReference
*        RedundantNullCheckOnNonNullMessageSend
*        CannotImplementIncompatibleNullness
*        RedundantNullAnnotation
*       RedundantNullDefaultAnnotation
*       RedundantNullDefaultAnnotationPackage
*       RedundantNullDefaultAnnotationType
*       RedundantNullDefaultAnnotationMethod
*       ContradictoryNullAnnotations
*       IllegalAnnotationForBaseType
*       RedundantNullCheckOnSpecdNonNullLocalVariable
*       SpecdNonNullLocalVariableComparisonYieldsFalse
*       RequiredNonNullButProvidedSpecdNullable
```

* MissingDefaultCase
* MissingEnumConstantCaseDespiteDefault
* UninitializedLocalVariableHintMissingDefault
* UninitializedBlankFinalFieldHintMissingDefault
* ShouldReturnValueHintMissingDefault
* IllegalModifierForInterfaceDefaultMethod
* InheritedDefaultMethodConflictsWithOtherInherited
* ConflictingNullAnnotations
* ConflictingInheritedNullAnnotations
* UnsafeElementTypeConversion
* ArrayReferencePotentialNullReference
* DereferencingNullableExpression
* NullityMismatchingTypeAnnotation
* NullityMismatchingTypeAnnotationSuperHint
* NullityUncheckedTypeAnnotationDetail
* NullityUncheckedTypeAnnotationDetailSuperHint
* NullableFieldReference
* UninitializedNonNullField
* UninitializedNonNullFieldHintMissingDefault
* NonNullMessageSendComparisonYieldsFalse
* RedundantNullCheckOnNonNullSpecdField
* NonNullSpecdFieldComparisonYieldsFalse
* NonNullExpressionComparisonYieldsFalse
* RedundantNullCheckOnNonNullExpression
* ReferenceExpressionParameterNullityMismatch
* ReferenceExpressionParameterNullityUnchecked
* ReferenceExpressionReturnNullRedef
* ReferenceExpressionReturnNullRedefUnchecked
* DuplicateInheritedDefaultMethods
* SuperAccessCannotBypassDirectSuper
* SuperCallCannotBypassOverride
* ConflictingNullAnnotations
* ConflictingInheritedNullAnnotations
* UnsafeElementTypeConversion
* PotentialNullUnboxing
* NullUnboxing
* NullExpressionReference
* PotentialNullExpressionReference
* RedundantNullCheckAgainstNonNullType
* NullAnnotationUnsupportedLocation
* NullAnnotationUnsupportedLocationAtType
* NullityMismatchTypeArgument
* ContradictoryNullAnnotationsOnBound
* UnsafeNullnessCast
* ContradictoryNullAnnotationsInferred
* NonNullDefaultDetailIsNotEvaluated
* NullNotCompatibleToFreeTypeVariable
* NullityMismatchAgainstFreeTypeVariable

* ImplicitObjectBoundNoNullDefault
* IllegalParameterNullityRedefinition
* ContradictoryNullAnnotationsInferredFunctionType
* IllegalReturnNullityRedefinitionFreeTypeVariable
* UnlikelyCollectionMethodArgumentType
* UnlikelyEqualsArgumentType
* Jesper S Moller - added the following constants
* TargetTypeNotAFunctionalInterface
* OuterLocalMustBeEffectivelyFinal
* IllegalModifiersForPackage
* DuplicateAnnotationNotMarkedRepeatable
* DisallowedTargetForContainerAnnotation
* RepeatedAnnotationWithContainerAnnotation
* ContainingAnnotationMustHaveValue
* ContainingAnnotationHasNonDefaultMembers
* ContainingAnnotationHasWrongValueType
* ContainingAnnotationHasShorterRetention
* RepeatableAnnotationHasTargets
* RepeatableAnnotationTargetMismatch
* RepeatableAnnotationIsDocumented
* RepeatableAnnotationIsInherited
* RepeatableAnnotationWithRepeatingContainerAnnotation
* VarLocalMultipleDeclarators
* VarLocalCannotBeArray
* VarLocalReferencesItself
* VarLocalWithoutInitizalier
* VarLocalInitializedToNull
* VarLocalCannotBeArrayInitalizers
* VarLocalCannotBeLambda
* VarLocalCannotBeMethodReference
* VarIsReserved
* VarIsReservedInFuture
* VarIsNotAllowedHere
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/util/JrtFileSystem.java
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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*          bug 185682 - Increment/decrement operators mark local variables as read
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365519 - editorial cleanup after bug 186342 and bug 365387
*       bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*       bug 345305 - [compiler][null]
 Compiler misidentifies a case of "variable can only be null"
*       Bug 414380 - [compiler][internal] QualifiedNameReference#indexOfFirstFieldBinding does not point to the first
```

field
*   Bug 458396 - NPE in CodeStream.invoke()
*   Jesper S Moller - Contributions for
*   bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*   Jesper S Moller <jesper@selskabet.org> - Contributions for
*   bug 378674 - "The method can be declared as static" is wrong
**********/

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*   Till Brychcy - Contributions for
*   Bug 467482 - TYPE_USE null annotations: Incorrect "Redundant null check"-warning
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*    Igor Fedorenko - extracted from ElementsImpl
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*         Bug 378674 - "The method can be declared as static" is wrong
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*         bug 383368 - [compiler][null] syntactic null analysis for field references
*         Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*         Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 417295 - [1.8[[null]
 Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*         Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*         Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*         Andy Clement - Contributions for
*                         Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/InstanceOfExpression.java
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*   Stephan Herrmann - Contribution for
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/IntersectionCastTypeReference.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/dispatch/BatchAnnotationProcessorManager.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/model/ElementImpl.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ClassFilePool.java
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*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*         bug 407191 - [1.8] Binary access support for type annotations
*     Stephan Herrmann - Contribution for
*         Bug 440474 - [null] textual encoding of external null annotations
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*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocImplicitTypeReference.java
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*    IBM Corporation - fix for 342470
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*   Jesper Steen Moller - Bug 412150 [1.8] [compiler] Enable reflected parameter names during annotation processing

***/

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*   bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/PackageExportImpl.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/UsesStatement.java
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*          bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*          bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 415790
 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ForStatement.java
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*       bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
```

*   Jesper Steen Moller - Contributions for
*   Bug 412150 [1.8] [compiler]
 Enable reflected parameter names during annotation processing
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*   wharley@bea.com - derived base class from BatchMessagerImpl
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*   bug 342671 - ClassCastException: org.eclipse.jdt.internal.compiler.lookup.SourceTypeBinding cannot be cast to
org.eclipse.jdt.internal.compiler.lookup.ArrayBinding
*   Bug 420894 - ClassCastException in DefaultBindingResolver.resolveType(Type)
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415043 - [1.8][null] Follow-up re null type
 annotations after bug 392099
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault

```
*       Bug 434600 - Incorrect null analysis error reporting on type parameters
*       Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*       Bug 456508 - Unexpected RHS PolyTypeBinding for: <code-snippet>
*       Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*       Andy Clement - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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*    Kenneth Olson - Contribution for bug 188796 - [jsr199] Using JSR199 to extend ECJ
*    Dennis Hendriks - Contribution for bug 188796 - [jsr199] Using JSR199 to extend ECJ
*    Frits Jalvingh  - fix for bug 533830.
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*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 415543 - [1.8][compiler] Incorrect bound index in RuntimeInvisibleTypeAnnotations attribute
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/SortedSimpleNameVector.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/SortedCharArrays.java
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* Contributors:
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*     Stephan Herrmann - contributions for
*          bug 337868 - [compiler][model] incomplete support for package-info.java when using SearchableEnvironment
*          bug 186342 - [compiler][null] Using annotations for null checking
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*     Frits Jalvingh  - fix for bug 533830.
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*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*        Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
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*            bug 349326 - [1.7] new warning for missing try-with-resources
*        bug 359334 - Analysis for resource leak warnings does not consider exceptions as method exit points
*        bug 358903 - Filter practically unimportant resource leak warnings
*        bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*        bug 370639 - [compiler][resource]
 restore the default for resource leak warnings
*        bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*        bug 379784 - [compiler] "Method can be static" is not getting reported
*        bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*        bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 371614 - [compiler][resource] Wrong "resource leak" problem on return/throw inside while loop
*        Bug 421035 - [resource] False alarm of resource leak warning when casting a closeable in its assignment
*        Bug 444964 - [1.7+][resource] False resource leak warning (try-with-resources for ByteArrayOutputStream -
return inside for loop)
*        Bug 396575 - [compiler][resources] Incorrect Errors/Warnings check for potential resource leak when surrounding
 with try-catch
*     Jesper S Moller <jesper@selskabet.org> - Contributions for
*        bug 378674 - "The method can be declared as static" is wrong
*     Keigo Imai - Contribution for  bug 388903 - Cannot extend inner class as an anonymous class when it extends
the outer class
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*     Stephan Herrmann - Contribution for bug 186342 - [compiler][null] Using annotations for null checking
*     Jesper S Moller  -. Contribution for bug 400830: [1.8][formatter] Code formatter for Java 8
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*   wharley@bea.com - initial API and implementation
*   IBM Corporation - fix for 342598
*   IBM Corporation - Java 8 support
*   het@google.com - Bug 427943 - The method
org.eclipse.jdt.internal.compiler.apt.model.Factory.getPrimitiveType does not throw IllegalArgumentException
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*   bug 343713 - [compiler] bogus line number in constructor of inner class in 1.5 compliance
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 361407 - Resource leak warning when resource is assigned to a field outside of constructor
*   bug 368546 - [compiler][resource] Avoid
 remaining false positives found when compiling the Eclipse SDK
*   bug 383690 - [compiler] location of error re uninitialized final field should be aligned
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject into account
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 415399 - [1.8][compiler] Type annotations on constructor results dropped by the code
generator
*   Ulrich Grave <ulrich.grave@gmx.de> - Contributions

for
* bug 386692 - Missing "unused" warning on "autowired" fields
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* Stephen Herrmann <stephan@cs.tu-berlin.de> - Contributions for
* bug 133125 - [compiler][null] need to report the null status of expressions and analyze them simultaneously
* bug 292478 - Report potentially null across variable assignment
* bug 324178 - [null] ConditionalExpression.nullStatus(..) doesn't take into account the analysis of condition itself
* bug 354554 - [null] conditional with redundant condition
 yields weak error message
* bug 349326 - [1.7] new warning for missing try-with-resources
* bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
* bug 383368 - [compiler][null] syntactic null analysis for field references
* bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
* Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 426078 - [1.8] VerifyError when conditional expression passed as an argument
* Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 * Bug 418537 - [1.8][null] Fix null type annotation analysis for poly conditional expressions
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
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*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*         Bug 407191 - [1.8] Binary access support for type annotations
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*     Benjamin Muskalla - Contribution for bug 239066
*     Stephan Herrmann - Contribution for
*           bug 382347 - [1.8][compiler] Compiler accepts incorrect default method inheritance
*       bug 388954 - [1.8][compiler] detect default methods in class files
*       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 388739 - [1.8][compiler] consider default methods when detecting whether a class needs
 to be declared abstract
*       bug 390883 - [1.8][compiler] Unable to override default method
*       bug 401796 - [1.8][compiler] don't treat default methods as overriding an independent inherited abstract method
*       bug 395681 - [compiler] Improve simulation of javac6 behavior from bug 317719 after fixing bug 388795
*       bug 406928 - computation of inherited methods seems damaged (affecting @Overrides)
*       bug 409473 - [compiler] JDT cannot compile against JRE 1.8
*       Bug 420080 - [1.8] Overridden Default method is reported as duplicated
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*        Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*        Bug 440687 - [compiler][batch][null] improve command line option for external annotations
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*     David Foerster - patch for toUpperCase as described in https://bugs.eclipse.org/bugs/show_bug.cgi?id=153125
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*   Nikolay Metchev (nikolaymetchev@gmail.com) - Contributions for
*   bug 411098 - [compiler][resource] Invalid Resource Leak Warning using ternary operator inside try-with-resource
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*   Stephan Herrmann - Contribution for bug 186342 - [compiler][null] Using annotations for null checking
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*           bug 336428 - [compiler][null] bogus warning "redundant null check" in condition of do {} while() loop
*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug 365519 - editorial cleanup after bug 186342 and bug 365387
*        bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*        bug 365859 - [compiler][null]
 distinguish warnings based on flow analysis vs. null annotations
*        bug 385626 - @NonNull fails across loop boundaries
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 376263 - Bogus "Potential null pointer access" warning
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        bug 406384 - Internal error with I20130413
*        Bug 415413 - [compiler][null] NullpointerException in Null Analysis caused by interaction of
LoopingFlowContext and FinallyFlowContext
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 455557 - [jdt] NPE LoopingFlowContext.recordNullReference
*        Bug 455723 - Nonnull argument not correctly inferred in loop
*        Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*        Bug 421035 - [resource]
 False alarm of resource leak warning when casting a closeable in its assignment
*     Jesper S Moller - contributions for
*        bug 404657 - [1.8][compiler] Analysis for effectively final variables fails to consider loops
**********************************************************************************/

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*          bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*          bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 265744 - Enum switch should warn about missing default
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
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*     Stephan Herrmann - Contributions for
*       bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*       Bug 392099
 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
```

* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 412076 - [compiler] @NonNullByDefault doesn't work for varargs parameter when in generic interface
* Bug 403216 - [1.8][null] TypeReference#captureTypeAnnotations treats type annotations as type argument annotations
* Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 416175 - [1.8][compiler][null] NPE with a code snippet that used null annotations on wildcards
* Bug 416174 - [1.8][compiler][null] Bogus name clash error with null annotations
* Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
* Bug 400874 - [1.8][compiler]
 Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
* Bug 425278 - [1.8][compiler] Suspect error: The target type of this expression is not a well formed parameterized type due to bound(s) mismatch
* Bug 425798 - [1.8][compiler] Another NPE in ConstraintTypeFormula.reduceSubType
* Bug 425156 - [1.8] Lambda as an argument is flagged with incompatible error
* Bug 426563 - [1.8] AIOOBE when method with error invoked with lambda expression as argument
* Bug 426792 - [1.8][inference][impl] generify new type inference engine
* Bug 428294 - [1.8][compiler] Type mismatch: cannot convert from List<Object> to Collection<Object[]>
* Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
* Bug 416182 - [1.8][compiler][null] Contradictory null annotations not rejected
 * Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
* Bug 438179 - [1.8][null] 'Contradictory null annotations' error on type variable with explicit null-annotation.
* Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
* Bug 446434 - [1.8][null] Enable interned captures also when analysing null type annotations
* Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
* Bug 456508 - Unexpected RHS PolyTypeBinding for: <code-snippet>
* Bug 390064 - [compiler][resource] Resource leak warning missing when extending parameterized class
* Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
* Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
**********/

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*     Matt McCutchen - partial fix for https://bugs.eclipse.org/bugs/show_bug.cgi?id=122995
*     Karen Moore - fix for https://bugs.eclipse.org/bugs/show_bug.cgi?id=207411
*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*           bug 185682 - Increment/decrement operators mark local variables as read
*           bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365519 - editorial cleanup after
 bug 186342 and bug 365387
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 384870 - [compiler] @Deprecated annotation not detected if preceded by other annotation
*       bug 393719 - [compiler] inconsistent warnings on iteration variables
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 424742 - [1.8] NPE in LambdaExpression.isCompatibleWith
*       Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*       Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
*       Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*       Bug 426366
 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
*       Bug 427282 - [1.8][compiler] AIOOB (-1) at
org.eclipse.jdt.internal.compiler.ClassFile.traverse(ClassFile.java:6209)
*       Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
*       Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*       Bug 427163 - [1.8][null] bogus error "Contradictory null specification" on varags
*       Bug 432348 - [1.8] Internal compiler error (NPE) after upgrade to 1.8
*       Bug 440143 - [1.8][null] one more case of contradictory null annotations regarding type variables
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
*       Bug 446442 - [1.8] merge null annotations from super methods
*       Bug 437072 - [compiler][null] Null analysis emits
 possibly incorrect warning for new int[][] despite @NonNullByDefault
*     Jesper S Moller - Contributions for
*       bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*       bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*       bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*       bug 412149 - [1.8][compiler] Emit repeated annotations into the designated container
*       bug 419209 - [1.8] Repeating container annotations should be rejected in the presence of annotation it contains
*     Till Brychcy - Contributions for
*             bug 467094 - [1.8][null] TYPE_USE NullAnnotations of array contents are applied to field.
*******************************************************************************/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ASTNode.java
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*   bug 342671 - ClassCastException: org.eclipse.jdt.internal.compiler.lookup.SourceTypeBinding cannot be cast to org.eclipse.jdt.internal.compiler.lookup.ArrayBinding
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 416181 - [1.8][compiler][null] Invalid assignment
 is not rejected by the compiler
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Bug 434600 - Incorrect null analysis error reporting on type parameters
*   Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*   Bug 456508 - Unexpected RHS PolyTypeBinding for: <code-snippet>
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*   Andy Clement - Contributions for
*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's work)
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*         Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                         Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
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*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         bug 388281 - [compiler][null] inheritance of null annotations as an option
*         Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 367203 - [compiler][null] detect assigning null to nonnull argument
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*   bug 382353 - [1.8][compiler] Implementation property modifiers
 should be accepted on default methods.
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 401030 - [1.8][null] Null analysis support for lambda methods.
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 403216 - [1.8][null] TypeReference#captureTypeAnnotations treats type annotations as type argument
annotations
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize
 declaration style null annotations
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
**/

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*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*******************************************************************************/
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredModule.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/PackageVisibilityStatement.java
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*     Stephan Herrmann - Contributions for
*         bug 292478 - Report potentially null across variable assignment
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*         bug 331649 - [compiler][null] consider null annotations for fields
*         bug 383368 - [compiler][null] syntactic null analysis for field
 references
*         bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*         bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*         bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 426792 - [1.8][inference][impl] generify new type inference engine
*         Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*         Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*         Bug 426996 - [1.8][inference]
 try to avoid method Expression.unresolve()?
*         Bug 428274 - [1.8] [compiler] Cannot cast from Number to double
*         Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*         Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*          Bug 473178
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*   Bug 407191 - [1.8] Binary access support for type annotations
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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 387612 - Unreachable catch block...exception is never thrown from the try
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 401456 - Code compiles from javac/intellij, but fails from eclipse
*   bug 401271 - StackOverflowError
 when searching for a methods references
*   bug 405706 - Eclipse compiler fails to give compiler error when return type is a inferred generic
*   Bug 408441 - Type mismatch using Arrays.asList with 3 or more implementations of an interface with the
interface type as the last parameter
*   Bug 413958 - Function override returning inherited Generic Type
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 416183 - [1.8][compiler][null] Overload resolution fails with null annotations
*   Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*   Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
*   Bug 424415 - [1.8][compiler]
 Eventual resolution of ReferenceExpression is not seen to be happening.
*   Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload
context

* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 426589 - [1.8][compiler] Compiler error with generic method/constructor invocation as vargs argument
* Bug 426590 - [1.8][compiler] Compiler error with tenary operator
* Bug 426764 - [1.8] Presence of conditional expression as method argument confuses compiler
* Bug 426998 - [1.8][compiler] method(java.lang.Class, java.lang.String) not applicable for the arguments (java.lang.Class, java.lang.String)
* Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
* Bug 427196 - [1.8][compiler] Compiler error for method reference to overloaded method
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug 427728 - [1.8] Type Inference rejects calls requiring boxing/unboxing
* Bug 427218 - [1.8][compiler] Verify error varargs + inference
* Bug 426836 - [1.8] special handling for return type in references to method getClass()?
* Bug 427628 - [1.8] regression : The method * is ambiguous for the type *
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 428366 - [1.8] [compiler] The method valueAt(ObservableList<Object>, int) is ambiguous for the type Bindings
* Bug 424728 - [1.8][null] Unexpected error: The nullness annotation 'XXXX' is not applicable at this location
* Bug 428811 - [1.8][compiler] Type witness unnecessarily required
* Bug 429424 - [1.8][inference] Problem inferring type of method's parameter
* Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
* Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
* Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
* Bug 441734 - [1.8][inference] Generic method with nested parameterized type argument fails on method reference
* Bug 452194 - Code no longer compiles in 4.4.1, but with confusing error
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Bug 456236 - [1.8][null] Cannot infer type when constructor argument is annotated with @Nullable
* Bug 437072 - [compiler][null] Null analysis emits possibly incorrect warning for new int[][] despite @NonNullByDefault
* Bug 462083 - [1.8][inference] Java 8 generic return type mismatch with interface involving type parameter.
* Jesper S Moller - Contributions for
* Bug 378674 - "The method can be declared as static" is wrong
* Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
* Pierre-Yves B. <pyvesdev@gmail.com> - Contributions for
* Bug 559618 - No compiler warning for import from same package
* Bug 560630 - No warning on unused import on class from same package
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*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
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*        Bug 440687 - [compiler][batch][null] improve command line option for external annotations
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*    philippe.marschall@netcetera.ch - Fix for 338370
*    IBM Corporation - Fix for validating relative name
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*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*          Bug 473178
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*   Benjamin Muskalla - Contribution for bug 239066
*   Stephan Herrmann - Contributions for
*   bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*   bug 295551 - Add option to automatically promote all warnings to errors
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug
 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 366063 - Compiler should not add synthetic @NonNull annotations
*   bug 374605 - Unreasonable warning for enum-based switch statements
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 381443 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*   Jesper Steen Moller - Contributions for
*   bug 404146 - [1.7][compiler] nested try-catch-finally-blocks leads to unrunnable Java byte code
*   bug 407297 - [1.8][compiler] Control generation of parameter names by option
**********/

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*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
**********/

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*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*                    Bug 384687 - [1.8] Wildcard type arguments should be rejected for lambda and reference
expressions
*       Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*    Stephan Herrmann - Contribution for
*       bug 402028 - [1.8][compiler] null analysis for reference
 expressions
*       bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super via I.super.m() syntax
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 424637 - [1.8][compiler][null] AIOOB in ReferenceExpression.resolveType with a method reference to
Files::walk
*       Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*       Bug 424403 - [1.8][compiler] Generic method call with method reference argument fails to resolve properly.
*       Bug 427196 - [1.8][compiler] Compiler error for method reference to overloaded method
*       Bug 427438
 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 428264 - [1.8] method reference of generic class causes problems (wrong inference result or NPE)
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 426537 - [1.8][inference] Eclipse compiler thinks I<? super J> is compatible with I<J<?>> - raw type J
involved
*       Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*       Bug 435689 - [1.8][inference] Type inference not occurring with lambda expression and method reference
*       Bug 438383 - [1.8][null] Bogus warning: Null type safety at method return type
*       Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
```

```
*       Bug 441734 - [1.8][inference] Generic method with nested parameterized type argument fails on method reference
*       Bug 438945 - [1.8] NullPointerException
 InferenceContext18.checkExpression in java 8 with generics, primitives, and overloading
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*       Bug 448709 - [1.8][null] ensure we don't infer types that violate null constraints on a type parameter's bound
*       Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
*       Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*       Bug 470542 - NullPointerException in ReferenceExpression.isPotentiallyCompatibleWith (962)
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contribution for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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*        Bug 440687 - [compiler][batch][null] improve command line option for external annotations
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*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*        bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*     Jesper S Moller - Contributions for
*        bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/tool/EclipseFileObject.java
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*   Stephan Herrmann - Contributions for
*   bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*   bug 295551 - Add option to automatically promote all warnings to errors
*   bug 359721 - [options] add command line option
 for new warning token "resource"
*   bug 365208 - [compiler][batch] command line options for annotation based null analysis
*   bug 374605 - Unreasonable warning for enum-based switch statements
*   bug 375366 - ECJ ignores unusedParameterIncludeDocCommentReference unless enableJavadoc option is set
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 381443 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*   Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*   Bug 440687 - [compiler][batch][null] improve command line option for external annotations
*   Bug 408815 - [batch][null] Add CLI option for
COMPILER_PB_SYNTACTIC_NULL_ANALYSIS_FOR_FIELDS
*   Jesper S Moller   - Contributions for
*   bug 407297 - [1.8][compiler] Control generation of parameter names by option
*   Mat Booth - Contribution for bug 405176
*   Frits Jalvingh -
 fix for bug 533830.
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```
*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*       Bug 406982 - [1.8][compiler] Generation of MethodParameters Attribute in classfile
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
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*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
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*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 421035 - [resource] False alarm of resource leak warning when casting a closeable in its assignment
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*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
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*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*   Bug 409246 - [1.8][compiler] Type annotations on catch parameters not handled properly
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*        bug 331649 - [compiler][null] consider null annotations for fields
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*     Jesper S Moller - Contributions for
*        Bug 378674 - "The method can be declared as static" is wrong
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ThisReference.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/TextBlock.java

* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/CompactConstructorDeclaration.java
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*         Bug 440474 - [null] textual encoding of external null annotations
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*   Nick Teryaev - fix for bug (https://bugs.eclipse.org/bugs/show_bug.cgi?id=40752)
*   Stephan Herrmann - Contributions for
*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 358903 - Filter practically unimportant resource leak warnings
*   bug
 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*   bug 379784 - [compiler] "Method can be static" is not getting reported
*   bug 379834 - Wrong "method can be static" in presence of qualified super and different staticness of nested super
class.
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*   bug 381445 - [compiler][resource] Can the resource leak check be made aware of Closeables.closeQuietly?
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null
 analysis for field references
*   bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*   bug 382350 - [1.8][compiler] Unable to invoke inherited default method via I.super.m() syntax
*   bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*   bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields

* bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred checking
* Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 405569 - Resource leak check false positive when using DbUtils.closeQuietly
* Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
* Bug 417295 - [1.8[[null] Massage type annotated null analysis
to gel well with deep encoded type bindings.
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
* Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
* Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
* Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
* Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
* Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug 427438 - [1.8][compiler]
NPE at org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
* Bug 426996 - [1.8][inference] try to avoid method Expression.unresolve()?
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException instead of IOException)
* Bug 441734 - [1.8][inference] Generic method with nested parameterized type argument fails on method reference
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Bug 456487 - [1.8][null] @Nullable type variant of @NonNull-constrained type parameter causes grief
* Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
* Bug 472618 - [compiler][null] assertNotNull vs. Assert.assertNotNull
* Bug 470958 - [1.8] Unable to convert lambda
* Bug 410218 - Optional warning
for arguments of "unexpected" types to Map#get(Object), Collection#remove(Object) et al.
* Jesper S Moller - Contributions for
* Bug 378674 - "The method can be declared as static" is wrong
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's work)
* Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic bridge method
**********/

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*   Stephan Herrmann - Contributions for
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 417295 - [1.8[[null] Massage type annotated
 null analysis to gel well with deep encoded type bindings.
*   Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
***/

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*   Stephan Herrmann - Contribution for
*   bug 382350 - [1.8][compiler] Unable to invoke inherited default method via I.super.m() syntax
*   bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*   Jesper S Moller <jesper@selskabet.org> - Contributions for
*   bug 378674 - "The method can be declared as static" is wrong
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/AnnotationMethodInfo.java
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*        bug 400710 - [1.8][compiler] synthetic access to default method generates wrong code
*        bug 391376 - [1.8] check interaction of default methods with bridge methods and generics
*        bug 421543 - [1.8][compiler] Compiler fails to recognize default method being turned into abstract by subtytpe
*     Jesper S Moller - Contributions for
*       Bug 405066 - [1.8][compiler][codegen] Implement
 code generation infrastructure for JSR335
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                     Bug 409247 - [1.8][compiler] Verify error with code allocating multidimensional array
*                     Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
*                     Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*                     Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*                     Bug 449467 - [1.8][compiler] Invalid lambda deserialization with anonymous class
*        Olivier Tardieu (tardieu@us.ibm.com) - Contributions for
```

```
*                    Bug 442418 - $deserializeLambda$ off-by-one error when deserializing the captured arguments
 of a lambda that also capture this
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IModuleAwareNameEnvironment.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IMultiModuleEntry.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ProvidesStatement.java
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*            Bug 407191 - [1.8] Binary access support for type annotations
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*        bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*        bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*        bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*        Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*        Bug 415850 - [1.8] Ensure RunJDTCoreTests
 can cope with null annotations enabled
*        Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*        Bug 427163 - [1.8][null] bogus error "Contradictory null specification" on varags
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
*        Bug 434600 - Incorrect null analysis error reporting on type parameters
*        Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
*        Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*        Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*        Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*        Bug 437072 - [compiler][null] Null analysis
 emits possibly incorrect warning for new int[][] despite @NonNullByDefault
*        Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                        Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                        Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
*                        Bug 415399 - [1.8][compiler] Type annotations on constructor results dropped by the code
generator
*      Jesper S Møller <jesper@selskabet.org> -  Contributions for
*                        bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*                        bug 529556 - [18.3] Add content assist support for 'var' as a type
*
*******************************************************************************/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/ISourceMethod.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IGenericField.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IBinaryNestedType.java
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*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        Bug 417758 - [1.8][null] Null safety compromise during array creation.
*        Bug
 427163 - [1.8][null] bogus error "Contradictory null specification" on varags
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                        Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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work)
*   Bug 409247 - [1.8][compiler] Verify error with code allocating multidimensional array
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*   Carmi Grushko - Bug 465048 - Binding is null for class literals in synchronized blocks
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*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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*     Stephan Herrmann - Contributions for
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365519 - editorial cleanup after bug 186342 and bug 365387
*         bug 388281 - [compiler][null] inheritance of null annotations as an option
*         bug 388795 - [compiler] detection of name clash depends on order of super interfaces
*         bug 388739 - [1.8][compiler] consider default methods when detecting whether
 a class needs to be declared abstract
*         bug 390883 - [1.8][compiler] Unable to override default method
*         bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*         bug 401246 - [1.8][compiler] abstract class method should now trump conflicting default methods
*         bug 401796 - [1.8][compiler] don't treat default methods as overriding an independent inherited abstract method
*         bug 403867 - [1.8][compiler] Suspect error about duplicate default methods
*         bug 391376 - [1.8] check interaction of default methods with bridge methods and generics
*         bug 395681 - [compiler] Improve simulation of javac6 behavior from bug 317719 after fixing bug 388795
*         bug 409473 - [compiler] JDT cannot compile against JRE 1.8
*         Bug 420080 - [1.8] Overridden Default method is reported as duplicated
*         Bug 404690 - [1.8][compiler] revisit bridge generation after VM bug is fixed
 *         Bug 410325 - [1.7][compiler] Generified method override different between javac and eclipse compiler
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 390889 - [1.8][compiler] Evaluate options to support 1.7- projects against 1.8 JRE.
*         Bug 440773 - [1.8][null]DefaultLocation.RETURN_TYPE erroneously affects method parameters in
@NonNullByDefault
*         Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*         Bug 446442 - [1.8] merge null annotations from super methods
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*         Bug 428811 - [1.8][compiler] Type witness unnecessarily required
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*     Stephan Herrmann - Contributions for
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 364890 - BinaryTypeBinding should use char constants from Util
*         bug 365387 - [compiler][null] bug 186342: Issues to follow up post review and verification.
*         bug 358903 - Filter practically unimportant resource leak warnings
*         bug
 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*         bug 388800 - [1.8][compiler] detect default methods in class files
*         bug 388281 - [compiler][null] inheritance of null annotations as an option
*         bug 331649 - [compiler][null] consider null annotations for fields
*         bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*         Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
```

```
*       Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*       Bug 392245 - [1.8][compiler][null] Define whether
 / how @NonNullByDefault applies to TYPE_USE locations
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 390889 - [1.8][compiler] Evaluate options to support 1.7- projects against 1.8 JRE.
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
*       Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*       Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 453475 - [1.8][null] Contradictory null annotations (4.5 M3 edition)
*       Bug 454182 - Internal compiler error when using
 1.8 compliance for simple project
*       Bug 470467 - [null] Nullness of special Enum methods not detected from .class file
*       Bug 447661 - [1.8][null] Incorrect 'expression needs unchecked conversion' warning
*    Jesper Steen Moller - Contributions for
*       Bug 412150 [1.8] [compiler] Enable reflected parameter names during annotation processing
*       Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*    Sebastian Zarnekow - Contributions for
*       bug 544921 - [performance] Poor performance with large source files
*    Alexander Lehmann - Contributions for
*       bug 566258 - Intermittent NPE in APT RoundDispatcher
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*     Stephan Herrmann - Contribution for
*       bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug
```

415291 - [1.8][null] differentiate type incompatibilities due to null annotations
* Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
* Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
* Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 425460 - [1.8] [inference] Type not inferred on stream.toArray
* Bug 426792 - [1.8][inference][impl] generify new type inference engine
* Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
* Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
* Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
* Bug 441693 -
[1.8][null] Bogus warning for type argument annotated with @NonNull
* Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
* Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
**********/

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* bug 400710 - [1.8][compiler] synthetic access to default method generates wrong code
* Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
* Bug 470467 - [null] Nullness of special Enum methods not detected from .class file
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 405104 -
[1.8][compiler][codegen] Implement support for serializeable lambdas
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*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*         bug 331649 - [compiler][null] consider null annotations for fields
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 415043 - [1.8][null] Follow-up re null type annotations
 after bug 392099
*         Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*         Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 424728 - [1.8][null] Unexpected error: The nullness annotation 'XXXX' is not applicable at this location
*         Bug 392245 - [1.8][compiler][null] Define whether / how @NonNullByDefault applies to TYPE_USE locations
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*         Bug 457210 - [1.8][compiler][null] Wrong Nullness errors given on full build build but not on incremental build?
*         Bug 469584 - ClassCastException in Annotation.detectStandardAnnotation (320)
*         Andy Clement (GoPivotal,
 Inc) aclement@gopivotal.com - Contributions for
*                         Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                         Bug 409517 - [1.8][compiler] Type annotation problems on more elaborate array references
*                         Bug 415397 - [1.8][compiler] Type Annotations on wildcard type argument dropped
*                         Bug 414384 - [1.8] type annotation on abbreviated inner class is not marked as inner type
*       Jesper S Moller <jesper@selskabet.org> -  Contributions for
*                         Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*                         Bug 412151 - [1.8][compiler] Check repeating annotation's collection type
*                         Bug 412149 - [1.8][compiler] Emit repeated annotations into the designated container
*                         Bug 419209 - [1.8] Repeating container annotations
 should be rejected in the presence of annotation it contains
*******************************************************************************/
```

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*   Bug 429813 - [1.8][dom ast] IMethodBinding#getJavaElement() should return IMethod for lambda
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*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 382353 - [1.8][compiler] Implementation property modifiers should
 be accepted on default methods.
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*   Bug 438012 - [1.8][null] Bogus Warning: The nullness annotation is redundant with a default that applies to this
location
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*   Jesper S Moller <jesper@selskabet.org> - Contributions for
*   bug 378674 - "The method can be declared as static" is wrong
**********/

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*         bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*         bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
 checking
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*         bug 332637 - Dead Code detection removing code that isn't dead
*         bug 391517 - java.lang.VerifyError on code that runs correctly in Eclipse 3.7 and eclipse 3.6
*         bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/tool/Archive.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/util/ArchiveFileObject.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/util/Util.java
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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*         bug 185682 - Increment/decrement operators mark local variables as read
*         bug 331649 - [compiler][null] consider null annotations for fields
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 447088 - [null] @Nullable on fully qualified field type is ignored
*         Bug 435805 - [1.8][compiler][null]
 Java 8 compiler does not recognize declaration style null annotations
*         Bug 458396 - NPE in CodeStream.invoke()
*         Bug 446217 - [null] @NonNullByDefault in package-info.java causes bogus "null type safety" warning
*     Till Brychcy - Contribution for
*               bug 467094 - [1.8][null] TYPE_USE NullAnnotations of array contents are applied to field.
**********************************************************************************/

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*        bug 349326 - [1.7] new warning for missing try-with-resources
```

```
*       bug 370930 - NonNull annotation not considered for enhanced for loops
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 393719 - [compiler] inconsistent warnings
 on iteration variables
*       Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*     Jesper S Moller -  Contribution for
*       bug 401853 - Eclipse Java compiler creates invalid bytecode (java.lang.VerifyError)
*                       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
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*                         Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                         Bug 409247 - [1.8][compiler] Verify error with code allocating multidimensional array
*                         Bug 409517 - [1.8][compiler] Type annotation problems on more elaborate array references
*
                  Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
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*     Jesper S Moller - Bug 392671
*         NPE with a method with explicit this and a following incomplete parameter
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*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
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*   IBM Corporation - Fix for bug 328575
*   IBM Corporation - Java 8 support
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*   Jesper S Moller - Contributions for
*   bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*   Stephan Herrmann - Contribution for

```
*       bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 416182 - [1.8][compiler][null]
 Contradictory null annotations not rejected
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*       bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*       bug 401030 - [1.8][null] Null analysis support for lambda methods.
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ClassLiteralAccess.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredField.java
*
/ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/CompilationResult.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredUnit.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/batch/ClasspathLocation.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredElement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/INameEnvironment.java
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*   bug 365519 - editorial cleanup after bug 186342 and bug 365387

* Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
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* IBM Corporation - initial API and implementation
* Stephan Herrmann - Contribution for
* Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
***/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/UnionTypeReference.java
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* Contributors:
* tyeung@bea.com - initial API and implementation
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* Contributors:
*   IBM Corporation - initial API and implementation
*   Stephen Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*   bug 317046 - Exception during debugging when hover mouse over a field
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class
 files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*   Bug 424712 - [1.8][compiler] NPE in TypeBinding.isProvablyDistinctTypeArgument
*   Bug 426792 - [1.8][inference][impl] generify new type inference engine
*   Bug 426764 - [1.8] Presence of conditional expression as method argument confuses compiler
*   Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*   Bug 427626 - [1.8] StackOverflow while typing new ArrayList<String>().toArray(and asking for code completion
 *   Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*   Bug 435962 - [RC2] StackOverFlowError when building
*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*   Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*   Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*   Bug 446434 - [1.8][null] Enable interned captures also when analysing null type annotations
*   Jesper S Moller <jesper@selskabet.org> - Contributions for
*   bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*   bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*
**/

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*     IBM Corporation - initial API and implementation
*     Erling Ellingsen -  patch for bug 125570
*     Stephan Herrmann - Contribution for
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
*     Sebastian Zarnekow - Contribution for
*         Bug 544921 - [performance] Poor performance with large source files
**********************************************************************************/
```

Found
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/CompilationUnitScope.java
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*     IBM Corporation - initial API and implementation
*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*           Bug 328281 - visibility leaks not detected when analyzing unused field in private class
*           Bug 300576 - NPE Computing type hierarchy when compliance doesn't match libraries
*           Bug 354536 - compiling package-info.java still depends on the order of compilation units
*           Bug 349326 - [1.7] new warning for missing try-with-resources
*           Bug
 358903 - Filter practically unimportant resource leak warnings
*       Bug 395977 - [compiler][resource] Resource leak warning behavior possibly incorrect for anonymous inner class
*       Bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*       Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
*       Bug 444024 - [1.8][compiler][null] Type mismatch error in annotation generics assignment which happens
"sometimes"
*       Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com -
```

Contributions for
* Bug 415821 - [1.8][compiler] CLASS_EXTENDS target type annotation missing for anonymous classes
* het@google.com - Bug 456986 - Bogus error when annotation processor generates annotation type
* Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
* Bug 473178
**********/

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* Contributors:
* Walter Harley - initial API and implementation
* IBM Corporation - fix for 342598, 382590
* Jean-Marie Henaff <jmhenaff@google.com> (Google) - Bug 481555
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/model/TypesImpl.java

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*    wharley@bea.com - initial API and implementation
*    IBM Corporation - fix for 342598
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/model/DeclaredTypeImpl.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/ISourceImport.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/CaseLabel.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/IDebugRequestor.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/FalseLiteral.java
*
 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/AccessRule.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfIntValues.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/CharLiteral.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/OperatorIds.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/Label.java

* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/SimpleSet.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/LongCache.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/StringLiteralConcatenation.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/InnerClassInfo.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/ClassFileStruct.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocReturnStatement.java
*
/ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ExtendedStringLiteral.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/MagicLiteral.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/AbortCompilationUnit.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/AbortMethod.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/IProblemFactory.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/AbortType.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/SimpleLookupTable.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/TrueLiteral.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemFieldBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ICompilerRequestor.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/FloatCache.java
*
/ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/antadapter/AntAdapterMessages.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/SyntheticArgumentBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocSingleNameReference.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/InnerEmulationDependency.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/SyntheticFieldBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfInt.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ParameterizedFieldBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/SubRoutineStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfLong.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/PostfixExpression.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/diagnose/RangeUtil.java
*
/ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/PrefixExpression.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/ObjectCache.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/AnnotationHolder.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/NLSTag.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/DoubleCache.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfType.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/ShouldNotImplement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/IntegerCache.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredImport.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfObjectToIntArray.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfObjectToInt.java
*
/ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocArrayQualifiedTypeReference.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/AccessRestriction.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/AbortCompilation.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ImportConflictBinding.java

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/batch/ClasspathSourceJar.java
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*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug 365519 - editorial cleanup after bug 186342 and bug 365387
*        bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*        bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*        bug 385626 - @NonNull fails across
 loop boundaries
*        bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 455723 - Nonnull argument not correctly inferred in loop
*     Jesper S Moller - Contributions for
*        bug 404657 - [1.8][compiler] Analysis for effectively final variables fails to consider loops
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*        bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*        Bug 416174 - [1.8][compiler][null] Bogus name clash error with null annotations
*        Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*        Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*        Bug 425783 - An
 internal error occurred during: "Requesting Java AST from selection". java.lang.StackOverflowError
*        Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*        Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/StackMapFrameCodeStream.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/util/EclipseFileManager.java
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*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred checking
*   Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
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*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 359362 - FUP of bug 349326: Resource leak on non-Closeable resource
*   bug 358903 - Filter practically unimportant resource leak warnings
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure

should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
* Bug 426676 - [1.8][compiler] Wrong generic method type inferred from lambda expression
* Bug 427411 - [1.8][generics] JDT reports type mismatch when using method that returns generic type
* Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
* Bug 435962 - [RC2] StackOverFlowError when building
* Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
* Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
* Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
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* Jesper S Moller - Contributions for
* Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
* Bug 406982 - [1.8][compiler] Generation of MethodParameters Attribute in classfile

* Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
* Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com
 - Contributions for
* Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
* Bug 439889 - [1.8][compiler] [lambda] Deserializing lambda fails with IllegalArgumentException: "Invalid lambda deserialization"
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/LabeledStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/DefaultProblem.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/Substitution.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/ITypeRequestor.java
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* Stephan Herrmann - Contribution for

```
*       bug 331649 - [compiler][null] consider null annotations for fields
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 458396 - NPE in CodeStream.invoke()
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/VariableBinding.java
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*       Bug 365992 - [builder] [null] Change of nullness for a parameter doesn't trigger a build for the files that call the
method
*       Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*       Bug 440687 - [compiler][batch][null] improve command line option for external annotations
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*
            bug 407191 - [1.8] Binary access support for type annotations
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/ClassFileReader.java
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```

```
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 433478 - [compiler][null] NPE in ReferenceBinding.isCompatibleWith
*******************************************************************************/
```

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemMethodBinding.java
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*           bug 332637 - Dead Code detection removing code that isn't dead
*           bug 358827 - [1.7] exception analysis for t-w-r spoils null analysis
*           bug 349326 - [1.7] new warning for missing try-with-resources
*           bug 359334 - Analysis for resource leak warnings does not consider exceptions as method exit points
*       bug 358903 - Filter practically unimportant resource
 leak warnings
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 401088 - [compiler][null] Wrong warning "Redundant null check" inside nested try statement
*       bug 401092 - [compiler][null] Wrong warning "Redundant null check" in outer catch of nested try
*       bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*       bug 384380 - False positive on a ?? Potential null pointer access ?? after a continue
*       Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*       Bug 371614 - [compiler][resource] Wrong "resource leak" problem on return/throw inside while loop
*       Bug 444964 - [1.7+][resource] False resource leak warning (try-with-resources for ByteArrayOutputStream -
return inside for loop)
*     Jesper Steen Moller -
 Contributions for
*       bug 404146 - [1.7][compiler] nested try-catch-finally-blocks leads to unrunnable Java byte code
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/TryStatement.java

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*     Stephan Herrmann - Contributions for
*         bug 335093 - [compiler][null] minimal hook for future null annotation support
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365983 - [compiler][null] AIOOB with null annotation analysis and varargs
*         bug 368546 - [compiler][resource] Avoid remaining false positives found when
 compiling the Eclipse SDK
*         bug 370930 - NonNull annotation not considered for enhanced for loops
*         bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*         bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*         bug 331649 - [compiler][null] consider null annotations for fields
*         bug 383368 - [compiler][null] syntactic null analysis for field references
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*         Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*         Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*         Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*         Bug 417758 - [1.8][null] Null safety compromise
 during array creation.
*         Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*         Bug 418537 - [1.8][null] Fix null type annotation analysis for poly conditional expressions
*         Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*         Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException
 instead of IOException)
*         Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*         Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*         Bug 455723 - Nonnull argument not correctly inferred in loop
*         Andy Clement - Contributions for
*                         Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
 work)
*                         Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/Statement.java
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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*       bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*       bug 292478 - Report potentially null across variable assignment
*       bug 335093 - [compiler][null] minimal hook for future null annotation support
*       bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
 *       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 370639 - [compiler][resource] restore the default for resource leak warnings
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 *       Bug 430150 - [1.8][null] stricter checking against type variables
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*     Jesper S Moller - Contributions for
*       Bug 378674 - "The method can be declared as static" is wrong
*       Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*       Bug 529556 - [18.3] Add content assist support for 'var' as a type
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*       Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*       Bug 426616 - [1.8][compiler] Type Annotations, multiple problems
*******************************************************************************/
```

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/LocalDeclaration.java

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*     Stephan Herrmann - Contribution for
*         Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*******************************************************************************/
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Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/VoidTypeBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/NullTypeBinding.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ReadManager.java
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*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*   Stephan Herrmann - Contribution for
*   Bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
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*            bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*            bug 349326 - [1.7] new warning for missing try-with-resources
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*        bug 403147 - [compiler][null] FUP of bug 400761:
 consolidate interaction between unboxing, NPE, and deferred checking
**********************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/IfStatement.java
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*     Perry James - nullStatus method improvement (165346)
*     Stephan Herrmann - Contribution for
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*******************************************************************************/
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*     Stephan Herrmann - Contribution for
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*******************************************************************************/
```

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*       bug 292478 - Report potentially null across variable assignment,
*       bug 185682 - Increment/decrement operators mark local variables as read
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       Bug 412203 - [compiler] Internal compiler error: java.lang.IllegalArgumentException:
 info cannot be null
*       Bug 458396 - NPE in CodeStream.invoke()
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
*     Jesper S Moller - <jesper@selskabet.org>   - Contributions for
*           bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*       bug 378674 - "The method can be declared as static" is wrong
*       bug 404657 - [1.8][compiler] Analysis for effectively final variables fails to consider loops
*       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*******************************************************************************/
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*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*       Bug 406982 - [1.8][compiler] Generation of MethodParameters Attribute in classfile
*       Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*       Bug 412149 - [1.8][compiler] Emit repeated annotations into the designated container
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com
 - Contributions for
*                    Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                    Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
*                    Bug 409246 - [1.8][compiler] Type annotations on catch parameters not handled properly
*                    Bug 415541 - [1.8][compiler] Type annotations in the body of static initializer get dropped
*                    Bug 415399 - [1.8][compiler] Type annotations on constructor results dropped by the code
generator
*                    Bug 415470 - [1.8][compiler] Type annotations on class declaration go vanishing
*                    Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*                    Bug 434556 - Broken class file generated for incorrect annotation usage
*                    Bug 442416
 - $deserializeLambda$ missing cases for nested lambdas
*     Stephan Herrmann - Contribution for
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*     Olivier Tardieu tardieu@us.ibm.com - Contributions for
*       Bug 442416 - $deserializeLambda$ missing cases for nested lambdas
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfModule.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredRequiresStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredUsesStatement.java
*
 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredModuleStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ModuleStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredPackageVisibilityStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredModuleReference.java

* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredExportsStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredExport.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ExportsStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredProvidesStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredTypeReference.java
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*   Stephan Herrmann - Contributions for
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 435570 - [1.8][null] @NonNullByDefault
 illegally tries to affect "throws E"
*   Bug 438012 - [1.8][null] Bogus Warning: The nullness annotation is redundant with a default that applies to this
location
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 409246 - [1.8][compiler] Type annotations on catch parameters not handled properly
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*     Stephan Herrmann - initial API and implementation
*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*          Bug 473178
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ConstraintExceptionFormula.java
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*        bug 349326 - [1.7] new warning for missing try-with-resources
*        bug 358903 - Filter practically unimportant resource leak warnings
*        bug 381445 - [compiler][resource] Can the resource leak check be made aware of Closeables.closeQuietly?
*        bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject into account
*        bug 382069 - [null] Make
 the null analysis consider JUnit's assertNotNull similarly to assertions
*        Bug 405569 - Resource leak check false positive when using DbUtils.closeQuietly
*        Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*        Bug 425183 - [1.8][inference] make CaptureBinding18 safe
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*     Jesper S Moller - Contributions for
*        Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*        Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*        bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                          Bug 405104 - [1.8][compiler][codegen]
 Implement support for serializeable lambdas
*     Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
*                          bug 386692 - Missing "unused" warning on "autowired" fields
*     Pierre-Yves B. <pyvesdev@gmail.com> - Contribution for
*                          bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
*******************************************************************************/
```

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/TypeConstants.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ReferenceBindingSetWrapper.java
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*     Stephan Herrmann - Contributions for
*         bug 359334 - Analysis for resource leak warnings does not consider exceptions as method exit points
*         bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with
 generics (RuntimeException instead of IOException)
*******************************************************************************/
```

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*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 467032 - TYPE_USE Null Annotations: IllegalStateException with annotated arrays of Enum when accessed
via BinaryTypeBinding
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*   Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*   bug 185682 - Increment/decrement operators mark local variables as read
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   Bug 412203 - [compiler] Internal compiler error: java.lang.IllegalArgumentException: info cannot be null
*   Bug 400874 - [1.8][compiler]
 Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 458396 - NPE in CodeStream.invoke()
*   Jesper S Moller - Contributions for
*   Bug 378674 - "The method can be declared as static" is wrong
*   Robert Roth <robert.roth.off@gmail.com> - Contributions for
*   Bug 361039 - NPE in FieldReference.optimizedBooleanConstant
***/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/FieldReference.java
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*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/flow/SwitchFlowContext.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/BreakStatement.java
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*   bug 366003 - CCE in ASTNode.resolveAnnotations(ASTNode.java:639)
*   bug 383973 - [1.8][compiler] syntax recovery in the presence of default methods
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredType.java
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*   Bug 473178
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*         Bug 407191 - [1.8] Binary access support for type annotations
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*     Stephan Herrmann - Contribution for
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
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*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocQualifiedTypeReference.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocSingleTypeReference.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/flow/InitializationFlowContext.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/NumberLiteral.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/FloatLiteral.java
*
 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/LongLiteralMinValue.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/IntLiteral.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/IntLiteralMinValue.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/DoubleLiteral.java
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*        Bug 432977 - [1.8][null] Incorrect 'type is not visible' compiler error
*        Bug 446434 - [1.8][null] Enable interned captures also when analysing null type annotations
*******************************************************************************/
/* Taking the binding of QTR as an example, there could be different annotatable components, but we come in a
with a single binding, e.g:

 @T Z;                          type => Z  annotations => [[@T]]
     @T Y.@T Z                      type => Z  annotations => [[@T][@T]]
     @T X.@T Y.@T Z                     type => Z  annotations => [[@T][@T][@T]]
     java.lang.@T X.@T Y.@T Z               type => Z  annotations => [[][@T][@T][@T]]
     in all these cases the incoming type binding is for Z, but annotations are for different levels. We need to align their
layout for proper attribution.
    */
```

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*          bug 282152 - [1.5][compiler] Generics code rejected by Eclipse but accepted by javac
*          bug 349326 - [1.7] new warning for missing try-with-resources
*          bug 359362 - FUP of bug 349326: Resource leak on non-Closeable resource
*        bug 358903 - Filter practically unimportant resource leak warnings
*        bug 395002 - Self bound generic class
 doesn't resolve bounds properly for wildcards for certain parametrisation.
*        bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*        Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*        Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
```

* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 426792 - [1.8][inference][impl] generify new type inference engine
* Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
* Bug 429384 - [1.8][null] implement conformance rules for null-annotated lower / upper type bounds
* Bug 431269 - [1.8][compiler][null] StackOverflow in nullAnnotatedReadableName
* Bug 431408 - Java 8 (1.8) generics bug
* Bug 435962 - [RC2] StackOverFlowError when building
* Bug
438458 - [1.8][null] clean up handling of null type annotations wrt type variables
* Bug 438250 - [1.8][null] NPE trying to report bogus null annotation conflict
* Bug 438179 - [1.8][null] 'Contradictory null annotations' error on type variable with explicit null-annotation.
* Bug 440143 - [1.8][null] one more case of contradictory null annotations regarding type variables
* Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
* Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
* Bug 456497 - [1.8][null] during inference nullness from target type is lost against weaker hint from applicability analysis
* Bug 456459 - Discrepancy between Eclipse compiler and javac - Enums, interfaces, and generics
* Bug 456487 - [1.8][null] @Nullable type variant of @NonNull-constrained type parameter causes grief
* Bug 462790 - [null] NPE in Expression.computeConversion()
* Bug 456532 - [1.8][null] ReferenceBinding.appendNullAnnotation() includes phantom annotations in error messages
* Jesper S Møller <jesper@selskabet.org> - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
* Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/DefaultProblemFactory.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/IErrorHandlingPolicy.java

* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/flow/InsideSubRoutineFlowContext.java
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 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/UnresolvedAnnotationBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/core/JDTCompilerAdapter.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/ReferenceContext.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/DefaultErrorHandlingPolicies.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/CompilerStats.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/Receiver.java
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*     Stephan Herrmann - Contributions for
*         bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 361407 - Resource leak warning when resource is assigned to a field outside of constructor
*         bug 370639 - [compiler][resource] restore the default for resource leak warnings
*         bug 388996 - [compiler][resource] Incorrect
 'potential resource leak'
*         bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*         Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*         Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
*         Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
*         Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*         Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload
context
*         Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
*         Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
*         Bug 427438 - [1.8][compiler]
 NPE at org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*         Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*         Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*         Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                         Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic
bridge method
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*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 374605 - Unreasonable warning for enum-based switch statements
*         bug 382353 - [1.8][compiler] Implementation property modifiers should be accepted on default methods.
*         bug 382354 - [1.8][compiler] Compiler silent on conflicting modifier
*         bug 401030 - [1.8][null] Null analysis support for lambda methods.
*         Bug
 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*     Jesper S Moller - Contributions for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
**********************************************************************************/
```

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/MethodScope.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/apt/dispatch/BatchProcessingEnvImpl.java
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* Contributors:
*   tyeung@bea.com - initial API and implementation
*   olivier_thomann@ca.ibm.com - add hashCode() and equals(..) methods
***/

Found in path(s):
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/EnumConstantSignature.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/ClassSignature.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ImportBinding.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/ImportReference.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/MissingTypeBinding.java
*
 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/JavadocParser.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/ISourceType.java
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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*          bug 185682 - Increment/decrement operators mark local variables as read
*          bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 331649 - [compiler][null] consider
```

null annotations for fields
* Bug 466308 - [hovering] Javadoc header for parameter is wrong with annotation-based null analysis
* Jesper S Møller <jesper@selskabet.org> - Contributions for
* Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/LocalVariableBinding.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/LongConstant.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/ByteConstant.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/IntConstant.java
*
 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/StringConstant.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/ShortConstant.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfObject.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/FloatConstant.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/AccessRuleSet.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/BooleanConstant.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocArgumentExpression.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/impl/Constant.java
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* Contributors:
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*     Stephan Herrmann - Contributions for
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         bug 383368 - [compiler][null] syntactic null analysis for field references
*         bug 402993 - [null] Follow up
 of bug 401088: Missing warning about redundant null check
*         Bug 440282 - [resource] Resource leak detection false negative with empty finally block
**********************************************************************************/
```

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*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*          Bug 473178
*     IBM Corporation - Bug fixes
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*         bug 383368 - [compiler][null] syntactic null analysis for field references
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/NullLiteral.java
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*     Patrick Wienands <pwienands@abit.de> - Contribution for bug 393749
*     Stephan Herrmann - Contribution for
*         bug 331649 - [compiler][null] consider null annotations for fields
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*     Benjamin Muskalla - Contribution for bug 239066
*     Stephan Herrmann  - Contributions for
```

* bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
* bug 338303 - Warning about Redundant assignment conflicts with definite assignment
* bug 349326 - [1.7] new warning for missing try-with-resources
* bug 186342 - [compiler][null] Using annotations for null checking
* bug 365519 - editorial cleanup after bug 186342 and bug 365387
* bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
* bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
* bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
* bug 374605 - Unreasonable warning for enum-based switch statements
* bug 382353 - [1.8][compiler] Implementation property modifiers should be accepted on default methods.
* bug 382347 - [1.8][compiler] Compiler accepts incorrect default method inheritance
* bug 388281 - [compiler][null] inheritance of null annotations as an option
* bug 376053 - [compiler][resource] Strange potential resource leak problems
* bug 381443 - [compiler][null] Allow parameter widening from @NonNull to unannotated
* bug 393719 - [compiler] inconsistent warnings on iteration
variables
* bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
* bug 388739 - [1.8][compiler] consider default methods when detecting whether a class needs to be declared abstract
* bug 331649 - [compiler][null] consider null annotations for fields
* bug 382789 - [compiler][null] warn when syntactically-nonnull expression is compared against null
* bug 376590 - Private fields with @Inject are ignored by unused field validation
* bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
* bug 402028 - [1.8][compiler] null analysis for reference expressions
* bug 401796 - [1.8][compiler] don't treat default methods as overriding an independent inherited abstract method
* bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
* bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
* Bug
392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
* Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
* Bug 414380 - [compiler][internal] QualifiedNameReference#indexOfFirstFieldBinding does not point to the first field
* Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
* Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 424637 - [1.8][compiler][null] AIOOB in ReferenceExpression.resolveType with a method reference to Files::walk
* Bug 428294 - [1.8][compiler] Type mismatch: cannot convert
from List<Object> to Collection<Object[]>
* Bug 428366 - [1.8] [compiler] The method valueAt(ObservableList<Object>, int) is ambiguous for the type Bindings
* Bug 416190 - [1.8][null] detect incompatible overrides due to null type annotations
* Bug 392245 - [1.8][compiler][null] Define whether / how @NonNullByDefault applies to TYPE_USE locations
* Bug 390889 - [1.8][compiler] Evaluate options to support 1.7- projects against 1.8 JRE.
* Bug 430150 - [1.8][null] stricter checking against type variables
* Bug 434600 - Incorrect null analysis error reporting on type parameters

* Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
* Bug 438467 - [compiler][null] Better error position for "The method _ cannot implement the corresponding method _ due to incompatible nullness constraints"
* Bug 439298 - [null] "Missing code implementation in the compiler" when using @NonNullByDefault in package-info.java
* Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
* Bug 446442 - [1.8] merge null annotations from super methods
* Bug 455723 - Nonnull argument not correctly inferred in loop
* Bug 458361 - [1.8][null] reconciler throws NPE in ProblemReporter.illegalReturnRedefinition()
* Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
* Bug 461878 - [1.7][1.8][compiler][null] ECJ compiler does not allow to use null annotations on annotations
* Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object), Collection#remove(Object) et al.
* Jesper S Moller <jesper@selskabet.org> - Contributions for
* bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
* bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
* bug 384567 - [1.5][compiler] Compiler accepts illegal modifiers on package declaration
* bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
* bug 412151 - [1.8][compiler] Check repeating annotation's collection type
* bug 419209 - [1.8] Repeating container annotations should be rejected in the presence of annotation it contains
* Bug 429384 - [1.8][null] implement conformance rules for null-annotated lower / upper type bounds
* Bug 416182 - [1.8][compiler][null] Contradictory null annotations not rejected
* bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
* Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
* bug 386692 - Missing "unused" warning on "autowired" fields
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*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*         Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*         bug 413958 - Function override returning inherited Generic Type
*         Bug 400874 - [1.8][compiler] Inference infrastructure
 should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*         Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*         Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*         Bug 418743 - [1.8][null] contradictory annotations on invocation of generic method not reported
*         Bug 416182 - [1.8][compiler][null] Contradictory null annotations not rejected
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*         Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
*         Bug 446442 - [1.8] merge null annotations from super methods
*         Bug 457079 - Regression: type
 inference
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*        Bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*     Keigo Imai - Contribution for  bug 388903 - Cannot extend inner class as an anonymous class when it
 extends the outer class
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*     Genady Beriozkin - added support for reporting assignment with no effect
*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*        bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*        bug 292478 - Report potentially null across variable assignment
*          bug 335093 - [compiler][null] minimal hook for future null annotation support
*          bug 349326 - [1.7] new warning for missing
 try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 370639 - [compiler][resource] restore the default for resource leak warnings
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 402993 - [null] Follow up of bug 401088: Missing
 warning about redundant null check
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
```

```
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
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*           bug 337868 - [compiler][model] incomplete support for package-info.java when using SearchableEnvironment
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*       bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       bug
 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*       bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 416183 - [1.8][compiler][null] Overload resolution fails with null annotations
*       Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 416190 - [1.8][null]
 detect incompatible overrides due to null type annotations
*       Bug 424624 - [1.8][null] if a static-object with annotation @NonNull is used, a warning is shown
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
*       Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 453475 - [1.8][null] Contradictory null annotations (4.5 M3 edition)
*       Bug 457079 - Regression: type inference
```

```
*       Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*       Bug 455180 - IllegalStateException in AnnotatableTypeSystem.getRawType
*       Bug 470467 - [null] Nullness of special Enum methods not detected from .class file
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*           bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*           bug 349326 - [1.7] new warning for missing try-with-resources
*           bug 360328 - [compiler][null] detect null problems in nested code (local class inside a loop)
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365835 - [compiler][null] inconsistent error reporting.
 *       bug 365519 - editorial cleanup after bug 186342 and bug 365387
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*       bug 370639 - [compiler][resource] restore the default for resource leak warnings
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*       bug 401030 - [1.8][null] Null analysis support for lambda
 methods.
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*       Bug 417758 - [1.8][null] Null safety compromise during array creation.
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 430150 - [1.8][null] stricter checking against type variables
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
```

```
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 455723 - Nonnull argument not correctly inferred in loop
*     Jesper S Moller - Contributions
 for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
**********************************************************************************/
```

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*     Stephan Herrmann - Contribution for
*       Bug 458396 - NPE in CodeStream.invoke()
*******************************************************************************/
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*     Stephan Herrmann - Contribution for
*        Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*******************************************************************************/
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/SingleTypeReference.java
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*     Stephan Herrmann - Contribution for
*        bug 384380 - False positive on a "Potential null pointer access" after a continue
*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*******************************************************************************/
```

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/InvocationSite.java
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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 367203 - [compiler][null] detect assigning null to nonnull argument
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*   bug 365531 - [compiler][null] investigate alternative strategy for internally
 encoding nullness defaults
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
*   Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Bug 438012 - [1.8][null] Bogus Warning: The nullness annotation is redundant with a default that applies to this
location
*   Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*   Bug 443347 - [1.8][null]
 @NonNullByDefault should not affect constructor arguments of an anonymous instantiation
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*   Bug 456584 - [1.8][null] Bogus warning for return type variable's @NonNull annotation being 'redundant'
*   Bug 471611 - Error on hover on call to generic method with null annotation
*   Jesper Steen Moller - Contributions for
*   Bug 412150 [1.8] [compiler] Enable reflected parameter names during annotation processing
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/MethodBinding.java
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashSetOfInt.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/core/compiler/InvalidInputException.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/CharArrayCache.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/VerificationTypeInfo.java
*
 /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/codegen/BranchLabel.java
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*   Stephan Herrmann - Contributions for
*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 349326 - [1.7] new warning for missing try-with-resources

```
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*       bug 370639 - [compiler][resource] restore the
 default for resource leak warnings
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 395977 - [compiler][resource] Resource leak warning behavior possibly incorrect for anonymous inner class
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 416267 - NPE in QualifiedAllocationExpression.resolveType
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 424415 - [1.8][compiler] Eventual
 resolution of ReferenceExpression is not seen to be happening.
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*     Jesper S Moller <jesper@selskabet.org> - Contributions for
*       bug 378674 - "The method can be declared as static" is wrong
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                       Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic
bridge method
*     Till Brychcy - Contributions for
*           bug 413460 - NonNullByDefault is not inherited to Constructors when accessed via Class File
**********************************************************************************/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/ConflictedParser.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfPackage.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/diagnose/DiagnoseParser.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/BranchStatement.java
*
/ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocAllocationExpression.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/TerminalTokens.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/JavadocMessageSend.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/core/compiler/CategorizedProblem.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/ParserBasicInformation.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/QualifiedTypeReference.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/classfmt/ClassFormatException.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/CaseStatement.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemPackageBinding.java
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*   bug 382350 - [1.8][compiler] Unable to invoke inherited default method via I.super.m() syntax
*   bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*   bug 404728 - [1.8]NPE on QualifiedSuperReference error
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/batch/messages.properties
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/messages.properties
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/problem/messages.properties

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*     Nick Teryaev - fix for bug (https://bugs.eclipse.org/bugs/show_bug.cgi?id=40752)
*     Stephan Herrmann - Contributions for
*        bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*        bug
 383368 - [compiler][null] syntactic null analysis for field references
*        bug 401017 - [compiler][null] casted reference to @Nullable field lacks a warning
*        bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*        Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*        Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*        Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*        Bug 430150 - [1.8][null] stricter checking against type variables
```

```
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style
 null annotations
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 415541 - [1.8][compiler] Type annotations in the body of static initializer get dropped
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*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*       bug 403086 - [compiler][null] include the effect of 'assert'
```

in syntactic null analysis for fields
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*   Stephan Herrmann - Contributions for
*   Bug 360328 - [compiler][null] detect null problems in nested code (local class inside a loop)
*   Bug 388630 - @NonNull diagnostics at line 0
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*   Bug 424727 - [compiler][null] NullPointerException
 in nullAnnotationUnsupportedLocation(ProblemReporter.java:5708)
*   Bug 457210 - [1.8][compiler][null] Wrong Nullness errors given on full build build but not on incremental build?
*   Keigo Imai - Contribution for bug 388903 - Cannot extend inner class as an anonymous class when it extends
the outer class
 *   Pierre-Yves B. <pyvesdev@gmail.com> - Contributions for
*   Bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
*   Bug 546084 - Using Junit 5s MethodSource leads to ClassCastException
***/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/ReductionResult.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/InferenceFailureException.java
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*     het@google.com - Bug 441790
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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*         bug 328281 - visibility leaks not detected when analyzing unused field in private class
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365836 - [compiler][null] Incomplete propagation of null defaults.
*         bug 365519 - editorial cleanup after bug 186342
```

```
 and bug 365387
*       bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*       bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*       bug 366063 - Compiler should not add synthetic @NonNull annotations
*       bug 384663 - Package Based Annotation Compilation Error in JDT 3.8/4.2 (works in 3.7.2)
*       bug 386356 - Type mismatch error with annotations and generics
*       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 380896 - [compiler][null] Enum constants not recognised as being NonNull.
*       bug 391376 - [1.8] check interaction of default methods with bridge methods and generics
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
 *       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 416172 - [1.8][compiler][null] null type annotation not evaluated on method return type
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 426048 - [1.8] NPE in TypeVariableBinding.internalBoundCheck when parentheses are not balanced
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 432348 - [1.8] Internal compiler error (NPE) after upgrade to 1.8
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*       Bug 441693 - [1.8][null]
 Bogus warning for type argument annotated with @NonNull
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 457210 - [1.8][compiler][null] Wrong Nullness errors given on full build build but not on incremental build?
*       Bug 461250 - ArrayIndexOutOfBoundsException in SourceTypeBinding.fields
*       Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*      Jesper S Moller <jesper@selskabet.org> -  Contributions for
*       Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*      Till Brychcy - Contributions for
*           bug 415269 - NonNullByDefault is not always inherited to nested classes
*      Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*      Sebastian Zarnekow - Contributions
 for
*       bug 544921 - [performance] Poor performance with large source files
*******************************************************************************/

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*   Stefan Xenos <sxenos@gmail.com> (Google) - initial API and implementation
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* Contributors:
*   wharley@bea.com - initial API and implementation
*   IBM Corporation - fix for 342598
*   IBM Corporation - Java 8 support
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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IBinaryModule.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/env/IUpdatableModule.java
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*         bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*         bug 370639 - [compiler][resource] restore the default for resource leak warnings
*         bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*         Bug 417758 - [1.8][null] Null safety compromise during array creation.
*         Bug 427438 - [1.8][compiler] NPE at
 org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*         Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*         Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                         Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*******************************************************************************/
```

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*     Stephan Herrmann - Contributions for
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365519 - editorial cleanup after bug 186342 and bug 365387
*         bug 358903 - Filter practically unimportant resource leak warnings
*         bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
 *         bug 388281 - [compiler][null] inheritance of null annotations as an option
*         bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*         bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*         bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject into account
*         bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*         bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*         Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*         Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*         Bug
 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*         Bug 426792 - [1.8][inference][impl] generify new type inference engine
*         Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*         Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*         Bug 418743 - [1.8][null] contradictory annotations on invocation of generic method not reported
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 431581 - Eclipse compiles what it should not
*         Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*         Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*         Bug 446442 - [1.8] merge null annotations from
 super methods
*         Bug 456532 - [1.8][null] ReferenceBinding.appendNullAnnotation() includes phantom annotations in error
messages
*         Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*      Jesper S Moller - Contributions for
*         bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*         bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*         bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*     Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
*                         bug 386692 - Missing "unused" warning on "autowired" fields
*     Pierre-Yves B. <pyvesdev@gmail.com> - Contribution for
*                         bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
```

when using MethodSource
* Sebastian Zarnekow - Contributions
 for
* bug 544921 - [performance] Poor performance with large source files
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* Jesper S Moller - Contributions for
* bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
* Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
* Stephan Herrmann - Contribution for
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 423504 - [1.8] Implement "18.5.3
 Functional Interface Parameterization Inference"
* Bug 425142 - [1.8][compiler] NPE in ConstraintTypeFormula.reduceSubType
* Bug 425153 - [1.8] Having wildcard allows incompatible types in a lambda expression
* Bug 425156 - [1.8] Lambda as an argument is flagged with incompatible error
* Bug 424403 - [1.8][compiler] Generic method call with method reference argument fails to resolve properly.
* Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 446442 - [1.8] merge null annotations from super methods
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
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*    wharley@bea.com - initial API and implementation
*                    (originally in org.eclipse.jdt.apt.core)
*    IBM Corporation - Bug 513790
*******************************************************************************/
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*     Jesper Steen Moller - Contributions for:
*         Bug 412149: [1.8][compiler] Emit repeated annotations into the designated container
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*         Bug 407297: [1.8][compiler] Control generation of parameter names by option
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*     IBM Corporation - Bug fixes
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*         Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*******************************************************************************/
```

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*   Jesper Steen Moeller - Contribution for bug 406973 - [compiler] Parse MethodParameters attribute
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 407191 - [1.8] Binary access support for type annotations
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* Contributors:
*   IBM Corporation - initial API and implementation
*   IBM Corporation - Fix for bug 328575
*   het@google.com - Bug 415274 - Annotation processing throws a NPE in getElementsAnnotatedWith()
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*   Stephan Herrmann - Contribution for bug 295551
*   Jesper S Moller   - Contributions for
*   Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*   Frits Jalvingh   - contributions for bug 533830.
*   Red Hat Inc.   - add module-info Javadoc support
**********/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/ast/CompilationUnitDeclaration.java

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*        Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*        Bug 456497 - [1.8][null] during inference nullness from target type is lost against weaker hint from applicability
analysis
*        Bug 456487 - [1.8][null] @Nullable type variant of @NonNull-constrained type parameter causes grief
*     Till Brychcy - Contribution for
*        Bug 473713
 - [1.8][null] Type mismatch: cannot convert from @NonNull A1 to @NonNull A1
*******************************************************************************/
/* Cache the derived type, tagging it as a derivative of both the key type and the naked type.
    E.g: int @NonNull [] would be tagged as a derived type of both int and int []. This is not
    needed for correctness, but for annotated object reuse. We provide two alternate ways to
    annotate a type:

    Taking parameterized types as an example, a call to getParamaterizedType can be made with annotations
    to create @NonNull List<@NonNull String> in one stroke. Or a parameterized type can be created first
    and then annotated via getAnnotatedType. In the former case, the tables get looked up with List as
    the key, in the latter with List<String> as the key.

    Binary vs source, substitutions, annotation re-attribution from SE7 locations etc trigger these
    alternate code paths. Unless care is exercised,
 we will end up with duplicate objects (that share
    the same TypeBinding.id => correctness is not an issue, but memory wastage is)
  */
```

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/lookup/TypeSystem.java
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*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 415397 - [1.8][compiler] Type Annotations on wildcard type argument dropped
*   Stephan Herrmann - Contribution for
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 429958
 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Bug 440462 - [null][compiler]NPE in EJC for erroneous null annotations
*   Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
***/

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* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/util/SimpleSetOfCharArray.java
* /ecj-3-26-0-sources-jar/org/eclipse/jdt/internal/compiler/parser/RecoveryScannerData.java
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*
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*     IBM Corporation - initial API and implementation
*     Tom Tromey - patch for readTable(String) as described in http://bugs.eclipse.org/bugs/show_bug.cgi?id=32196
*     Stephan Herrmann - Contributions for
*        bug 366003 - CCE in ASTNode.resolveAnnotations(ASTNode.java:639)
*        bug 374605 - Unreasonable warning for enum-based switch statements
*        bug 393719 - [compiler] inconsistent warnings on iteration variables
*        bug 382353 - [1.8][compiler] Implementation property modifiers
 should be accepted on default methods.
*        bug 383973 - [1.8][compiler] syntax recovery in the presence of default methods
*        bug 401035 - [1.8] A few tests have started failing recently
*     Jesper S Moller - Contributions for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*       bug 399695 - [1.8][compiler] [1.8][compiler] migrate parser to other syntax for default methods
*       bug 384567 - [1.5][compiler] Compiler accepts illegal modifiers on package declaration
*       bug 393192 - Incomplete type hierarchy with > 10 annotations
*       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*        Andy Clement - Contributions for
*                     Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                     Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*
            Bug 415821 - [1.8][compiler] CLASS_EXTENDS target type annotation missing for anonymous classes
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* Contributors:
*   tyeung@bea.com - initial API and implementation
*   olivier_thomann@ca.ibm.com - add hashCode() and equals(..) methods
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*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 429384 - [1.8][null] implement conformance rules for null-annotated lower / upper type bounds
*        Bug 441797 - [1.8] synchronize type annotations on capture and its wildcard
*        Bug 456497 - [1.8][null] during inference nullness from target type is lost against weaker
 hint from applicability analysis
*        Bug 456924 - StackOverflowError during compilation
*        Bug 462790 - [null] NPE in Expression.computeConversion()
*     Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
*        Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
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*         bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
```

* bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
* bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred checking
* Bug 422796 - [compiler][null] boxed boolean reported as potentially null after
 null test in lazy disjunction
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*   bug 325755 - [compiler] wrong initialization state after conditional expression
*   bug 320170 - [compiler] [null] Whitebox issues in null analysis
*   bug 292478 - Report potentially null across variable assignment
*   bug 332637 - Dead Code detection removing code that isn't dead
*   bug 341499 - [compiler][null] allocate extra bits in all methods of UnconditionalFlowInfo
 *   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 386181 - [compiler][null] wrong transition in UnconditionalFlowInfo.mergedWith()
*   bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*   Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*   Bug 454031 - [compiler][null][loop] bug in null analysis; wrong "dead code" detection
*   Bug 421035 - [resource] False alarm of resource leak warning when casting a closeable in its assignment
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*           bug 292478 - Report potentially null across variable assignment
*           bug 332637 - Dead Code detection removing code that isn't dead
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
*       Bug 421035 - [resource]
 False alarm of resource leak warning when casting a closeable in its assignment
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*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 *       Bug 429403 - [1.8][null] null mismatch from type arguments is not reported at field initializer
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 458396 - NPE in CodeStream.invoke()
*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*       Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
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1.216 vapi-samltoken 1.0.0

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OpenSSL Notes

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1.239 velocity 1.6.2

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1.240 log4j-1.2-api 2.17.1

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1.241 pstore 0.1.1

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1.242 commons-beanutils 1.9.4

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1.243 drb 2.1.0

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1.244 glibc 2.34-168.el9_6.23

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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative
of the original library. The ordinary
General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

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libraries. However, the Lesser license provides advantages in certain
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a de-facto standard. To achieve this, non-free programs must be
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library does the
same job as widely used non-free libraries. In this
case, there is little to gain by limiting the free library to free
software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free
programs enables a greater number of people to use a large body of
free software. For example, permission to use the GNU C Library in
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Although the Lesser General Public License is Less protective of the
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all the source code for all modules it contains, plus any associated
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You must
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table of data to be supplied by an application program that uses
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is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
root function must still compute square roots.)
@end enumerate

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In addition, mere aggregation of another work not based on the Library
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2 above provided that you accompany
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must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

@item
A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a ``work that uses the Library''. Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a ``work that uses the Library'' with the Library
creates an executable that is a derivative of the Library (because
it
contains portions of the Library), rather than a ``work that uses the
library''. The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a ``work that uses the Library'' uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The

threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise,
if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

@item
As an exception to the Sections above, you may also combine or
link a ``work that uses the Library'' with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
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during execution displays copyright notices, you must include the
copyright notice for the Library among them, as well as a reference
directing the user to the copy of
this License. Also, you must do one
of these things:

@enumerate a
@item
Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable ``work that
uses the Library'', as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

@item
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and (2) will operate
properly with a modified version of the library, if the user installs
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Accompany the work with a written offer, valid for at
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@item
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@page
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this license or the ordinary General Public License is the better
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Finally, software
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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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1.250 iproute 6.11.0-1.el9

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1.251 systemd-python 234

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1.255 ncurses 6.2-10.20210508.el9_6.2

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1.264 jnr-x86asm 1.0.2

1.265 snake-yaml 2.2

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* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/CustomClassLoaderConstructor.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/error/MarkedYAMLException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/scanner/ScannerImpl.java
*
 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/representer/SafeRepresenter.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/Node.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/serializer/SerializerException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/inspector/TagInspector.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/error/YAMLException.java
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* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/Yaml.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/DocumentEndEvent.java
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* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/SequenceStartEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/serializer/NumberAnchorGenerator.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/CollectionNode.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/BaseConstructor.java
*
 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/emitter/EmitterState.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/FlowSequenceStartToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/comments/CommentEventsCollector.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/util/ArrayUtils.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/composer/ComposerException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/comments/CommentType.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/SequenceNode.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/parser/VersionTagsTuple.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/ValueToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/NodeTuple.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/util/ArrayStack.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/reader/StreamReader.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/composer/Composer.java
*
 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/emitter/ScalarAnalysis.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/internal/Logger.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/FlowMappingStartToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/NodeEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/reader/ReaderException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/DocumentEndToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/util/EnumUtils.java

* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/parser/ParserImpl.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/emitter/Emitter.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/CollectionEndEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/PropertySubstitute.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/Property.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/ScalarNode.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/TagTuple.java
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* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/SafeConstructor.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/StreamStartEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/representer/BaseRepresenter.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/BlockEntryToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/CommentToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/StreamEndToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/SequenceEndEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/scanner/ScannerException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/emitter/Emitable.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/AliasEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/DirectiveToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/DocumentStartEvent.java
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 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/CollectionStartEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/AliasToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/emitter/EmitterException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/env/EnvScalarConstructor.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/GenericProperty.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/AnchorToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/util/PlatformFeatureDetector.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/NodeId.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/DocumentStartToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/BlockMappingStartToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/BeanAccess.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/StreamStartToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/parser/Production.java
*
 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/parser/ParserException.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/AnchorNode.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/FlowSequenceEndToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/scanner/Constant.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/scanner/Scanner.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/ScalarToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/comments/CommentLine.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/CommentEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/Construct.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/ImplicitTuple.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/ScalarEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/error/MissingEnvironmentVariableException.java

* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/representer/Represent.java
*
 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/Tag.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/StreamEndEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/extensions/compactnotation/PackageCompactConstructor.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/AbstractConstruct.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/Constructor.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/PropertyUtils.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/DumperOptions.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/MissingProperty.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/nodes/MappingNode.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/FlowEntryToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/TagToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/parser/Parser.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/MappingEndEvent.java
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* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/extensions/compactnotation/CompactData.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/reader/UnicodeReader.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/representer/Representer.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/util/UriEncoder.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/KeyToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/events/MappingStartEvent.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/TypeDescription.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/LoaderOptions.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/MethodProperty.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/BlockEndToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/resolver/ResolverTuple.java
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 /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/BlockSequenceStartToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/serializer/AnchorGenerator.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/introspector/FieldProperty.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/FlowMappingEndToken.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/tokens/Token.java
* /snakeyaml-2-2-sources-jar/org/yaml/snakeyaml/constructor/DuplicateKeyException.java
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* A Base64 encoder/decoder.
*
* <p>
* This class is used to encode and decode data in Base64 format as described in RFC 1521.
*
* <p>
* Project home page: www.
* source-code.biz/base64coder/java
* Author: Christian d'Heureuse, Inventec Informatik AG, Zurich, Switzerland
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*/

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1.266 system.memory 4.5.5

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1.267 net-protocol 0.1.1

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1.268 jwalterweatherman 1.1.0

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1.269 open-ldap 2.6.8-4.el9

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File: PSReadLine
---

https://github.com/PowerShell/PSReadLine

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ThreadJob

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```
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trigger: none

variables:
 - name: ob_outputDirectory
   value: '$(Build.ArtifactStagingDirectory)/ONEBRANCH_ARTIFACT'
 - name: CDP_DEFINITION_BUILD_COUNT
   value: $[counter('', 0)]
 # Defines the variables AzureFileCopySubscription, StorageAccount, StorageAccountKey, StorageResourceGroup, StorageSubscriptionName
 - group: 'Azure Blob variable group'
 # Defines the variables CgPat, CgOrganization, and CgProject
 - group: 'ComponentGovernance'
 - group: 'PoolNames'
 - name: LinuxContainerImage
   value: onebranch.azurecr.io/linux/ubuntu-2004:latest
 - name: WindowsContainerImage
   value: onebranch.azurecr.io/windows/ltsc2022/vse2022:latest

resources:
 repositories:
 - repository: templates
   type: git
   name: OneBranch.Pipelines/GovernedTemplates
   ref: refs/heads/main

extends:
 template: v2/OneBranch.NonOfficial.CrossPlat.yml@templates
 parameters:
   featureFlags:
     WindowsHostVersion:
       Version: 2022
   globalSdl:
     compiled:
```

```
    enabled: true
  armory:
    enabled: false
  sbom:
    enabled: false
  cg:
    enabled: true
    ignoreDirectories: '.devcontainer,demos,docker,docs,src,test,tools/packaging'
  tsa:
    enabled: true  # onebranch publish all SDL results to TSA. If TSA is disabled all SDL tools will forced into
'break' build mode.
  credscan:
    enabled: true
    scanFolder:  $(Build.SourcesDirectory)
    suppressionsFile: $(Build.SourcesDirectory)\.config\suppress.json
  binskim:
    break: true  # always break the build on binskim issues in addition to TSA upload
  policheck:
    break: true  # always break the build on policheck issues. You can disable it by setting to 'false'
        # APIScan requires a non-Ready-To-Run build
  apiscan:
    enabled: true
    softwareName: "PowerShell" # Default is repo name
    versionNumber: "7.5" # Default is
 build number
    isLargeApp: false # Default: false.
#softwareFolder - relative path to a folder to be scanned. Default value is root of artifacts folder.
#symbolsFolder - relative path to a folder that contains symbols. Default value is root of artifacts folder.

  tsaOptionsFile: .config\tsaoptions.json

stages:
  - stage: APIScan
    displayName: 'ApiScan'
    dependsOn: []
    jobs:
      - template: /.pipelines/templates/compliance/apiscan.yml@self
        parameters:
          parentJobs: []
  - stage: notice
    displayName: Generate Notice File
    dependsOn: []
    jobs:
      - template: /.pipelines/templates/compliance/generateNotice.yml@self
        parameters:
          parentJobs: []
```

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1.273 asm 9.2

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Found in path(s):
* /asm-9-2-sources-1-jar/org/objectweb/asm/AnnotationWriter.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ModuleWriter.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/SymbolTable.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/RecordComponentVisitor.java
*
/asm-9-2-sources-1-jar/org/objectweb/asm/Symbol.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/FieldVisitor.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/TypeReference.java

* /asm-9-2-sources-1-jar/org/objectweb/asm/Type.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/MethodTooLargeException.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Constants.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/signature/SignatureReader.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ByteVector.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/MethodWriter.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/TypePath.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ClassTooLargeException.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Opcodes.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/RecordComponentWriter.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/signature/SignatureWriter.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Label.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Context.java
*
/asm-9-2-sources-1-jar/org/objectweb/asm/Handler.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/MethodVisitor.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ConstantDynamic.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Edge.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/AnnotationVisitor.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Attribute.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ClassVisitor.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Handle.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/CurrentFrame.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/Frame.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/signature/SignatureVisitor.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ModuleVisitor.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ClassWriter.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/ClassReader.java
* /asm-9-2-sources-1-jar/org/objectweb/asm/FieldWriter.java
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* /asm-9-2-sources-1-jar/org/objectweb/asm/package.html
* /asm-9-2-sources-1-jar/org/objectweb/asm/signature/package.html

1.274 jzlib 1.1.5

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* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/Deflater.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/InfBlocks.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/ZInputStream.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/GZIPHeader.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/InflaterInputStream.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/Inflater.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/GZIPOutputStream.java
*
/jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/ZOutputStream.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/GZIPException.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/CRC32.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/JZlib.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/GZIPInputStream.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/Checksum.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/DeflaterOutputStream.java
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* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/ZStream.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/Inflate.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/Adler32.java
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* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/ZStreamException.java
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* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/StaticTree.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/InfCodes.java
* /jzlib-1-1-5-sources-jar/bayern/steinbrecher/jzlib/Tree.java

1.275 dirgra 0.3

1.276 parted 3.5-3.el9

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```
/* Extract the last component (base name) of a file name.

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#ifndef _BASENAME_LGPL_H
#define _BASENAME_LGPL_H

#include <stddef.h>

#ifndef DOUBLE_SLASH_IS_DISTINCT_ROOT
# define DOUBLE_SLASH_IS_DISTINCT_ROOT 0
#endif

#ifdef __cplusplus
extern "C" {
#endif



/*
 Return the address of the last file name component of FILENAME.
  If FILENAME has some trailing slash(es), they are considered to be
  part of the last component.
  If FILENAME has no relative file name components because it is a file
  system root, return the empty string.
  Examples:
          FILENAME      RESULT
          "foo.c"       "foo.c"
          "foo/bar.c"   "bar.c"
          "/foo/bar.c"  "bar.c"
          "foo/bar/"    "bar/"
          "foo/bar//"   "bar//"
          "/"           ""
          "//"          ""
          ""            ""
```

```
  The return value is a tail of the given FILENAME; do NOT free() it!  */

/* This function was traditionally called 'basename', but we avoid this
  function name because
    * Various platforms have different functions in their libc.
      In particular, the glibc basename(), defined in <string.h>, does
      not consider trailing slashes to be part of the component:

 FILENAME      RESULT
           "foo/bar/"    ""
           "foo/bar//"   ""
    * The 'basename' command eliminates trailing slashes and for a root
      produces a non-empty result:
           FILENAME      RESULT
           "foo/bar/"    "bar"
           "foo/bar//"   "bar"
           "/"           "/"
           "//"          "/"
*/
extern char *last_component (char const *filename) _GL_ATTRIBUTE_PURE;

/* Return the length of the basename FILENAME.
  Typically FILENAME is the value returned by base_name or last_component.
  Act like strlen (FILENAME), except omit all trailing slashes.  */
extern size_t base_len (char const *filename) _GL_ATTRIBUTE_PURE;


#ifdef __cplusplus
} /* extern "C" */
#endif

#endif /* _BASENAME_LGPL_H */
```

1.277 reline 0.4.2

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1.278 go-shellwords 1.0.12

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1.279 audit 3.1.5-4.el9

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(which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly
into another language. (Hereinafter, translation is
included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control
compilation
and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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 This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

 4. You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and 2 above provided that you accompany
it with the
 complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

 If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

 5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

 However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a
 "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

 When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

 If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative

work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute
the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.
^L
6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2)

will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials

specified in Subsection 6a, above, for a charge no more
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d) If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above
specified materials from the same place.

e) Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the
Library" must include any data and utility programs needed for
reproducing the executable from it. However, as a special exception,
the materials to be distributed need not include anything
that is
normally distributed (in either source or binary form) with the major
components (compiler, kernel, and so on) of the operating system on
which the executable runs, unless that component itself accompanies
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It may happen that this requirement contradicts the license
restrictions of other proprietary libraries that do not normally
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use both them and the Library together in an executable that you
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a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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3. You may opt to apply the terms of the ordinary
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License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding
machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that
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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the
object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany
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machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2)

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1.282 yajl 2.1.0-25.el9

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1.283 httpclient-cache 4.3

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 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/
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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/UserNotice.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: IOHelper.java 2744 2013-06-25 20:20:29Z dfisher $

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 Author: Middleware Services

Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/IOHelper.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractAlgorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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Author: Middleware Services
Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/AbstractAlgorithm.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Base64FilterOutputStream.java 2744 2013-06-25 20:20:29Z dfisher $

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Author: Middleware Services
Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/Base64FilterOutputStream.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Reasons.java 2745 2013-06-25 21:16:10Z dfisher $

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Author: Middleware Services
Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/Reasons.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PublicKeyUtils.java 2744 2013-06-25 20:20:29Z dfisher $

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Author: Middleware Services
Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/asymmetric/PublicKeyUtils.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Converter.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/Converter.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: RC2.java 2744 2013-06-25 20:20:29Z dfisher $

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 Author: Middleware Services
 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/RC2.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: AES.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/AES.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: RelativeDistinguishedName.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/RelativeDistinguishedName.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Algorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/Algorithm.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractEncryptionCli.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/AbstractEncryptionCli.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractPKCSKeyGenerator.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/AbstractPKCSKeyGenerator.java
No license file was found, but licenses were detected in source scan.

/*
$Id: RipeMD320.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/RipeMD320.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PolicyInformation.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/PolicyInformation.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: PrivateKeyCredentialReader.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/PrivateKeyCredentialReader.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: DigestAlgorithm.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/DigestAlgorithm.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: SHA1.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/SHA1.java
No license file was found, but licenses were detected in source scan.

/*
$Id: MD4.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/MD4.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AccessDescriptionList.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/AccessDescriptionList.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PolicyQualifierInfo.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/PolicyQualifierInfo.java
No license file was found, but licenses were detected in source scan.

/*
$Id: DigestCli.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/DigestCli.java
No license file was found, but licenses were detected in source scan.

/*
$Id: KeyUsageBits.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/KeyUsageBits.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: SignatureCli.java 2745 2013-06-25 21:16:10Z dfisher $

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 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/signature/SignatureCli.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: ECDSASignature.java 2744 2013-06-25 20:20:29Z dfisher $

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 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/signature/ECDSASignature.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: HexConverter.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/HexConverter.java
No license file was found, but licenses were detected in source scan.

/*
$Id: GeneralNameList.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/GeneralNameList.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Base64FilterInputStream.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/Base64FilterInputStream.java
No license file was found, but licenses were detected in source scan.

/*
$Id: OpenSSLEncryptionScheme.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/OpenSSLEncryptionScheme.java
No license file was found, but licenses were detected in source scan.

/*
$Id: DES.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/DES.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractEncryptionAlgorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/AbstractEncryptionAlgorithm.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: RC5.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/RC5.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: MD5.java 2744 2013-06-25 20:20:29Z dfisher $

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 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/MD5.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: PemHelper.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/PemHelper.java
No license file was found, but licenses were detected in source scan.

/*
$Id: KeyUsage.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/KeyUsage.java
No license file was found, but licenses were detected in source scan.

/*
$Id: SHA512.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/SHA512.java
No license file was found, but licenses were detected in source scan.

/*
$Id: SecretKeyUtils.java 2744 2013-06-25 20:20:29Z dfisher $

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Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/SecretKeyUtils.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PBES2EncryptionScheme.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/PBES2EncryptionScheme.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractRandomizableAlgorithm.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/AbstractRandomizableAlgorithm.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: PBES1Algorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pkcs/PBES1Algorithm.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: DNFormatter.java 2745 2013-06-25 21:16:10Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/DNFormatter.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: RDNSequenceIterator.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/RDNSequenceIterator.java
No license file was found, but licenses were detected in source scan.

/*
$Id: RSA.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/asymmetric/RSA.java
No license file was found, but licenses were detected in source scan.

/*
$Id: TeePrintStream.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/TeePrintStream.java
No license file was found, but licenses were detected in source scan.

/*
$Id: X509CertificateCredentialReader.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/X509CertificateCredentialReader.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AlgorithmSpec.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/AlgorithmSpec.java
No license file was found, but licenses were detected in source scan.

/*
$Id: KeyIdentifier.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/KeyIdentifier.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: SignatureAlgorithm.java 2744 2013-06-25 20:20:29Z dfisher $

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 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/signature/SignatureAlgorithm.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: Randomizable.java 2745 2013-06-25 21:16:10Z dfisher $

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 Author: Middleware Services
 Email: middleware@vt.edu
 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/Randomizable.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: DistributionPointList.java 2744 2013-06-25 20:20:29Z dfisher $

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 Author: Middleware Services

Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/DistributionPointList.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PBES2CipherGenerator.java 2745 2013-06-25 21:16:10Z dfisher $

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Author: Middleware Services
Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pkcs/PBES2CipherGenerator.java
No license file was found, but licenses were detected in source scan.

/*
$Id: SymmetricCli.java 2745 2013-06-25 21:16:10Z dfisher $

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Author: Middleware Services
Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/SymmetricCli.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Convert.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/Convert.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PBKDF2Parameters.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pkcs/PBKDF2Parameters.java
No license file was found, but licenses were detected in source scan.

/*
$Id: RC4.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/RC4.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: ReasonFlags.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/ReasonFlags.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: AbstractEncodingConverter.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/AbstractEncodingConverter.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: PBKDF1KeyGenerator.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/PBKDF1KeyGenerator.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractDSASignature.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/signature/AbstractDSASignature.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AuthorityKeyIdentifier.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/AuthorityKeyIdentifier.java
No license file was found, but licenses were detected in source scan.

/*
$Id: DistributionPoint.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/DistributionPoint.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PKCS12KeyGenerator.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/PKCS12KeyGenerator.java
No license file was found, but licenses were detected in source scan.

/*
$Id: CryptReader.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/CryptReader.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: EncryptPropertyTask.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/tasks/EncryptPropertyTask.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: EncryptionAlgorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/EncryptionAlgorithm.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: HexFilterInputStream.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/HexFilterInputStream.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Base64Converter.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/Base64Converter.java
No license file was found, but licenses were detected in source scan.

/*
$Id: RipeMD256.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/RipeMD256.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Whirlpool.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/Whirlpool.java
No license file was found, but licenses were detected in source scan.

/*
$Id: CredentialReader.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/CredentialReader.java
No license file was found, but licenses were detected in source scan.

/*
$Id: KeyPurposeIdList.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/KeyPurposeIdList.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: AccessDescription.java 2745 2013-06-25 21:16:10Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/AccessDescription.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: AbstractX509CredentialReader.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/AbstractX509CredentialReader.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: CAST6.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/CAST6.java
No license file was found, but licenses were detected in source scan.

/*
$Id: DERHelper.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/DERHelper.java
No license file was found, but licenses were detected in source scan.

/*
$Id: GeneralNameType.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/GeneralNameType.java
No license file was found, but licenses were detected in source scan.

/*
$Id: RSASignature.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/signature/RSASignature.java
No license file was found, but licenses were detected in source scan.

/*
$Id: PBES2Algorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pkcs/PBES2Algorithm.java
No license file was found, but licenses were detected in source scan.

/*
$Id: ExtensionReader.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/ExtensionReader.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: PBKDF2KeyGenerator.java 2744 2013-06-25 20:20:29Z dfisher $

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 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/PBKDF2KeyGenerator.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: RipeMD128.java 2744 2013-06-25 20:20:29Z dfisher $

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 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/RipeMD128.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: Tiger.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/Tiger.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Serpent.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/Serpent.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AttributeTypeAndValue.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/AttributeTypeAndValue.java
No license file was found, but licenses were detected in source scan.

/*
$Id: Twofish.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/Twofish.java
No license file was found, but licenses were detected in source scan.

/*
$Id: CAST5.java 2744 2013-06-25 20:20:29Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/CAST5.java
No license file was found, but licenses were detected in source scan.

/*
$Id: BitString.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/BitString.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: DESede.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/symmetric/DESede.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: ExtensionType.java 2745 2013-06-25 21:16:10Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2745 $
 Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/ExtensionType.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: GeneralName.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/GeneralName.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AsymmetricCli.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/asymmetric/AsymmetricCli.java
No license file was found, but licenses were detected in source scan.

/*
$Id: KeyGenerator.java 2744 2013-06-25 20:20:29Z dfisher $

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Version: $Revision: 2744 $
Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/KeyGenerator.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AttributeType.java 2745 2013-06-25 21:16:10Z dfisher $

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Email: middleware@vt.edu
Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/AttributeType.java
No license file was found, but licenses were detected in source scan.

/*
$Id: KeyPurposeId.java 2745 2013-06-25 21:16:10Z dfisher $

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Version: $Revision: 2745 $
Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/KeyPurposeId.java
No license file was found, but licenses were detected in source scan.

/*
$Id: AbstractCli.java 2745 2013-06-25 21:16:10Z dfisher $

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Updated: $Date: 2013-06-25 17:16:10 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):

* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/AbstractCli.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: AbstractVariableKeySizeEncryptionScheme.java 2744 2013-06-25 20:20:29Z dfisher $

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 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/AbstractVariableKeySizeEncryptionScheme.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: X509CertificatesCredentialReader.java 2744 2013-06-25 20:20:29Z dfisher $

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 Email: middleware@vt.edu
 Version: $Revision: 2744 $
 Updated: $Date: 2013-06-25 16:20:29 -0400 (Tue, 25 Jun 2013) $
*/

Found in path(s):
* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/X509CertificatesCredentialReader.java
No license file was found, but licenses were detected in source scan.

/*
 $Id: AbstractEncodingFilterInputStream.java 2744 2013-06-25 20:20:29Z dfisher $

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*/

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/AbstractEncodingFilterInputStream.java
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$Id: PBEParameter.java 2744 2013-06-25 20:20:29Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/MD2.java
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/*
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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/types/NoticeReference.java
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$Id: EncryptionScheme.java 2744 2013-06-25 20:20:29Z dfisher $

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$Id: PBES1EncryptionScheme.java 2744 2013-06-25 20:20:29Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/pbe/PBES1EncryptionScheme.java
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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/CryptProvider.java
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*/

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/SHA384.java
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/*
 $Id: LDAPv3DNFormatter.java 2745 2013-06-25 21:16:10Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/LDAPv3DNFormatter.java
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/*
$Id: SymmetricAlgorithm.java 2745 2013-06-25 21:16:10Z dfisher $

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$Id: X509CRLCredentialReader.java 2744 2013-06-25 20:20:29Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/digest/SHA256.java
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 $Id: HexFilterOutputStream.java 2745 2013-06-25 21:16:10Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/io/HexFilterOutputStream.java
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$Id: Skipjack.java 2744 2013-06-25 20:20:29Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/util/CryptWriter.java
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$Id: DNUtils.java 2744 2013-06-25 20:20:29Z dfisher $

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* /vt-crypt-2-2-sources-4-jar/edu/vt/middleware/crypt/x509/DNUtils.java

1.285 ostruct 0.3.1

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1.286 quota 4.09-4.el9

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1.291 fluent-hc 4.3

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1.292 tylerb-graceful 1.2.15

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1.293 jsoup 1.18.3

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1.294 etc 1.3.0

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1.295 rdoc 6.3.4.1

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1.296 log-rotate 3.18.0-9.el9

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1.302 jackson-xc 2.16.1

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1.304 numpy 1.23.5

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1.305 libpciaccess 0.16-7.el9

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1.306 asm 5.0.4

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/**
    * Creates a new JSRInliner. <i>Subclasses must not use this
    * constructor</i>. Instead, they must use the
    * {@link #JSRInlinerAdapter(int, MethodVisitor, int, String, String, String, String[])}
    * version.
    *
    * @param mv
    *
   the <code>MethodVisitor</code> to send the resulting inlined
    *           method code to (use <code>null</code> for none).
    * @param access
    *           the method's access flags (see {@link Opcodes}). This
    *           parameter also indicates if the method is synthetic and/or
    *           deprecated.
```

```
   * @param name
   *          the method's name.
   * @param desc
   *          the method's descriptor (see {@link Type}).
   * @param signature
   *          the method's signature. May be <tt>null</tt>.
   * @param exceptions
   *          the internal names of the method's exception classes (see
   *          {@link Type#getInternalName() getInternalName}). May be
   *          <tt>null</tt>.
   * @throws IllegalStateException
   *           If a subclass calls this constructor.
   */
```

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```

```
/**
   * Constructs a new {@link LocalVariableAnnotationNode}. <i>Subclasses must
   * not use this constructor</i>. Instead, they must use the
   * {@link #LocalVariableAnnotationNode(int, TypePath, LabelNode[], LabelNode[], int[], String)}
   * version.
   *

   * @param typeRef
   *          a reference to the annotated type. See {@link TypeReference}.
   * @param typePath
   *          the path to the annotated type argument, wildcard bound, array
   *          element type, or static inner type within 'typeRef'. May be
   *          <tt>null</tt> if the annotation targets 'typeRef' as a whole.
   * @param start
   *          the fist instructions corresponding to the continuous ranges
   *          that make the scope of this local variable (inclusive).
   * @param end
   *          the last instructions corresponding to the continuous ranges
   *          that make the scope of this local variable (exclusive). This
   *          array must have the same size as the 'start' array.
   * @param index
   *          the local variable's index in each range. This array must have
   *          the same size as the 'start' array.
   * @param desc
   *          the class descriptor of
 the annotation class.
   */
```

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/**
   * Constructs a new {@link ClassNode}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the {@link #ClassNode(int)}
   * version.
   *
   * @throws IllegalStateException
   *             If a subclass calls this constructor.
    */

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/**
   * Creates a new {@link GeneratorAdapter}. Subclasses must not use this
   * constructor. Instead, they must use the
   * {@link #GeneratorAdapter(int, MethodVisitor, int, String, String)}
   * version.
   *
   * @param mv
   *   the
 method visitor to which this adapter delegates calls.
   * @param access
   *   the method's access flags (see {@link Opcodes}).
   * @param name
   *   the method's name.
   * @param desc
   *   the method's descriptor (see {@link Type Type}).
   * @throws IllegalStateException
   *   If a subclass calls this constructor.
   */

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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/xml/Processor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/xml/SAXFieldAdapter.java
*
/asm-5-0-4-sources-6-jar/org/objectweb/asm/xml/ASMContentHandler.java
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*/
/**
   * Creates a new {@link AnalyzerAdapter}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the
   * {@link #AnalyzerAdapter(int, String, int, String, String, MethodVisitor)}
   * version.
   *
   * @param owner
   *
     the owner's class name.
   * @param access
   *          the method's access flags (see {@link Opcodes}).
   * @param name
   *          the method's name.
   * @param desc
   *          the method's descriptor (see {@link Type Type}).
   * @param mv
```

```
   *          the method visitor to which this adapter delegates calls. May
   *          be <tt>null</tt>.
   * @throws IllegalStateException
   *           If a subclass calls this constructor.
   */
```

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*/
/**
   * Constructs a new {@link Textifier}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the {@link #Textifier(int)}
   * version.
   *
   * @throws IllegalStateException
   *           If a subclass calls this constructor.
```

*/

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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/Textifier.java
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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/ASMifiable.java
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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Label.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Type.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/AnnotationWriter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/SimpleVerifier.java
*
 /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/NameMapping.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/Printer.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/Shrinker.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/Method.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/CheckAnnotationAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/RemappingSignatureAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/RemappingFieldAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/ClassWriter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Attribute.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/SourceInterpreter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/MethodInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/MethodVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/TraceFieldVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/SourceValue.java
*
 /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/AdviceAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/Remapper.java

* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/BasicValue.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/FieldWriter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/ByteVector.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/AbstractInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/Interpreter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/JumpInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/TableSwitchGenerator.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/FieldNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/FieldInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/LabelNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/ClassConstantsCollector.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/CheckSignatureAdapter.java
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 /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/CodeSizeEvaluator.java
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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/MultiANewArrayInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/ClassReader.java
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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/StaticInitMerger.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/AnnotationConstantsCollector.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/AnnotationVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/InsnNode.java
*
 /asm-5-0-4-sources-6-jar/org/objectweb/asm/signature/SignatureReader.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/LdcInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/LookupSwitchInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/LineNumberNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/JarOptimizer.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/LocalVariablesSorter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/TraceAnnotationVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/Constant.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/TypeAnnotationNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Handle.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/TryCatchBlockNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/ClassOptimizer.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/ASMifier.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/TraceClassVisitor.java
*
 /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/TableSwitchInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/Frame.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/InvokeDynamicInsnNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/AnalyzerException.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/Subroutine.java

* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/SerialVersionUIDAdder.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/InnerClassNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/ParameterNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/LocalVariableNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/Analyzer.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/analysis/SmallSet.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/RemappingAnnotationAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/TypeInsnNode.java
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/asm-5-0-4-sources-6-jar/org/objectweb/asm/util/TraceSignatureVisitor.java
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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/MethodOptimizer.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/CheckFieldAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Frame.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/CheckMethodAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/SimpleRemapper.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/AnnotationNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/FieldVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Context.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/TraceMethodVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/MethodConstantsCollector.java
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/asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/FieldConstantsCollector.java
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* /asm-5-0-4-sources-6-jar/org/objectweb/asm/commons/TryCatchBlockSorter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Item.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Edge.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/ClassVisitor.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/tree/FrameNode.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/optimizer/ConstantPool.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/Opcodes.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/util/CheckClassAdapter.java
* /asm-5-0-4-sources-6-jar/org/objectweb/asm/signature/SignatureVisitor.java

1.307 libnl3 3.11.0-1.el9

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* /jclouds-master-zip/jclouds-master/apis/docker/src/test/resources/SimpleDockerfile
No license file was found, but licenses were detected in source scan.

```
# The ASF licenses this file to You under the Apache License, Version 2.0
# (the "License"); you may not use this file except in compliance with
# the License.  You may obtain a copy of the License at
#     http://www.apache.org/licenses/LICENSE-2.0
# distributed under the License is distributed on an "AS IS" BASIS,
```

Found in path(s):
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/abort.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/installRuby.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/findPid.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/sourceEnvFile.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/installOpenJDK.sh
* /jclouds-master-zip/jclouds-master/.travis.yml
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/validateEnvFile.sh
*
 /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/installGit.sh
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/resources/Dockerfile
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/forget.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/installJDKFromURL.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/resources/functions/setupPublicCurl.sh
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/test/resources/test_ebs.sh
No license file was found, but licenses were detected in source scan.

```
/*
* Licensed to the Apache Software Foundation (ASF) under one or more
* contributor license agreements.  See the NOTICE file distributed with
* this work for additional information regarding copyright ownership.
* The ASF licenses this file to You under the Apache License, Version 2.0
* (the "License"); you may not use this file except in compliance with
* the License.  You may obtain a copy of the License at
*
*     http://www.apache.org/licenses/LICENSE-2.0
*
* Unless required by applicable law or agreed to in writing, software
* distributed under the License is distributed on an "AS IS" BASIS,
* WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied.
* See the License for the specific language governing permissions and
* limitations under the License.
*/
```

Found in path(s):
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/binders/BindUserMetadataToHeaders.java
*
/jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/compute/config/AzureComputeServiceContextModule.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/StorageService.java
* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azureblob/blobstore/functions/ContainerToResourceMetadata.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/domain/VolumeAttachment.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/test/java/org/jclouds/googlecloudstorage/features/ObjectApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/test/java/org/jclouds/sqs/options/ListQueuesOptionsTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/v2_0/domain/Access.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/functions/internal/GleSYSTypeAdapters.java
*
/jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/ovf/NetworkSection.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/OSProfile.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/DescribeSecurityGroupsResponseHandler.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/domain/SQSConstants.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/callables/InitScriptConfigurationForTasks.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/functions/GroupToBootScriptTest.java

* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/parse/GetResourceRecordsOfResourceRecordResponseTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/NetworkSegment.java
*
/jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/binders/GroupNameTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/JcloudsVersion.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/functions/DefaultCredentialsFromImageOrOverridingCredentials.java
* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/handlers/Route53ErrorHandlerTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/v3/domain/Token.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/extensions/QuotaApi.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/CreateNetworkOptions.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/functions/ListOfKeyValuesDelimitedByBlankLinesToDriveInfoSet.java
*
/jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/fallbacks/PropagateIfRetryAfter.java
* /jclouds-master-zip/jclouds-master/apis/oauth/src/test/java/org/jclouds/oauth/v2/config/PrivateKeySupplierTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/xml/internal/JAXBParser.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/test/testng/UnitTestStatusListener.java
* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/test/java/org/jclouds/openstack/swift/v1/blobstore/RegionScopedBlobStoreContextLiveTest.java
* /jclouds-master-zip/jclouds-master/common/openstack/src/test/java/org/jclouds/openstack/options/BaseListOptionsTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/Iso3166.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/compute/functions/ResourcesMockTest.java
*
/jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/ListMultipartUploadResponse.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/DescribeSubnetsResponseHandler.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/config/PacketComputeParserModule.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/xml/DimensionHandler.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/features/ImageApiMockTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/ObjectMetadata.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/main/java/org/jclouds/rackspace/clouddns/v1/CloudDNSExceptions.java
* /jclouds-master-zip/jclouds-

master/core/src/test/java/org/jclouds/http/functions/ParseFirstJsonValueNamedTest.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/compute/functions/Resources.java
*
/jclouds-master-zip/jclouds-master/providers/rackspace-cloudservers-us/src/test/java/org/jclouds/rackspace/cloudservers/us/features/CloudServersUSFlavorApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/test/java/org/jclouds/googlecloudstorage/internal/BaseGoogleCloudStorageApiMockTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/catalog/functions/InternalURL.java
* /jclouds-master-zip/jclouds-master/apis/byon/src/main/java/org/jclouds/byon/config/CacheNodeStoreModule.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/LocalNetworkGatewayApi.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/compute/GoogleComputeEngineServiceLiveTest.java
*
/jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/binders/BindAccountSecurityGroupPairsToIndexedQueryParams.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/VirtualMachineApiTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/test/java/org/jclouds/s3/xml/PartIdsFromHttpResponseTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/main/java/org/jclouds/openstack/cinder/v1/domain/ZoneState.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/test/java/org/jclouds/dynect/v3/parse/GetAAAARecordResponseTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/AzureComputeApi.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/domain/VolumeBuilderTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/suppliers/WellKnownImageSupplier.java
*
/jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/ProfitBricksProviderMetadataTest.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/domain/internal/ImageTemplateImpl.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/test/java/org/jclouds/googlecloudstorage/features/BucketAccessControlsApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/features/FirewallApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/domain/RDSConstants.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/ListZonesOptionsTest.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudblockstorage-uk/src/main/java/org/jclouds/rackspace/cloudblockstorage/uk/CloudBlockStorageUKProviderMetadata.java
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/jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/functions/RegionToEndpointOrProviderIfNull.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/compute/loaders/CreateSecurityGroupIfNeeded.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/Secret.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/test/java/org/jclouds/googlecloudstorage/parse/ParseGoogleCloudStorageObject.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/options/CreateServerOptions.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/parse/NetworkParseTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/Certificate.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/functions/ParseCookbookVersionFromJsonTest.java
*
/jclouds-master-zip/jclouds-master/providers/gogrid/src/test/java/org/jclouds/gogrid/features/GridLoadBalancerApiTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/functions/MapToDriveInfoTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/LocationApi.java
* /jclouds-master-zip/jclouds-master/apis/sts/src/test/java/org/jclouds/sts/internal/BaseSTSApiExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/options/SaveAlarmOptions.java
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* /jclouds-master-zip/jclouds-master/providers/softlayer/src/test/java/org/jclouds/softlayer/parse/VirtualGuestBlockDeviceTemplateGroupParseTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/extensions/HostAdministrationApiExpectTest.java
*
/jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/util/ChefUtilsTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/compute/options/IpOptions.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/DeleteTemplateOptions.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/http/parser/loadbalancer/LoadBalancerIdOnlyResponseHandlerTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/domain/ExtractArchiveResponse.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/GoGridApiMetadata.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/domain/SshKey.java
* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azureblob/predicates/validators/ContainerNameValidator.java
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/jclouds-master-zip/jclouds-master/providers/softlayer/src/test/java/org/jclouds/softlayer/compute/functions/VirtualGuestToNodeMetadataTest.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/main/java/org/jclouds/ultradns/ws/domain/Task.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/handlers/AzureComputeErrorHandler.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/VaultProperties.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/GoogleComputeEngineFallbacks.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/PublicIPAddressApi.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/binder/storage/ConnectStorageToServerRequestBinder.java
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/jclouds-master-zip/jclouds-master/apis/oauth/src/main/java/org/jclouds/oauth/v2/config/Authorization.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/strategy/impl/AdaptingComputeServiceStrategies.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/main/java/org/jclouds/googlecloudstorage/blobstore/functions/ObjectListToStorageMetadata.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/compute/strategy/EC2RebootNodeStrategy.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/main/java/org/jclouds/docker/features/NetworkApi.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/functions/ListOfMapsToListOfKeyValuesDelimitedByBlankLinesTest.java
* /jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/aws/xml/SessionCredentialsHandler.java
*
/jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/strategy/DeleteDirectoryStrategy.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/test/java/org/jclouds/rackspace/clouddns/v1/features/ReverseDNSApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/handlers/GleSYSErrorHandler.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListVirtualMachinesOptions.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/functions/SubnetsToPagedIterable.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/GoGridProviderMetadata.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/functions/ParseImageIdFromLocationHeader.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/strategy/CustomizeNodeAndAddToGoodMapOrPutExceptionIntoBadMapTest.java
*

/jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListOSTypesOptions.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/date/internal/SimpleDateFormatDateServiceTest.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/domain/Attachment.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/test/java/org/jclouds/dynect/v3/features/RecordApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/options/AddServerOptions.java
* /jclouds-master-zip/jclouds-master/apis/route53/src/main/java/org/jclouds/route53/xml/ChangeHandler.java
* /jclouds-master-zip/jclouds-master/apis/openstack-trove/src/main/java/org/jclouds/openstack/trove/v1/binders/BindCreateInstanceToJson.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudnetworks-uk/src/main/java/org/jclouds/rackspace/cloudnetworks/uk/CloudNetworksUKProviderMetadata.java
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/jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/aws/AWSResponseException.java
* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/test/java/org/jclouds/digitalocean2/ssh/DSAKeysTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/predicates/DriveClaimed.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/http/parser/nic/NicResponseHandler.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/DomainAccountApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/compute/functions/PlanToHardware.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/main/java/org/jclouds/rackspace/cloudloadbalancers/v1/domain/Metadata.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/functions/ClientForGroupTest.java
*
/jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/reference/GleSYSConstants.java
* /jclouds-master-zip/jclouds-master/common/openstack/src/main/java/org/jclouds/openstack/reference/AuthHeaders.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudservers-us/src/test/java/org/jclouds/rackspace/cloudservers/us/compute/extensions/CloudServersUSVolumeAttachmentExtensionLiveTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/functions/ReleasePayloadAndReturn.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudnetworks-us/src/test/java/org/jclouds/rackspace/cloudnetworks/us/CloudNetworksUSPortApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/reference/S3Constants.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/predicates/DirectionalPoolPredicatesTest.java
*
/jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/handlers/InvalidateSessionAndRetryOn401AndLogoutOnClose.java
* /jclouds-master-zip/jclouds-

master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/compute/function/StorageToVolume.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/apis/ApiMetadata.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/functions/ExpandProperties.java
* /jclouds-master-zip/jclouds-master/apis/openstack-
swift/src/test/java/org/jclouds/openstack/swift/v1/features/BulkApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/test/java/org/jclouds/glesys/features/IpApiExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-
cinder/src/test/java/org/jclouds/openstack/cinder/v1/internal/BaseCinderApiLiveTest.java
* /jclouds-master-zip/jclouds-
master/apis/ec2/src/main/java/org/jclouds/ec2/binders/BindBundleIdsToIndexedFormParams.java
*
/jclouds-master-zip/jclouds-
master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/features/VMGroupApi.java
* /jclouds-master-zip/jclouds-
master/providers/packet/src/test/java/org/jclouds/packet/compute/PacketComputeProviderMetadataTest.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/features/ImageApiLiveTest.java
* /jclouds-master-zip/jclouds-
master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/features/GlobalDomainApi.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-
ws/src/main/java/org/jclouds/ultradns/ws/UltraDNSWSResponseException.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-
engine/src/main/java/org/jclouds/googlecomputeengine/domain/Project.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova-
ec2/src/test/java/org/jclouds/openstack/nova/ec2/features/NovaEC2AMIApiLiveTest.java
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/jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/config/SQSHttpApiModule.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/config/ChefBootstrapModule.java
* /jclouds-master-zip/jclouds-
master/compute/src/main/java/org/jclouds/compute/callables/BlockUntilInitScriptStatusIsZeroThenReturnOutput.ja
va
* /jclouds-master-zip/jclouds-
master/providers/dynect/src/main/java/org/jclouds/dynect/v3/DynECTApiMetadata.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/apis/openstack-
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* /jclouds-master-zip/jclouds-master/providers/google-compute-
engine/src/main/java/org/jclouds/googlecomputeengine/options/UrlMapOptions.java
*
/jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/util/AutomaticHardwareIdSpec.java
* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/domain/UploadFileResponse.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/features/GridServerApi.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-
ws/src/test/java/org/jclouds/ultradns/ws/parse/GetDirectionalPoolsByZoneResponseTest.java

* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudservers-uk/src/test/java/org/jclouds/rackspace/cloudservers/uk/compute/CloudServersUKTemplateBuilderLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/functions/BaseDriveToMap.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/features/SessionApi.java
* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/test/java/org/jclouds/openstack/swift/v1/TemporaryUrlSignerMockTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/features/RegionApi.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/DescribeInstancesResponseHandler.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/xml/AttributesHandler.java
* /jclouds-master-zip/jclouds-master/apis/oauth/src/test/java/org/jclouds/oauth/v2/AuthorizationApiMockTest.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/cim/functions/HardwareBuilderFromResourceAllocations.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/AddClusterOptionsTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/DescribeVolumesResponseHandler.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/v2_0/auth/V2AuthenticationApi.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/test/java/org/jclouds/gogrid/features/GridIpApiTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/SessionApiExpectTest.java

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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/handlers/InvalidateSessionAndRetryOn401AndLogoutOnCloseTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-trove/src/test/java/org/jclouds/openstack/trove/v1/parse/ParseFlavorListTest.java
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* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/main/java/org/jclouds/aws/ec2/compute/config/AWSEC2BindComputeSuppliersByClass.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListDomainChildrenOptions.java
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* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/handlers/UltraDNSWSErrorHandlerTest.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/LoadBalancerApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/features/ZoneApi.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/LocationApiMockTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/handlers/ParseS3ErrorFromXmlContent.java
* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/test/java/org/jclouds/digitalocean2/internal/BaseDigitalOcean2ApiMockTest.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/options/CreateServerOptions.java
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* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/filters/AwsHostNameUtils.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListServiceOfferingsOptions.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/VirtualMachineInstance.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudfiles-us/src/test/java/org/jclouds/rackspace/cloudfiles/us/features/CloudFilesUSDynamicLargeObjectApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/compute/function/ServerToNodeMetadataTest.java
*
/jclouds-master-zip/jclouds-master/providers/elastichosts-sjc-c/src/test/java/org/jclouds/elastichosts/compute/ElasticHostsSanJoseComputeServiceLiveTest.java

* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/UltraDNSWSApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/domain/Network.java
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* /jclouds-master-zip/jclouds-master/providers/packet/src/test/java/org/jclouds/packet/compute/internal/BasePacketApiLiveTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/logging/ConsoleLogger.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/functions/ParseFirewalls.java
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* /jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/binders/BindHexEncodedMD5sToJsonPayloadTest.java
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* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/RunNodesException.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/predicates/VirtualMachineExpunged.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/NetworkInterfaceConfiguration.java

* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/VirtualMachineScaleSetDNSSettings.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/http/handlers/DelegatingErrorHandlerTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/internal/S3ObjectImpl.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/util/InetAddresses2Test.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/test/java/org/jclouds/s3/options/CopyObjectOptionsTest.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/test/java/org/jclouds/glesys/parse/ParseFullIpDetailsTest.java
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* /jclouds-master-zip/jclouds-master/apis/byon/src/test/java/org/jclouds/byon/suppliers/SupplyFromProviderURIOrNodesPropertyTest.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/compute/functions/ContainerToNodeMetadataTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-trove/src/test/java/org/jclouds/openstack/trove/v1/internal/BaseTroveApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/java/org/jclouds/scriptbuilder/statements/chef/InstallChefGems.java
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* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/options/ServerStatusOptions.java
* /jclouds-master-zip/jclouds-master/providers/elastichosts-syd-v/src/test/java/org/jclouds/elastichosts/compute/ElasticHostsSydneyComputeServiceLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/test/java/org/jclouds/digitalocean2/compute/functions/DropletStatusToStatusTest.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/util/TypeTokenUtils.java
* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azureblob/domain/BlobBlockProperties.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/domain/internal/NullEqualToIsParentOrIsGrandparentOfCurrentLocation.java
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piLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/Version.java
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* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/domain/Archive.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/parse/GetAccountLevelDirectionalGroupsResponseTest.java
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* /jclouds-master-zip/jclouds-master/providers/gogrid/src/test/java/org/jclouds/gogrid/functions/ParseServerNameToCredentialsMapFromJsonResponseTest.java
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/jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/domain/InstanceState.java
* /jclouds-master-zip/jclouds-master/providers/aws-route53/src/test/java/org/jclouds/aws/route53/AWSRoute53ProviderTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/main/java/org/jclouds/aws/ec2/functions/ImportOrReturnExistingKeypair.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/providers/internal/UpdateProviderMetadataFromProperties.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/main/java/org/jclouds/googlecloudstorage/options/InsertObjectOptions.java
* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/main/java/org/jclouds/digitalocean2/handlers/DigitalOcean2RateLimitRetryHandler.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/ovf/Property.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/rest/annotationparsing/DelegateAnnotationExpectTest.java
*
/jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/concurrent/config/ExecutorServiceModuleTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/domain/NetworkOfferingAvailabilityType.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/main/java/org/jclouds/aws/ec2/compute/config/AWSEC2ComputeServiceDependenciesModule.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/domain/OperatingSystem.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/test/java/org/jclouds/s3/binders/BindAsHostPrefixIfConfiguredNoPathTest.java

* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/functions/GroupNamingConvention.java
* /jclouds-master-zip/jclouds-master/drivers/jsch/src/main/java/org/jclouds/ssh/jsch/config/JschSshClientModule.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/VirtualNetworkApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/atmos/src/test/java/org/jclouds/atmos/binders/BindMetadataToHeadersTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/test/java/org/jclouds/aws/s3/blobstore/AWSS3BlobSignerV4ExpectTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/FrontendIPConfigurationsProperties.java
* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/options/ListContainerOptions.java
* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/binders/UploadFileBinder.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/binders/TargetPoolAddInstanceBinderTest.java
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* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/attr/BlobCapability.java
* /jclouds-master-zip/jclouds-master/providers/softlayer/src/test/java/org/jclouds/softlayer/features/DatacenterApiMockTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/main/java/org/jclouds/aws/ec2/domain/MonitoringState.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/compute/extensions/NovaSecurityGroupExtensionLiveTest.java

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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/binders/BindBundleIdsToIndexedFormParamsTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/UpdateAccountOptions.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/NetworkInterfaceCardProperties.java
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* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/test/java/org/jclouds/digitalocean2/features/KeyApiMockTest.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/vpn/VPNType.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/domain/GetMetricStatisticsResponse.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/parse/UltraWSExceptionTest.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/parse/ParseUrlMapListTest.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/reflect/Reflection2Test.java
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* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/SQSApi.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/HttpUtils.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/domain/Plan.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/functions/ReturnTrueIfGroupACLIsOtherRead.java
* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/main/java/org/jclouds/openstack/cinder/v1/features/VolumeTypeApi.java

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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/AddSecondaryStorageOptions.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/functions/ParseImageFromJsonResponse.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/providers/JcloudsTestBlobStoreProviderMetadata.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/test/java/org/jclouds/openstack/keystone/v3/features/RegionApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/main/java/org/jclouds/digitalocean2/functions/BaseToPagedIterable.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/main/java/org/jclouds/docker/compute/config/LoginPortLookupModule.java
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* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/strategy/ListContainerStrategy.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/CloudStackContext.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/features/GlobalStoragePoolApi.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/parse/ParseAddressListTest.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/features/NetworkApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/domain/B2Error.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/config/NovaParserModule.java
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* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/domain/Option.java
* /jclouds-master-zip/jclouds-master/apis/oauth/src/main/java/org/jclouds/oauth/v2/config/CredentialType.java
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* /jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/sts/config/STSHttpApiModule.java
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* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azure/storage/AzureStorageResponseException.java
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* /jclouds-master-zip/jclouds-master/providers/dynect/src/test/java/org/jclouds/dynect/v3/features/GeoServiceApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/reference/AtmosErrorCode.java
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* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/main/java/org/jclouds/aws/ec2/binders/BindVpcIdsToIndexedFormParams.java

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* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/domain/Processor.java
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* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/test/java/org/jclouds/aws/s3/blobstore/integration/AWSS3ServiceIntegrationLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/main/java/org/jclouds/openstack/cinder/v1/features/SnapshotApi.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/options/CreateBackupOfServerOptions.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/options/CreateVolumeOptionsTest.java
* /jclouds-master-zip/jclouds-
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*
/jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/options/SearchOptions.java
* /jclouds-master-zip/jclouds-
master/apis/ec2/src/main/java/org/jclouds/ec2/binders/BindUserIdsToIndexedFormParams.java

* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/features/ElasticBlockStoreApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/VPCApiMockTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/test/java/org/jclouds/s3/xml/BucketLoggingHandlerTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/UpdateNetworkOfferingOptionsTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/domain/StoragePool.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/internal/BaseComputeServiceContextLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/DeleteISOOptions.java
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 /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/functions/MapToServerInfoTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/domain/regionscoped/Hypervisor.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/CreatePodOptionsTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/functions/ListOfKeyValuesDelimitedByBlankLinesToStandardDriveSetTest.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/parse/ContainersParseTest.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/main/java/org/jclouds/ultradns/ws/domain/IdAndName.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/main/java/org/jclouds/rackspace/clouddns/v1/functions/ParseOnlyRecord.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/Payload.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/events/StatementOnNodeCompletion.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/annotations/XMLResponseParser.java
* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/features/StaticLargeObjectApi.java
* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/binders/BindUserMetadataToHeadersWithPrefix.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/NetworkSecurityRuleProperties.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/features/InstanceApiTest.java

* /jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/config/ChefParserModuleTest.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/ComputeService.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/compute/functions/NovaSecurityGroupToSecurityGroup.java
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* /jclouds-master-zip/jclouds-master/apis/sts/src/test/java/org/jclouds/sts/parse/AssumeRoleResponseTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/domain/Deprecated.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/collect/TransformingMap.java
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* /jclouds-master-zip/jclouds-

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* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/main/java/org/jclouds/digitalocean2/config/DigitalOcean2HttpApiModule.java
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* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/http/parser/publicip/PublicIpListResponseHandler.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/domain/RootDeviceType.java
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* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/config/BindComputeStrategiesByClass.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/config/InvocationConfig.java
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* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/features/TaskApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/compute/internal/EC2TemplateBuilderImplTest.java
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* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/binders/BindCannedAclToRequest.java
* /jclouds-master-zip/jclouds-master/blobstore/src/test/java/org/jclouds/blobstore/binders/BindBlobToMultipartFormTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/RuleEthertype.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/binders/BindUserIdGroupPairToSourceSecurityGroupFormParamsTest.java
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* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/predicates/ResourceRecordSetPredicatesTest.java
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* /jclouds-master-zip/jclouds-master/providers/packet/src/test/java/org/jclouds/packet/features/FacilityApiLiveTest.java
* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/domain/internal/PageSetImpl.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/internal/CopyObjectResult.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/LoadBalancerApiMockTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/Ports.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/InstanceStateChangeHandler.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/compute/extensions/EC2ImageExtensionExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/main/java/org/jclouds/rackspace/clouddns/v1/binders/CreateReverseDNSToJSON.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/config/ValueOfConfigurationKeyOrNullTest.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-

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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/DescribeSnapshotsResponseHandler.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/json/internal/DeserializationConstructorAndReflectiveTypeAdapterFactoryTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListResourceLimitsOptions.java
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* /jclouds-master-zip/jclouds-master/providers/rackspace-clouddatabases-us/src/main/java/org/jclouds/rackspace/clouddatabases/us/CloudDatabasesUSProviderMetadata.java
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* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/main/java/org/jclouds/rackspace/clouddns/v1/config/CloudDNS.java
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* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/binders/MetricDataBinder.java
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* /jclouds-master-zip/jclouds-master/providers/packet/src/test/java/org/jclouds/packet/features/OperatingSystemApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/http/parser/state/GetProvisioningStateResponseHandlerTest.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/osgi/BundlesTest.java
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* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/strategy/UpdateAutomaticAttributesOnNode.java

* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/config/SwiftHttpApiModule.java
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/jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/CanonicalUser.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/suppliers/derived/RegionIdsFromRegionIdToURIKeySet.java
* /jclouds-master-zip/jclouds-master/providers/softlayer/src/main/java/org/jclouds/softlayer/domain/TagReference.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/callables/InitScriptConfigurationForTasksTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/lbaas/v1/Pools.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/v3/parsers/ParseTokenFromHttpResponse.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/parse/ParseSecurityGroupTest.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/payloads/BaseImmutableContentMetadata.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/domain/LocationBuilder.java
* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/main/java/org/jclouds/googlecloudstorage/domain/ProjectTeam.java
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* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/main/java/org/jclouds/googlecloudstorage/options/ComposeObjectOptions.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/util/HttpUtilsTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/test/java/org/jclouds/openstack/cinder/v1/internal/BaseCinderExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/suppliers/DescribeRegionsForRegionURIsMockTest.java
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* /jclouds-master-zip/jclouds-master/providers/softlayer/src/main/java/org/jclouds/softlayer/features/DatacenterApi.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/functions/ImportOrReturnExistingKeypairTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/main/java/org/jclouds/aws/ec2/AWSEC2ProviderMetadata.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/internal/GeneratedHttpRequest.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/strategy/internal/ListNodesInEnvironmentImpl.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/http/filters/StripExpectHeaderTest.java
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* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/AWSAMIApiMockTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/features/OperationApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/TagSecurityGroupLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/compute/CloudStackComputeService.java
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* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/config/BindComputeSuppliersByClass.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/domain/UserIdGroupPair.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/binders/TempAuthBinder.java
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* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/binders/BindSendMessageBatchRequestEntryWithDelaysToIndexedFormParams.java
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* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/domain/EBSConstants.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/functions/ExpandPropertiesTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/main/java/org/jclouds/googlecloudstorage/domain/templates/BucketTemplate.java
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* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/test/java/org/jclouds/cloudwatch/internal/BaseCloudWatchApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/binders/ResourceLimitToQueryParams.java
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* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/test/java/org/jclouds/googlecloudstorage/features/ResumableUploadApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/ByteSources.java
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/RateLimitExceededException.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/providers/google-compute-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-
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storage/src/main/java/org/jclouds/googlecloudstorage/options/UpdateBucketOptions.java
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* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/reference/SQSParameters.java

* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/parse/ListUsageRecordsResponseTest.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/domain/LoadBalancerPersistenceType.java
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* /jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/domain/Session.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/MetricsApi.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/test/java/org/jclouds/openstack/neutron/v2/extensions/lbaas/v1/LBaaSApiMockTest.java
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* /jclouds-master-zip/jclouds-master/apis/oauth/src/test/java/org/jclouds/oauth/v2/filters/TestJWTBearerTokenFlow.java
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* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/SpotInstanceApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/suppliers/derived/ZoneIdsFromZoneIdToURIKeySet.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/CloudStackApiMetadata.java
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* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/DeleteResult.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/options/DescribeSpotPriceHistoryOptionsTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/binders/BindSnapshotPolicyScheduleToQueryParam.java
* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/domain/MultipartPart.java
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* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/parse/GetRRLoadBalancingPoolsByZoneResponseTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/VNICType.java

* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/VirtualMachineScaleSetIpConfiguration.java
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* /jclouds-master-zip/jclouds-master/apis/sts/src/test/java/org/jclouds/aws/util/AWSUtilsTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/domain/BackendService.java
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/jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/features/Messages.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/handlers/DelegatingRetryHandler.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/GenerateUsageRecordsOptions.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/S3Client.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/AWSSubnetApiMockTest.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/callables/RunScriptOnNodeUsingSshTest.java
* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/attr/ContainerVisibilities.java

* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/parse/SimpleParsingTests.java
* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/test/java/org/jclouds/openstack/cinder/v1/features/QuotasApiExpectTest.java
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/jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/handlers/DynECTErrorHandler.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/xml/DescribeVolumesResponseHandlerTest.java
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* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/JCloudsNativeComputeServiceAdapter.java
* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azureblob/domain/ListBlobBlocksResponse.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/options/RebuildServerOptions.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/domain/PortForwardingRule.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/features/KeyPairApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/test/java/org/jclouds/rackspace/cloudloadbalancers/v1/features/SSLTerminationApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/catalog/suppliers/RegionIdToAdminURISupplier.java
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* /jclouds-master-zip/jclouds-master/apis/sqs/src/test/java/org/jclouds/sqs/features/BulkMessageApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/features/DirectionalGroupApiExpectTest.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/location/suppliers/implicit/OnlyLocationOrFirstRegionOptionallyMatchingRegionIdTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/functions/PortsToPagedIterable.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/test/java/org/jclouds/gogrid/parse/ParseServerListTest.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/compute/ProfitBricksComputeServiceLiveTest.java

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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/util/Multimaps2.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/features/SubnetworkApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/xml/RunInstancesResponseHandlerTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova-ec2/src/test/java/org/jclouds/openstack/nova/ec2/features/NovaEC2AMIApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/parse/ListHostsResponseTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/test/java/org/jclouds/openstack/swift/v1/blobstore/integration/SwiftServiceIntegrationLiveTest.java
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/jclouds-master-zip/jclouds-master/common/googlecloud/src/main/java/org/jclouds/googlecloud/config/CurrentProject.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/main/java/org/jclouds/rackspace/cloudloadbalancers/v1/domain/LoadBalancerUsage.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/xml/IdHandler.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/logging/config/ConsoleLoggingModule.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/ovf/xml/ProductSectionHandlerTest.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/main/java/org/jclouds/rackspace/cloudloadbalancers/v1/functions/ParseLoadBalancers.java
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/jclouds-master-zip/jclouds-master/apis/s3/src/test/java/org/jclouds/s3/binders/BindS3ObjectMetadataToRequestTest.java

* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/filters/B2RetryHandler.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/domain/TargetInstance.java
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* /jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/aws/binders/BindTableToIndexedFormParams.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/domain/SnapshotPolicy.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListClustersOptions.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/test/java/org/jclouds/cloudwatch/features/AlarmApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/compute/predicates/AllNodesInGroupTerminated.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/domain/EmailOverview.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/main/java/org/jclouds/rackspace/cloudloadbalancers/v1/functions/LB.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/AccessKeyAndSecretKeyAuthenticationExpectTest.java
* /jclouds-master-zip/jclouds-master/common/openstack/src/test/java/org/jclouds/openstack/keystone/v1_1/internal/BaseKeystoneRestClientExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/binders/BindServerToPlainTextStringTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/functions/CreateSecurityGroupIfNeededTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListDiskOfferingsOptions.java
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/jclouds-master-zip/jclouds-master/providers/rackspace-cloudfiles-
us/src/test/java/org/jclouds/rackspace/cloudfiles/us/features/CloudFilesUSObjectApiLiveTest.java
* /jclouds-master-zip/jclouds-
master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/binders/BindServerToPlainTextString.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/config/SQSProperties.java
* /jclouds-master-zip/jclouds-
master/common/openstack/src/main/java/org/jclouds/openstack/keystone/v1_1/functions/PublicURLOrInternalIfNul
l.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudservers-
uk/src/test/java/org/jclouds/rackspace/cloudservers/uk/compute/extensions/CloudServersUKVolumeAttachmentExte
nsionLiveTest.java
* /jclouds-master-zip/jclouds-
master/apis/ec2/src/main/java/org/jclouds/ec2/xml/InstanceInitiatedShutdownBehaviorHandler.java

* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/rest/annotationparsing/PATCHAnnotationExpectTest.java
*
/jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/UpdateUserOptions.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/http/BaseMockWebServerTest.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/options/SaveImageOptions.java
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/test/java/org/jclouds/scriptbuilder/domain/AppendFileTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/compute/functions/NetworkSecurityRuleToIpPermission.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/domain/Environment.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/domain/AtmosError.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/predicates/InstanceStateTerminated.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-clouddatabases-us/src/test/java/org/jclouds/rackspace/clouddatabases/us/features/CloudDatabasesUSFlavorApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/binders/BindPartIdsAndETagsToRequest.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/ssh/SshKeys.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/fallbacks/lbaas/v1/EmptyVIPsFallback.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/Image.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/fallbacks/HeaderToRetryAfterException.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/features/NetworkApi.java
* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/domain/HideFileResponse.java
* /jclouds-master-zip/jclouds-master/scriptbuilder/src/test/java/org/jclouds/scriptbuilder/statements/login/ReplaceShadowPasswordEntryOfLoginUserTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/annotations/Delegate.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/ProfitBricksApiMetadata.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/compute/functions/CreateSecurityGroupIfNeeded.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/binders/BindAsHostPrefix.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/predicates/LocationPredicates.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudidentity/src/test/java/org/jclouds/rackspace/cloudidentity/v2_0/CloudIdentityApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/rackspace-
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* /jclouds-master-zip/jclouds-master/apis/openstack-
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/CloudStackApiMetadataTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/util/SnapshotPolicySchedules.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/test/java/org/jclouds/packet/features/ProjectApiMockTest.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/main/java/org/jclouds/docker/DockerApiMetadata.java
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* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/domain/ServerMetrics.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/compute/options/AWSEC2TemplateOptionsTest.java
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* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/functions/ListOfKeyValuesDelimitedByBlankLinesToStandardDriveSet.java
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/jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/features/GlobalAccountApi.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/location/suppliers/implicit/OnlyLocationOrFirstZoneTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/test/java/org/jclouds/aws/s3/blobstore/AWSS3BlobSignerExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/test/java/org/jclouds/chef/functions/ParseCookbookVersionsFromJsonTest.java

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* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/main/java/org/jclouds/openstack/cinder/v1/domain/VolumeAttachment.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/TemplateApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/internal/BaseAzureComputeApiMockTest.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/callables/BlockUntilInitScriptStatusIsZeroThenReturnOutputTest.java
* /jclouds-master-zip/jclouds-master/providers/softlayer/src/main/java/org/jclouds/softlayer/domain/VirtualGuestNetworkComponent.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/test/java/org/jclouds/openstack/keystone/v2_0/extensions/ServiceAdminApiMockTest.java
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* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/domain/MultipartUploadResponse.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/strategy/internal/DeleteAllNodesInListImpl.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/parse/NetworksParseTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/compute/suppliers/EC2HardwareSupplier.java
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* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudfiles/src/test/java/org/jclouds/rackspace/cloudfiles/v1/features/CloudFilesContainerApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/functions/ParseAsyncJobsFromHttpResponse.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/PlacementGroupApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/SubnetApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/internal/BaseProfitBricksMockTest.java
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/jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/HttpException.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/main/java/org/jclouds/rackspace/cloudloadbalancers/v1/predicates/LoadBalancerPredicates.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/test/java/org/jclouds/profitbricks/features/ServerApiLiveTest.java
* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/LocalBlobRequestSigner.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/domain/ListMetricsResponse.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/domain/internal/NullEqualToIsParentOrIsGrandparentOfCurrentLocationTest.java
* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/test/java/org/jclouds/aws/s3/AWSS3ClientLiveTest.java
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* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/java/org/jclouds/scriptbuilder/statements/ruby/InstallRubyGems.java
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* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudloadbalancers-uk/src/test/java/org/jclouds/rackspace/cloudloadbalancers/uk/CloudLoadBalancersUKLoadBalancerClientLiveTest.java
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/jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/binders/BindDriveDataToPlainTextStringTest.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/apis/Compute.java
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* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudservers-uk/src/test/java/org/jclouds/rackspace/cloudservers/uk/features/CloudServersUKServerApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/SSHKeyPairApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/scriptbuilder/src/test/java/org/jclouds/scriptbuilder/ScriptBuilderTest.java
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* /jclouds-master-zip/jclouds-master/providers/elastichosts-dal-a/src/main/java/org/jclouds/elastichosts/ElasticHostsDallasMetadata.java
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* /jclouds-master-zip/jclouds-master/providers/elastichosts-sat-p/src/test/java/org/jclouds/elastichosts/ElasticHostsPeer1SanAntonioApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/blobstore/AtmosBlobRequestSigner.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/xml/BundleTaskHandlerTest.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/functions/ToRecordIds.java
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/jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/extensions/SimpleTenantUsageApi.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/suppliers/ImplicitRegionIdSupplier.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/blobstore/config/AtmosBlobStoreContextModule.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/internal/InvokeHttpMethod.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/ListISOsOptionsTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/GraphRBACApi.java
* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/internal/BaseGenericComputeServiceContextLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/binders/BindDeleteMessageBatchRequestEntryToIndexedFormParams.java
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/jclouds-master-zip/jclouds-master/providers/rackspace-cloudfiles-us/src/test/java/org/jclouds/rackspace/cloudfiles/us/blobstore/integration/CloudFilesUSServiceIntegrationLiveTest.java

* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/functions/ParseAccountFromHeaders.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/json/internal/NullFilteringTypeAdapterFactoriesTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/FloatingIP.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/parse/GetResourceRecordsOfDNameByTypeResponseTest.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/features/RoundRobinPoolApiExpectTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/VirtualNetworkApiMockTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/features/TagApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/SnapshotApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/config/GraphRBAC.java

* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudfiles/src/test/java/org/jclouds/rackspace/cloudfiles/v1/CloudFilesApiMetadataTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/functions/ETagHeader.java
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* /jclouds-master-zip/jclouds-master/apis/oauth/src/main/java/org/jclouds/oauth/v2/config/PrivateKeySupplier.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/functions/FindSecurityGroupWithNameAndReturnTrueExpectTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/compute/extensions/NeutronSecurityGroupExtensionLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/softlayer/src/test/java/org/jclouds/softlayer/features/SoftwareDescriptionApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-trove/src/main/java/org/jclouds/openstack/trove/v1/config/TroveParserModule.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/internal/UtilsImpl.java
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* /jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/aws/reference/FormParameters.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-
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* /jclouds-master-zip/jclouds-master/providers/google-compute-

engine/src/main/java/org/jclouds/googlecomputeengine/domain/TargetHttpProxy.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-
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*
/jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/reference/package-info.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/providers/google-compute-
engine/src/main/java/org/jclouds/googlecomputeengine/compute/functions/OrphanedGroupsFromDeadNodes.java
* /jclouds-master-zip/jclouds-master/providers/aws-
s3/src/test/java/org/jclouds/aws/s3/blobstore/integration/AWSS3BlobLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/domain/BoundedSet.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-
cloudloadbalancers/src/test/java/org/jclouds/rackspace/cloudloadbalancers/v1/options/ListOptionsTest.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-
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* /jclouds-master-zip/jclouds-master/providers/google-compute-
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* /jclouds-master-zip/jclouds-master/apis/openstack-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/Route53ApiMetadataTest.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/apis/openstack-
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* /jclouds-master-zip/jclouds-master/apis/route53/src/main/java/org/jclouds/route53/handlers/Route53ErrorHandler.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/date/internal/DateServiceDateCodecFactoryTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/test/java/org/jclouds/cloudwatch/xml/ListAlarmsForMetricResponseHandlerTest.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/ohai/functions/MapSetToMultimap.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/functions/ParseEventTypesFromHttpResponse.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/osgi/ApiListener.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/features/DomainApi.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/BucketMetadata.java
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* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/compute/functions/ParseOsFamilyVersion64BitFromImageName.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/predicates/ZonePredicatesTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/EC2ContextBuilderText.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/test/java/org/jclouds/glesys/features/IpApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/config/GoGridParserModule.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/domain/FirewallPolicy.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/concurrent/config/ScheduledExecutorServiceModule.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/xml/PayerHandler.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/options/GetServerListOptions.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/functions/ReturnInputStream.java
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* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/internal/BlobStoreContextImpl.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/domain/CookbookVersion.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/domain/LocationScope.java
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* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/java/org/jclouds/scriptbuilder/statements/ssh/InstallRSAPrivateKey.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/compute/CloudStackComputeServiceLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/test/java/org/jclouds/gogrid/features/GridJobApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/glesys/src/test/java/org/jclouds/glesys/GleSYSApiTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/compute/CloudStackComputeServiceAdapterExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/features/HostedZoneApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/extensions/internal/BaseImageExtensionLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/xml/ListDirectoryResponseHandler.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudfiles/src/test/java/org/jclouds/rackspace/cloudfiles/v1/CloudFilesApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/functions/BaseDriveToMapTest.java
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* /jclouds-master-zip/jclouds-master/providers/softlayer/src/test/java/org/jclouds/softlayer/features/AccountApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/drivers/gae/src/test/java/org/jclouds/gae/ConvertToGaeRequestTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/UpdateDomainOptions.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/domain/ServerImageType.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/domain/Href.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/features/ZoneApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/predicates/VolumeDetached.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova-ec2/src/test/java/org/jclouds/openstack/nova/ec2/features/NovaEC2InstanceApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/openhosting-east1/src/test/java/org/jclouds/openhosting/OpenHostingEast1ProviderTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/functions/config/SaxParserModule.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/test/java/org/jclouds/openstack/keystone/v2_0/parse/ParseDevstackApiMetadataTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/annotation/ServerError.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/net/util/IpPermissions.java

* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/v2_0/domain/Limit.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/features/AMIApi.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/domain/regionscoped/HypervisorDetails.java
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* /jclouds-master-zip/jclouds-master/common/openstack/src/main/java/org/jclouds/openstack/services/Compute.java
* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/http/parser/server/ServerInfoResponseHandler.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-clouddns-uk/src/test/java/org/jclouds/rackspace/clouddns/uk/features/CloudDNSUKRecordApiLiveTest.java
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/apis/rackspace-
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/blobstore/src/test/java/org/jclouds/blobstore/functions/BlobNameTest.java
* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/location/suppliers/all/ZoneToRegionToProviderOrJustProviderTest.java
* /jclouds-master-zip/jclouds-master/blobstore/src/test/java/org/jclouds/blobstore/options/GetOptionsTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/PasswordAuthenticationWithTenantNameExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/test/java/org/jclouds/elasticstack/suppliers/MockStandardDiskImageSupplier.java
*
/jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/http/parser/loadbalancer/LoadBalancerIdOnlyResponseHandler.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/suppliers/ProviderURISupplier.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/config/OAuthResource.java
* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/domain/Image.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/config/AzureComputeRateLimitModule.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListEventsOptions.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/domain/IpType.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/parse/GetAllTasksResponseTest.java
*
/jclouds-master-zip/jclouds-master/blobstore/src/test/java/org/jclouds/blobstore/util/BlobStoreUtilsTest.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/test/java/org/jclouds/dynect/v3/parse/GetPTRRecordResponseTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/v3/binders/BindAuthToJsonPayload.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/domain/Quota.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListHostsOptions.java
* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/location/predicates/fromconfig/AnyOrConfiguredRegionIdTest.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/strategy/PrioritizeCredentialsFromTemplate.java
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/jclouds-master-zip/jclouds-master/blobstore/src/test/java/org/jclouds/blobstore/integration/TransientServiceIntegrationTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/payloads/RSAEncryptingPayload.java
* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/parse/InvalidChangeBatchResponseTest.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/AccountApiExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/main/java/org/jclouds/docker/options/AttachOptions.java
* /jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/aws/handlers/AWSClientErrorRetryHandler.java
* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/compute/config/PacketComputeServiceContextModule.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/SessionApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/v3/domain/Region.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/main/java/org/jclouds/openstack/cinder/v1/options/CreateVolumeOptions.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/domain/SshKeyPair.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/http/IOExceptionRetryHandler.java
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* /jclouds-master-zip/jclouds-master/apis/sqs/src/test/java/org/jclouds/sqs/internal/BaseSQSExpectTest.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/predicates/RegionIdFilter.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/test/java/org/jclouds/rackspace/clouddns/v1/internal/BaseCloudDNSApiExpectTest.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/ohai/config/OhaiModule.java
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* /jclouds-master-zip/jclouds-master/apis/atmos/src/test/java/org/jclouds/atmos/AtmosClientLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/predicates/NetworkOfferingPredicates.java
* /jclouds-master-zip/jclouds-master/skeletons/standalone-compute/src/main/java/org/jclouds/servermanager/ServerManager.java
* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/test/java/org/jclouds/aws/s3/blobstore/integration/AWSS3BlobIntegrationLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/compute/functions/TemplateToOperatingSystem.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/compute/functions/AWSEC2SecurityGroupToSecurityGroupTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-cloud-storage/src/test/java/org/jclouds/googlecloudstorage/blobstore/integration/GoogleCloudStorageContainerLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/domain/AllowedArgumentsForCreateServer.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/domain/MutableResourceMetadata.java
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* /jclouds-master-zip/jclouds-master/drivers/okhttp/src/main/java/org/jclouds/http/okhttp/OkHttpClientSupplier.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/domain/KeyVaultReference.java
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* /jclouds-master-zip/jclouds-
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/functions/ZoneToEndpoint.java
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* /jclouds-master-zip/jclouds-master/providers/aws-
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Test.java
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* /jclouds-master-zip/jclouds-master/compute/src/test/java/org/jclouds/compute/functions/NodeAndTemplateOptionsToStatementWithoutPublicKeyTest.java
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* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudfiles/src/test/java/org/jclouds/rackspace/cloudfiles/v1/blobstore/CloudFilesRegionScopedSwiftBlobStoreParallelLiveTest.java

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* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azure/storage/options/CreateOptions.java
* /jclouds-master-zip/jclouds-master/providers/elastichosts-lax-p/src/test/java/org/jclouds/elastichosts/ElasticHostsPeer1LosAngelesProviderTest.java
* /jclouds-master-zip/jclouds-master/compute/src/main/java/org/jclouds/compute/predicates/AtomicNodeRunning.java
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/jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/http/parser/storage/StorageListResponseHandler

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* /jclouds-master-zip/jclouds-master/apis/sqs/src/test/java/org/jclouds/sqs/parse/SendMessageBatchResponseTest.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/xml/MessageHandler.java
* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/internal/BaseRoute53ApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/profitbricks/src/main/java/org/jclouds/profitbricks/config/ProfitBricksHttpApiModule.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/compute/EC2TemplateBuilderTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova/src/test/java/org/jclouds/openstack/nova/v2_0/compute/loaders/LoadFloatingIpsForInstanceTest.java

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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/MutableContentMetadata.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/test/java/org/jclouds/openstack/keystone/v2_0/features/UserApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/test/java/org/jclouds/digitalocean2/compute/options/DigitalOcean2TemplateOptionsTest.java
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* /jclouds-master-zip/jclouds-master/blobstore/src/main/java/org/jclouds/blobstore/strategy/PutBlobsStrategy.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/NeutronApiMetadata.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/main/java/org/jclouds/docker/domain/ExecCreateParams.java
* /jclouds-master-zip/jclouds-master/apis/elasticstack/src/main/java/org/jclouds/elasticstack/compute/functions/WellKnownImageToImage.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/ListMultipartUploadsResponse.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/CreateVlanIPRangeOptionsTest.java
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/jclouds-master-zip/jclouds-master/apis/openstack-nova/src/main/java/org/jclouds/openstack/nova/v2_0/domain/Volume.java
* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/main/java/org/jclouds/ultradns/ws/xml/TaskHandler.java
* /jclouds-master-zip/jclouds-master/providers/digitalocean2/src/main/java/org/jclouds/digitalocean2/ssh/ECDSAKeys.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/domain/JobState.java
* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/main/java/org/jclouds/openstack/keystone/catalog/functions/BaseEndpointResolutionStrategy.java
* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/functions/SpotInstanceRequestToAWSRunningInstanceTest.java
* /jclouds-master-zip/jclouds-master/skeletons/standalone-compute/src/main/java/org/jclouds/servermanager/ServerManagerApiMetadata.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/binders/CreateRecordBinder.java
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/jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/location/functions/RegionToEndpointOrProviderIfNullTest.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/javax/annotation/concurrent/NotThreadSafe.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/test/java/org/jclouds/azurecompute/arm/features/LocalNetworkGatewayApiLiveTest.java
* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/functions/VersionAwareRegionToEndpoint.java
* /jclouds-master-zip/jclouds-master/apis/docker/src/test/java/org/jclouds/docker/internal/BaseDockerParseTest.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/domain/AccessControlList.java
* /jclouds-master-zip/jclouds-master/providers/gogrid/src/test/java/org/jclouds/gogrid/GoGridLiveTestDisabled.java
* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/test/java/org/jclouds/openstack/swift/v1/features/ObjectApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/loadbalancer/src/main/java/org/jclouds/loadbalancer/strategy/DestroyLoadBalancerStrategy.java
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* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/domain/StorageGatewayConstants.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/BlockDeviceMappingHandler.java
* /jclouds-master-zip/jclouds-master/common/openstack/src/main/java/org/jclouds/openstack/functions/URIFromAuthenticationResponseForService.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/xml/BatchResponseHandler.java
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* /jclouds-master-zip/jclouds-master/core/src/test/java/org/jclouds/utils/TestUtils.java
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* /jclouds-master-zip/jclouds-master/apis/sts/src/main/java/org/jclouds/aws/reference/AWSConstants.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/features/ProjectApi.java

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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/features/NetworkApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/providers/dynect/src/main/java/org/jclouds/dynect/v3/features/GeoServiceApi.java
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* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/blobstore/functions/DirectoryEntryListToResourceMetadataList.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/options/RouteOptions.java
* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/strategy/DeleteAllNodesInList.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/features/GlobalConfigurationApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/azureblob/src/main/java/org/jclouds/azureblob/domain/ListBlobsInclude.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/test/java/org/jclouds/s3/fallbacks/FalseIfBucketAlreadyOwnedByYouOrOperationAbortedWhenBucketExistsTest.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/xml/DescribeTagsResponseHandler.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/rest/internal/TransformerForRequest.java
* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudblockstorage-

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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/compute/extensions/CloudStackSecurityGroupExtension.java
* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/util/GetEnumValue.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/domain/InstanceStateChange.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/location/suppliers/RegionIdToURISupplier.java
* /jclouds-master-zip/jclouds-master/apis/sqs/src/test/java/org/jclouds/sqs/internal/BaseSQSApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/apis/rackspace-clouddns/src/test/java/org/jclouds/rackspace/clouddns/v1/features/RecordApiLiveTest.java
* /jclouds-master-zip/jclouds-master/providers/softlayer/src/main/java/org/jclouds/softlayer/reference/SoftLayerConstants.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-cinder/src/main/java/org/jclouds/openstack/cinder/v1/domain/Snapshot.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/OSImageApi.java
* /jclouds-master-zip/jclouds-master/providers/b2/src/main/java/org/jclouds/b2/domain/BucketList.java
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* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/main/java/org/jclouds/aws/s3/predicates/validators/AWSS3BucketNameValidator.java
* /jclouds-master-zip/jclouds-master/apis/route53/src/test/java/org/jclouds/route53/parse/ListHostedZonesResponseTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/test/java/org/jclouds/cloudwatch/options/ListAlarmHistoryOptionsTest.java
* /jclouds-master-zip/jclouds-master/providers/dynect/src/test/java/org/jclouds/dynect/v3/features/GeoRegionGroupApiExpectTest.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/test/java/org/jclouds/googlecomputeengine/features/InstanceApiLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/options/ListUsageRecordsOptions.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-keystone/src/test/java/org/jclouds/openstack/keystone/catalog/functions/ReturnRegionOrProviderTest.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/domain/Segment.java
* /jclouds-master-zip/jclouds-master/apis/atmos/src/main/java/org/jclouds/atmos/domain/SystemMetadata.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/binders/BindProductCodesToIndexedFormParamsTest.java
* /jclouds-master-zip/jclouds-master/apis/ec2/src/main/java/org/jclouds/ec2/features/WindowsApi.java

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* /jclouds-master-zip/jclouds-master/providers/rackspace-cloudfiles-uk/src/test/java/org/jclouds/rackspace/cloudfiles/uk/blobstore/integration/CloudFilesUKContainerIntegrationLiveTest.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/binders/BindGroupNamesToIndexedFormParamsTest.java
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* /jclouds-master-zip/jclouds-master/providers/gogrid/src/main/java/org/jclouds/gogrid/GoGridResponseException.java
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* /jclouds-master-zip/jclouds-master/providers/softlayer/src/main/java/org/jclouds/softlayer/domain/ContainerVirtualGuestConfiguration.java
* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/xml/CopyObjectHandler.java
* /jclouds-master-zip/jclouds-master/drivers/okhttp/src/main/java/org/jclouds/http/okhttp/OkHttpCommandExecutorService.java
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* /jclouds-master-zip/jclouds-master/apis/cloudwatch/src/main/java/org/jclouds/cloudwatch/options/ListMetricsOptions.java

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* /jclouds-master-zip/jclouds-master/apis/byon/src/test/java/org/jclouds/byon/config/CacheNodeStoreModuleTest.java
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* /jclouds-master-zip/jclouds-master/apis/sqs/src/main/java/org/jclouds/sqs/xml/DeleteMessageBatchResponseHandler.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-swift/src/main/java/org/jclouds/openstack/swift/v1/functions/ParseContainerFromHeaders.java
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* /jclouds-master-zip/jclouds-master/providers/azureblob/src/test/java/org/jclouds/azure/storage/xml/ErrorHandlerTest.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/payloads/FilePayload.java
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* /jclouds-master-zip/jclouds-master/common/openstack/src/main/java/org/jclouds/openstack/config/OpenStackAuthenticationModule.java
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* /jclouds-master-zip/jclouds-master/scriptbuilder/src/main/java/org/jclouds/scriptbuilder/functionloader/osgi/ServiceFunctionLoader.java
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* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/functions/ParseObjectMetadataFromHeaders.java
* /jclouds-master-zip/jclouds-master/apis/oauth/src/main/java/org/jclouds/oauth/v2/filters/BearerTokenFromCredentials.java
* /jclouds-master-zip/jclouds-master/providers/azureblob/src/test/java/org/jclouds/azureblob/blobstore/functions/ListBlobsOptionsToListOptions.java
* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/test/java/org/jclouds/cloudstack/options/CreateTagsOptionsTest.java
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* /jclouds-master-zip/jclouds-master/providers/ultradns-ws/src/test/java/org/jclouds/ultradns/ws/UltraDNSWSProviderMetadataTest.java
* /jclouds-master-zip/jclouds-master/apis/rackspace-cloudloadbalancers/src/main/java/org/jclouds/rackspace/cloudloadbalancers/v1/domain/UpdateNode.java
* /jclouds-master-zip/jclouds-master/providers/glesys/src/main/java/org/jclouds/glesys/options/CreateAccountOptions.java
* /jclouds-master-zip/jclouds-master/providers/b2/src/test/java/org/jclouds/b2/B2ProviderMetadataTest.java
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* /jclouds-master-zip/jclouds-master/apis/s3/src/main/java/org/jclouds/s3/blobstore/functions/BlobToObject.java
* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/io/Payloads.java
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* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/compute/functions/OperatingSystemToImage.java

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/jclouds-master-zip/jclouds-master/common/openstack/src/main/java/org/jclouds/openstack/keystone/v1_1/domain/Token.java
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* /jclouds-master-zip/jclouds-master/apis/docker/src/main/java/org/jclouds/docker/domain/ExposedPorts.java
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* /jclouds-master-zip/jclouds-master/providers/packet/src/main/java/org/jclouds/packet/functions/HrefToListOptions.java
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* /jclouds-master-zip/jclouds-master/providers/aws-s3/src/main/java/org/jclouds/aws/s3/blobstore/internal/AWSS3BlobStoreContextImpl.java
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* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/options/ImageCreationOptions.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-neutron/src/main/java/org/jclouds/openstack/neutron/v2/NeutronApi.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/suppliers/ZoneIdToZoneSupplier.java
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* /jclouds-master-zip/jclouds-master/apis/cloudstack/src/main/java/org/jclouds/cloudstack/strategy/BlockUntilJobCompletesAndReturnResult.java
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* /jclouds-master-zip/jclouds-master/apis/ec2/src/test/java/org/jclouds/ec2/features/WindowsApiTest.java
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* /jclouds-master-zip/jclouds-master/providers/azurecompute-arm/src/main/java/org/jclouds/azurecompute/arm/features/NetworkSecurityGroupApi.java
* /jclouds-master-zip/jclouds-master/providers/google-compute-engine/src/main/java/org/jclouds/googlecomputeengine/options/AddressCreationOptions.java
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* /jclouds-master-zip/jclouds-master/providers/aws-ec2/src/test/java/org/jclouds/aws/ec2/features/PlacementGroupApiMockTest.java
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* /jclouds-master-zip/jclouds-master/apis/chef/src/main/java/org/jclouds/chef/strategy/DeleteAllClientsInList.java
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* /jclouds-master-zip/jclouds-master/core/src/main/java/org/jclouds/osgi/MetadataBundleListener.java
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* /jclouds-master-zip/jclouds-master/apis/openstack-nova-ec2/src/test/java/org/jclouds/openstack/nova/ec2/features/NovaSubnetApiLiveTest.java
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======

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/*
  Sha512Crypt.java

  Created: 18 December 2007

Last Changed By: $Author: broccol $
Version: $Revision: 7692 $
Last
Mod Date: $Date: 2007-12-30 01:55:31 -0600 (Sun, 30 Dec 2007) $

Java Port By: James Ratcliff, falazar@arlut.utexas.edu

This class implements the new generation, scalable, SHA512-based
Unix 'crypt' algorithm developed by a group of engineers from Red
Hat, Sun, IBM, and HP for common use in the Unix and Linux
/etc/shadow files.

The Linux glibc library (starting at version 2.7) includes support
for validating passwords hashed using this algorithm.

The algorithm itself was released into the Public Domain by Ulrich
Drepper <drepper@redhat.com>. A discussion of the rationale and
development of this algorithm is at

http://people.redhat.com/drepper/sha-crypt.html

and the specification and a sample C language implementation is at

http://people.redhat.com/drepper/SHA-crypt.txt

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*/
/**
* This class defines a method,
* {@link Sha512Crypt#Sha512_crypt(java.lang.String, java.lang.String, int)
* Sha512_crypt()}, which takes a password and a salt string and generates a
* Sha512 encrypted password entry.
*

* This class implements the new generation, scalable, SHA512-based Unix 'crypt'
* algorithm developed by a group of engineers from Red Hat, Sun, IBM, and HP
* for common use in the Unix and Linux /etc/shadow files.
*
* The Linux glibc library (starting at version
 2.7) includes support for
* validating passwords hashed using this algorithm.
*
* The algorithm itself was released into the Public Domain by Ulrich Drepper
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* this algorithm is at
*
* http://people.redhat.com/drepper/sha-crypt.html
*
* and the specification and a sample C language implementation is at
*
* http://people.redhat.com/drepper/SHA-crypt.txt
*/

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1.310 hcl 1.0.1-vault-3

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1.318 libxml2 2.9.13-9.el9_6

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1.319 byte-buddy 1.14.13

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1.320 jruby-stdlib 9.4.6.0

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@(#)subr_prf.c 8.3 (Berkeley) 1/21/94

[ccan/build_assert/build_assert.h]
[ccan/check_type/check_type.h]
[ccan/container_of/container_of.h]
[ccan/str/str.h]

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[ccan/list/list.h]

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[coroutine]

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[include/ruby/onigmo.h]
[include/ruby/oniguruma.h]
[regcomp.c]
[regenc.c]
[regenc.h]
[regerror.c]
[regexec.c]
[regint.h]
[regparse.c]
[regparse.h]
[enc/ascii.c]
[enc/big5.c]
[enc/cp949.c]
[enc/emacs_mule.c]
[enc/encdb.c]
[enc/euc_jp.c]
[enc/euc_kr.c]
[enc/euc_tw.c]
[enc/gb18030.c]
[enc/gb2312.c]
[enc/gbk.c]
[enc/iso_8859_1.c]
[enc/iso_8859_10.c]
[enc/iso_8859_11.c]
[enc/iso_8859_13.c]
[enc/iso_8859_14.c]
[enc/iso_8859_15.c]
[enc/iso_8859_16.c]
[enc/iso_8859_2.c]
[enc/iso_8859_3.c]
[enc/iso_8859_4.c]
[enc/iso_8859_5.c]
[enc/iso_8859_6.c]
[enc/iso_8859_7.c]
[enc/iso_8859_8.c]
[enc/iso_8859_9.c]
[enc/koi8_r.c]
[enc/koi8_u.c]

[enc/shift_jis.c]
[enc/unicode.c]
[enc/us_ascii.c]
[enc/utf_16be.c]
[enc/utf_16le.c]
[enc/utf_32be.c]
[enc/utf_32le.c]
[enc/utf_8.c]
[enc/windows_1251.c]
[enc/windows_31j.c]

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* https://github.com/k-takata/Onigmo/
* https://github.com/kkos/oniguruma
* https://svnweb.freebsd.org/ports/head/devel/oniguruma/

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[enc/windows_1250.c]
[enc/windows_1252.c]

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[enc/cesu_8.c]
[enc/windows_1253.c]
[enc/windows_1254.c]
[enc/windows_1257.c]

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[enc/trans/GB/GB12345%UCS.src]
[enc/trans/GB/UCS%GB12345.src]
[enc/trans/GB/GB2312%UCS.src]
[enc/trans/GB/UCS%GB2312.src]

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[enc/trans/JIS/JISX0208\@1990%UCS.src]
[enc/trans/JIS/JISX0212%UCS.src]
[enc/trans/JIS/UCS%JISX0201-KANA.src]
[enc/trans/JIS/UCS%JISX0208@1990.src]
[enc/trans/JIS/UCS%JISX0212.src]

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[enc/trans/JIS/UCS@BMP%JISX0213-2.src]
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[enc/trans/ucm/glibc-BIG5HKSCS-2.3.3.ucm]

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[enc/trans/ucm/windows-950_hkscs-2001.ucm]

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[aclocal.m4]

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[tool/config.guess]
[tool/config.sub]

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[parse.h]

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[missing/dtoa.c]

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[include/ruby/win32.h]

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>>>
A C-program for MT19937, with
initialization improved 2002/2/10.::
Coded by Takuji Nishimura and Makoto Matsumoto.
This is a faster version by taking Shawn Cokus's optimization,
Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using init_genrand(seed)
or init_by_array(init_key, key_length).

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[missing/procstat_vm.c]

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[vsnprintf.c]

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[st.c]
[strftime.c]
[include/ruby/st.h]
[missing/acosh.c]
[missing/alloca.c]
[missing/dup2.c]
[missing/erf.c]
[missing/finite.c]
[missing/hypot.c]
[missing/isinf.c]
[missing/isnan.c]
[missing/lgamma_r.c]
[missing/memcmp.c]
[missing/memmove.c]
[missing/strchr.c]
[missing/strerror.c]
[missing/strstr.c]
[missing/tgamma.c]
[ext/date/date_strftime.c]
[ext/digest/sha1/sha1.c]
[ext/digest/sha1/sha1.h]

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[ext/digest/md5/md5.h]

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[ext/digest/rmd160/rmd160.c]
[ext/digest/rmd160/rmd160.h]

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[ext/digest/sha2/sha2.h]

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[ext/json/generator/generator.c]

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[ext/nkf/nkf-utf8/nkf.c]
[ext/nkf/nkf-utf8/utf8tbl.c]

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[spec/ruby]

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1.324 pigz 2.8-1.el9

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1.332 datanucleus-api-jdo 4.1.4

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1.333 open3 0.1.2

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1.334 pcre 8.44-4.el9

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    all fields (only present with certain compiler settings).
  * Added generation of field number constants.

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Oliver Jowett <oliver.jowett@gmail.com>
   * Detect whether zlib is new enough in configure script.
  * Fixes for Solaris 10 32/64-bit confusion.
Evan Jones <evanj@mit.edu>
  * Optimize Java serialization code when writing a small message to a stream.
  * Optimize Java serialization of strings so that UTF-8 encoding happens only
    once per string per serialization call.
  * Clean up some Java warnings.
  * Fix bug with permanent callbacks that delete themselves when run.
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  * Added atomicops for Solaris
Andrew Paprocki <andrew@ishiboo.com>
  * Fixed minor IBM xlC compiler build issues
  * Added atomicops for AIX (POWER)
This file contains a list of people who've made non-trivial
contribution to the Google C++ Testing Framework project. People
who commit code to the project are encouraged to add their names
here. Please keep the list sorted by first names.

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1.342 tracer 0.1.1

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1.343 logger 1.4.3

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1.344 sg3-utils 1.47-10.el9

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4th October 2021

1.345 cgi 0.2.2

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1.346 log4net 1.2

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1.347 paesslerag-jsonpath 0.1.1

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1.348 find 0.1.0

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1.349 colorama 0.4.4

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1.350 eventmachine 1.0.7-java

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1.351 goexpect 0.0.0-20210430020637-ab937bf7fd6f

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1.352 logger 1.5.1

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1.353 tomcat-i18n-zh-cn 9.0.108

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Schemas for Java EE Deployment Descriptors:
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- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
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- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
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- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
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- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
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- web-app_3_0.xsd
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- javaee_7.xsd
- javaee_web_services_1_4.xsd
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1.355 time-out 0.3.2

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1.357 protobuf-go 1.30.0

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1.358 zap-exp 1.23.0

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1.359 resolv-replace 0.1.0

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1.360 rake-ant 1.0.6

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1.361 cachecontrol 0.12.6

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1.362 io-nonblock 0.1.0

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1.363 commons-pool 1.6

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1.364 racc 1.6.0

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1.365 commons-http-client 3.0.1

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* $Header: /home/jerenkrantz/tmp/commons/commons-convert/cvs/home/cvs/jakarta-commons//httpclient/src/java/org/apache/commons/httpclient/AutoCloseInputStream.java,v 1.9 2004/04/18 23:51:34 jsdever Exp $
* $Revision: 291181 $
* $Date: 2005-09-23 14:13:25 -0400 (Fri, 23 Sep 2005) $

```
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*/

package org.apache.commons.httpclient;

import java.io.FilterInputStream;
import java.io.IOException;
import java.io.InputStream;

/**
* Closes an underlying stream as soon as the end of the stream is reached, and
* notifies a client when it has done so.
*
* @author Ortwin Glueck
* @author Eric Johnson
* @author <a href="mailto:mbowler@GargoyleSoftware.com">Mike Bowler</a>
*
* @since 2.0
*/
class AutoCloseInputStream extends FilterInputStream {

  /**
   * True if this stream is open.  Assume that the underlying stream
   * is open until we get an EOF indication.
   */
  private boolean streamOpen = true;
```

```
 /** True if the
stream closed itself. */
 private boolean selfClosed = false;

 /**
  * The watcher is notified when the contents of the stream have
  * been  exhausted
  */
 private ResponseConsumedWatcher watcher = null;

 /**
  * Create a new auto closing stream for the provided connection
  *
  * @param in the input stream to read from
  * @param watcher   To be notified when the contents of the stream have been
  *  consumed.
  */
 public AutoCloseInputStream(
        final InputStream in, final ResponseConsumedWatcher watcher) {
    super(in);
    this.watcher = watcher;
 }

 /**
  * Reads the next byte of data from the input stream.
  *
  * @throws IOException when there is an error reading
  * @return the character read, or -1 for EOF
  */
 public int read() throws IOException {
    int l = -1;

    if (isReadAllowed()) {
        // underlying stream not closed, go ahead
and read.
        l = super.read();
        checkClose(l);
    }

    return l;
 }

 /**
  * Reads up to <code>len</code> bytes of data from the stream.
  *
  * @param b a <code>byte</code> array to read data into
  * @param off an offset within the array to store data
```

```
     * @param len the maximum number of bytes to read
     * @return the number of bytes read or -1 for EOF
     * @throws IOException if there are errors reading
     */
    public int read(byte[] b, int off, int len) throws IOException {
        int l = -1;

        if (isReadAllowed()) {
            l = super.read(b,  off,  len);
            checkClose(l);
        }

        return l;
    }

    /**
     * Reads some number of bytes from the input stream and stores them into the
     * buffer array b.
     *
     * @param b a <code>byte</code> array to read data into
     * @return the number of bytes read or -1 for EOF
     * @throws IOException if
    there are errors reading
     */
    public int read(byte[] b) throws IOException {
        int l = -1;

        if (isReadAllowed()) {
            l = super.read(b);
            checkClose(l);
        }
        return l;
    }

    /**
     * Close the stream, and also close the underlying stream if it is not
     * already closed.
     * @throws IOException If an IO problem occurs.
     */
    public void close() throws IOException {
        if (!selfClosed) {
            selfClosed = true;
            notifyWatcher();
        }
    }

    /**
     * Close the underlying stream should the end of the stream arrive.
```

```
     *
     * @param readResult    The result of the read operation to check.
     * @throws IOException If an IO problem occurs.
     */
    private void checkClose(int readResult) throws IOException {
        if (readResult == -1) {
            notifyWatcher();
        }
    }

    /**
     * See whether a read of the underlying stream
  should be allowed, and if
     * not, check to see whether our stream has already been closed!
     *
     * @return <code>true</code> if it is still OK to read from the stream.
     * @throws IOException If an IO problem occurs.
     */
    private boolean isReadAllowed() throws IOException {
        if (!streamOpen && selfClosed) {
            throw new IOException("Attempted read on closed stream.");
        }
        return streamOpen;
    }

    /**
     * Notify the watcher that the contents have been consumed.
     * @throws IOException If an IO problem occurs.
     */
    private void notifyWatcher() throws IOException {
        if (streamOpen) {
            super.close();
            streamOpen = false;

            if (watcher != null) {
                watcher.responseConsumed();
            }
        }
    }
}
```

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1.371 glib 2.68.4-16.el9_6.2

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1.374 humanizer.core 2.14.1

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- javaee_5.xsd

- javaee_web_services_1_2.xsd
- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- web-app_3_0.xsd
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- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
- javaee_8.xsd
- web-app_4_0.xsd
- web-common_4_0.xsd
- web-fragment_4_0.xsd

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- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
- javaee_8.xsd
- web-app_4_0.xsd
- web-common_4_0.xsd
- web-fragment_4_0.xsd

may be obtained from:
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org.apache.tomcat.util.json.JSONParser.jj is
 a public domain javacc grammar
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https://github.com/RobertFischer/json-parser

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- javaee_web_services_1_3.xsd

- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
- javaee_8.xsd
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1.376 kerberos 1.21.1-6.el9

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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
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lib/libgss/g_store_cred.c
lib/libgss/g_unseal.c
lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
uts/common/gssapi/include/gssapi_err_generic.h
uts/common/gssapi/include/mechglueP.h

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lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c

lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c
kprop/kproplog.c

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- web-app_3_0.xsd
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- web-fragment_3_0.xsd
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- javaee_6.xsd

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- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
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- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
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- -

There are currently two explicit copyrights on portions of UnZip
code (at least, of which Info-ZIP is aware):
Jim Luther's Mac OS File Manager interface code; and Christopher Evans'
MacBinaryIII coding code (for the MacOS port).. These copyrights
are discussed in more detail below.

All remaining
code is now (starting with UnZip version 5.41) covered
by the new Info-ZIP license. For details, please read the acompaning
file LICENSE. The terms and conditions in this license supersede the
copyright conditions of the contributions by Igor Mandrichenko
(vms/vms.c), Greg Roelofs (zipinfo.c, new version of unshrink.c),
Mike White (Windows DLL code in "windll/*"), Steve P. Miller (Pocket
UnZip GUI "wince/*"), and Mark Adler (inflate/explode decompresseion

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Frequently Asked Questions regarding (re)distribution of Zip and UnZip
are near the end of this file.

There are no known patents on any of the code in UnZip. Unisys
claims a patent on LZW encoding and on LZW decoding _in an apparatus
that performs LZW encoding_, but the patent appears to exempt a stand-
alone decoder (as in UnZip's unshrink.c). Unisys has publicly claimed
otherwise, but the issue has never been tested in court. Since this
point is unclear, unshrinking is not enabled by default. It is the
responsibility of the user to make his or her peace with Unisys and
its licensing requirements. (unshrink.c may be removed from future
releases altogether.)

The original unzip source code has been extensively modified and
almost entirely rewritten (changes include random zipfile access
rather than sequential; replacement of unimplode() with explode();
replacement of old unshrink() with new (unrelated) unshrink(); re-
placement of output routines; addition of inflate(), wildcards,
filename-mapping, text translation, ...; etc.). As far as we can
tell, only the core code of the unreduce method remained substantially
similar to Mr. Smith's original source. As of UnZip
5.42, the complete
core code is now covered by the Info-ZIP Licence. Therefore, support
for the reduce method has been removed.
The drop of the reduce method should only affect some test archives,
reducing was never used in any publically distributed Zip program.
For pathologic cases where support for reduced archive entries is
needed, the unreduce code copyrighted by Samuel H. Smith is available
as a separate distribution (the restricted copyright of this code is
cited below in the "historical" section).

The following copyright applies to the Mac OS File Manager interface code
(macos/source/macstuff.[ch]), distributed with UnZip 5.4 and later:

```
  * MoreFiles
  *
  * A collection of File Manager and related routines
  *
  * by Jim Luther (Apple Macintosh Developer Technical Support Emeritus)
  * with significant code contributions by Nitin Ganatra
  * (Apple Macintosh Developer Technical Support Emeritus)
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  * MacBinaryIII.h
  *
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  *
  * Basic encoding and decoding of Macintosh files to the
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  * --
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```
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Regarding the first stipulation, Mr. Smith was tracked down in southern
California some years back [Samuel H. Smith, The Tool Shop; as of mid-
May 1994, (213) 851-9969 (voice), (213) 887-2127(?) (subscription BBS),
71150.2731@compuserve.com]:

"He says that he thought that whoever contacted him understood that
he has no objection to the Info-ZIP group's inclusion of his code.
His primary concern is that it remain freely distributable, he said."

Despite the fact that our "normal" code has been entirely rewritten
and by default
no longer contains any of Mr. Smith's code, Info-ZIP
remains indebted and grateful to him. We hope he finds our contribu-
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Note that the third and fourth stipulations still apply to any com-
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The remaining code was written by many people associated with the
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Greg Roelofs (overall program logic, ZipInfo, unshrink, filename
mapping/portability, etc.), Mark Adler (inflate, explode, funzip),
Kai Uwe Rommel (OS/2), John Bush and Paul Kienitz (Amiga), Antoine
Verheijen (Macintosh), Hunter Goatley (more VMS), Mike White (Windows
DLLs), Christian Spieler (overall logic, optimization, VMS, etc.) and
others. See the file CONTRIBS in the source distribution for a much
more complete list of contributors.
The decompression core code for the deflate method (inflate.[ch],
explode.c) was originally written by Mark Adler who submitted it
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1.391 x-exp 0.0.0-20230314191032-db074128a8ec

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* /gson-1-6-sources-7-jar/com/google/gson/ModifyFirstLetterNamingPolicy.java
* /gson-1-6-sources-7-jar/com/google/gson/NullExclusionStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/Escaper.java
* /gson-1-6-sources-7-jar/com/google/gson/annotations/SerializedName.java
* /gson-1-6-sources-7-jar/com/google/gson/InstanceCreator.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonObjectDeserializationVisitor.java
* /gson-1-6-sources-7-jar/com/google/gson/SerializedNameAnnotationInterceptingNamingPolicy.java
*
/gson-1-6-sources-7-jar/com/google/gson/GenericArrayTypeImpl.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonDeserializationContext.java
* /gson-1-6-sources-7-jar/com/google/gson/ParameterizedTypeHandlerMap.java
* /gson-1-6-sources-7-jar/com/google/gson/ObjectNavigator.java
* /gson-1-6-sources-7-jar/com/google/gson/VersionConstants.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonDeserializationVisitor.java
* /gson-1-6-sources-7-jar/com/google/gson/ModifierBasedExclusionStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonNull.java
* /gson-1-6-sources-7-jar/com/google/gson/CompositionFieldNamingPolicy.java
* /gson-1-6-sources-7-jar/com/google/gson/TypeInfoArray.java
* /gson-1-6-sources-7-jar/com/google/gson/DisjunctionExclusionStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/TypeInfoFactory.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonElementVisitor.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonParseException.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonDeserializerExceptionWrapper.java
*
/gson-1-6-sources-7-jar/com/google/gson/InnerClassExclusionStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/JavaFieldNamingPolicy.java
* /gson-1-6-sources-7-jar/com/google/gson/CamelCaseSeparatorNamingPolicy.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonArrayDeserializationVisitor.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonElement.java
* /gson-1-6-sources-7-jar/com/google/gson/ExclusionStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/Preconditions.java
* /gson-1-6-sources-7-jar/com/google/gson/ObjectConstructor.java
* /gson-1-6-sources-7-jar/com/google/gson/MappedObjectConstructor.java
* /gson-1-6-sources-7-jar/com/google/gson/GsonBuilder.java
* /gson-1-6-sources-7-jar/com/google/gson/ParameterizedTypeImpl.java
* /gson-1-6-sources-7-jar/com/google/gson/TypeInfoCollection.java
* /gson-1-6-sources-7-jar/com/google/gson/TypeUtils.java
* /gson-1-6-sources-7-jar/com/google/gson/DelegatingJsonElementVisitor.java
*
/gson-1-6-sources-7-jar/com/google/gson/FieldNamingStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonDeserializer.java
* /gson-1-6-sources-7-jar/com/google/gson/annotations/Expose.java
* /gson-1-6-sources-7-jar/com/google/gson/annotations/Since.java
* /gson-1-6-sources-7-jar/com/google/gson/VersionExclusionStrategy.java
* /gson-1-6-sources-7-jar/com/google/gson/JsonSerializer.java
* /gson-1-6-sources-7-jar/com/google/gson/LowerCamelCaseSeparatorNamingPolicy.java
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```

Found in path(s):
* /gson-1-6-sources-7-jar/com/google/gson/JsonSyntaxException.java
* /gson-1-6-sources-7-jar/com/google/gson/FieldNamingStrategy2Adapter.java
* /gson-1-6-sources-7-jar/com/google/gson/Cache.java
* /gson-1-6-sources-7-jar/com/google/gson/Streams.java
* /gson-1-6-sources-7-jar/com/google/gson/LruCache.java
* /gson-1-6-sources-7-jar/com/google/gson/FieldNamingStrategy2.java

1.395 golang-lru 0.5.4

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*          bug 292478 - Report potentially null across variable assignment
*          bug 332637 - Dead Code detection removing code that isn't dead
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
*       Bug 421035 - [resource]
 False alarm of resource leak warning when casting a closeable in its assignment
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*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 265744 - Enum switch should warn about missing default
*   bug 374605 - Unreasonable warning for enum-based switch statements
*   bug 381443
 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*   Bug 441208 - [1.8][null]SuppressWarnings("null") does not suppress / marked Unnecessary
*   Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
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*   bug 383368 - [compiler][null] syntactic null analysis for field references
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*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
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*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE

```
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop
 with close in try/catch
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*       bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365519 - editorial cleanup after bug 186342 and bug 365387
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
 *       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*       bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject into account
*       bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*       bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*       Bug
 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 426792 - [1.8][inference][impl] generify new type inference engine
*       Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*       Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*       Bug 418743 - [1.8][null] contradictory annotations on invocation of generic method not reported
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 431581 - Eclipse compiles what it should not
```

```
*       Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*       Bug 446442 - [1.8] merge null annotations from
 super methods
*       Bug 456532 - [1.8][null] ReferenceBinding.appendNullAnnotation() includes phantom annotations in error
messages
*       Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*     Jesper S Moller - Contributions for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*       bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*     Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
*                         bug 386692 - Missing "unused" warning on "autowired" fields
*     Pierre-Yves B. <pyvesdev@gmail.com> - Contribution for
*                         bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
*     Sebastian Zarnekow - Contributions
 for
*       bug 544921 - [performance] Poor performance with large source files
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*           bug 343713 - [compiler] bogus line number in constructor of inner class in 1.5 compliance
*           bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 361407 - Resource leak warning when resource is assigned to a field outside of constructor
*       bug 368546 - [compiler][resource] Avoid
 remaining false positives found when compiling the Eclipse SDK
*       bug 383690 - [compiler] location of error re uninitialized final field should be aligned
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 383368 - [compiler][null] syntactic null analysis for field references
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*       bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject into account
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 415399 - [1.8][compiler] Type annotations on constructor results dropped by the code
generator
*     Ulrich Grave <ulrich.grave@gmx.de> - Contributions
 for
*                         bug 386692 - Missing "unused" warning on "autowired" fields
*********************************************************************************/
```

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ConstructorDeclaration.java
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*     Stephan Herrmann - Contribution for
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/SingleTypeReference.java
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```
*     IBM Corporation - initial API and implementation
*     Stephan Herrmann - Contributions for
*       Bug 360328 - [compiler][null] detect null problems in nested code (local class inside a loop)
*       Bug 388630 - @NonNull diagnostics at line 0
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*       Bug 424727 - [compiler][null] NullPointerException
 in nullAnnotationUnsupportedLocation(ProblemReporter.java:5708)
*       Bug 457210 - [1.8][compiler][null] Wrong Nullness errors given on full build build but not on incremental build?
*     Keigo Imai - Contribution for  bug 388903 - Cannot extend inner class as an anonymous class when it extends
the outer class
 *    Pierre-Yves B. <pyvesdev@gmail.com> - Contributions for
*                       Bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
*                       Bug 546084 - Using Junit 5s MethodSource leads to ClassCastException
**********************************************************************************/
```

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*   wharley@bea.com - initial API and implementation
*   philippe.marschall@netcetera.ch - Fix for 338370
*   IBM Corporation - Fix for validating relative name
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/batch/BatchCompilerRequestor.java

* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/EclipseCompilerRequestor.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/ByteConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/AccessRuleSet.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/DoubleConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/FloatConstant.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/CharConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/StringConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/BooleanConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/LongConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfObject.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocArgumentExpression.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/ShortConstant.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/IntConstant.java
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*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
```

work)
**********/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ImportBinding.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ASTVisitor.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/JavadocTagConstants.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/MissingTypeBinding.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/JavadocParser.java
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*     Stephen Herrmann <stephan@cs.tu-berlin.de> -  Contributions for
*          bug 133125 - [compiler][null] need to report the null status of expressions and analyze them simultaneously
*          bug 292478 - Report potentially null across variable assignment
*       bug 324178 - [null] ConditionalExpression.nullStatus(..) doesn't take into account the analysis of condition itself
*       bug 354554 - [null] conditional with redundant condition
 yields weak error message
*          bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 426078 - [1.8] VerifyError when conditional expression passed as an argument
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 *       Bug 418537 - [1.8][null] Fix null type annotation analysis for poly conditional expressions
*       Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
*******************************************************************************/
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*   Jesper Steen Moller - Contributions for:
*   Bug 412149: [1.8][compiler] Emit repeated annotations into the designated container
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*   bug 358827 - [1.7] exception analysis for t-w-r spoils null analysis
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*   bug 345305 - [compiler][null]
 Compiler misidentifies a case of "variable can only be null"
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*   bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*   bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*   Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*   Bug 455723 - Nonnull argument not correctly inferred in loop
*   Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*   Bug 446691 - [1.8][null][compiler] NullPointerException in SingleNameReference.analyseCode
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*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 467032 - TYPE_USE Null Annotations: IllegalStateException with annotated arrays of Enum when accessed via BinaryTypeBinding
**********/

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*          Bug 407191 - [1.8] Binary access support for type annotations
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*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*       Bug 406982 - [1.8][compiler] Generation of MethodParameters Attribute in classfile
*       Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*       Bug 412149 - [1.8][compiler] Emit repeated annotations into the designated container
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com
 - Contributions for
*                    Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                    Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
*                    Bug 409246 - [1.8][compiler] Type annotations on catch parameters not handled properly
*                    Bug 415541 - [1.8][compiler] Type annotations in the body of static initializer get dropped
*                    Bug 415399 - [1.8][compiler] Type annotations on constructor results dropped by the code
generator
*                    Bug 415470 - [1.8][compiler] Type annotations on class declaration go vanishing
*                    Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*                    Bug 434556 - Broken class file generated for incorrect annotation usage
*                    Bug 442416
 - $deserializeLambda$ missing cases for nested lambdas
*     Stephan Herrmann - Contribution for
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*     Olivier Tardieu tardieu@us.ibm.com - Contributions for
```

* Bug 442416 - $deserializeLambda$ missing cases for nested lambdas
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*          bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*          bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 415790
 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
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Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/WhileStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ForStatement.java
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*     Karen Moore - fix for https://bugs.eclipse.org/bugs/show_bug.cgi?id=207411
*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*          bug 185682 - Increment/decrement operators mark local variables as read
*          bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365519 - editorial cleanup after
 bug 186342 and bug 365387
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 384870 - [compiler] @Deprecated annotation not detected if preceded by other annotation
*       bug 393719 - [compiler] inconsistent warnings on iteration variables
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 424742 - [1.8] NPE in LambdaExpression.isCompatibleWith
```

```
*       Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*       Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
*       Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*       Bug 426366
 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
*       Bug 427282 - [1.8][compiler] AIOOB (-1) at
org.eclipse.jdt.internal.compiler.ClassFile.traverse(ClassFile.java:6209)
*       Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
*       Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*       Bug 427163 - [1.8][null] bogus error "Contradictory null specification" on varags
*       Bug 432348 - [1.8] Internal compiler error (NPE) after upgrade to 1.8
*       Bug 440143 - [1.8][null] one more case of contradictory null annotations regarding type variables
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
*       Bug 446442 - [1.8] merge null annotations from super methods
*       Bug 437072 - [compiler][null] Null analysis emits
 possibly incorrect warning for new int[][] despite @NonNullByDefault
*     Jesper S Moller - Contributions for
*       bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*       bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*       bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*       bug 412149 - [1.8][compiler] Emit repeated annotations into the designated container
*       bug 419209 - [1.8] Repeating container annotations should be rejected in the presence of annotation it contains
*     Till Brychcy - Contributions for
*               bug 467094 - [1.8][null] TYPE_USE NullAnnotations of array contents are applied to field.
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*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*******************************************************************************/
```

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/AbstractVariableDeclaration.java
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*    wharley@bea.com - initial API and implementation
*    IBM Corporation - Java 8 support
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/model/TypeMirrorImpl.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/model/PrimitiveTypeImpl.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/batch/BasicModule.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/ModuleReferenceImpl.java
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*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
***/

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* Contributors:
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*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's

work)
*   Stephan Herrmann - Contribution for
*   Bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
***/

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 path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/AnnotationTargetTypeConstants.java
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*   Frits Jalvingh - fix for bug 533830.
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*     Stephan Herrmann - Contributions for
*        Bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*     Keigo Imai - Contribution for  bug 388903 - Cannot extend inner class as an anonymous class when it
 extends the outer class
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/NestedTypeBinding.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/MethodInfoWithAnnotations.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IBinaryAnnotation.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/Messages.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredInitializer.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/MemberTypeBinding.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/ProblemSeverities.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/SuffixConstants.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/SourceTypeCollisionException.java
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*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/Javadoc.java
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*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 359362 - FUP of bug 349326: Resource leak on non-Closeable resource
*   bug 358903 - Filter practically unimportant resource leak warnings
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure
 should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*   Bug 426676 - [1.8][compiler] Wrong generic method type inferred from lambda expression
*   Bug 427411 - [1.8][generics] JDT reports type mismatch when using method that returns generic type
*   Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*   Bug 435962 - [RC2] StackOverFlowError when building
*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*   Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*   Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
**/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/WildcardBinding.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredOpensStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredModuleStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredUsesStatement.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfModule.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredExport.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ExportsStatement.java

* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredProvidesStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredTypeReference.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredExportsStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredPackageVisibilityStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredModuleReference.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredRequiresStatement.java
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*   IBM Corporation - initial API and implementation
*   Jesper S Moller - Bug 392671
*   NPE with a method with explicit this and a following incomplete parameter
**********/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/model/ElementImpl.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ClassFilePool.java
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*   bug 384380 - False positive on a "Potential null pointer access" after a continue
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
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*     Perry James - nullStatus method improvement (165346)
*     Stephan Herrmann - Contribution for
*         bug 383368 - [compiler][null] syntactic null analysis for field references
*******************************************************************************/
```

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*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         bug 383368 - [compiler][null] syntactic null analysis for field references
*         bug 402993 - [null] Follow up
 of bug 401088: Missing warning about redundant null check
*         Bug 440282 - [resource] Resource leak detection false negative with empty finally block
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IBinaryNestedType.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IGenericType.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/ISourceField.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IGenericField.java
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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 388795 - [compiler] detection of name clash depends on order of super interfaces
*   bug 388739 - [1.8][compiler] consider default methods when detecting whether
 a class needs to be declared abstract
*   bug 390883 - [1.8][compiler] Unable to override default method
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 401246 - [1.8][compiler] abstract class method should now trump conflicting default methods
*   bug 401796 - [1.8][compiler] don't treat default methods as overriding an independent inherited abstract method
*   bug 403867 - [1.8][compiler] Suspect error about duplicate default methods
*   bug 391376 - [1.8] check interaction of default methods with bridge methods and generics
*   bug 395681 - [compiler] Improve simulation of javac6 behavior from bug 317719 after fixing bug 388795
*   bug 409473 - [compiler] JDT cannot compile against JRE 1.8

```
*       Bug 420080 - [1.8] Overridden Default method is reported as duplicated
*       Bug 404690 - [1.8][compiler] revisit bridge generation after VM bug is fixed
 *       Bug 410325 - [1.7][compiler] Generified method override different between javac and eclipse compiler
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 390889 - [1.8][compiler] Evaluate options to support 1.7- projects against 1.8 JRE.
*       Bug 440773 - [1.8][null]DefaultLocation.RETURN_TYPE erroneously affects method parameters in
@NonNullByDefault
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 446442 - [1.8] merge null annotations from super methods
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*       bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 364890 - BinaryTypeBinding should use char constants from Util
*       bug 365387 - [compiler][null] bug 186342: Issues to follow up post review and verification.
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug
 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*       bug 388800 - [1.8][compiler] detect default methods in class files
*       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*       Bug 392245 - [1.8][compiler][null] Define whether
 / how @NonNullByDefault applies to TYPE_USE locations
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 390889 - [1.8][compiler] Evaluate options to support 1.7- projects against 1.8 JRE.
```

```
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
*       Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*       Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 453475 - [1.8][null] Contradictory null annotations (4.5 M3 edition)
*       Bug 454182 - Internal compiler error when using
 1.8 compliance for simple project
*       Bug 470467 - [null] Nullness of special Enum methods not detected from .class file
*       Bug 447661 - [1.8][null] Incorrect 'expression needs unchecked conversion' warning
*    Jesper Steen Moller - Contributions for
*       Bug 412150 [1.8] [compiler] Enable reflected parameter names during annotation processing
*       Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*    Sebastian Zarnekow - Contributions for
*       bug 544921 - [performance] Poor performance with large source files
*    Alexander Lehmann - Contributions for
*       bug 566258 - Intermittent NPE in APT RoundDispatcher
**********************************************************************************/

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* Contributors:
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*     Stephan Herrmann - Contributions for
*       bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug
 422796 - [compiler][null] boxed boolean reported as potentially null after null test in lazy disjunction
**********************************************************************************/

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/OR_OR_Expression.java
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*    IBM Corporation - fix for 342936
*    Kenneth Olson - Contribution for bug 188796 - [jsr199] Using JSR199 to extend ECJ
*    Dennis Hendriks - Contribution for bug 188796 - [jsr199] Using JSR199 to extend ECJ
*    Frits Jalvingh  - fix for bug 533830.
*******************************************************************************/
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*        bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug 361407 - Resource leak warning when resource is assigned to a field outside of constructor
*        bug 370639 - [compiler][resource] restore the default for resource leak warnings
*        bug 388996 - [compiler][resource] Incorrect
 'potential resource leak'
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*        Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
```

* Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
* Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
* Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug 427438 - [1.8][compiler] NPE at org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic bridge method
**********/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ExplicitConstructorCall.java
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* Contributors:
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* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 407191 - [1.8] Binary access support for type annotations
***/

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*   Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*   Bug 473178
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*   Stephan Herrmann - Contribution for
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*   Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)

```
 *       Bug 429403 - [1.8][null] null mismatch from type arguments is not reported at field initializer
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 458396 - NPE in CodeStream.invoke()
*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*       Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
********************************************************************************/
```

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/FieldDeclaration.java
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* Contributors:
*     IBM Corporation - initial API and implementation
*     Genady Beriozkin - added support for reporting assignment with no effect
*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*       bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*       bug 292478 - Report potentially null across variable assignment
*         bug 335093 - [compiler][null] minimal hook for future null annotation support
*         bug 349326 - [1.7] new warning for missing
 try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 370639 - [compiler][resource] restore the default for resource leak warnings
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 402993 - [null] Follow up of bug 401088: Missing
 warning about redundant null check
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
```

```
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
*********************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/Assignment.java
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*       bug 400710 - [1.8][compiler] synthetic access to default method generates wrong code
*       Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
*       Bug 470467 - [null] Nullness of special Enum methods not detected from .class file
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 405104 -
 [1.8][compiler][codegen] Implement support for serializeable lambdas
*********************************************************************************/
```

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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*       bug 292478 - Report potentially null across variable assignment,
*       bug 185682 - Increment/decrement operators mark local variables as read
*       bug 331649 - [compiler][null] consider null annotations for fields
```

```
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       Bug 412203 - [compiler] Internal compiler error: java.lang.IllegalArgumentException:
 info cannot be null
*       Bug 458396 - NPE in CodeStream.invoke()
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
*     Jesper S Moller - <jesper@selskabet.org>   - Contributions for
*          bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*       bug 378674 - "The method can be declared as static" is wrong
*       bug 404657 - [1.8][compiler] Analysis for effectively final variables fails to consider loops
*       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*******************************************************************************/
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*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 426676 - [1.8][compiler] Wrong generic method type inferred from lambda expression
*       Bug 426542 - [1.8] Most specific method not picked when one method has intersection type as type parameter
*       Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*     Andy
 Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*     Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
*                       Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*******************************************************************************/
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*         bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*         bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
 checking
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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
***/

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*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)

* Bug 409246 - [1.8][compiler] Type annotations on catch parameters not handled properly
***/

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*        bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*        bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*        bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*        Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*        Bug 415850 - [1.8] Ensure RunJDTCoreTests
 can cope with null annotations enabled
*        Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*        Bug 427163 - [1.8][null] bogus error "Contradictory null specification" on varags
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
*        Bug 434600 - Incorrect null analysis error reporting on type parameters
*        Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
*        Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*        Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*        Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*        Bug 437072 - [compiler][null] Null analysis
 emits possibly incorrect warning for new int[][] despite @NonNullByDefault
*        Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                        Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                        Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
*                        Bug 415399 - [1.8][compiler] Type annotations on constructor results dropped by the code
generator
*      Jesper S Møller <jesper@selskabet.org> -  Contributions for
*                        bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*                        bug 529556 - [18.3] Add content assist support for 'var' as a type
*
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/EclipseFileManager.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/StackMapFrameCodeStream.java
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*     Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*         bug 328281 - visibility leaks not detected when analyzing unused field in private class
*         bug 382353 - [1.8][compiler] Implementation property modifiers should be accepted on default methods.
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/GenericXMLWriter.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/ManifestAnalyzer.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/UnresolvedAnnotationBinding.java
*
/ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/ReferenceContext.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/DefaultErrorHandlingPolicies.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/DefaultProblemFactory.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/Receiver.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/flow/InsideSubRoutineFlowContext.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/core/JDTCompilerAdapter.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/CompilerStats.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/IErrorHandlingPolicy.java
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*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
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*     Jesper S Moller - Contributions for
*        bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*    Stephan Herrmann - Contribution for
*        bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 416182 - [1.8][compiler][null]
 Contradictory null annotations not rejected
*******************************************************************************/
```

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*     Stephan Herrmann - Contribution for
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*******************************************************************************/
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*         Bug 428811 - [1.8][compiler] Type witness unnecessarily required
*******************************************************************************/
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*     Stephan Herrmann - Contributions for
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 358903 - Filter practically unimportant resource leak warnings
*         bug 381445 - [compiler][resource] Can the resource leak check be made aware of Closeables.closeQuietly?
*         bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject into account
*         bug 382069 - [null] Make
 the null analysis consider JUnit's assertNotNull similarly to assertions
*         Bug 405569 - Resource leak check false positive when using DbUtils.closeQuietly
*         Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*         Bug 425183 - [1.8][inference] make CaptureBinding18 safe
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
```

Collection#remove(Object) et al.
*   Jesper S Moller - Contributions for
*   Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*   Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*   bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 405104 - [1.8][compiler][codegen]
 Implement support for serializeable lambdas
*   Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
*   bug 386692 - Missing "unused" warning on "autowired" fields
*   Pierre-Yves B. <pyvesdev@gmail.com> - Contribution for
*   bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
**/

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*     Stephan Herrmann - Contribution for
*       bug 331649 - [compiler][null] consider null annotations for fields
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 458396 - NPE in CodeStream.invoke()
*******************************************************************************/
```

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*     Stephan Herrmann - Contributions for
*           bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*           bug 349326 - [1.7] new warning for missing try-with-resources
*           bug 360328 - [compiler][null] detect null problems in nested code (local class inside a loop)
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365835 - [compiler][null] inconsistent error reporting.
```

```
 *       bug 365519 - editorial cleanup after bug 186342 and bug 365387
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*       bug 370639 - [compiler][resource] restore the default for resource leak warnings
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*       bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*       bug 401030 - [1.8][null] Null analysis support for lambda
 methods.
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*       Bug 417758 - [1.8][null] Null safety compromise during array creation.
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 430150 - [1.8][null] stricter checking against type variables
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 455723 - Nonnull argument not correctly inferred in loop
*     Jesper S Moller - Contributions
 for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
**********************************************************************************/

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*       bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*       bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
```

checking
* Bug 422796 - [compiler][null] boxed boolean reported as potentially null after
null test in lazy disjunction
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* bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
* Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
* Stephan Herrmann - Contribution for
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 423504 - [1.8] Implement "18.5.3
Functional Interface Parameterization Inference"
* Bug 425142 - [1.8][compiler] NPE in ConstraintTypeFormula.reduceSubType
* Bug 425153 - [1.8] Having wildcard allows incompatible types in a lambda expression
* Bug 425156 - [1.8] Lambda as an argument is flagged with incompatible error
* Bug 424403 - [1.8][compiler] Generic method call with method reference argument fails to resolve properly.
* Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 446442 - [1.8] merge null annotations from super methods
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
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*   IBM Corporation - fix for 342598
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*   Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
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*   Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*   bug 185682 - Increment/decrement operators mark local variables as read
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 392384 - [1.8][compiler][null] Restore nullness
 info from type annotations in class files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
*   Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
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*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*                    Bug 384687 - [1.8] Wildcard type arguments should be rejected for lambda and reference
expressions
*       Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*    Stephan Herrmann - Contribution for
*       bug 402028 - [1.8][compiler] null analysis for reference
 expressions
*       bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super via I.super.m() syntax
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 424637 - [1.8][compiler][null] AIOOB in ReferenceExpression.resolveType with a method reference to
Files::walk
*       Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*       Bug 424403 - [1.8][compiler] Generic method call with method reference argument fails to resolve properly.
*       Bug 427196 - [1.8][compiler] Compiler error for method reference to overloaded method
*       Bug 427438
 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*       Bug 428264 - [1.8] method reference of generic class causes problems (wrong inference result or NPE)
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 426537 - [1.8][inference] Eclipse compiler thinks I<? super J> is compatible with I<J<?>> - raw type J
involved
*       Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*       Bug 435689 - [1.8][inference] Type inference not occurring with lambda expression and method reference
*       Bug 438383 - [1.8][null] Bogus warning: Null type safety at method return type
*       Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
```

```
*       Bug 441734 - [1.8][inference] Generic method with nested parameterized type argument fails on method reference
*       Bug 438945 - [1.8] NullPointerException
 InferenceContext18.checkExpression in java 8 with generics, primitives, and overloading
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*       Bug 448709 - [1.8][null] ensure we don't infer types that violate null constraints on a type parameter's bound
*       Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
*       Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*       Bug 470542 - NullPointerException in ReferenceExpression.isPotentiallyCompatibleWith (962)
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contribution for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
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*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*        bug 331649 - [compiler][null] consider null annotations for fields
*        Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*        Bug 415043 - [1.8][null] Follow-up re null type annotations
 after bug 392099
*        Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*        Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*        Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*        Bug 424728 - [1.8][null] Unexpected error: The nullness annotation 'XXXX' is not applicable at this location
*        Bug 392245 - [1.8][compiler][null] Define whether / how @NonNullByDefault applies to TYPE_USE locations
*        Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*        Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*        Bug 457210 - [1.8][compiler][null] Wrong Nullness errors given on full build build but not on incremental build?
*        Bug 469584 - ClassCastException in Annotation.detectStandardAnnotation (320)
*        Andy Clement (GoPivotal,
 Inc) aclement@gopivotal.com - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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```
work)
*                 Bug 409517 - [1.8][compiler] Type annotation problems on more elaborate array references
*                 Bug 415397 - [1.8][compiler] Type Annotations on wildcard type argument dropped
*                 Bug 414384 - [1.8] type annotation on abbreviated inner class is not marked as inner type
*     Jesper S Moller <jesper@selskabet.org> -  Contributions for
*                 Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*                 Bug 412151 - [1.8][compiler] Check repeating annotation's collection type
*                 Bug 412149 - [1.8][compiler] Emit repeated annotations into the designated container
*                 Bug 419209 - [1.8] Repeating container annotations
 should be rejected in the presence of annotation it contains
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*     Stephan Herrmann - Contribution for
*       Bug 440687 - [compiler][batch][null] improve command line option for external annotations
*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*         Bug 473178
*******************************************************************************/
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*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 417295 - [1.8[[null]
 Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*   Andy Clement - Contributions for
*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
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*   bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*   bug 401030 - [1.8][null] Null analysis support for lambda methods.
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
**/

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*   Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 292478 - Report potentially null across variable assignment
*   bug 335093 - [compiler][null] minimal hook for future null annotation support
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
 *   bug 358903 - Filter practically unimportant resource leak warnings
*   bug 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*   bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*   bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 *   Bug 430150 - [1.8][null] stricter checking against type variables
*   Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*   Jesper S Moller - Contributions for
*   Bug 378674 - "The method can be declared as static" is wrong

* Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
* Bug 529556 - [18.3] Add content assist support for 'var' as a type
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
* Bug 426616 - [1.8][compiler] Type Annotations, multiple problems
**********/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/diagnose/LexStream.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/NormalAnnotation.java
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* Contributors:
*   wharley@bea.com - derived base class from BatchMessagerImpl
*
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*   Bug 440474 - [null] textual encoding of external null annotations
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*   bug 342671 - ClassCastException: org.eclipse.jdt.internal.compiler.lookup.SourceTypeBinding cannot be cast to
org.eclipse.jdt.internal.compiler.lookup.ArrayBinding
*   Bug 420894 - ClassCastException in DefaultBindingResolver.resolveType(Type)
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415043 - [1.8][null] Follow-up re null type
 annotations after bug 392099
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Bug 434600 - Incorrect null analysis error reporting on type parameters
*   Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"

* Bug 456508 - Unexpected RHS PolyTypeBinding for: <code-snippet>
* Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
* Andy Clement - Contributions for
* Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's work)
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*     Carmi Grushko - Bug 465048 - Binding is null for class literals in synchronized blocks
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*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
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*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
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*   bug 325755 - [compiler] wrong initialization state after conditional expression
*   bug 320170 - [compiler] [null] Whitebox issues in null analysis
*   bug 292478 - Report potentially null across variable assignment
*   bug 332637 - Dead Code detection removing code that isn't dead
*   bug 341499 - [compiler][null] allocate extra bits in all methods of UnconditionalFlowInfo
 *   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 386181 - [compiler][null] wrong transition in UnconditionalFlowInfo.mergedWith()
*   bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*   Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*   Bug 454031 - [compiler][null][loop] bug in null analysis; wrong "dead code" detection
*   Bug 421035 - [resource] False alarm of resource leak warning when casting a closeable in its assignment
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/batch/messages.properties
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/messages.properties
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*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 429384 - [1.8][null] implement conformance rules for null-annotated lower / upper type bounds
*   Bug 441797 - [1.8] synchronize type annotations on capture and its wildcard
*   Bug 456497 - [1.8][null] during inference nullness from target type is lost against weaker
 hint from applicability analysis
*   Bug 456924 - StackOverflowError during compilation
*   Bug 462790 - [null] NPE in Expression.computeConversion()
*   Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
*   Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
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*          Bug 349326 - [1.7] new warning for missing try-with-resources
*          Bug
 358903 - Filter practically unimportant resource leak warnings
*       Bug 395977 - [compiler][resource] Resource leak warning behavior possibly incorrect for anonymous inner class
*       Bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*       Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*       Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
*       Bug 444024 - [1.8][compiler][null] Type mismatch error in annotation generics assignment which happens
"sometimes"
*       Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com -
 Contributions for
*                       Bug 415821 - [1.8][compiler] CLASS_EXTENDS target type annotation missing for anonymous
classes
*     het@google.com - Bug 456986 - Bogus error when annotation processor generates annotation type
*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*          Bug 473178
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*       bug 328281 - visibility leaks not detected when analyzing unused field in private class
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*       bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365836 - [compiler][null] Incomplete propagation of null defaults.
*       bug 365519 - editorial cleanup after bug 186342
 and bug 365387
*       bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*       bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*       bug 366063 - Compiler should not add synthetic @NonNull annotations
*       bug 384663 - Package Based Annotation Compilation Error in JDT 3.8/4.2 (works in 3.7.2)
*       bug 386356 - Type mismatch error with annotations and generics
*       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 380896 - [compiler][null] Enum constants not recognised as being NonNull.
*       bug 391376 - [1.8] check interaction of default methods with bridge methods and generics
*       Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
 *       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 416172 - [1.8][compiler][null] null type annotation not evaluated on method return type
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 426048 - [1.8] NPE in TypeVariableBinding.internalBoundCheck when parentheses are not balanced
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 432348 - [1.8] Internal compiler error (NPE) after upgrade to 1.8
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*       Bug 441693 - [1.8][null]
 Bogus warning for type argument annotated with @NonNull
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*       Bug 457210 - [1.8][compiler][null] Wrong Nullness errors given on full build build but not on incremental build?
*       Bug 461250 - ArrayIndexOutOfBoundsException in SourceTypeBinding.fields
*       Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*      Jesper S Moller <jesper@selskabet.org> -  Contributions for
*       Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*      Till Brychcy - Contributions for
*           bug 415269 - NonNullByDefault is not always inherited to nested classes
*      Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*      Sebastian Zarnekow - Contributions
 for
*       bug 544921 - [performance] Poor performance with large source files
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/SourceTypeBinding.java
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*   Stephan Herrmann - Contribution for
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
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*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
**********/

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocQualifiedTypeReference.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocSingleTypeReference.java
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*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*         bug 407191 - [1.8] Binary access support for type annotations
*     Stephan Herrmann - Contribution for
*         Bug 440474 - [null] textual encoding of external null annotations
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*     Benjamin Muskalla - Contribution for bug 239066
*     Stephan Herrmann  - Contributions for
*           bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*           bug 338303 - Warning about Redundant assignment conflicts with definite assignment
*       bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
 *       bug 365519 - editorial cleanup after bug 186342 and bug 365387
*       bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*       bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 382353 - [1.8][compiler] Implementation property modifiers should be accepted on default methods.
*       bug 382347 - [1.8][compiler] Compiler accepts incorrect default method inheritance
*       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 376053 - [compiler][resource] Strange potential resource leak problems
*       bug 381443 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*       bug 393719 - [compiler] inconsistent warnings on iteration
 variables
*       bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
```

* bug 388739 - [1.8][compiler] consider default methods when detecting whether a class needs to be declared abstract
* bug 331649 - [compiler][null] consider null annotations for fields
* bug 382789 - [compiler][null] warn when syntactically-nonnull expression is compared against null
* bug 376590 - Private fields with @Inject are ignored by unused field validation
* bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
* bug 402028 - [1.8][compiler] null analysis for reference expressions
* bug 401796 - [1.8][compiler] don't treat default methods as overriding an independent inherited abstract method
* bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
* bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
* Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
* Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
* Bug 414380 - [compiler][internal] QualifiedNameReference#indexOfFirstFieldBinding does not point to the first field
* Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
* Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 424637 - [1.8][compiler][null] AIOOB in ReferenceExpression.resolveType with a method reference to Files::walk
* Bug 428294 - [1.8][compiler] Type mismatch: cannot convert from List<Object> to Collection<Object[]>
* Bug 428366 - [1.8] [compiler] The method valueAt(ObservableList<Object>, int) is ambiguous for the type Bindings
* Bug 416190 - [1.8][null] detect incompatible overrides due to null type annotations
* Bug 392245 - [1.8][compiler][null] Define whether / how @NonNullByDefault applies to TYPE_USE locations
* Bug 390889 - [1.8][compiler] Evaluate options to support 1.7- projects against 1.8 JRE.
* Bug 430150 - [1.8][null] stricter checking against type variables
* Bug 434600 - Incorrect null analysis error reporting on type parameters
* Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
* Bug 438467 - [compiler][null] Better error position for "The method _ cannot implement the corresponding method _ due to incompatible nullness constraints"
* Bug 439298 - [null] "Missing code implementation in the compiler" when using @NonNullByDefault in package-info.java
* Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
* Bug 446442 - [1.8] merge null annotations from super methods
* Bug 455723 - Nonnull argument not correctly inferred in loop
* Bug 458361 - [1.8][null] reconciler throws NPE in ProblemReporter.illegalReturnRedefinition()
* Bug 459967 - [null] compiler should know about nullness of special methods like MyEnum.valueOf()
* Bug 461878 - [1.7][1.8][compiler][null] ECJ compiler does not allow to use null annotations on annotations
* Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object), Collection#remove(Object) et al.
* Jesper S Moller <jesper@selskabet.org> - Contributions for
* bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
* bug 382721 - [1.8][compiler] Effectively final variables needs

special treatment
* bug 384567 - [1.5][compiler] Compiler accepts illegal modifiers on package declaration
* bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
* bug 412151 - [1.8][compiler] Check repeating annotation's collection type
* bug 419209 - [1.8] Repeating container annotations should be rejected in the presence of annotation it contains
* Bug 429384 - [1.8][null] implement conformance rules for null-annotated lower / upper type bounds
* Bug 416182 - [1.8][compiler][null] Contradictory null annotations not rejected
* bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
* Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
* bug 386692 - Missing "unused" warning on "autowired" fields
**********/

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/ProblemReporter.java
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* Nick Teryaev - fix for bug (https://bugs.eclipse.org/bugs/show_bug.cgi?id=40752)
* Stephan Herrmann - Contributions for
* bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
* bug 349326 - [1.7] new warning for missing try-with-resources
* bug 186342 - [compiler][null] Using annotations for null checking
* bug 358903 - Filter practically unimportant resource leak warnings
* bug
370639 - [compiler][resource] restore the default for resource leak warnings
* bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
* bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
* bug 379784 - [compiler] "Method can be static" is not getting reported
* bug 379834 - Wrong "method can be static" in presence of qualified super and different staticness of nested super class.
* bug 388281 - [compiler][null] inheritance of null annotations as an option
* bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
* bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
* bug 381445 - [compiler][resource] Can the resource leak check be made aware of Closeables.closeQuietly?
* bug 331649 - [compiler][null] consider null annotations for fields
* bug 383368 - [compiler][null] syntactic null

analysis for field references
* bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
* bug 382350 - [1.8][compiler] Unable to invoke inherited default method via I.super.m() syntax
* bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
* bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
* bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred checking
* Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
* Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
* Bug 405569 - Resource leak check false positive when using DbUtils.closeQuietly
* Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
* Bug 417295 - [1.8[[null] Massage type annotated null analysis
to gel well with deep encoded type bindings.
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
* Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
* Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
* Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
* Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
* Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug 427438 - [1.8][compiler]
NPE at org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
* Bug 426996 - [1.8][inference] try to avoid method Expression.unresolve()?
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException instead of IOException)
* Bug 441734 - [1.8][inference] Generic method with nested parameterized type argument fails on method reference
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Bug 456487 - [1.8][null] @Nullable type variant of @NonNull-constrained type parameter causes grief
* Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
* Bug 472618 - [compiler][null] assertNotNull vs. Assert.assertNotNull
* Bug 470958 - [1.8] Unable to convert lambda
* Bug 410218 - Optional warning
for arguments of "unexpected" types to Map#get(Object), Collection#remove(Object) et al.
* Jesper S Moller - Contributions for
* Bug 378674 - "The method can be declared as static" is wrong
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's work)
* Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic bridge method
**********/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/MessageSend.java

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*     Stephan Herrmann - Contributions for
*        bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*        bug
 383368 - [compiler][null] syntactic null analysis for field references
*        bug 401017 - [compiler][null] casted reference to @Nullable field lacks a warning
*        bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*        Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*        Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*        Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*        Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*        Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*        Bug 430150 - [1.8][null] stricter checking against type variables
```

```
*       Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style
 null annotations
*       Bug 407414 - [compiler][null] Incorrect warning on a primitive type being null
*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 415541 - [1.8][compiler] Type annotations in the body of static initializer get dropped
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*    IBM Corporation - Fix for bug 328575
*    het@google.com - Bug 415274 - Annotation processing throws a NPE in getElementsAnnotatedWith()
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/dispatch/RoundEnvImpl.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashSetOfInt.java

* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/core/compiler/InvalidInputException.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/BranchLabel.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/ObjectVector.java
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 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/VerificationTypeInfo.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IModuleAwareNameEnvironment.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ProvidesStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/INameEnvironmentExtension.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IMultiModuleEntry.java
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*   Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*   Bug 473178
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ConstraintFormula.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ConstraintExceptionFormula.java

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*    tyeung@bea.com  - initial API and implementation
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*    Stephan Herrmann - Contribution for bug 186342 - [compiler][null] Using annotations for null checking
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*     Jesper S Moller - Contributions for
*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*       Bug 406973 - [compiler] Parse MethodParameters attribute
*        Andy Clement - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
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*         Bug 440687 - [compiler][batch][null] improve command line option for external annotations
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*   Benjamin Muskalla - Contribution for bug 239066
*   Stephan Herrmann - Contribution for
*   bug 382347 - [1.8][compiler] Compiler accepts incorrect default method inheritance
*   bug 388954 - [1.8][compiler] detect default methods in class files
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 388739 - [1.8][compiler] consider default methods when detecting whether a class needs
 to be declared abstract
*   bug 390883 - [1.8][compiler] Unable to override default method
*   bug 401796 - [1.8][compiler] don't treat default methods as overriding an independent inherited abstract method
*   bug 395681 - [compiler] Improve simulation of javac6 behavior from bug 317719 after fixing bug 388795
*   bug 406928 - computation of inherited methods seems damaged (affecting @Overrides)
*   bug 409473 - [compiler] JDT cannot compile against JRE 1.8
*   Bug 420080 - [1.8] Overridden Default method is reported as duplicated
**********/

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*   Stephan Herrmann - Contribution for bug 186342 - [compiler][null] Using annotations for null checking
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 407191 - [1.8] Binary access support for type annotations
**********/

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*   tyeung@bea.com - initial API and implementation
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* Contributors:
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*   Jesper S Moller - Contributions for
*   bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*   bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*   Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*   Stephan Herrmann - Contribution for
*   bug 401030 - [1.8][null] Null analysis support for lambda methods.
*   Bug 392099
 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*   Bug 425142 - [1.8][compiler] NPE in ConstraintTypeFormula.reduceSubType
*   Bug 425153 - [1.8] Having wildcard allows incompatible types in a lambda expression
*   Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
*   Bug 425798 - [1.8][compiler] Another NPE in ConstraintTypeFormula.reduceSubType
*   Bug 425156 - [1.8] Lambda as an argument is flagged with incompatible error
*   Bug 424403 - [1.8][compiler] Generic method call with method reference argument fails to resolve properly.
*   Bug 426563 - [1.8] AIOOBE when method with error
 invoked with lambda expression as argument
*   Bug 420525 - [1.8] [compiler] Incorrect error "The type Integer does not define sum(Object, Object) that is
applicable here"
*   Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*   Bug 428294 - [1.8][compiler] Type mismatch: cannot convert from List<Object> to Collection<Object[]>

```
*       Bug 428786 - [1.8][compiler] Inference needs to compute the "ground target type" when reducing a lambda
compatibility constraint
*       Bug 428980 - [1.8][null] simple expression as lambda body doesn't leverage null annotation on argument
*       Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException
instead of IOException)
*       Bug 432110 - [1.8][compiler] nested lambda type incorrectly inferred vs javac
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type
 variables
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*       Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*       Bug 455723 - Nonnull argument not correctly inferred in loop
*       Bug 463728 - [1.8][compiler][inference] Ternary operator in lambda derives wrong type
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
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*     Stephan Herrmann - Contributions for
*       bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 417758 - [1.8][null] Null safety compromise during array creation.
*       Bug
 427163 - [1.8][null] bogus error "Contradictory null specification" on varags
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                     Bug 409247 - [1.8][compiler] Verify error with code allocating multidimensional array
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*     Gauthier JACQUES - initial API and implementation
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*     Stephan Herrmann - Contributions for
*          bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*          bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*       bug 370639 - [compiler][resource] restore the
 default for resource leak warnings
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 395977 - [compiler][resource] Resource leak warning behavior possibly incorrect for anonymous inner class
*       bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*       Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*       Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
```

```
*       Bug 416267 - NPE in QualifiedAllocationExpression.resolveType
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 424415 - [1.8][compiler] Eventual
 resolution of ReferenceExpression is not seen to be happening.
*       Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*     Jesper S Moller <jesper@selskabet.org> - Contributions for
*       bug 378674 - "The method can be declared as static" is wrong
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                       Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic
bridge method
*     Till Brychcy - Contributions for
*           bug 413460 - NonNullByDefault is not inherited to Constructors when accessed via Class File
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* Contributors:
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*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
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*     IBM Corporation - Bug fixes
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*    wharley@bea.com - initial API and implementation
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*    IBM Corporation - Bug 513790
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* Contributors:
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*     Stephan Herrmann - Contributions for
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 387612 - Unreachable catch block...exception is never thrown from the try
*         bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
```

* bug 401456 - Code compiles from javac/intellij, but fails from eclipse
* bug 401271 - StackOverflowError
when searching for a methods references
* bug 405706 - Eclipse compiler fails to give compiler error when return type is a inferred generic
* Bug 408441 - Type mismatch using Arrays.asList with 3 or more implementations of an interface with the interface type as the last parameter
* Bug 413958 - Function override returning inherited Generic Type
* Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
* Bug 416183 - [1.8][compiler][null] Overload resolution fails with null annotations
* Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
* Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
* Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
* Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
* Bug 424415 - [1.8][compiler]
Eventual resolution of ReferenceExpression is not seen to be happening.
* Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 426589 - [1.8][compiler] Compiler error with generic method/constructor invocation as vargs argument
* Bug 426590 - [1.8][compiler] Compiler error with tenary operator
* Bug 426764 - [1.8] Presence of conditional expression as method argument confuses compiler
* Bug 426998 - [1.8][compiler] method(java.lang.Class, java.lang.String) not applicable for the arguments (java.lang.Class, java.lang.String)
* Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
* Bug 427196 - [1.8][compiler] Compiler error for method reference to overloaded method
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug
427728 - [1.8] Type Inference rejects calls requiring boxing/unboxing
* Bug 427218 - [1.8][compiler] Verify error varargs + inference
* Bug 426836 - [1.8] special handling for return type in references to method getClass()?
* Bug 427628 - [1.8] regression : The method * is ambiguous for the type *
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 428366 - [1.8] [compiler] The method valueAt(ObservableList<Object>, int) is ambiguous for the type Bindings
* Bug 424728 - [1.8][null] Unexpected error: The nullness annotation 'XXXX' is not applicable at this location
* Bug 428811 - [1.8][compiler] Type witness unnecessarily required
* Bug 429424 - [1.8][inference] Problem inferring type of method's parameter
* Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
* Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
* Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
* Bug 441734 - [1.8][inference] Generic method with nested parameterized type argument fails on method reference
* Bug 452194 - Code no longer compiles in 4.4.1, but with confusing error
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Bug 456236 - [1.8][null] Cannot infer type when constructor argument is annotated with @Nullable
* Bug 437072 - [compiler][null] Null analysis emits possibly incorrect warning for new int[][] despite @NonNullByDefault
* Bug 462083 - [1.8][inference] Java 8 generic return type mismatch with interface involving type parameter.

```
*     Jesper S Moller - Contributions for
*       Bug 378674 - "The method can be declared as static" is wrong
*        Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*     Andy Clement (GoPivotal, Inc)
 aclement@gopivotal.com - Contributions for
*                       Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*     Pierre-Yves B. <pyvesdev@gmail.com> - Contributions for
*                         Bug 559618 - No compiler warning for import from same package
*                         Bug 560630 - No warning on unused import on class from same package
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/SimpleSetOfCharArray.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/CachedIndexEntry.java
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*   Bug 473178
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*        bug 400710 - [1.8][compiler] synthetic access to default method generates wrong code
*        bug 391376 - [1.8] check interaction of default methods with bridge methods and generics
*        bug 421543 - [1.8][compiler] Compiler fails to recognize default method being turned into abstract by subtytpe
*     Jesper S Moller - Contributions for
*       Bug 405066 - [1.8][compiler][codegen] Implement
 code generation infrastructure for JSR335
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                     Bug 409247 - [1.8][compiler] Verify error with code allocating multidimensional array
*                     Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
*                     Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*                     Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*                     Bug 449467 - [1.8][compiler] Invalid lambda deserialization with anonymous class
*       Olivier Tardieu (tardieu@us.ibm.com) - Contributions for
*                     Bug 442418 - $deserializeLambda$ off-by-one error when deserializing the captured arguments
 of a lambda that also capture this
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*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 421035 - [resource] False alarm of resource leak warning when casting a closeable in its assignment
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* Contributors:
*    tyeung@bea.com - initial API and implementation
*    olivier_thomann@ca.ibm.com - add hashCode() and equals(..) methods
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/ClassSignature.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/EnumConstantSignature.java
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*     Tom Tromey - Contribution for bug 159641
*     Benjamin Muskalla - Contribution for bug 239066
*     Stephan Herrmann  - Contributions for
*          bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*          bug 295551 - Add option to automatically promote all warnings to errors
*          bug 359721 - [options] add command line option
 for new warning token "resource"
*       bug 365208 - [compiler][batch] command line options for annotation based null analysis
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 375366 - ECJ ignores unusedParameterIncludeDocCommentReference unless enableJavadoc option is set
*       bug 388281 - [compiler][null] inheritance of null annotations as an option
*       bug 381443 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*       Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*       Bug 440687 - [compiler][batch][null] improve command line option for external annotations
*       Bug 408815 - [batch][null] Add CLI option for
COMPILER_PB_SYNTACTIC_NULL_ANALYSIS_FOR_FIELDS
*     Jesper S Moller   - Contributions for
*       bug 407297 - [1.8][compiler] Control generation of parameter names by option
*    Mat Booth - Contribution for bug 405176
*    Frits Jalvingh -
 fix for bug 533830.
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*
*Contributors:
*   IBM Corporation - initial API and implementation
*   Stephan Herrmann - Contribution for
*   Bug 429813 - [1.8][dom ast] IMethodBinding#getJavaElement() should return IMethod for lambda
**/

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/Initializer.java
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* Contributors:
*   IBM Corporation - initial API and implementation
*   Stephen Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*   bug 317046 - Exception during debugging when hover mouse over a field
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class
 files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis

```
*       Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 424712 - [1.8][compiler] NPE in TypeBinding.isProvablyDistinctTypeArgument
*       Bug 426792 - [1.8][inference][impl] generify new type inference engine
*       Bug 426764 - [1.8] Presence of conditional expression as method argument confuses compiler
*       Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*       Bug 427626 - [1.8] StackOverflow while typing new ArrayList<String>().toArray( and asking for code completion
 *       Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*       Bug 435962 - [RC2] StackOverFlowError when building
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*       Bug 446434 - [1.8][null] Enable interned captures also when analysing null type annotations
*     Jesper S Moller <jesper@selskabet.org> -  Contributions for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*
**********************************************************************************/

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* Contributors:
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*     Stephan Herrmann - Contribution for
*       bug 331649 - [compiler][null] consider null annotations for fields
*       bug 383368 - [compiler][null] syntactic null analysis for field references
*     Jesper S Moller - Contributions for
*       Bug 378674 - "The method can be declared as static" is wrong
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ThisReference.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/ArchiveFileObject.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/util/Util.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/Util.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/util/ArchiveFileObject.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/Archive.java
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*
* Contributors:
*   wharley@bea.com - initial API and implementation
*   IBM Corporation - fix for 342598
*   IBM Corporation - Java 8 support
*   het@google.com - Bug 427943 - The method
org.eclipse.jdt.internal.compiler.apt.model.Factory.getPrimitiveType does not throw IllegalArgumentException
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/model/Factory.java
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* Contributors:
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*   Stephan Herrmann - Contribution for
*   Bug 427438 - [1.8][compiler] NPE at

org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ExpressionContext.java
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* Contributors:
*   Stephan Herrmann - initial API and implementation
*   Till Brychcy - Contributions for
*   Bug 467482 - TYPE_USE null annotations: Incorrect "Redundant null check"-warning
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/core/compiler/CompilationProgress.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredAnnotation.java
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*   Frits Jalvingh - fix for bug 533830.
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/PackageExportImpl.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/UsesStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/OpensStatement.java
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* Contributors:
*   IBM Corporation - initial API and implementation
*   Stephan Herrmann - Contributions for
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 417295 - [1.8[[null] Massage type annotated
 null analysis to gel well with deep encoded type bindings.
*   Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ParameterizedMethodBinding.java
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* Contributors:
*   IBM Corporation - initial API and implementation
*   Tom Tromey - patch for readTable(String) as described in http://bugs.eclipse.org/bugs/show_bug.cgi?id=32196
*   Stephan Herrmann - Contributions for

```
*       bug 366003 - CCE in ASTNode.resolveAnnotations(ASTNode.java:639)
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 393719 - [compiler] inconsistent warnings on iteration variables
*       bug 382353 - [1.8][compiler] Implementation property modifiers
 should be accepted on default methods.
*       bug 383973 - [1.8][compiler] syntax recovery in the presence of default methods
*       bug 401035 - [1.8] A few tests have started failing recently
*     Jesper S Moller - Contributions for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
*       bug 399695 - [1.8][compiler] [1.8][compiler] migrate parser to other syntax for default methods
*       bug 384567 - [1.5][compiler] Compiler accepts illegal modifiers on package declaration
*       bug 393192 - Incomplete type hierarchy with > 10 annotations
*       bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*       Andy Clement - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                       Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*
           Bug 415821 - [1.8][compiler] CLASS_EXTENDS target type annotation missing for anonymous classes
*******************************************************************************/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/Parser.java
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*     Stephan Herrmann - Contribution for
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
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*     IBM Corporation - added the following constants
*          NonStaticAccessToStaticField
*          NonStaticAccessToStaticMethod
*          Task
*          ExpressionShouldBeAVariable
*          AssignmentHasNoEffect
*     IBM Corporation - added the following constants
*          TooManySyntheticArgumentSlots
*          TooManyArrayDimensions
*          TooManyBytesForStringConstant
*          TooManyMethods
*          TooManyFields
 *          NonBlankFinalLocalAssignment
*          ObjectCannotHaveSuperTypes
*          MissingSemiColon
*          InvalidParenthesizedExpression
*          EnclosingInstanceInConstructorCall
*          BytecodeExceeds64KLimitForConstructor
*          IncompatibleReturnTypeForNonInheritedInterfaceMethod
*          UnusedPrivateMethod
*          UnusedPrivateConstructor
*          UnusedPrivateType
*          UnusedPrivateField
*          IncompatibleExceptionInThrowsClauseForNonInheritedInterfaceMethod
*          InvalidExplicitConstructorCall
*     IBM Corporation - added the following constants
*          PossibleAccidentalBooleanAssignment
*          SuperfluousSemicolon
*          IndirectAccessToStaticField
*          IndirectAccessToStaticMethod
*          IndirectAccessToStaticType
*          BooleanMethodThrowingException
*          UnnecessaryCast
*          UnnecessaryArgumentCast
*          UnnecessaryInstanceof
*
   FinallyMustCompleteNormally
*          UnusedMethodDeclaredThrownException
*          UnusedConstructorDeclaredThrownException
```

```
*         InvalidCatchBlockSequence
*         UnqualifiedFieldAccess
*     IBM Corporation - added the following constants
*         Javadoc
*         JavadocUnexpectedTag
*         JavadocMissingParamTag
*         JavadocMissingParamName
*         JavadocDuplicateParamName
*         JavadocInvalidParamName
*         JavadocMissingReturnTag
*         JavadocDuplicateReturnTag
*         JavadocMissingThrowsTag
*         JavadocMissingThrowsClassName
*         JavadocInvalidThrowsClass
*         JavadocDuplicateThrowsClassName
*         JavadocInvalidThrowsClassName
*         JavadocMissingSeeReference
*         JavadocInvalidSeeReference
*         JavadocInvalidSeeHref
*         JavadocInvalidSeeArgs
*         JavadocMissing
*         JavadocInvalidTag
*         JavadocMessagePrefix
 *         EmptyControlFlowStatement
*     IBM Corporation - added the following constants
*         IllegalUsageOfQualifiedTypeReference
*         InvalidDigit
*     IBM Corporation - added the following constants
*         ParameterAssignment
*         FallthroughCase
*     IBM Corporation - added the following constants
*                         UnusedLabel
*                         UnnecessaryNLSTag
*                         LocalVariableMayBeNull
*                         EnumConstantsCannotBeSurroundedByParenthesis
*                         JavadocMissingIdentifier
*                         JavadocNonStaticTypeFromStaticInvocation
*                         RawTypeReference
*                         NoAdditionalBoundAfterTypeVariable
*                         UnsafeGenericArrayForVarargs
*                         IllegalAccessFromTypeVariable
*
   AnnotationValueMustBeArrayInitializer
*                         InvalidEncoding
*                         CannotReadSource
*                         EnumStaticFieldInInInitializerContext
*                         ExternalProblemNotFixable
*                         ExternalProblemFixable
```

```
*    IBM Corporation - added the following constants
*                        AnnotationValueMustBeAnEnumConstant
*                        OverridingMethodWithoutSuperInvocation
*                        MethodMustOverrideOrImplement
*                        TypeHidingTypeParameterFromType
*                        TypeHidingTypeParameterFromMethod
*                        TypeHidingType
*    IBM Corporation - added the following constants
*        NullLocalVariableReference
*        PotentialNullLocalVariableReference
*        RedundantNullCheckOnNullLocalVariable
*        NullLocalVariableComparisonYieldsFalse
 *        RedundantLocalVariableNullAssignment
*        NullLocalVariableInstanceofYieldsFalse
*        RedundantNullCheckOnNonNullLocalVariable
*        NonNullLocalVariableComparisonYieldsFalse
*    IBM Corporation - added the following constants
*                        InvalidUsageOfTypeParametersForAnnotationDeclaration
*                        InvalidUsageOfTypeParametersForEnumDeclaration
*    IBM Corporation - added the following constants
*        RedundantSuperinterface
*  Benjamin Muskalla - added the following constants
*       MissingSynchronizedModifierInInheritedMethod
*  Stephan Herrmann  - added the following constants
*       UnusedObjectAllocation
*       PotentiallyUnclosedCloseable
*       PotentiallyUnclosedCloseableAtExit
*       UnclosedCloseable
*       UnclosedCloseableAtExit
*       ExplicitlyClosedAutoCloseable
*          RequiredNonNullButProvidedNull
 *        RequiredNonNullButProvidedPotentialNull
*        RequiredNonNullButProvidedUnknown
*        NullAnnotationNameMustBeQualified
*        IllegalReturnNullityRedefinition
*        IllegalRedefinitionToNonNullParameter
*        IllegalDefinitionToNonNullParameter
*        ParameterLackingNonNullAnnotation
*        ParameterLackingNullableAnnotation
*        PotentialNullMessageSendReference
*        RedundantNullCheckOnNonNullMessageSend
*        CannotImplementIncompatibleNullness
*        RedundantNullAnnotation
*       RedundantNullDefaultAnnotation
*       RedundantNullDefaultAnnotationPackage
*       RedundantNullDefaultAnnotationType
*       RedundantNullDefaultAnnotationMethod
*       ContradictoryNullAnnotations
```

* IllegalAnnotationForBaseType
* RedundantNullCheckOnSpecdNonNullLocalVariable
* SpecdNonNullLocalVariableComparisonYieldsFalse
* RequiredNonNullButProvidedSpecdNullable
* MissingDefaultCase
* MissingEnumConstantCaseDespiteDefault
* UninitializedLocalVariableHintMissingDefault
* UninitializedBlankFinalFieldHintMissingDefault
* ShouldReturnValueHintMissingDefault
* IllegalModifierForInterfaceDefaultMethod
* InheritedDefaultMethodConflictsWithOtherInherited
* ConflictingNullAnnotations
* ConflictingInheritedNullAnnotations
* UnsafeElementTypeConversion
* ArrayReferencePotentialNullReference
* DereferencingNullableExpression
* NullityMismatchingTypeAnnotation
* NullityMismatchingTypeAnnotationSuperHint
* NullityUncheckedTypeAnnotationDetail
* NullityUncheckedTypeAnnotationDetailSuperHint
* NullableFieldReference
* UninitializedNonNullField
* UninitializedNonNullFieldHintMissingDefault
* NonNullMessageSendComparisonYieldsFalse
* RedundantNullCheckOnNonNullSpecdField
* NonNullSpecdFieldComparisonYieldsFalse
* NonNullExpressionComparisonYieldsFalse
* RedundantNullCheckOnNonNullExpression
* ReferenceExpressionParameterNullityMismatch
* ReferenceExpressionParameterNullityUnchecked
* ReferenceExpressionReturnNullRedef
* ReferenceExpressionReturnNullRedefUnchecked
* DuplicateInheritedDefaultMethods
* SuperAccessCannotBypassDirectSuper
* SuperCallCannotBypassOverride
* ConflictingNullAnnotations
* ConflictingInheritedNullAnnotations
* UnsafeElementTypeConversion
* PotentialNullUnboxing
* NullUnboxing
* NullExpressionReference
* PotentialNullExpressionReference
* RedundantNullCheckAgainstNonNullType
* NullAnnotationUnsupportedLocation
* NullAnnotationUnsupportedLocationAtType
* NullityMismatchTypeArgument
* ContradictoryNullAnnotationsOnBound
* UnsafeNullnessCast

```
 *        ContradictoryNullAnnotationsInferred
*        NonNullDefaultDetailIsNotEvaluated
*        NullNotCompatibleToFreeTypeVariable
*        NullityMismatchAgainstFreeTypeVariable
*        ImplicitObjectBoundNoNullDefault
*        IllegalParameterNullityRedefinition
*        ContradictoryNullAnnotationsInferredFunctionType
*        IllegalReturnNullityRedefinitionFreeTypeVariable
*        UnlikelyCollectionMethodArgumentType
*        UnlikelyEqualsArgumentType
*      Jesper S Moller  - added the following constants
*        TargetTypeNotAFunctionalInterface
*        OuterLocalMustBeEffectivelyFinal
*        IllegalModifiersForPackage
*        DuplicateAnnotationNotMarkedRepeatable
*        DisallowedTargetForContainerAnnotation
*        RepeatedAnnotationWithContainerAnnotation
*        ContainingAnnotationMustHaveValue
*        ContainingAnnotationHasNonDefaultMembers
*        ContainingAnnotationHasWrongValueType
*        ContainingAnnotationHasShorterRetention
 *        RepeatableAnnotationHasTargets
*        RepeatableAnnotationTargetMismatch
*        RepeatableAnnotationIsDocumented
*        RepeatableAnnotationIsInherited
*        RepeatableAnnotationWithRepeatingContainerAnnotation
*        VarLocalMultipleDeclarators
*        VarLocalCannotBeArray
*        VarLocalReferencesItself
*        VarLocalWithoutInitizalier
*        VarLocalInitializedToNull
*        VarLocalCannotBeArrayInitalizers
*        VarLocalCannotBeLambda
*        VarLocalCannotBeMethodReference
*        VarIsReserved
*        VarIsReservedInFuture
*        VarIsNotAllowedHere
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*     Stephan Herrmann - Contribution for
*         bug 382350 - [1.8][compiler] Unable to invoke inherited default method via I.super.m() syntax
*         bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*     Jesper S Moller <jesper@selskabet.org> - Contributions for
*         bug 378674 - "The method can be declared as static" is wrong
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*     Stephan Herrmann - initial API and implementation
*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*          Bug 473178
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*   Stephan Herrmann - Contribution for
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug
 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*   Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*   Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 425460 - [1.8] [inference] Type not inferred on stream.toArray
*   Bug 426792 - [1.8][inference][impl] generify new type inference engine
*   Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*   Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*   Bug 441693 -
 [1.8][null] Bogus warning for type argument annotated with @NonNull
*   Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
*   Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
**********/

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*   Stephan Herrmann - Contributions for
*   Bug 186342 - [compiler][null] Using annotations for null checking
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
**********/

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/MostSpecificExceptionMethodBinding.java

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*     Walter Harley   - Patch for ensuring the parent folders are created
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/util/EclipseFileObject.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/EclipseFileObject.java
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*     Stephan Herrmann - Contribution for
*        bug 349326 - [1.7] new warning for missing try-with-resources
*        bug 370930 - NonNull annotation not considered for enhanced for loops
*        bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 393719 - [compiler] inconsistent warnings
 on iteration variables
*        Bug 411964 - [1.8][null] leverage null type annotation in foreach statement
*        Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*     Jesper S Moller -  Contribution for
*        bug 401853 - Eclipse Java compiler creates invalid bytecode (java.lang.VerifyError)
*                             bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
```

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*   Jesper Steen Moller - Bug 412150 [1.8] [compiler] Enable reflected parameter names during annotation processing
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredModule.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ModuleReference.java
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*   Erling Ellingsen - patch for bug 125570

```
*     Stephan Herrmann - Contribution for
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 434570 - Generic type mismatch for parametrized class annotation attribute with inner class
*     Sebastian Zarnekow - Contribution for
*       Bug 544921 - [performance] Poor performance with large source files
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/CompilationUnitScope.java
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* Contributors:
*    tyeung@bea.com - initial API and implementation
*    olivier_thomann@ca.ibm.com - add hashCode() and equals(..) methods
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/AnnotationInfo.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/ElementValuePairInfo.java
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*   Stephan Herrmann <stephan@cs.tu-berlin.de> - Contribution for bug 185682 - Increment/decrement operators
mark local variables as read
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*   Jesper Steen Moeller - Contributions for:
*   Bug 407297: [1.8][compiler] Control generation of parameter names by option
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/Options.java

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*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug 365519 - editorial cleanup after bug 186342 and bug 365387
*        bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*        bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*        bug 385626 - @NonNull fails across
 loop boundaries
*        bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 455723 - Nonnull argument not correctly inferred in loop
*     Jesper S Moller - Contributions for
*        bug 404657 - [1.8][compiler] Analysis for effectively final variables fails to consider loops
*******************************************************************************/
```

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/flow/FinallyFlowContext.java
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*          bug 332637 - Dead Code detection removing code that isn't dead
*          bug 358827 - [1.7] exception analysis for t-w-r spoils null analysis
*          bug 349326 - [1.7] new warning for missing try-with-resources
*          bug 359334 - Analysis for resource leak warnings does not consider exceptions as method exit points
*       bug 358903 - Filter practically unimportant resource
 leak warnings
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 401088 - [compiler][null] Wrong warning "Redundant null check" inside nested try statement
*       bug 401092 - [compiler][null] Wrong warning "Redundant null check" in outer catch of nested try
*       bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*       bug 384380 - False positive on a ?? Potential null pointer access ?? after a continue
*       Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
*       Bug 371614 - [compiler][resource] Wrong "resource leak" problem on return/throw inside while loop
*       Bug 444964 - [1.7+][resource] False resource leak warning (try-with-resources for ByteArrayOutputStream -
return inside for loop)
*     Jesper Steen Moller -
 Contributions for
*       bug 404146 - [1.7][compiler] nested try-catch-finally-blocks leads to unrunnable Java byte code
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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*
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*        bug 382350 - [1.8][compiler] Unable to invoke inherited default method via I.super.m() syntax
*        bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*        bug 404728 - [1.8]NPE on QualifiedSuperReference error
*******************************************************************************/
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*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*   Bug 417758 - [1.8][null] Null safety compromise during array creation.
*   Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
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*   bug 331649 - [compiler][null] consider null annotations for fields
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 426996 - [1.8][inference] try to avoid method Expression.unresolve()?
*   Jesper S Moller - Contributions for
*   bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
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*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
**/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IBinaryModule.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IUpdatableModule.java
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*   het@google.com - Bug 441790
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*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365519 - editorial cleanup after bug 186342 and bug 365387
*         bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*******************************************************************************/
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*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
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*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*                       Bug 409247 - [1.8][compiler] Verify error with code allocating multidimensional array
*                       Bug 409517 - [1.8][compiler] Type annotation problems on more elaborate array references
*
                Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
*******************************************************************************/
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*           bug 185682 - Increment/decrement operators mark local variables as read
*           bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*       bug 331649 - [compiler][null] consider
 null annotations for fields
*       Bug 466308 - [hovering] Javadoc header for parameter is wrong with annotation-based null analysis
*     Jesper S Møller <jesper@selskabet.org> -  Contributions for
*       Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
*
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*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred checking
***/

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*         bug 185682 - Increment/decrement operators mark local variables as read
*         bug 331649 - [compiler][null] consider null annotations for fields
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 447088 - [null] @Nullable on fully qualified field type is ignored
*         Bug 435805 - [1.8][compiler][null]
 Java 8 compiler does not recognize declaration style null annotations
*         Bug 458396 - NPE in CodeStream.invoke()
*         Bug 446217 - [null] @NonNullByDefault in package-info.java causes bogus "null type safety" warning
*     Till Brychcy - Contribution for
*               bug 467094 - [1.8][null] TYPE_USE NullAnnotations of array contents are applied to field.
**********************************************************************************/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredUnit.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredField.java
*
```

/ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/AnnotationMethodDeclaration.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/batch/ClasspathLocation.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/ICompilationUnit.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/NameEnvironmentAnswer.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ClassLiteralAccess.java
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*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*   Jesper S Moller - Contributions for
*   bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
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*   Jesper Steen Moeller - Contribution for bug 406973 - [compiler] Parse MethodParameters attribute
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 407191 - [1.8] Binary access support for type annotations

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*   bug 282152 - [1.5][compiler] Generics code rejected by Eclipse but accepted by javac
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 359362 - FUP of bug 349326: Resource leak on non-Closeable resource
*   bug 358903 - Filter practically unimportant resource leak warnings
*   bug 395002 - Self bound generic class
 doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 426792 - [1.8][inference][impl] generify new type inference engine
*   Bug 428019 - [1.8][compiler] Type inference failure with nested generic invocation.
*   Bug 429384 - [1.8][null] implement conformance rules for null-annotated lower / upper type bounds
*   Bug 431269 - [1.8][compiler][null] StackOverflow in nullAnnotatedReadableName
*   Bug 431408 - Java 8 (1.8) generics bug
*   Bug 435962 - [RC2] StackOverFlowError when building
*   Bug
 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*   Bug 438250 - [1.8][null] NPE trying to report bogus null annotation conflict
*   Bug 438179 - [1.8][null] 'Contradictory null annotations' error on type variable with explicit null-annotation.

* Bug 440143 - [1.8][null] one more case of contradictory null annotations regarding type variables
* Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
* Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
* Bug 456497 - [1.8][null] during inference nullness from target type is lost against weaker hint from applicability analysis
* Bug 456459 - Discrepancy between Eclipse compiler and javac - Enums, interfaces, and generics
* Bug 456487 - [1.8][null] @Nullable type variant of @NonNull-constrained type parameter causes grief
* Bug 462790 - [null] NPE in Expression.computeConversion()
* Bug 456532 - [1.8][null] ReferenceBinding.appendNullAnnotation() includes phantom annotations in error messages
* Jesper S Møller <jesper@selskabet.org> - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
* Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
**********/

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* Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's work)
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*   Luiz-Otavio Zorzella <zorzella at gmail dot com> - Improve CamelCase algorithm
*   Gábor Kövesdán - Contribution for Bug 350000 - [content assist] Include non-prefix matches in auto-complete suggestions
*   Stefan Xenos <sxenos@gmail.com> (Google) - Bug 501283 - Lots of hash collisions during indexing
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*       Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*       Bug 433478 - [compiler][null] NPE in ReferenceBinding.isCompatibleWith
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/CompactConstructorDeclaration.java
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*     Stephan Herrmann - Contributions for
*         bug 292478 - Report potentially null across variable assignment
*         bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*         bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*         bug 331649 - [compiler][null] consider null annotations for fields
*         bug 383368 - [compiler][null] syntactic null analysis for field
 references
*         bug 400761 - [compiler][null] null may be return as boolean without a diagnostic
*         bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*         bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 426792 - [1.8][inference][impl] generify new type inference engine
*         Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*         Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*         Bug 426996 - [1.8][inference]
 try to avoid method Expression.unresolve()?
*         Bug 428274 - [1.8] [compiler] Cannot cast from Number to double
*         Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*         Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
*     Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
*         Bug 473178
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*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 392099
 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 412076 - [compiler] @NonNullByDefault doesn't work for varargs parameter when in generic interface
*   Bug 403216 - [1.8][null] TypeReference#captureTypeAnnotations treats type annotations as type argument annotations
*   Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 416175 - [1.8][compiler][null] NPE with a code snippet that used null annotations on wildcards
*   Bug 416174 - [1.8][compiler][null] Bogus name clash error with null annotations
*   Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*   Bug 400874 - [1.8][compiler]
 Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*   Bug 425278 - [1.8][compiler] Suspect error: The target type of this expression is not a well formed parameterized type due to bound(s) mismatch
*   Bug 425798 - [1.8][compiler] Another NPE in ConstraintTypeFormula.reduceSubType
*   Bug 425156 - [1.8] Lambda as an argument is flagged with incompatible error
*   Bug 426563 - [1.8] AIOOBE when method with error invoked with lambda expression as argument
*   Bug 426792 - [1.8][inference][impl] generify new type inference engine
*   Bug 428294 - [1.8][compiler] Type mismatch: cannot convert from List<Object> to Collection<Object[]>
*   Bug 427199 - [1.8][resource] avoid resource leak warnings on Streams that have no resource
*   Bug 416182 - [1.8][compiler][null] Contradictory null annotations not rejected
 *   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*   Bug 438179 - [1.8][null] 'Contradictory null annotations' error on type variable with explicit null-annotation.
*   Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
*   Bug 446434 - [1.8][null] Enable interned captures also when analysing null type annotations
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Bug 456508 - Unexpected RHS PolyTypeBinding for: <code-snippet>
*   Bug 390064 - [compiler][resource] Resource leak warning missing when extending parameterized class
*   Jesper S Møller - Contributions for bug 381345 : [1.8] Take care of the Java 8 major version
*   Bug 527554 - [18.3] Compiler support for JEP 286 Local-Variable Type
**********/

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*     Stephan Herrmann - Contribution for bug 186342 - [compiler][null] Using annotations for null checking
*     Jesper S Moller  -. Contribution for bug 400830: [1.8][formatter] Code formatter for Java 8
*******************************************************************************/
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*   Stephan Herrmann <stephan@cs.tu-berlin.de> - Contributions for
*   bug 185682 - Increment/decrement operators mark local variables as read
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   Bug 412203 - [compiler] Internal compiler error: java.lang.IllegalArgumentException: info cannot be null
*   Bug 400874 - [1.8][compiler]
 Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 458396 - NPE in CodeStream.invoke()
*   Jesper S Moller - Contributions for
*   Bug 378674 - "The method can be declared as static" is wrong
*   Robert Roth <robert.roth.off@gmail.com> - Contributions for
*   Bug 361039 - NPE in FieldReference.optimizedBooleanConstant
**********/

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*   Walter Harley - initial API and implementation

*   IBM Corporation - fix for 342598, 382590
*   Jean-Marie Henaff <jmhenaff@google.com> (Google) - Bug 481555
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/model/TypesImpl.java
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*   Jesper S Moller - Contributions for
*   Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
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jar/org/eclipse/jdt/internal/compiler/classfmt/AnnotationMethodInfoWithTypeAnnotations.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/flow/FieldInitsFakingFlowContext.java

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*         Bug 378674 - "The method can be declared as static" is wrong
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*     Nikolay Metchev (nikolaymetchev@gmail.com) - Contributions for
*         bug 411098 - [compiler][resource] Invalid Resource Leak Warning using ternary operator inside try-with-resource
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/DefaultProblem.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/LabeledStatement.java

* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/batch/CompilationUnit.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/impl/ITypeRequestor.java
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*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*           Bug 407191 - [1.8] Binary access support for type annotations
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ReductionResult.java
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*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 403147 - [compiler][null] FUP of bug 400761:
 consolidate interaction between unboxing, NPE, and deferred checking
**********/

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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*   bug 403086 - [compiler][null] include the effect of 'assert'
 in syntactic null analysis for fields
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*   bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*   bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 358903 - Filter practically unimportant resource
 leak warnings
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 370639 - [compiler][resource] restore the default for resource leak warnings
*   bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*   bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*   bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 424727 - [compiler][null] NullPointerException in
nullAnnotationUnsupportedLocation(ProblemReporter.java:5708)
*   Bug 424710
 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*   Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.

* Bug 424205 - [1.8] Cannot infer type for diamond type with lambda on method invocation
* Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
* Bug 426366 - [1.8][compiler] Type inference doesn't handle multiple candidate target types in outer overload context
* Bug 426290 - [1.8][compiler] Inference + overloading => wrong method resolution ?
* Bug 426764 - [1.8] Presence of conditional expression as method argument confuses compiler
* Bug 424930 - [1.8][compiler] Regression: "Cannot infer type arguments" error from compiler.
* Bug 427483 - [Java 8] Variables in lambdas sometimes can't be resolved
* Bug 427438 - [1.8][compiler] NPE at org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
 * Bug 426996 - [1.8][inference] try to avoid method Expression.unresolve()?
* Bug 428352 - [1.8][compiler] Resolution errors don't always surface
* Bug 429203 - [1.8][compiler] NPE in AllocationExpression.binding
* Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException instead of IOException)
* Bug 434297 - [1.8] NPE in LamdaExpression.analyseCode with lamda expression nested in a conditional expression
* Bug 452788 - [1.8][compiler] Type not correctly inferred in lambda expression
* Bug 448709 - [1.8][null] ensure we don't infer types that violate null constraints on a type parameter's bound
* Jesper S Moller <jesper@selskabet.org> - Contributions for
* bug 378674 - "The method can be declared as static" is wrong
* Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
* Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's work)
* Bug 409245 - [1.8][compiler] Type annotations dropped when call is routed through a synthetic bridge method
* Till Brychcy - Contributions for
* bug 413460 - NonNullByDefault is not inherited to Constructors when accessed via Class File
* Lars Vogel <Lars.Vogel@vogella.com> - Contributions for
* Bug 473178
**********/

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*   Bug 432977 - [1.8][null] Incorrect 'type is not visible' compiler error
*   Bug 446434 - [1.8][null] Enable interned captures also when analysing null type annotations
**********/
/* Taking the binding of QTR as an example, there could be different annotatable components, but we come in a with a single binding, e.g:

 @T Z;   type => Z annotations => [[@T]]
     @T Y.@T Z   type => Z annotations => [[@T][@T]]
     @T X.@T Y.@T Z   type => Z annotations => [[@T][@T][@T]]
     java.lang.@T X.@T Y.@T Z   type => Z annotations => [[][@T][@T][@T]]

in all these cases the incoming type binding is for Z, but annotations are for different levels. We need to align their layout for proper attribution.
*/

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*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 435570 - [1.8][null] @NonNullByDefault
 illegally tries to affect "throws E"
*   Bug 438012 - [1.8][null] Bogus Warning: The nullness annotation is redundant with a default that applies to this location
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 409246 - [1.8][compiler] Type annotations on catch parameters not handled properly
***/

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*   Stephan Herrmann - Contributions for
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*   Bug 415543 - [1.8][compiler] Incorrect bound index in RuntimeInvisibleTypeAnnotations attribute
**********/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/BranchStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocAllocationExpression.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemPackageBinding.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocMessageSend.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/CaseStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/ParserBasicInformation.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/ClassFormatException.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/ConflictedParser.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/diagnose/DiagnoseParser.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/QualifiedTypeReference.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/TerminalTokens.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/core/compiler/CategorizedProblem.java
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Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/StringLiteralConcatenation.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ICompilerRequestor.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/AccessRestriction.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/diagnose/RangeUtil.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/OperatorIds.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/AccessRule.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/FalseLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/ClassFileStruct.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IDependent.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemBinding.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfIntValues.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/SimpleLookupTable.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/ISourceImport.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/AbortMethod.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/ShouldNotImplement.java

* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/RecoveredImport.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfType.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/SubRoutineStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/DoubleCache.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/InnerEmulationDependency.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocReturnStatement.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/AbortCompilationUnit.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/parser/NLSTag.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/PrefixExpression.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/IDebugRequestor.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/TrueLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/SimpleSet.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/CharLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/antadapter/AntAdapterMessages.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/AbortType.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfObjectToInt.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/AbortCompilation.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/IProblemFactory.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/SyntheticFieldBinding.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfInt.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/FloatCache.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ImportConflictBinding.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ExtendedStringLiteral.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/Label.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/AnnotationHolder.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/InnerClassInfo.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/PostfixExpression.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocArrayQualifiedTypeReference.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfLong.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ProblemFieldBinding.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/JavadocSingleNameReference.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/IntegerCache.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/ObjectCache.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/SyntheticArgumentBinding.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/MagicLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/ParameterizedFieldBinding.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfObjectToIntArray.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/LongCache.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/CaseLabel.java
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*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 403086 - [compiler][null] include the effect of 'assert' in syntactic null analysis for fields
**********/

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```
*     Stephan Herrmann - Contributions for
*          bug 349326 - [1.7] new warning for missing try-with-resources
*          bug 359362 - FUP of bug 349326: Resource leak on non-Closeable resource
*       bug 186342 - [compiler][null] Using annotations for null checking
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 400421 - [compiler] Null analysis for fields does not take @com.google.inject.Inject
 into account
*       bug 382069 - [null] Make the null analysis consider JUnit's assertNotNull similarly to assertions
*       Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*     Jesper S Moller <jesper@selskabet.org> -  Contributions for
*       Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*     Ulrich Grave <ulrich.grave@gmx.de> - Contributions for
*                       bug 386692 - Missing "unused" warning on "autowired" fields
*     Pierre-Yves B. <pyvesdev@gmail.com> - Contribution for
*                       bug 542520 - [JUnit 5] Warning The method xxx from the type X is never used locally is shown
when using MethodSource
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*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*          Bug 407191 - [1.8] Binary access support for type annotations
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/FieldInfoWithAnnotation.java
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*     Jesper S Moller   - Contributions for
*          Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*     Frits Jalvingh    - contributions for bug 533830.
*     Red Hat Inc.      - add module-info Javadoc support
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/CompilationUnitDeclaration.java
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*         bug 332637 - Dead Code detection removing code that isn't dead
*         bug 391517 - java.lang.VerifyError on code that runs correctly in Eclipse 3.7 and eclipse 3.6
*         bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/flow/ConditionalFlowInfo.java
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*           bug 337868 - [compiler][model] incomplete support for package-info.java when using SearchableEnvironment
*           bug 186342 - [compiler][null] Using annotations for null checking
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*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                       Bug 409236 - [1.8][compiler] Type annotations on intersection cast types dropped by code
generator
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/IntersectionCastTypeReference.java
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*   Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*   Bug 440687 - [compiler][batch][null] improve command line option for external annotations
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*   bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*   Jesper Steen Moller - Contributions for

```
*       Bug 412150 [1.8] [compiler]
 Enable reflected parameter names during annotation processing
**********************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/Binding.java
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*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
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*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 374605 - Unreasonable warning for enum-based switch statements
*         bug 382353 - [1.8][compiler] Implementation property modifiers should be accepted on default methods.
*         bug 382354 - [1.8][compiler] Compiler silent on conflicting modifier
*         bug 401030 - [1.8][null] Null analysis support for lambda methods.
*         Bug
 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*     Jesper S Moller - Contributions for
*       bug 382701 - [1.8][compiler] Implement semantic analysis of Lambda expressions & Reference expression
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Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/DoubleLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/NumberLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/LongLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/flow/InitializationFlowContext.java
*
 /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/IntLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/FloatLiteral.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/IntLiteralMinValue.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/InferenceContext.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/LongLiteralMinValue.java
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* Contributors:
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*   Stephan Herrmann - contributions for
*   bug 337868 - [compiler][model] incomplete support for package-info.java when using SearchableEnvironment
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug
 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 415850 - [1.8] Ensure RunJDTCoreTests can cope with null annotations enabled
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 416183 - [1.8][compiler][null] Overload resolution fails with null annotations
*   Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 416190 - [1.8][null]
 detect incompatible overrides due to null type annotations
*   Bug 424624 - [1.8][null] if a static-object with annotation @NonNull is used, a warning is shown
*   Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*   Bug 439516 - [1.8][null] NonNullByDefault wrongly applied to implicit type bound of binary type
*   Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Bug 453475 - [1.8][null] Contradictory null annotations (4.5 M3 edition)
*   Bug 457079 - Regression: type inference
*   Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*   Bug 455180 - IllegalStateException in AnnotatableTypeSystem.getRawType
*   Bug 470467 - [null] Nullness of special Enum methods not detected from .class file
 ***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/LookupEnvironment.java
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*   wharley@bea.com - initial API and implementation

*   IBM Corporation - fix for 342598
*   IBM Corporation - Java 8 support
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/model/ArrayTypeImpl.java
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*   Stephan Herrmann - Contributions for
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 367203 - [compiler][null] detect assigning null to nonnull argument
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   bug 365531 - [compiler][null] investigate alternative strategy for internally encoding nullness defaults
*   bug 382353 - [1.8][compiler] Implementation property modifiers
 should be accepted on default methods.
*   bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   bug 388281 - [compiler][null] inheritance of null annotations as an option
*   bug 401030 - [1.8][null] Null analysis support for lambda methods.
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 403216 - [1.8][null] TypeReference#captureTypeAnnotations treats type annotations as type argument
annotations
*   Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize
 declaration style null annotations
*   Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/AbstractMethodDeclaration.java
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*   IBM Corporation - initial API and implementation
*   David Foerster - patch for toUpperCase as described in https://bugs.eclipse.org/bugs/show_bug.cgi?id=153125
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/SortedCompoundNameVector.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/SortedCharArrays.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/SortedSimpleNameVector.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/util/JrtFileSystem.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/tool/JrtFileSystem.java
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*   Jesper S Moller - Contributions for
*   Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*   Bug 406982 - [1.8][compiler] Generation of MethodParameters Attribute in classfile
*   Bug 416885 - [1.8][compiler]IncompatibleClassChange error (edit)
*   Bug 412153 - [1.8][compiler] Check validity of annotations which may be repeatable
*   Andy Clement (GoPivotal, Inc) aclement@gopivotal.com
 - Contributions for
*   Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
*   Bug 439889 - [1.8][compiler] [lambda] Deserializing lambda fails with IllegalArgumentException:
"Invalid lambda deserialization"
***/

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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/codegen/ConstantPool.java
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*     Stephan Herrmann - Contributions for
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 367203 - [compiler][null] detect assigning null to nonnull argument
*         bug 365519 - editorial cleanup after bug 186342 and bug 365387
*         bug 365662 - [compiler][null] warn on contradictory and redundant null annotations
*         bug 365531 - [compiler][null] investigate alternative strategy for internally
 encoding nullness defaults
*         bug 388281 - [compiler][null] inheritance of null annotations as an option
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*         Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 425152 - [1.8] [compiler] Lambda Expression not resolved but flow analyzed leading to NPE.
*         Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 438012 - [1.8][null] Bogus Warning: The nullness annotation is redundant with a default that applies to this
location
*         Bug 440759 - [1.8][null] @NonNullByDefault should never affect wildcards and uses of a type variable
*         Bug 443347 - [1.8][null]
 @NonNullByDefault should not affect constructor arguments of an anonymous instantiation
*         Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*         Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*         Bug 456584 - [1.8][null] Bogus warning for return type variable's @NonNull annotation being 'redundant'
*         Bug 471611 - Error on hover on call to generic method with null annotation
*     Jesper Steen Moller - Contributions for
*         Bug 412150 [1.8] [compiler] Enable reflected parameter names during annotation processing
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/MethodBinding.java
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*    tyeung@bea.com - initial API and implementation
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/AnnotationMethodInfo.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/AnnotationMethodInfoWithAnnotations.java

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*     Stephan Herrmann - Contributions for
*         bug 349326 - [1.7] new warning for missing try-with-resources
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 365519 - editorial cleanup after bug 186342 and bug 365387
*         bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*         bug 382353 - [1.8][compiler] Implementation property modifiers should
 be accepted on default methods.
*         bug 383368 - [compiler][null] syntactic null analysis for field references
*         Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*         Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*         Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*         Bug 438012 - [1.8][null] Bogus Warning: The nullness annotation is redundant with a default that applies to this
location
*         Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*         Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*     Jesper S Moller <jesper@selskabet.org> - Contributions for
```

```
*       bug 378674 - "The method can be declared as static" is wrong
*******************************************************************************/
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*     Stephan Herrmann - Contributions for
*       bug 342671 - ClassCastException: org.eclipse.jdt.internal.compiler.lookup.SourceTypeBinding cannot be cast to
org.eclipse.jdt.internal.compiler.lookup.ArrayBinding
*       bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 416181 - [1.8][compiler][null] Invalid assignment
 is not rejected by the compiler
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 434600 - Incorrect null analysis error reporting on type parameters
*       Bug 435570 - [1.8][null] @NonNullByDefault illegally tries to affect "throws E"
*       Bug 456508 - Unexpected RHS PolyTypeBinding for: <code-snippet>
*       Bug 466713 - Null Annotations: NullPointerException using <int @Nullable []> as Type Param
*       Andy Clement - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
work)
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ParameterizedQualifiedTypeReference.java
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*                       Bug 415397 - [1.8][compiler] Type Annotations on wildcard type argument dropped
*        Stephan Herrmann - Contribution for
*       Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*       Bug 417295 - [1.8[[null] Massage type annotated null analysis to gel well with deep encoded type bindings.
*       Bug 429958
 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 440462 - [null][compiler]NPE in EJC for erroneous null annotations
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/Wildcard.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/util/HashtableOfInteger.java
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*     Stephan Herrmann - Contributions for
*        bug 359334 - Analysis for resource leak warnings does not consider exceptions as method exit points
*        bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with
 generics (RuntimeException instead of IOException)
*******************************************************************************/
```

Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/ast/ThrowStatement.java
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/apt/dispatch/RoundDispatcher.java
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*        Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*        Bug 456497 - [1.8][null] during inference nullness from target type is lost against weaker hint from applicability
analysis
*        Bug 456487 - [1.8][null] @Nullable type variant of @NonNull-constrained type parameter causes grief
*     Till Brychcy - Contribution for
*        Bug 473713
 - [1.8][null] Type mismatch: cannot convert from @NonNull A1 to @NonNull A1
*******************************************************************************/
/* Cache the derived type, tagging it as a derivative of both the key type and the naked type.
    E.g: int @NonNull [] would be tagged as a derived type of both int and int []. This is not
    needed for correctness, but for annotated object reuse. We provide two alternate ways to
    annotate a type:

    Taking parameterized types as an example, a call to getParamaterizedType can be made with annotations
    to create @NonNull List<@NonNull String> in one stroke. Or a parameterized type can be created first
    and then annotated via getAnnotatedType. In the former case, the tables get looked up with List as
    the key, in the latter with List<String> as the key.

    Binary vs source, substitutions, annotation re-attribution from SE7 locations etc trigger these
    alternate code paths. Unless care is exercised,
 we will end up with duplicate objects (that share
    the same TypeBinding.id => correctness is not an issue, but memory wastage is)
  */

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* Contributors:
*        Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*         Bug 407191 - [1.8] Binary access support for type annotations
*******************************************************************************/
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Found in path(s):
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/env/IBinaryTypeAnnotation.java
* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/FieldInfoWithTypeAnnotation.java
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*     Stephan Herrmann - Contribution for
*         Bug 458396 - NPE in CodeStream.invoke()
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/problem/ProblemHandler.java
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*   Patrick Wienands <pwienands@abit.de> - Contribution for bug 393749
*   Stephan Herrmann - Contribution for
*   bug 331649 - [compiler][null] consider null annotations for fields
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*   IBM Corporation - Fix for bug 341494
*   IBM Corporation - Fix for bug 328575
*   IBM Corporation - Java 8 support
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*   Stephan Herrmann - Contributions for
*   bug 335093 - [compiler][null] minimal hook for future null annotation support
*   bug 349326 - [1.7] new warning for missing try-with-resources
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365983 - [compiler][null] AIOOB with null annotation analysis and varargs
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when
 compiling the Eclipse SDK
*   bug 370930 - NonNull annotation not considered for enhanced for loops
*   bug 365859 - [compiler][null] distinguish warnings based on flow analysis vs. null annotations
*   bug 392862 - [1.8][compiler][null] Evaluate null annotations on array types
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   Bug 392099 - [1.8][compiler][null] Apply null annotation on types for null analysis
*   Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*   Bug 415291 - [1.8][null] differentiate type incompatibilities due to null annotations
*   Bug 392238 - [1.8][compiler][null] Detect semantically invalid null type annotations
*   Bug 416307 - [1.8][compiler][null] subclass with type parameter substitution confuses null checking
*   Bug 417758 - [1.8][null] Null safety compromise
 during array creation.
*   Bug 400874 - [1.8][compiler] Inference infrastructure should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*   Bug 424415 - [1.8][compiler] Eventual resolution of ReferenceExpression is not seen to be happening.
*   Bug 418537 - [1.8][null] Fix null type annotation analysis for poly conditional expressions
*   Bug 428352 - [1.8][compiler] Resolution errors don't always surface
*   Bug 429430 - [1.8] Lambdas and method reference infer wrong exception type with generics (RuntimeException
instead of IOException)
*   Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*   Bug 453483 - [compiler][null][loop] Improve null analysis for loops

```
*       Bug 455723 - Nonnull argument not correctly inferred in loop
*       Andy Clement - Contributions for
*                       Bug 383624 - [1.8][compiler] Revive code generation support for type annotations (from Olivier's
 work)
*                       Bug 409250 - [1.8][compiler] Various loose ends in 308 code generation
**********************************************************************************/
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*           bug 319201 - [null] no warning when unboxing SingleNameReference causes NPE
*           bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 265744 - Enum switch should warn about missing default
*       bug 374605 - Unreasonable warning for enum-based switch statements
*       bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
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```
*       bug 392384 - [1.8][compiler][null] Restore nullness info from type annotations in class files
*       Bug 416174 - [1.8][compiler][null] Bogus name clash error with null annotations
*       Bug 416176 - [1.8][compiler][null] null type annotations cause grief on type variables
*       Bug 423504 - [1.8] Implement "18.5.3 Functional Interface Parameterization Inference"
*       Bug 425783 - An
 internal error occurred during: "Requesting Java AST from selection". java.lang.StackOverflowError
*       Bug 438458 - [1.8][null] clean up handling of null type annotations wrt type variables
*       Bug 441693 - [1.8][null] Bogus warning for type argument annotated with @NonNull
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*     Jesper S Moller - Contributions for
*       Bug 405066 - [1.8][compiler][codegen] Implement code generation infrastructure for JSR335
*       Bug 406982 - [1.8][compiler] Generation of MethodParameters Attribute in classfile
```

```
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*                     Bug 405104 - [1.8][compiler][codegen] Implement support for serializeable lambdas
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*           bug 349326 - [1.7] new warning for missing try-with-resources
*       bug 359334 - Analysis for resource leak warnings does not consider exceptions as method exit points
*       bug 358903 - Filter practically unimportant resource leak warnings
*       bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*       bug 370639 - [compiler][resource]
 restore the default for resource leak warnings
*       bug 388996 - [compiler][resource] Incorrect 'potential resource leak'
*       bug 379784 - [compiler] "Method can be static" is not getting reported
*       bug 394768 - [compiler][resource] Incorrect resource leak warning when creating stream in conditional
*       bug 404649 - [1.8][compiler] detect illegal reference to indirect or redundant super
*       Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*       Bug 371614 - [compiler][resource] Wrong "resource leak" problem on return/throw inside while loop
*       Bug 421035 - [resource] False alarm of resource leak warning when casting a closeable in its assignment
*       Bug 444964 - [1.7+][resource] False resource leak warning (try-with-resources for ByteArrayOutputStream -
return inside for loop)
*       Bug 396575 - [compiler][resources] Incorrect Errors/Warnings check for potential resource leak when surrounding
 with try-catch
*     Jesper S Moller <jesper@selskabet.org> - Contributions for
*       bug 378674 - "The method can be declared as static" is wrong
*     Keigo Imai - Contribution for  bug 388903 - Cannot extend inner class as an anonymous class when it extends
the outer class
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/lookup/BlockScope.java

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*         Bug 365992 - [builder] [null] Change of nullness for a parameter doesn't trigger a build for the files that call the method
*         Bug 440477 - [null] Infrastructure for feeding external annotations into compilation
*         Bug 440687 - [compiler][batch][null] improve command line option for external annotations
*     Andy Clement (GoPivotal, Inc) aclement@gopivotal.com - Contributions for
*
              bug 407191 - [1.8] Binary access support for type annotations
*******************************************************************************/
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* /ecj-3-26-0-sources-3-jar/org/eclipse/jdt/internal/compiler/classfmt/ClassFileReader.java
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*     Stephan Herrmann  - Contributions for
*           bug 236385 - [compiler] Warn for potential programming problem if an object is created but not used
*           bug 295551 - Add option to automatically promote all warnings to errors
*           bug 349326 - [1.7] new warning for missing try-with-resources
*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug
 370639 - [compiler][resource] restore the default for resource leak warnings
*        bug 366063 - Compiler should not add synthetic @NonNull annotations
*        bug 374605 - Unreasonable warning for enum-based switch statements
*        bug 388281 - [compiler][null] inheritance of null annotations as an option
*        bug 381443 - [compiler][null] Allow parameter widening from @NonNull to unannotated
*        bug 383368 - [compiler][null] syntactic null analysis for field references
*        Bug 435805 - [1.8][compiler][null] Java 8 compiler does not recognize declaration style null annotations
*        Bug 410218 - Optional warning for arguments of "unexpected" types to Map#get(Object),
Collection#remove(Object) et al.
*     Jesper Steen Moller - Contributions for
*        bug 404146 - [1.7][compiler] nested try-catch-finally-blocks leads to unrunnable Java byte code
*        bug 407297 - [1.8][compiler] Control generation of parameter names by option
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*     Stephan Herrmann - Contributions for
*         bug 186342 - [compiler][null] Using annotations for null checking
*         bug 395002 - Self bound generic class doesn't resolve bounds properly for wildcards for certain parametrisation.
*         Bug 415043 - [1.8][null] Follow-up re null type annotations after bug 392099
*         bug 413958 - Function override returning inherited Generic Type
*         Bug 400874 - [1.8][compiler] Inference infrastructure
 should evolve to meet JLS8 18.x (Part G of JSR335 spec)
*         Bug 424710 - [1.8][compiler] CCE in SingleNameReference.localVariableBinding
*         Bug 423505 - [1.8] Implement "18.5.4 More Specific Method Inference"
*         Bug 427438 - [1.8][compiler] NPE at
org.eclipse.jdt.internal.compiler.ast.ConditionalExpression.generateCode(ConditionalExpression.java:280)
*         Bug 418743 - [1.8][null] contradictory annotations on invocation of generic method not reported
*         Bug 416182 - [1.8][compiler][null] Contradictory null annotations not rejected
*         Bug 429958 - [1.8][null] evaluate new DefaultLocation attribute of @NonNullByDefault
*         Bug 434602 - Possible error with inferred null annotations leading to contradictory null annotations
*         Bug 434483 - [1.8][compiler][inference] Type inference not picked up with method reference
*         Bug 446442 - [1.8] merge null annotations from super methods
*         Bug 457079 - Regression: type
 inference
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*         bug 366003 - CCE in ASTNode.resolveAnnotations(ASTNode.java:639)
*         bug 383973 - [1.8][compiler] syntax recovery in the presence of default methods
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*            bug 336428 - [compiler][null] bogus warning "redundant null check" in condition of do {} while() loop
*        bug 186342 - [compiler][null] Using annotations for null checking
*        bug 365519 - editorial cleanup after bug 186342 and bug 365387
*        bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*        bug 365859 - [compiler][null]
 distinguish warnings based on flow analysis vs. null annotations
*        bug 385626 - @NonNull fails across loop boundaries
*        bug 345305 - [compiler][null] Compiler misidentifies a case of "variable can only be null"
*        bug 376263 - Bogus "Potential null pointer access" warning
*        bug 403147 - [compiler][null] FUP of bug 400761: consolidate interaction between unboxing, NPE, and deferred
checking
*        bug 406384 - Internal error with I20130413
*        Bug 415413 - [compiler][null] NullpointerException in Null Analysis caused by interaction of
LoopingFlowContext and FinallyFlowContext
*        Bug 453483 - [compiler][null][loop] Improve null analysis for loops
*        Bug 455557 - [jdt] NPE LoopingFlowContext.recordNullReference
*        Bug 455723 - Nonnull argument not correctly inferred in loop
*        Bug 415790 - [compiler][resource]Incorrect potential resource leak warning in for loop with close in try/catch
```

```
*       Bug 421035 - [resource]
 False alarm of resource leak warning when casting a closeable in its assignment
*     Jesper S Moller - contributions for
*       bug 404657 - [1.8][compiler] Analysis for effectively final variables fails to consider loops
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*   bug 185682 - Increment/decrement operators mark local variables as read
*   bug 186342 - [compiler][null] Using annotations for null checking
*   bug 365519 - editorial cleanup after bug 186342 and bug 365387
*   bug 368546 - [compiler][resource] Avoid remaining false positives found when compiling the Eclipse SDK
*   bug 345305 - [compiler][null]
 Compiler misidentifies a case of "variable can only be null"
*   Bug 414380 - [compiler][internal] QualifiedNameReference#indexOfFirstFieldBinding does not point to the first
field
*   Bug 458396 - NPE in CodeStream.invoke()
*   Jesper S Moller - Contributions for
*   bug 382721 - [1.8][compiler] Effectively final variables needs special treatment
*   bug 331649 - [compiler][null] consider null annotations for fields
*   bug 383368 - [compiler][null] syntactic null analysis for field references
*   bug 402993 - [null] Follow up of bug 401088: Missing warning about redundant null check
*   Jesper S Moller <jesper@selskabet.org> - Contributions for
*   bug 378674 - "The method can be declared as static" is wrong
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1.401 elfutils 0.192-6.el9_6

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1.402 fileutils 1.6.0

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1.403 javax-inject 2.3.0-b10

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* /javax-inject-2-3-0-b10-sources-7-jar/javax/inject/Singleton.java
* /javax-inject-2-3-0-b10-sources-7-jar/javax/inject/Named.java

* /javax-inject-2-3-0-b10-sources-7-jar/javax/inject/Qualifier.java
* /javax-inject-2-3-0-b10-sources-7-jar/javax/inject/package-info.java
*
/javax-inject-2-3-0-b10-sources-7-jar/javax/inject/Provider.java
* /javax-inject-2-3-0-b10-sources-7-jar/javax/inject/Scope.java

1.404 tomcat-i18n-ja 9.0.108

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1.405 mutex_m 0.1.1

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* /vt-password-3-1-2-sources-jar/edu/vt/middleware/password/AbstractCharacterRule.java
* /vt-password-3-1-2-sources-jar/edu/vt/middleware/password/RuleResultDetail.java
* /vt-password-3-1-2-sources-jar/edu/vt/middleware/password/NonAlphanumericCharacterRule.java
* /vt-password-3-1-2-sources-jar/edu/vt/middleware/password/LowercaseCharacterRule.java
* /vt-password-3-1-2-sources-jar/edu/vt/middleware/password/UppercaseCharacterRule.java
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1.408 log4j-jcl 2.17.1

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1.409 libx11 1.7.0-11.el9

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* /asm-5-0-1-sources-jar/org/objectweb/asm/xml/SAXAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/xml/SAXFieldAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/xml/ASMContentHandler.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/xml/Processor.java
*
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*/
/**
   * Creates a new JSRInliner. Subclasses must not use this
   * constructor. Instead, they must use the
   * {@link #JSRInlinerAdapter(int, MethodVisitor, int, String, String, String, String[])}
   * version.
   *
   * @param mv
   *
  the <code>MethodVisitor</code> to send the resulting inlined
   *   method code to (use <code>null</code> for none).
   * @param access
   *   the method's access flags (see {@link Opcodes}). This
   *   parameter also indicates if the method is synthetic and/or
   *   deprecated.
   * @param name
   *   the method's name.
   * @param desc
   *   the method's descriptor (see {@link Type}).
   * @param signature
   *   the method's signature. May be <tt>null</tt>.
   * @param exceptions
   *   the internal names of the method's exception classes (see
   *   {@link Type#getInternalName() getInternalName}). May be
   *   <tt>null</tt>.
   * @throws IllegalStateException
   *   If a subclass calls this constructor.
   */

Found in path(s):
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/JSRInlinerAdapter.java
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*/
/**
   * Constructs a new {@link LocalVariableAnnotationNode}. <i>Subclasses must
   * not use this constructor</i>. Instead, they must use the
   * {@link #LocalVariableAnnotationNode(int, TypePath, LabelNode[], LabelNode[], int[], String)}
   * version.
   *

   * @param typeRef
   *          a reference to the annotated type. See {@link TypeReference}.
   * @param typePath
   *          the path to the annotated type argument, wildcard bound, array
   *          element type, or static inner type within 'typeRef'. May be
   *          <tt>null</tt> if the annotation targets 'typeRef' as a whole.
   * @param start
   *          the fist instructions corresponding to the continuous ranges
   *          that make the scope of this local variable (inclusive).
   * @param end
   *          the last instructions corresponding to the continuous ranges
   *          that make the scope of this local variable (exclusive). This
   *          array must have the same size as the 'start' array.
   * @param index
   *          the local variable's index in each range. This array must have
```

```
   *          the same size as the 'start' array.
   * @param desc
   *          the class descriptor of
 the annotation class.
   */
```

Found in path(s):
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/LocalVariableAnnotationNode.java
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/**
   * Creates a new {@link AnalyzerAdapter}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the
   * {@link #AnalyzerAdapter(int, String, int, String, String, MethodVisitor)}
   * version.
   *
   * @param owner
   *
```

```
     the owner's class name.
   * @param access
   *          the method's access flags (see {@link Opcodes}).
   * @param name
   *          the method's name.
   * @param desc
   *          the method's descriptor (see {@link Type Type}).
   * @param mv
   *          the method visitor to which this adapter delegates calls. May
   *          be <tt>null</tt>.
   * @throws IllegalStateException
   *           If a subclass calls this constructor.
   */
```

Found in path(s):
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/AnalyzerAdapter.java
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* /asm-5-0-1-sources-jar/org/objectweb/asm/util/ASMifiable.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/Textifiable.java
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*/
/**
   * Constructs a new {@link Textifier}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the {@link #Textifier(int)}
   * version.
   *
   * @throws IllegalStateException
   *             If a subclass calls this constructor.
    */
```

Found in path(s):
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/Textifier.java

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* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/package.html
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/package.html
* /asm-5-0-1-sources-jar/org/objectweb/asm/attrs/package.html
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/package.html
* /asm-5-0-1-sources-jar/org/objectweb/asm/package.html
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/**
   * Creates a new {@link GeneratorAdapter}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the
   * {@link #GeneratorAdapter(int, MethodVisitor, int, String, String)}
   * version.
   *
   * @param mv
   *            the
 method visitor to which this adapter delegates calls.
   * @param access
   *            the method's access flags (see {@link Opcodes}).
   * @param name
   *            the method's name.
   * @param desc
   *            the method's descriptor (see {@link Type Type}).
   * @throws IllegalStateException
   *             If a subclass calls this constructor.
   */

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* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/GeneratorAdapter.java
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/**
   * Constructs a new {@link ClassNode}. <i>Subclasses must not use this
   * constructor</i>. Instead, they must use the {@link #ClassNode(int)}
   * version.
   *
   * @throws IllegalStateException
   *             If a subclass calls this constructor.
    */

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* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/AnalyzerException.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/FieldVisitor.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/TableSwitchInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/ConstantPool.java
*
/asm-5-0-1-sources-jar/org/objectweb/asm/tree/LookupSwitchInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/TryCatchBlockNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/SourceValue.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/InnerClassNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/Attribute.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/SimpleRemapper.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/ParameterNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/MethodOptimizer.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/Handle.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/IntInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/ASMifier.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/AdviceAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/LocalVariableNode.java
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* /asm-5-0-1-sources-jar/org/objectweb/asm/Type.java
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* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/LineNumberNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/BasicInterpreter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/Item.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/StaticInitMerger.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/CheckMethodAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/FieldConstantsCollector.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/FrameNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/Shrinker.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/ClassConstantsCollector.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/ClassWriter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/RemappingClassAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/TraceClassVisitor.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/Constant.java
*

/asm-5-0-1-sources-jar/org/objectweb/asm/AnnotationWriter.java
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* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/CodeSizeEvaluator.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/FieldNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/CheckClassAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/LabelNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/ClassReader.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/AnnotationNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/Frame.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/FieldInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/SerialVersionUIDAdder.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/signature/SignatureVisitor.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/AnnotationConstantsCollector.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/InvokeDynamicInsnNode.java
*
/asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/BasicVerifier.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/signature/SignatureReader.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/SimpleVerifier.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/Remapper.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/SourceInterpreter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/TryCatchBlockSorter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/Value.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/Subroutine.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/TypeInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/MethodNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/MethodConstantsCollector.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/NameMapping.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/Printer.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/TraceAnnotationVisitor.java
*
/asm-5-0-1-sources-jar/org/objectweb/asm/signature/SignatureWriter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/VarInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/Edge.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/RemappingSignatureAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/MethodVisitor.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/FieldWriter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/RemappingAnnotationAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/LdcInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/SmallSet.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/MethodInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/MethodWriter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/JarOptimizer.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/InsnList.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/optimizer/ClassOptimizer.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/TraceSignatureVisitor.java
*
/asm-5-0-1-sources-jar/org/objectweb/asm/util/CheckFieldAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/CheckAnnotationAdapter.java

* /asm-5-0-1-sources-jar/org/objectweb/asm/Frame.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/CheckSignatureAdapter.java
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* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/RemappingFieldAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/TableSwitchGenerator.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/TypeAnnotationNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/Context.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/ByteVector.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/Label.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/AbstractInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/IincInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/JumpInsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/Interpreter.java
*
/asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/Analyzer.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/InstructionAdapter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/InsnNode.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/util/TraceFieldVisitor.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/ClassVisitor.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/LocalVariablesSorter.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/analysis/BasicValue.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/commons/Method.java
* /asm-5-0-1-sources-jar/org/objectweb/asm/tree/MultiANewArrayInsnNode.java
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1.412 libdrm 2.4.123-2.el9

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```
/* xf86drmHash.c -- Small hash table support for integer -> integer mapping
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*
* Authors: Rickard E. (Rik) Faith <faith@valinux.com>
*
* DESCRIPTION
*
* This file contains a straightforward implementation of a fixed-sized
* hash table using self-organizing linked lists [Knuth73, pp. 398-399] for
* collision resolution.  There are two potentially interesting things
* about this implementation:
*
* 1) The table is power-of-two sized.  Prime sized tables are more
* traditional, but do not have a significant advantage over power-of-two
* sized table, especially when double hashing is not used for collision
* resolution.
*
* 2) The hash computation
 uses a table of random integers [Hanson97,
* pp. 39-41].
*
* FUTURE ENHANCEMENTS
*
```

```
* With a table size of 512, the current implementation is sufficient for a
* few hundred keys.  Since this is well above the expected size of the
* tables for which this implementation was designed, the implementation of
* dynamic hash tables was postponed until the need arises.  A common (and
* naive) approach to dynamic hash table implementation simply creates a
* new hash table when necessary, rehashes all the data into the new table,
* and destroys the old table.  The approach in [Larson88] is superior in
* two ways: 1) only a portion of the table is expanded when needed,
* distributing the expansion cost over several insertions, and 2) portions
* of the table can be locked, enabling a scalable thread-safe
* implementation.
*
* REFERENCES
*
* [Hanson97] David R. Hanson.  C Interfaces and Implementations:
* Techniques for Creating Reusable Software.  Reading, Massachusetts:
 * Addison-Wesley, 1997.
*
* [Knuth73] Donald E. Knuth. The Art of Computer Programming.  Volume 3:
* Sorting and Searching.  Reading, Massachusetts: Addison-Wesley, 1973.
*
* [Larson88] Per-Ake Larson. "Dynamic Hash Tables".  CACM 31(4), April
* 1988, pp. 446-457.
*
*/
```

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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/xf86drmHash.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/hash.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/amdgpu/handle_table.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
4oQHCRMH/libdrm-2.4.123/tests/tegra/vic42.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
4oQHCRMH/libdrm-2.4.123/tests/tegra/vic-clear.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
4oQHCRMH/libdrm-2.4.123/tests/tegra/vic-flip.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
4oQHCRMH/libdrm-2.4.123/tests/tegra/syncpt-wait.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
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*
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
4oQHCRMH/libdrm-2.4.123/tests/tegra/vic.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
4oQHCRMH/libdrm-2.4.123/tests/tegra/vic41.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-
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*
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/* mga_drm.h -- Public header for the Matrox g200/g400 driver -*- linux-c -*-
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/decode_messages.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/amdgpu/amdgpu_vm.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/exynos/exynos_fimg2d_perf.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/radeon/bof.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/etnaviv/write_bmp.c
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```
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/amdgpu/amdgpu_device.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/amdgpu/amdgpu_cs.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/amdgpu/amdgpu.h
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/vce_ib.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/vce_tests.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/hotunplug_tests.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/ttmtest/src/xf86dri.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/omap/omap_drmif.h
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The release criteria for libdrm is essentially "if you need a release,
make one". There is no designated release engineer or maintainer.
Anybody is free to make a release if there's a certain feature or bug

fix they need in a released version of libdrm.

When new ioctl definitions are merged into drm-next, we will add
support to libdrm, at which point we typically create a new release.
However, this is up to whoever is driving the feature in question.

Follow these steps to release a new version of libdrm:

1) Bump the version number in meson.build. We seem to have settled for
   2.4.x as the versioning scheme for libdrm, so just bump the micro
   version.

2) Run `ninja -C builddir/ dist` to generate the tarballs.
   Make sure that the version number of the tarball name in
   builddir/meson-dist/ matches the number you bumped to. Move that
   tarball to the libdrm repo root for the release script to pick
up.

3) Push the updated main branch with the bumped version number:

git push origin main

   assuming the remote for the upstream libdrm repo is called origin.

4) Use the release.sh script from the xorg/util/modular repo to
   upload the tarballs to the freedesktop.org download area and
   create an announce email template. The script takes one argument:
   the path to the libdrm checkout. So, if a checkout of modular is
   at the same level than the libdrm repo:

./modular/release.sh libdrm

   This copies the two tarballs to freedesktop.org and creates
   libdrm-2.4.16.announce which has a detailed summary of the
   changes, links to the tarballs, MD5 and SHA1 sums and pre-filled
   out email headers. Fill out the blank between the email headers
   and the list of changes with a brief message of what changed or
   what prompted this release. Send out the email and you're done!

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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/RELEASING
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/exynos/exynos_fimg2d_test.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/exynos/exynos_fimg2d.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/exynos/exynos_fimg2d.h
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/etnaviv/etnaviv_drmif.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/etnaviv/etnaviv_2d_test.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/etnaviv/etnaviv_cmd_stream.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/etnaviv/etnaviv_priv.h
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/etnaviv/etnaviv_device.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/freedreno_priv.h
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*
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/freedreno_pipe.c
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Contributing to libdrm
==========

Submitting Patches

Patches should be sent to dri-devel@lists.freedesktop.org, using git
send-email. For patches only touching driver specific code one of the driver
mailing lists (like amd-gfx@lists.freedesktop.org) is also appropriate. See git
documentation for help:

http://git-scm.com/documentation

Since dri-devel is a very busy mailing list please use --subject-prefix="PATCH
libdrm" to make it easier to find libdrm patches. This is best done by running

  git config --local format.subjectprefix "PATCH libdrm"

The first line of a commit message should contain a prefix indicating what part

is affected by the patch followed by one sentence that describes the change. For examples:

amdgpu: Use uint32_t i in amdgpu_find_bo_by_cpu_mapping

The body of the commit message should describe what the patch changes and why, and also note any particular
side effects. For a recommended reading on
writing commit messages, see:

http://who-t.blogspot.de/2009/12/on-commit-messages.html

Your patches should also include a Signed-off-by line with your name and email address. If you're not the patch's original author, you should also gather S-o-b's by them (and/or whomever gave the patch to you.) The significance of this is that it certifies that you created the patch, that it was created under an appropriate open source license, or provided to you under those terms. This lets us indicate a chain of responsibility for the copyright status of the code. For more details:

https://developercertificate.org/

We won't reject patches that lack S-o-b, but it is strongly recommended.

Review and Merging

Patches should have at least one positive review (Reviewed-by: tag) or indication of approval (Acked-by: tag) before merging. For any code shared between drivers this is mandatory.

Please note that kernel/userspace API header files
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include/drm/README.

Coding style in the project loosely follows the CodingStyle of the linux kernel:

https://www.kernel.org/doc/html/latest/process/coding-style.html?highlight=coding%20style

Commit Rights

Commit rights will be granted to anyone who requests them and fulfills the below criteria:

- Submitted a few (5-10 as a rule of thumb) non-trivial (not just simple spelling fixes and whitespace adjustment) patches that have been merged already. Since libdrm is just a glue library between the kernel and userspace

drivers, merged patches to those components also count towards the commit criteria.

- Are actively participating on discussions about their work (on the mailing list or IRC). This should not be interpreted as a requirement to review other peoples patches but just make sure that patch submission isn't one-way communication. Cross-review is still highly encouraged.

- Will be regularly contributing further patches. This includes regular contributors to other parts of the open source graphics stack who only do the oddball rare patch within libdrm itself.

- Agrees to use their commit rights in accordance with the documented merge criteria, tools, and processes.

To apply for commit rights ("Developer" role in gitlab) send a mail to dri-devel@lists.freedesktop.org and please ping the maintainers if your request is stuck.

Committers are encouraged to request their commit rights get removed when they no longer contribute to the project. Commit rights will be reinstated when they come back to the project.

Maintainers and committers should encourage contributors to request commit rights, as especially junior contributors tend to underestimate their skills.

Code of Conduct

Please be aware the fd.o Code of Conduct also applies to libdrm:

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See the gitlab project owners for contact details of the libdrm maintainers.

Abuse of commit rights, like engaging in commit fights or willfully pushing patches that violate the documented merge criteria, will also be handled through the Code of Conduct enforcement process.

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* Authors:
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-

4oQHCRMH/libdrm-2.4.123/tests/amdgpu/syncobj_tests.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/uve_ib.h
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*
/libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/deadlock_tests.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/modetest/buffers.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/modetest/buffers.h
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/exynos/exynos_drmif.h
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/radeon/radeon_bo.h
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```
prebuilt_etc {
  name: "amdgpu.ids",
  proprietary: true,
  sub_dir: "hwdata",
  src: "amdgpu.ids",
}
```

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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/radeon/radeon_bo_gem.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/radeon/radeon_bo.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/amdgpu/bo_tests.c
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*
* Authors: Rickard E. (Rik) Faith <faith@valinux.com>
*
* DESCRIPTION
*
* This file contains a straightforward skip list implementation.n
*
* FUTURE ENHANCEMENTS

*
* REFERENCES
*
* [Pugh90] William Pugh. Skip Lists: A Probabilistic Alternative to
* Balanced Trees. CACM 33(6), June 1990, pp. 668-676.
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/vc4/vc4_packet.h
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/include/drm/qxl_drm.h
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* Authors: Ben Skeggs
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/nouveau/pushbuf.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/nouveau/abi16.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/nouveau/nouveau.c
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/* xf86drmRandom.c -- "Minimal Standard" PRNG Implementation
* Created: Mon Apr 19 08:28:13 1999 by faith@precisioninsight.com
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*
* Authors: Rickard E. (Rik) Faith <faith@valinux.com>
*
* DESCRIPTION
*
* This file contains a simple, straightforward implementation of the Park
* & Miller "Minimal Standard" PRNG [PM88, PMS93], which is a Lehmer
* multiplicative linear congruential generator (MLCG) with a period of
* 2^31-1.
*
* This implementation is intended to provide a reliable, portable PRNG
* that is suitable for testing a hash table implementation and for
* implementing skip lists.
*
* FUTURE ENHANCEMENTS
*
* If initial seeds are not selected randomly, two instances of the PRNG
* can be correlated.
  [Knuth81, pp. 32-33] describes a shuffling technique
* that can eliminate this problem.
*
* If PRNGs are used for simulation, the period of the current
* implementation may be too short.  [LE88] discusses methods of combining
* MLCGs to produce much longer periods, and suggests some alternative
* values for A and M.  [LE90 and Sch92] also provide information on
* long-period PRNGs.
*
* REFERENCES
*
* [Knuth81] Donald E. Knuth. The Art of Computer Programming.  Volume 2:
* Seminumerical Algorithms.  Reading, Massachusetts: Addison-Wesley, 1981.
*
* [LE88] Pierre L'Ecuyer. "Efficient and Portable Combined Random Number
* Generators".  CACM 31(6), June 1988, pp. 742-774.
*
* [LE90] Pierre L'Ecuyer. "Random Numbers for Simulation". CACM 33(10,
* October 1990, pp. 85-97.
*
```

```
* [PM88] Stephen K. Park and Keith W. Miller. "Random Number Generators:
* Good Ones are Hard to Find". CACM 31(10), October 1988, pp. 1192-1201.
*
* [Sch92] Bruce Schneier. "Pseudo-Ransom Sequence
 Generator for 32-Bit
* CPUs".  Dr. Dobb's Journal 17(2), February 1992, pp. 34, 37-38, 40.
*
* [PMS93] Stephen K. Park, Keith W. Miller, and Paul K. Stockmeyer.  In
* "Technical Correspondence: Remarks on Choosing and Implementing Random
* Number Generators". CACM 36(7), July 1993, pp. 105-110.
*
*/
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*/
/*
* Arm Framebuffer Compression (AFBC) modifiers
*
* AFBC is a proprietary lossless image compression protocol and format.
* It provides fine-grained random access and minimizes the amount of data
* transferred between IP blocks.
*
```

```
* AFBC has several features which may be supported and/or used, which are
* represented using bits in the modifier. Not all combinations are valid,
* and different devices or use-cases may support different combinations.
*
* Further information on the use of AFBC modifiers can be found in
* Documentation/gpu/afbc.rst
*/
/*
* Arm Fixed-Rate Compression (AFRC) modifiers
*
* AFRC is a proprietary fixed rate image compression protocol and format,
* designed to provide guaranteed bandwidth and memory footprint
* reductions
 in graphics and media use-cases.
*
* AFRC buffers consist of one or more planes, with the same components
* and meaning as an uncompressed buffer using the same pixel format.
*
* Within each plane, the pixel/luma/chroma values are grouped into
* "coding unit" blocks which are individually compressed to a
* fixed size (in bytes). All coding units within a given plane of a buffer
* store the same number of values, and have the same compressed size.
*
* The coding unit size is configurable, allowing different rates of compression.
*
* The start of each AFRC buffer plane must be aligned to an alignment granule which
* depends on the coding unit size.
*
* Coding Unit Size   Plane Alignment
* ---------------   ---------------
* 16 bytes          1024 bytes
* 24 bytes          512  bytes
* 32 bytes          2048 bytes
*
* Coding units are grouped into paging tiles. AFRC buffer dimensions must be aligned
* to a multiple of the paging tile dimensions.
* The dimensions
 of each paging tile depend on whether the buffer is optimised for
* scanline (SCAN layout) or rotated (ROT layout) access.
*
* Layout   Paging Tile Width   Paging Tile Height
* ------   -----------------   ------------------
* SCAN     16 coding units     4 coding units
* ROT      8  coding units     8 coding units
*
* The dimensions of each coding unit depend on the number of components
* in the compressed plane and whether the buffer is optimised for
* scanline (SCAN layout) or rotated (ROT layout) access.
```

```
*
* Number of Components in Plane   Layout      Coding Unit Width   Coding Unit Height
* -----------------------------   ---------   -----------------   ------------------
* 1                               SCAN        16 samples          4 samples
* Example: 16x4 luma samples in a 'Y' plane
*          16x4 chroma 'V' values, in the 'V' plane of a fully-planar YUV buffer
* -----------------------------   ---------   -----------------   ------------------
* 1
                                  ROT         8 samples           8 samples
* Example: 8x8 luma samples in a 'Y' plane
*          8x8 chroma 'V' values, in the 'V' plane of a fully-planar YUV buffer
* -----------------------------   ---------   -----------------   ------------------
* 2                               DONT CARE   8 samples           4 samples
* Example: 8x4 chroma pairs in the 'UV' plane of a semi-planar YUV buffer
* -----------------------------   ---------   -----------------   ------------------
* 3                               DONT CARE   4 samples           4 samples
* Example: 4x4 pixels in an RGB buffer without alpha
* -----------------------------   ---------   -----------------   ------------------
* 4                               DONT CARE   4 samples           4 samples
* Example: 4x4 pixels in an RGB buffer with alpha
*/
/*
* Amlogic Video Framebuffer Compression modifiers
*
* Amlogic uses a proprietary lossless image compression protocol and format
 * for their hardware video codec accelerators, either video decoders or
* video input encoders.
*
* It considerably reduces memory bandwidth while writing and reading
* frames in memory.
*
* The underlying storage is considered to be 3 components, 8bit or 10-bit
* per component YCbCr 420, single plane :
* - DRM_FORMAT_YUV420_8BIT
* - DRM_FORMAT_YUV420_10BIT
*
* The first 8 bits of the mode defines the layout, then the following 8 bits
* defines the options changing the layout.
*
* Not all combinations are valid, and different SoCs may support different
* combinations of layout and options.
*/
/*
* 64K_S is the same for GFX9/GFX10/GFX10_RBPLUS and hence has GFX9 as canonical
* version.
*/
```

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*
* Authors: Rickard E. (Rik) Faith <faith@valinux.com>
*
* DESCRIPTION
*

* This file contains a straightforward skip list implementation.n
*
* FUTURE ENHANCEMENTS
*
* REFERENCES
*
* [Pugh90] William Pugh. Skip Lists: A Probabilistic Alternative to
* Balanced Trees. CACM 33(6), June 1990, pp. 668-676.
*
*/

Found in path(s):
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/kgsl/kgsl_ringbuffer.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/kgsl/kgsl_pipe.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/kgsl/kgsl_device.c
*
 /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/msm/msm_ringbuffer.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/kgsl/kgsl_bo.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-

4oQHCRMH/libdrm-2.4.123/freedreno/msm/msm_priv.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/freedreno/msm/msm_device.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/etnaviv/etnaviv_bo_cache_test.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/etnaviv/etnaviv_cmd_stream_test.c
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* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/util/kms.c
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/util/format.h
* /libdrm-2.4.123-2.el9.src.rpm-cosi-expand-archive-at8Bf9ad/libdrm-2.4.123.tar.xz-cosi-expand-archive-4oQHCRMH/libdrm-2.4.123/tests/util/common.h
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1.413 icu 67.1-10.el9_6

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Commands to generate dependency files
GEN_DEPS.c= $(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc= $(CXX) -E -MM $(DEFS) $(CPPFLAGS)

Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE= -Wl,-rpath,

```
## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation
 rules
%.$(STATIC_O): $(srcdir)/%.c
 $(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
 $(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
 $(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
 $(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<


## Dependency rules
%.d: $(srcdir)/%.c
 @echo "generating dependency information for $<"
 @$(SHELL) -ec '$(GEN_DEPS.c) $< \
  | sed '\"s/\($*\)\.o[ :]*/\1.o $@ : /g'\" > $@; \
  [ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
 @echo "generating dependency information for $<"
 @$(SHELL) -ec '$(GEN_DEPS.cc) $< \
  | sed '\"s/\($*\)\.o[ :]*/\1.o $@ : /g'\" > $@; \
  [ -s $@ ] || rm -f $@'

## Versioned libraries rules

%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
 $(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
 $(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

##  Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic
```

Dependencies [i.e. map files] for the final library
BIR_DEPS=

Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

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****
*
* @author: zhang
* @version: $Revision$
* @created: Apr 24, 2009
*
* Description: A class to decode PEM files
*
**/

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*
****
*
* @author: zhang
* @version: $Revision$
* @created: Apr 24, 2009
*

* Description: A KeySpec for PKCS#1 encoded RSA private key
*
**/

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in path(s):
* /oauth-20100527-sources-jar/net/oauth/signature/pem/PKCS1EncodedKeySpec.java

1.417 postgresql-jdbc 9.4.1208

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1.418 matrix 0.3.1

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1.420 ehcache 2.6.6

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visible_title: "Ehcache Licensing"
active_sub_menu_id: ehc_mnu_about_license

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1.421 go-toml 1.9.4

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localtime.go, localtime_test.go:

Originals:
  https://raw.githubusercontent.com/googleapis/google-cloud-go/ed46f5086358513cf8c25f8e3f022cb838a49d66/civil/civil.go
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Changes:
  * Renamed files from civil* to localtime*.
  * Package changed from civil to toml.
  * 'Local' prefix added to all structs.
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1.422 azure-.net-sdk 1.11.4

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1.423 dotnetnuke-instrumentation 1.2

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1.424 golang-jwt 4.4.2

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1.426 nuget.dependencyresolver.core 6.9.1

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1.427 yaml 0.1.1

1.428 commons-logging 1.0.4

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1.429 graceful 1.2.15

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1.430 packagemanagement 1.0.0

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Additional
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File: PSReadLine
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https://github.com/lzybkr/PSReadLine

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File: Hashtables from ConvertFrom-json
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https://stackoverflow.com/questions/22002748/hashtables-from-convertfrom-json-have-different-type-from-powershells-built-in-h

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1.432 swagger 1.6.12

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1.433 strscan 3.0.1

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* /strscan-3-0-1-1-gem/data-tar-gz/ext/strscan/strscan.c

1.434 jcommander 1.48

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Found in path(s):
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/ISO8601DateConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/PathConverter.java
*
 /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/IntegerConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/ParametersDelegate.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/DoubleConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/IDefaultProvider.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/ResourceBundle.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/Parameters.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/MissingCommandException.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/JCommander.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/StringConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/LongConverter.java

* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/FloatConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/internal/Lists.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/ParameterDescription.java
*
 /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/NoConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/URLConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/ParameterException.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/FileConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/BigDecimalConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/defaultprovider/PropertyFileDefaultProvider.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/URIConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/internal/Maps.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/BaseConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/internal/DefaultConverterFactory.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/internal/Sets.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/Parameter.java
*
 /jcommander-1-48-sources-1-jar/com/beust/jcommander/converters/BooleanConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/IStringConverter.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/IStringConverterFactory.java
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* /jcommander-1-48-sources-1-jar/com/beust/jcommander/IParameterValidator2.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/IParameterValidator.java
*
 /jcommander-1-48-sources-1-jar/com/beust/jcommander/validators/NoValueValidator.java
* /jcommander-1-48-sources-1-jar/com/beust/jcommander/validators/NoValidator.java

1.435 subspawn 0.1.1

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1.438 swagger-annotations 1.6.12

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Manifest-Version: 1.0

Created-By: Maven JAR Plugin 3.3.0
Build-Jdk-Spec: 11
Automatic-Module-Name: io.swagger.annotations
implementation-version: 1.6.12
mode: development
package: io.swagger
url: https://github.com/swagger-api/swagger-core/modules/swagger-annotat
ions
Bundle-Description: Sonatype helps open source projects to set up Maven
repositories on https://oss.sonatype.org/
Bundle-Developers: fehguy;email="fehguy@gmail.com";name="Tony Tam"
Bundle-DocURL: https://github.com/swagger-api/swagger-core/modules/swagg
er-annotations
Bundle-License: "Apache License 2.0";link="http://www.apache.org/license
s/LICENSE-2.0.html"
Bundle-ManifestVersion: 2
Bundle-Name: swagger-annotations
Bundle-SCM: url="https://github.com/swagger-api/swagger-core/modules/swa
gger-annotations",connection="scm:git:git@github.com:swagger-api/swagge
r-core.git/modules/swagger-annotations",developer-connection="scm:git:g
it@github.com:swagger-api/swagger-core.git/modules/swagger-annotations"
 ,tag=HEAD
Bundle-SymbolicName: io.swagger.swagger-annotations
Bundle-Version: 1.6.12
Export-Package: io.swagger.annotations;version="1.6.12"
Require-Capability: osgi.ee;filter:="(&(osgi.ee=JavaSE)(version=1.7))"

Found in path(s):
* /swagger-annotations-1-6-12-4-jar/META-INF/MANIFEST.MF

1.439 pp 0.3.0

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1.440 go-secure-stdlib-parseutil 0.1.4

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1.442 sdk 4.2.0.alpha2

1.443 go-jmespath 1.1.1-0.20230531184041-4a4bfbff5d01

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1.449 net-smtp 0.2.1

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1.450 base-64 0.1.1

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1.451 pam 1.5.1-23.el9

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1.452 csv 3.1.9

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1.453 bouncy-castle 1.74

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* The NTRU algorithm based on the round 3 submission of the NIST post-quantum cryptography. For the old NTRU, see
* {@link org.bouncycastle.pqc.legacy.crypto.ntru}.
*
*
* This implementation is based on the C reference implementation submitted for the round 3 NIST PQC competition,
* released under CC0-1.0 license.
*
*
* NIST submission files
* License of reference implementation
*
* @see NIST round 3 PQC submissions page
* @see NTRU website
* @see NTRU specification
*/

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/**
* An implementation of RFC 7253 on The OCB
* Authenticated-Encryption Algorithm. For those still concerned about the original patents
* around this, please see:
*
* https://mailarchive.ietf.org/arch/msg/cfrg/qLTveWOdTJcLn4HP3ev-vrj05Vg/
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public DomainParameters(BigInteger p, BigInteger g, BigInteger q, BigInteger j,

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/**
* Magic value for proprietary OpenSSH private key.
**/

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/**
* SPHINCS-256 signer.
*
* This implementation is heavily based on the reference implementation in SUPERCOP, the main difference being the digests used
* for message hashing and tree construction are now configurable (within limits...) and that the implementation produces
* detached signatures.
*
*
* The SPHINCS reference implementation is public domain, as per the statement in the second last paragraph of
* section 1 in https://eprint.iacr.org/2014/795.pdf
*
*/

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/**
* Poly1305 message authentication code, designed by D. J. Bernstein.
*
* Poly1305 computes a 128-bit (16 bytes) authenticator, using a 128 bit nonce and a 256 bit key
* consisting of a 128 bit key applied to an underlying cipher, and a 128 bit key (with 106
* effective key bits) used in the authenticator.
*
* The polynomial calculation in this implementation is adapted from the public domain <a
* href="https://github.com/floodyberry/poly1305-donna">poly1305-donna-unrolled C implementation
* by Andrew M (@floodyberry).
* @see Poly1305KeyGenerator
*/

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* /bcprov-jdk18on-1-74-sources-jar/org/bouncycastle/crypto/macs/Poly1305.java
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/**
* Contains the polynomial classes used in the NTRU round 3 algorithm.
*
*
* This implementation is based on the C reference implementation submitted for the round 3 NIST PQC competition,
* released under CC0-1.0 license.
*
*
* NIST submission files
* License of reference implementation
*/

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* /bcprov-jdk18on-1-74-sources-jar/org/bouncycastle/pqc/math/ntru/package-info.java
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// now keep space is free to use

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1.454 httpclient-osgi 4.3

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   *
   ********************************************************************************
   * Convert PKCS#1 encoded private key into RSAPrivateCrtKeySpec.
   *
   * <p/>The ASN.1 syntax for the private key
 with CRT is
   *
   * <pre>
   * --
   * -- Representation of RSA private key with information for the CRT algorithm.
   * --
   * RSAPrivateKey ::= SEQUENCE {
   *   version           Version,
   *   modulus           INTEGER,  -- n
   *   publicExponent    INTEGER,  -- e
   *   privateExponent   INTEGER,  -- d
   *   prime1            INTEGER,  -- p
   *   prime2            INTEGER,  -- q
   *   exponent1         INTEGER,  -- d mod (p-1)
   *   exponent2         INTEGER,  -- d mod (q-1)
   *   coefficient       INTEGER,  -- (inverse of q) mod p
   *   otherPrimeInfos   OtherPrimeInfos OPTIONAL
   * }
   * </pre>
   *
   * @param keyInPkcs1 PKCS#1 encoded key
   * @throws java.io.IOException
   */
```

Found in path(s):
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/RSA.java
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Found in path(s):
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/UnsupportedAlgorithmException.java
*
/tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/Verifier.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/EC.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/InvalidCreatedFieldException.java
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*
/tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/InvalidExpiresFieldException.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/Signature.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/UnparsableSignatureException.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/Base64.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/SigningAlgorithm.java
* /tomitribe-http-signatures-1-5-sources-4-jar/org/tomitribe/auth/signatures/Join.java

1.456 libarchive 3.5.3-4.el9

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1.457 tomcat-tribes 9.0.108

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1.458 ikvm-windward 8.1.5717

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1.466 fcntl 1.0.1

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1.467 expat 2.5.0-5.el9_6

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1.468 go-metrics 0.3.11

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1.470 ruby2_keywords 0.0.5

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1.471 matrix 0.4.2

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1.472 eventmachine 1.0.7

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1.473 xerces-j 2.7.1

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The reason we have a separate public license for some libraries is that
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Because of this blurred distinction, using the ordinary General
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not effectively promote software
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concluded that weaker conditions might promote sharing better.

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function must still compute square roots.)

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work.
(Executables containing this object code plus portions of the Library will still fall under Section 6.)

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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1.475 libbpf 1.5.0-1.el9

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/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)
* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
*
* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
* http://www.hypermall.com/
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
* interrupts us (except possibly for removal/insertion of the cable?)
* 10/4/97 - began heavy inline documentation of the code. Corrected typos
* and spelling mistakes.
* 10/5/97 - added code to handle PHY interrupts, disable PHY on

```
* loss of link, and correctly re-enable PHY when link is
* re-established. (put back CFG_PHYIE)
*
*   Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
*
* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
*
* Linux driver for the IDT77201 NICStAR PCI ATM controller.
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
* see init_nicstar() for PHY initialization to change this. This driver
* expects the Linux ATM stack to support scatter-gather
 lists
* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
*
* Implementing minimal-copy of received data:
*   IDT always receives data into a small buffer, then large buffers
*     as needed. This means that data must always be copied to create
*     the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
*     Fix is simple: make large buffers large enough to hold entire
*     SDU, and leave <small_buffer_data> bytes empty at the start. Then
*     copy small buffer contents to head of large buffer.
*   Trick is to avoid fragmenting Linux, due to need for a lot of large
*     buffers. This is done by 2 things:
*       1) skb->destructor / skb->atm.recycle_buffer
*            combined, allow nicstar_free_rx_skb to be called to
*            recycle large data buffers
*       2) skb_clone of received buffers
*   See nicstar_free_rx_skb and linearize_buffer for implementation
*     details.
*
*
*
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```

*
* M. Welsh, 6 July 1996
*
*
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Purpose: This test checks SPDX-License-Identifier: tags in all source files against redhat/LICENSES.

load test-lib.bash

SPEC=$BATS_TEST_DIRNAME/../kernel.spec.template

ERRMSG="ERROR: The kernel RPM spec file License: field does not match the licenses available in the source tree.
Fedora, Centos Stream, and RHEL require that the RPM License field match the licenses in the source tree. See https://docs.fedoraproject.org/en-US/legal/license-approval/ for further information.
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_verify_one_license_tag()
 {
 if [[$(grep -c '^License:' $SPEC) != "1"]]; then
    echo "ERROR: The specfile is supposed to have exactly one License: tag!"
    return 1
 fi
}

_verify_spdx_licenses() {
 spec_licenses=`mktemp`

```
 source_licenses=`mktemp`
 grep '^License:' $SPEC | sed 's,License[[:space:]]*:[[:space:]]*,,' | sed 's, AND ,\n,g' | sed 's,^(\(.*\))$,\1,' | sort -u >
$spec_licenses

 if ! $BATS_TEST_DIRNAME/../scripts/kspdx-tool/kspdx.py $BATS_TEST_DIRNAME/../../ > $source_licenses;
then
    echo "ERROR: Failed to gather SPDX-License-Identifier: information from source files!"
    return 1
 fi
 sort -o $source_licenses $source_licenses
 if ! diff -u $spec_licenses $source_licenses; then
     echo $ERRMSG
     ret=1
 else
     ret=0
 fi
 rm -f $spec_licenses $source_licenses
 return $ret
}

@test "Verify SPDX-License-Identifier tags" {
 if ! test -x $BATS_TEST_DIRNAME/../scripts/kspdx-tool/kspdx.py ;
 then
     skip "kspdx-tool is missing"
 fi

 run _verify_one_license_tag
 check_status

 _verify_spdx_licenses
 check_status
}
```


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static inline int license_is_gpl_compatible(const char *license)
{
 return (strcmp(license, "GPL") == 0
  || strcmp(license, "GPL v2") == 0
  || strcmp(license, "GPL and additional rights") == 0
  || strcmp(license, "Dual BSD/GPL") == 0
  || strcmp(license, "Dual MIT/GPL") == 0
  || strcmp(license, "Dual MPL/GPL") == 0);
}

#endif
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1.476 properties 1.8.5

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1.477 go.mongodb.org-mongo-driver 1.11.4

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1.478 distlib 0.3.3

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A. HISTORY OF THE SOFTWARE
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member
of
the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

Release	Derived from	Year	Owner	GPL-compatible? (1)

0.9.0 thru 1.2		1991-1995	CWI	yes
1.3 thru 1.5.2	1.2	1995-1999	CNRI	yes
1.6	1.5.2	2000	CNRI	no
2.0	1.6	2000	BeOpen.com	no
1.6.1	1.6	2001	CNRI	yes (2)
2.1	2.0+1.6.1	2001	PSF	no
2.0.1	2.0+1.6.1	2001	PSF	yes
2.1.1	2.1+2.0.1	2001	PSF	yes
2.2	2.1.1	2001	PSF	yes
2.1.2	2.1.1	2002	PSF	yes
2.1.3				
2.1.2	2002	PSF	yes	
2.2.1	2.2	2002	PSF	yes
2.2.2	2.2.1	2002	PSF	yes
2.2.3	2.2.2	2003	PSF	yes
2.3	2.2.2	2002-2003	PSF	yes
2.3.1	2.3	2002-2003	PSF	yes
2.3.2	2.3.1	2002-2003	PSF	yes
2.3.3	2.3.2	2002-2003	PSF	yes
2.3.4	2.3.3	2004	PSF	yes
2.3.5	2.3.4	2005	PSF	yes
2.4	2.3	2004	PSF	yes
2.4.1	2.4	2005	PSF	yes
2.4.2	2.4.1	2005	PSF	yes
2.4.3	2.4.2	2006	PSF	yes
2.4.4	2.4.3	2006	PSF	yes
2.5	2.4	2006	PSF	yes
2.5.1	2.5	2007	PSF	yes
2.5.2	2.5.1	2008	PSF	yes
2.5.3	2.5.2	2008	PSF	yes
2.6	2.5	2008	PSF	yes
2.6.1	2.6	2008	PSF	yes
2.6.2	2.6.1	2009	PSF	yes
2.6.3	2.6.2	2009	PSF	yes
2.6.4	2.6.3	2009	PSF	yes
2.6.5	2.6.4	2010	PSF	yes
3.0	2.6	2008	PSF	yes
3.0.1	3.0	2009	PSF	yes
3.1	3.0.1	2009	PSF	yes
3.1.1	3.1	2009	PSF	yes
3.1.2	3.1	2010	PSF	yes
3.2	3.1	2010	PSF	yes

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Thanks to:

- Rajiv Abraham
- Ali Afshar
- David Barnett
- Pior Bastida
- Anthony Baxter
- Erik Bray
- C. Titus Brown

- Francisco Martn Brugu
- Nicolas Cadou
- Godefroid Chapelle
- Julien Courteau
- Christophe Combelles
- Jason R. Coombs
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1.479 jruby-readline 1.3.7

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1.480 sysstat 12.5.4-9.el9

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1.481 go-jose 2.6.0

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1.482 mariadb-java-client 2.7.2

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1.483 drb 2.0.5

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azurequeue.sync=org.jclouds.azure.storage.queue.AzureQueueClient
azurequeue.async=org.jclouds.azure.storage.queue.AzureQueueAsyncClient
azurequeue.propertiesbuilder=org.jclouds.azure.storage.AzureStoragePropertiesBuilder
azurequeue.endpoint=https://{identity}.queue.core.windows.net

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azureblob.endpoint=https://{identity}.blob.core.windows.net

pcs.contextbuilder=org.jclouds.mezeo.pcs2.PCSContextBuilder
pcs.apiVersion=unknown

sdn.contextbuilder=org.jclouds.nirvanix.sdn.SDNContextBuilder
sdn.propertiesbuilder=org.jclouds.nirvanix.sdn.SDNPropertiesBuilder

sqs.contextbuilder=org.jclouds.aws.sqs.SQSContextBuilder
sqs.propertiesbuilder=org.jclouds.aws.sqs.SQSPropertiesBuilder

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elb.propertiesbuilder=org.jclouds.aws.elb.ELBPropertiesBuilder

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cloudwatch.propertiesbuilder=org.jclouds.aws.cloudwatch.CloudWatchPropertiesBuilder

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s3.propertiesbuilder=org.jclouds.aws.s3.S3PropertiesBuilder

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ec2.propertiesbuilder=org.jclouds.aws.ec2.EC2PropertiesBuilder

rimuhosting.contextbuilder=org.jclouds.rimuhosting.miro.RimuHostingContextBuilder
rimuhosting.propertiesbuilder=org.jclouds.rimuhosting.miro.RimuHostingPropertiesBuilder

slicehost.contextbuilder=org.jclouds.slicehost.SlicehostContextBuilder
slicehost.propertiesbuilder=org.jclouds.slicehost.SlicehostPropertiesBuilder

trmk-vcloudexpress.contextbuilder=org.jclouds.vcloud.terremark.TerremarkVCloudExpressContextBuilder
trmk-vcloudexpress.propertiesbuilder=org.jclouds.vcloud.terremark.TerremarkVCloudExpressPropertiesBuilder

trmk-ecloud.contextbuilder=org.jclouds.vcloud.terremark.TerremarkECloudContextBuilder
trmk-ecloud.propertiesbuilder=org.jclouds.vcloud.terremark.TerremarkECloudPropertiesBuilder
```

```
chef.contextbuilder=org.jclouds.chef.ChefContextBuilder
chef.propertiesbuilder=org.jclouds.chef.ChefPropertiesBuilder

transientchef.contextbuilder=org.jclouds.chef.test.TransientChefContextBuilder
transientchef.propertiesbuilder=org.jclouds.chef.ChefPropertiesBuilder

opscodeplatform.contextbuilder=org.jclouds.opscodeplatform.OpscodePlatformContextBuilder
opscodeplatform.propertiesbuilder=org.jclouds.opscodeplatform.OpscodePlatformPropertiesBuilder

vcloud.contextbuilder=org.jclouds.vcloud.VCloudContextBuilder
vcloud.propertiesbuilder=org.jclouds.vcloud.VCloudPropertiesBuilder

vcloudexpress.contextbuilder=org.jclouds.vcloud.VCloudExpressContextBuilder
vcloudexpress.propertiesbuilder=org.jclouds.vcloud.VCloudExpressPropertiesBuilder

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eucalyptus.propertiesbuilder=org.jclouds.aws.ec2.EucalyptusPropertiesBuilder

nova.contextbuilder=org.jclouds.aws.ec2.EC2ContextBuilder
nova.propertiesbuilder=org.jclouds.aws.ec2.NovaPropertiesBuilder

cloudservers.contextbuilder=org.jclouds.rackspace.cloudservers.CloudServersContextBuilder
cloudservers.propertiesbuilder=org.jclouds.rackspace.RackspacePropertiesBuilder

bluelock-vcdirector.contextbuilder=org.jclouds.vcloud.bluelock.BlueLockVCloudDirectorContextBuilder
bluelock-vcdirector.propertiesbuilder=org.jclouds.vcloud.bluelock.BlueLockVCloudDirectorPropertiesBuilder

gogrid.propertiesbuilder=org.jclouds.gogrid.GoGridPropertiesBuilder
gogrid.contextbuilder=org.jclouds.gogrid.GoGridContextBuilder

ibmdev.propertiesbuilder=org.jclouds.ibmdev.IBMDeveloperCloudPropertiesBuilder
ibmdev.contextbuilder=org.jclouds.ibmdev.IBMDeveloperCloudContextBuilder

stub.propertiesbuilder=org.jclouds.compute.stub.StubComputeServicePropertiesBuilder
stub.contextbuilder=org.jclouds.compute.stub.StubComputeServiceContextBuilder
#
 example of where to change your endpoint
# bluelock.endpoint=https://express3.bluelock.com/api

atmosonline.contextbuilder=org.jclouds.atmosonline.saas.AtmosStorageContextBuilder
atmosonline.endpoint=https://accesspoint.atmosonline.com
atmosonline.apiversion=1.3.0

synaptic.contextbuilder=org.jclouds.atmosonline.saas.AtmosStorageContextBuilder
synaptic.endpoint=https://storage.synaptic.att.com
synaptic.apiversion=1.3.0

#
```

TODO peer1 and hostedsolutions use atmos

cloudfiles.contextbuilder=org.jclouds.rackspace.cloudfiles.CloudFilesContextBuilder
cloudfiles.propertiesbuilder=org.jclouds.rackspace.cloudfiles.CloudFilesPropertiesBuilder

walrus.contextbuilder=org.jclouds.aws.s3.S3ContextBuilder
walrus.propertiesbuilder=org.jclouds.aws.s3.WalrusPropertiesBuilder

googlestorage.contextbuilder=org.jclouds.aws.s3.S3ContextBuilder
googlestorage.propertiesbuilder=org.jclouds.aws.s3.GoogleStoragePropertiesBuilder

transient.contextbuilder=org.jclouds.blobstore.TransientBlobStoreContextBuilder
transient.propertiesbuilder=org.jclouds.blobstore.TransientBlobStorePropertiesBuilder

filesystem.contextbuilder=org.jclouds.filesystem.FilesystemBlobStoreContextBuilder
filesystem.propertiesbuilder=org.jclouds.filesystem.FilesystemBlobStorePropertiesBuilder

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 * </p>
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*   some convenience methods for reading and writing to and from files.</li>
*  <li>v2.0.2 - Now specifies UTF-8 encoding in places where the code fails on systems
*   with other encodings (like EBCDIC).</li>
*  <li>v2.0.1 - Fixed an error when decoding a single byte, that is, when the
*   encoded data was a single byte.</li>
*  <li>v2.0 - I got rid of methods that used booleans to set options.
*   Now everything is more consolidated and cleaner. The code now detects
*   when data that's being decoded is gzip-compressed and will decompress it
*   automatically. Generally things are cleaner. You'll probably have to
*   change some method calls that you were making to support the new
*   options format (<tt>int</tt>s that you "OR" together).</li>
*  <li>v1.5.1 - Fixed bug when decompressing and decoding to a
*   byte[] using <tt>decode( String s, boolean gzipCompressed
 )</tt>.
*   Added the ability to "suspend" encoding in the Output Stream so
*   you can turn on and off the encoding if you need to embed base64
*   data in an otherwise "normal" stream (like an XML file).</li>
*  <li>v1.5 - Output stream pases on flush() command but doesn't do anything itself.
*      This helps when using GZIP streams.
*      Added the ability to GZip-compress objects before encoding them.</li>
*  <li>v1.4 - Added helper methods to read/write files.</li>
*  <li>v1.3.6 - Fixed OutputStream.flush() so that 'position' is reset.</li>
*  <li>v1.3.5 - Added flag to turn on and off line breaks. Fixed bug in input stream
*      where last buffer being read, if not completely full, was not returned.</li>
*  <li>v1.3.4 - Fixed when "improperly padded stream" error was thrown at the wrong time.</li>
*  <li>v1.3.3 - Fixed I/O streams which were totally messed up.</li>
* </ul>
*
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* Please visit <a href="http://iharder.net/base64">http://iharder.net/base64</a>
* periodically to check for updates or to contribute improvements.
* </p>
*
```

* @author Robert Harder
* @author rob@iharder.net
* @version 2.1
*/

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* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/encryption/internal/Base64.java
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* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/lifecycle/Closer.java
*
 /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/PayloadEnclosing.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpCommandExecutorService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/Credentials.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ParseURIFromListOrLocationHeaderIf20x.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Unwrap.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/InvocationContext.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/lifecycle/LifeCycle.java
* /jclouds-core-1-0-beta-7-sources-jar/com/google/gson/ExposedEscaper.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/RestContextBuilder.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/UnwrapOnlyJsonValue.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/options/GetOptions.java
* /jclouds-core-1-0-beta-7-sources-jar/net/oauth/signature/pem/PKCS1EncodedPublicKeySpec.java
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 /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/RestContext.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/internal/PayloadEnclosingImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/annotation/Redirection.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/TransformingHttpCommandImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/internal/SignatureWire.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/jdk/config/JDKLoggingModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/RequestFilters.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/MapPayloadParam.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/RequiresHttp.java

* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/internal/RestAnnotationProcessor.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/ApiVersion.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/functions/ExceptionToValueOrPropagate.java
*
 /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/ResourceNotFoundException.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/config/ConsoleLoggingModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/AsyncClientFactory.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/BaseLogger.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/handlers/DelegatingErrorHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/encryption/internal/JCECrypto.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/options/HttpRequestOptions.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ReturnInputStream.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/XMLResponseParser.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Delegate.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/Constants.java
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 /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/predicates/validators/DnsNameValidator.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpMessage.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/HttpClient.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/crypto/Crypto.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/ParamParser.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/internal/BaseHttpCommandExecutorService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/annotation/ServerError.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpUtils.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/BinderParam.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpRequestFilter.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ParseJson.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/Timeout.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ReturnFalseOn404.java
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 /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/binders/BindMapToMatrixParams.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/BaseImmutableContentMetadata.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/pool/PoolConstants.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/config/NullLoggingModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/json/internal/GsonWrapper.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/JsonBall.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/LocationScope.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/lifecycle/BaseLifeCycle.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/InputParamValidator.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/util/Patterns.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/date/TimeStamp.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/handlers/RedirectionRetryHandler.java
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 /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/RetryOnTimeOutExceptionSupplier.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/RSADecryptingPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/DynamicExecutors.java
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* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/InputSuppliers.java

* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/functions/ReturnVoidOnNotFoundOr404.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ParseETagHeader.java
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* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/QueryParams.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/SingleThreaded.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/internal/HttpWire.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/json/Json.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/config/CreateClientForCaller.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpCommand.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/ResourceMetadata.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/internal/Wire.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/handlers/BackoffLimitedRetryHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Api.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpCommandRendezvous.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpResponseException.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/FutureIterables.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/BaseCipherPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/BaseMutableContentMetadata.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/binders/BindToJsonPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/config/ClientProvider.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/MapBinder.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/internal/GeneratedHttpRequest.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/binders/BindToStringPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Endpoint.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/predicates/SocketOpen.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/predicates/RetryablePredicate.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/crypto/Pems.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/json/config/GsonModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/Part.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/annotation/ClientError.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/config/SaxParserModule.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/RestContextFactory.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/AuthorizationException.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/util/Utils.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/functions/MapHttp4xxCodesToExceptions.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/MoreExecutors.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/DelegatingPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/net/IPSocket.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/Location.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/ExceptionParsingListenableFuture.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/MapBinder.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/IOExceptionRetryHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/config/BinderUtils.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/RSAEncryptingPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/OverrideRequestFilters.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/date/internal/SimpleDateFormatDateService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpRetryHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/config/RestModule.java
* /jclouds-core-1-0-beta-7-sources-jar/com/google/gson/JcloudsTreeNavigator.java
* /jclouds-core-1-0-beta-7-sources-
jar/org/jclouds/rest/suppliers/RetryOnTimeOutButNotOnAuthorizationExceptionSupplier.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ReturnTrueOn404.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/config/SSLModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Provider.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/PropertiesBuilder.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/ConsoleLogger.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/internal/ResourceMetadataImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/Payload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpErrorHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/internal/RestContextImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/Logger.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/PhantomPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/internal/MutableResourceMetadataImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/functions/ReturnFalseOnNotFoundOr404.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/util/InputStreamChain.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Credential.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ReleasePayloadAndReturn.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/functions/ReturnEmptySetOnNotFoundOr404.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/NullLogger.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpException.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/MutableResourceMetadata.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpResponse.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/binders/BindAsHostPrefix.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/MutableContentMetadata.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/RequestSigner.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/StreamingPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/handlers/DelegatingRetryHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/jdk/JDKLogger.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/ParamValidators.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/Binder.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/config/JavaUrlHttpCommandExecutorServiceModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/HttpRequest.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/config/ExecutorServiceModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/VirtualHost.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/MatrixParams.java
* /jclouds-core-1-0-beta-7-sources-jar/com/google/gson/JcloudsCompactFormatter.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/InputStreamPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/filters/BasicAuthentication.java

* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/PartParam.java
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* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/config/RestClientModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/predicates/SocketOpenUnsupported.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/config/AsyncClientProvider.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ReturnTrueIf2xx.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/options/BaseHttpRequestOptions.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/ConfiguresRestClient.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/ResponseParser.java
* /jclouds-core-1-0-beta-7-sources-jar/com/google/gson/JsonLiteral.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/predicates/validators/AllLowerCaseValidator.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/predicates/Validator.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/Futures.java
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/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/logging/config/LoggingModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ReturnStringIf2xx.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ParseContentMD5FromHeaders.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/functions/ParseSax.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/config/ConfiguresExecutorService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/Payloads.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/ByteArrayPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/SkipEncoding.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/internal/UtilsImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/BasePayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/MultipartForm.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/ConfiguresRestContext.java
*
/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Headers.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/internal/JavaUrlHttpCommandExecutorService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/TransformingHttpCommandExecutorService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/FormParams.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/ExceptionParser.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/WriteTo.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/HttpAsyncClient.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/lifecycle/config/LifeCycleModule.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/TransformingHttpCommand.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/domain/internal/LocationImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/StringPayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/UrlEncodedFormPayload.java
*
/jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/EndpointParam.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/date/DateService.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/handlers/CloseContentAndSetExceptionErrorHandler.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/annotations/Identity.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/crypto/CryptoStreams.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/internal/ClassMethodArgs.java

* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/ContentMetadata.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/io/payloads/FilePayload.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/http/TransformingHttpCommandExecutorServiceImpl.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/concurrent/internal/SyncProxy.java
* /jclouds-core-1-0-beta-7-sources-jar/org/jclouds/rest/Utils.java

1.485 hibernate-validator 5.1.3.Final

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1.488 swagger-parser-core 2.1.21

1.488.1 Available under license :

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1.489 bcpkix-jdk18on 1.74

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```
/**
   * Base constructor.
   * <p>
   * The authorizer attempts to perform matching (including the use of the wildcard) in accordance with RFC 6125.
   * </p>
   * <p>
   * Known suffixes is a list of public domain suffixes that can't be used as wild cards for
   * example *.com, or c*c.com, as a dns wildcard could match every/most .com domains if a registrar were issue it.
   * If *.com is in the known suffixes list will not be allowed to match.
   * </p>
   *
   * @param knownSuffixes a set of suffixes that cannot be wild-carded, e.g. { ".com", ".net", ".org" }
   */
```

Found in path(s):
* /bcpkix-jdk18on-1-74-java-jar/org/bouncycastle/est/jcajce/JsseDefaultHostnameAuthorizer.java

1.490 httpcomponents-client 4.3.2

1.490.1 Available under license :

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1.502 pep517 0.12.0

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* /guava-r06-sources-jar/com/google/common/base/Defaults.java
* /guava-r06-sources-jar/com/google/common/base/FinalizablePhantomReference.java
*
 /guava-r06-sources-jar/com/google/common/base/FinalizableReference.java
* /guava-r06-sources-jar/com/google/common/base/FinalizableSoftReference.java
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*/
/**
  * Wrapper around {@link System#arraycopy} so that it can be emulated
  * correctly in GWT.
  *
  * <p>It is only intended for the case {@code src} and {@code dest} are
  * different. It also doesn't validate the types and indices.
  *
  * <p>As of GWT 2.0, The built-in {@link System#arraycopy} doesn't work
  * in general

case. See
 * http://code.google.com/p/google-web-toolkit/issues/detail?id=3621
 * for more details.
 */

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* /guava-r06-sources-jar/com/google/common/io/MultiReader.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableBiMap.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableMap.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableSortedSet.java
*
/guava-r06-sources-jar/com/google/common/collect/RegularImmutableMap.java
* /guava-r06-sources-jar/com/google/common/primitives/Bytes.java
* /guava-r06-sources-jar/com/google/common/primitives/Floats.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/AbstractCheckedFuture.java
* /guava-r06-sources-jar/com/google/common/collect/UnmodifiableIterator.java
* /guava-r06-sources-jar/com/google/common/primitives/Booleans.java
* /guava-r06-sources-jar/com/google/common/collect/EmptyImmutableMap.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableMultimap.java
* /guava-r06-sources-jar/com/google/common/collect/SingletonImmutableMap.java
* /guava-r06-sources-jar/com/google/common/collect/EmptyImmutableSortedSet.java
* /guava-r06-sources-jar/com/google/common/net/InetAddresses.java
* /guava-r06-sources-jar/com/google/common/primitives/Longs.java
* /guava-r06-sources-jar/com/google/common/primitives/Doubles.java
* /guava-r06-sources-jar/com/google/common/collect/EmptyImmutableListMultimap.java
*
/guava-r06-sources-jar/com/google/common/collect/ImmutableListMultimap.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableEntry.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableMultiset.java
* /guava-r06-sources-jar/com/google/common/io/FileBackedOutputStream.java
* /guava-r06-sources-jar/com/google/common/primitives/Chars.java
* /guava-r06-sources-jar/com/google/common/collect/Collections2.java
* /guava-r06-sources-jar/com/google/common/collect/EmptyImmutableMultiset.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/ListenableFutureTask.java
* /guava-r06-sources-jar/com/google/common/collect/PeekingIterator.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/CheckedFuture.java

* /guava-r06-sources-jar/com/google/common/collect/RegularImmutableBiMap.java
* /guava-r06-sources-jar/com/google/common/primitives/Ints.java
* /guava-r06-sources-jar/com/google/common/primitives/Shorts.java
*
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* /guava-r06-sources-jar/com/google/common/collect/NullOutputException.java
* /guava-r06-sources-jar/com/google/common/annotations/GwtCompatible.java
*

/guava-r06-sources-jar/com/google/common/collect/MapMaker.java
* /guava-r06-sources-jar/com/google/common/io/ByteArrayDataOutput.java
* /guava-r06-sources-jar/com/google/common/collect/ExplicitOrderedImmutableSortedSet.java
* /guava-r06-sources-jar/com/google/common/collect/CustomConcurrentHashMap.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableSortedMapFauxverideShim.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/AbstractIdleService.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableEnumSet.java
* /guava-r06-sources-jar/com/google/common/collect/ExpirationTimer.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableSortedSetFauxverideShim.java
* /guava-r06-sources-jar/com/google/common/collect/EmptyImmutableSetMultimap.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/ForwardingService.java
* /guava-r06-sources-jar/com/google/common/io/LineProcessor.java
*
/guava-r06-sources-jar/com/google/common/collect/SingletonImmutableList.java
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* /guava-r06-sources-jar/com/google/common/primitives/UnsignedBytes.java
* /guava-r06-sources-jar/com/google/common/collect/ComparisonChain.java
* /guava-r06-sources-jar/com/google/common/base/Service.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableAsList.java
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* /guava-r06-sources-jar/com/google/common/collect/ImmutableSortedMap.java
* /guava-r06-sources-jar/com/google/common/net/InternetDomainName.java
*
/guava-r06-sources-jar/com/google/common/util/concurrent/UninterruptibleFuture.java
* /guava-r06-sources-jar/com/google/common/collect/RegularImmutableSortedSet.java
* /guava-r06-sources-jar/com/google/common/io/ByteArrayDataInput.java
* /guava-r06-sources-jar/com/google/common/annotations/GwtIncompatible.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/AbstractExecutionThreadService.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/ForwardingListenableFuture.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/ValueFuture.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableSetMultimap.java
* /guava-r06-sources-jar/com/google/common/base/Platform.java
* /guava-r06-sources-jar/com/google/common/collect/ImmutableSortedAsList.java
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```
*/
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  *
  * As of 2010/06/11, this method is identical to the (package private) hash
  * method in OpenJDK 7's java.util.HashMap
 class.
  */
```

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* /guava-r06-sources-jar/com/google/common/util/concurrent/ThreadFactoryBuilder.java
* /guava-r06-sources-jar/com/google/common/collect/ComputingConcurrentHashMap.java
*
/guava-r06-sources-jar/com/google/common/net/package-info.java
* /guava-r06-sources-jar/com/google/common/primitives/package-info.java
* /guava-r06-sources-jar/com/google/common/base/Strings.java
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* /guava-r06-sources-jar/com/google/common/collect/RegularImmutableSet.java
* /guava-r06-sources-jar/com/google/common/base/Supplier.java
* /guava-r06-sources-jar/com/google/common/collect/ForwardingMap.java
*
 /guava-r06-sources-jar/com/google/common/collect/ForwardingMultimap.java
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* /guava-r06-sources-jar/com/google/common/collect/LinkedHashMultiset.java
* /guava-r06-sources-jar/com/google/common/collect/LexicographicalOrdering.java
* /guava-r06-sources-jar/com/google/common/collect/ObjectArrays.java
* /guava-r06-sources-jar/com/google/common/collect/MapDifference.java
* /guava-r06-sources-jar/com/google/common/base/Throwables.java
* /guava-r06-sources-jar/com/google/common/collect/MutableClassToInstanceMap.java
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* /guava-r06-sources-jar/com/google/common/collect/NullsLastOrdering.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/MoreExecutors.java
* /guava-r06-sources-jar/com/google/common/collect/ClassToInstanceMap.java
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* /guava-r06-sources-jar/com/google/common/util/concurrent/AbstractListenableFuture.java
* /guava-r06-sources-jar/com/google/common/collect/ForwardingConcurrentMap.java
* /guava-r06-sources-jar/com/google/common/collect/package-info.java
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 /guava-r06-sources-jar/com/google/common/collect/Synchronized.java
* /guava-r06-sources-jar/com/google/common/collect/AbstractIterator.java
* /guava-r06-sources-jar/com/google/common/collect/ExplicitOrdering.java
* /guava-r06-sources-jar/com/google/common/collect/Interners.java
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* /guava-r06-sources-jar/com/google/common/base/Predicate.java
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* /guava-r06-sources-jar/com/google/common/collect/SingletonImmutableSet.java
* /guava-r06-sources-jar/com/google/common/primitives/Primitives.java
* /guava-r06-sources-jar/com/google/common/collect/Lists.java
* /guava-r06-sources-jar/com/google/common/collect/Multisets.java
* /guava-r06-sources-jar/com/google/common/collect/Multimaps.java
* /guava-r06-sources-jar/com/google/common/io/ByteStreams.java
* /guava-r06-sources-jar/com/google/common/collect/Constraint.java
* /guava-r06-sources-jar/com/google/common/collect/NaturalOrdering.java
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* /guava-r06-sources-jar/com/google/common/collect/HashMultiset.java
*
 /guava-r06-sources-jar/com/google/common/collect/Maps.java
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* /guava-r06-sources-jar/com/google/common/util/concurrent/UncheckedTimeoutException.java
* /guava-r06-sources-jar/com/google/common/io/PatternFilenameFilter.java
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/guava-r06-sources-jar/com/google/common/util/concurrent/Futures.java
* /guava-r06-sources-jar/com/google/common/util/concurrent/SimpleTimeLimiter.java
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1.504 nuget.configuration 6.9.1

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1.511 telnetd-x 2.1.1

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1.512 pcre2 10.40-6.el9

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* /jclouds-log4j-1-0-beta-7-sources-jar/org/jclouds/logging/log4j/config/Log4JLoggingModule.java

1.521 tomcat-ssi 9.0.108

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02110-1301, USA. */

```
#ifndef __VTV_H__
#define __VTV_H__

/* We could have used an enumeration here but it just makes it more
  difficult for the compiler to generate a call to this.  These are
  used as arguments to the function __VLTChangePermission, declared
  below.  */
#define __VLTP_READ_ONLY  0
#define __VLTP_READ_WRITE 1

#ifdef __cplusplus
extern "C" void __VLTChangePermission (int);
#else
extern void __VLTChangePermission (int);
#endif

#ifdef BIG_PAGE_SIZE
/* TODO - Replace '4096' below with correct big page size.  */
#define VTV_PAGE_SIZE 4096
#else
#if defined(__sun__) && defined(__svr4__) && defined(__sparc__)
#define VTV_PAGE_SIZE 8192
#else
#define VTV_PAGE_SIZE 4096
#endif
#endif



#endif /* __VTV_H__ */
```

(This file is under construction.) -*- text -*-

If you've contributed to gas and your name isn't listed here, it is
not meant as a slight. I just don't know about it. Email me,

nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into
the documentation, and info on specific files will go into an AUTHORS
file, as requested by the FSF.

++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration
 including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of
Buffalo University and Torbjorn Granlund of the Swedish Institute of
Computer Science.

Keith
 Knowles at the Open Software Foundation wrote the original MIPS
back end (tc-mips.c, tc-mips.h), and contributed Rose format support
that hasn't been merged in yet. Ralph Campbell worked with the MIPS
code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation
that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many
small
bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30
(tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error
checking, updating the code, and improving the 16 bit support, using
patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If
you've contributed significant work and are not mentioned on this list, and
want to be, let us know. Some of the history has been lost;
 we aren't
intentionally leaving anyone out.

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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free

library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we
use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without
limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated

interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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Library refers to a function or a
table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the

application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
root function must still compute square roots.)

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 4. You may copy and distribute the Library (or a portion or
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under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which

must be
distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered
by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any
executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
link a "work that uses the Library" with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse

engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is

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the major
components (compiler, kernel, and so on) of the operating system on
which the executable runs, unless that component itself accompanies
the executable.

It may happen that this requirement contradicts the license
restrictions of other proprietary libraries that do not normally
accompany the operating system. Such a contradiction means you cannot
use both them and the Library together in an executable that you
distribute.

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Version 3, 29 June 2007

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Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

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"This License" refers to version 3 of the GNU General Public License.

"Copyright" also means copyright-like laws that apply to other kinds of works, such as semiconductor masks.

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1. Source Code.

The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source

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1.534 stringio 3.0.1.1

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StringIO

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Pseudo `IO` class from/to `String`.

This library is based on MoonWolf version written in Ruby. Thanks a lot.

Differences to `IO`

* `fileno` raises `NotImplementedError`.
* encoding conversion is not implemented, and ignored silently.

Installation

Add this line to your application's Gemfile:

```ruby
gem 'stringio'
```

And then execute:

  $ bundle

Or install it yourself as:

  $ gem install stringio

Development

After checking out the repo, run `bin/setup` to install dependencies. Then, run `rake test` to run the tests. You can also run `bin/console` for an interactive prompt that will allow you to experiment.

To
 install this gem onto your local machine, run `bundle exec rake install`. To release a new version, update the version number in `version.rb`, and then run `bundle exec rake release`, which will create a git tag for the version, push git commits and tags, and push the `.gem` file to rubygems.org.

Contributing

Bug reports and pull requests are welcome on GitHub at https://github.com/ruby/stringio.

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$RoughId: stringio.c,v 1.13 2002/03/14 03:24:18 nobu Exp $
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* /jnr-enxio-0-32-17-sources-5-jar/jnr/enxio/channels/NativeServerSocketChannel.java
*
 /jnr-enxio-0-32-17-sources-5-jar/jnr/enxio/channels/NativeFileSelectorProvider.java
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* /jnr-enxio-0-32-17-sources-5-jar/jnr/enxio/channels/KQSelectionKey.java
* /jnr-enxio-0-32-17-sources-5-jar/jnr/enxio/channels/NativeSelectorProvider.java
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* /jnr-enxio-0-32-17-sources-5-jar/jnr/enxio/channels/PollSelectionKey.java

1.537 libgcrypt 1.10.0-11.el9

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^L
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with the Library, with the complete machine-readable "work that
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1.538 kr-fs 0.1.0

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1.539 nfs-utils 2.5.4-34.el9

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1.552 gnutls 3.8.3-6.el9

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding
machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that
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threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the
object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany
the work with the complete corresponding
machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate

properly with a modified version of the library, if
the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at
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d) If distribution of the work is made by offering access to copy
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e) Verify that the user has already received a copy of these
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For an executable, the required form of the "work that uses the
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components (compiler, kernel, and so on) of the operating system on
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=========

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

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d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source

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as a System Library, need not be
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typical or common use of that class of product, regardless of the status
of the particular user or of the way in which the particular user
actually uses, or expects or is expected to use, the product. A product
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#ifndef _BASENAME_LGPL_H

```
#define _BASENAME_LGPL_H

#include <stddef.h>

#ifndef DOUBLE_SLASH_IS_DISTINCT_ROOT
# define DOUBLE_SLASH_IS_DISTINCT_ROOT 0
#endif

#ifdef __cplusplus
extern "C" {
#endif


/*
 Return the address of the last file name component of FILENAME.
  If FILENAME has some trailing slash(es), they are considered to be
  part of the last component.
  If FILENAME has no relative file name components because it is a file
  system root, return the empty string.
  Examples:
          FILENAME      RESULT
          "foo.c"       "foo.c"
          "foo/bar.c"   "bar.c"
          "/foo/bar.c"  "bar.c"
          "foo/bar/"    "bar/"
          "foo/bar//"   "bar//"
          "/"           ""
          "//"          ""
          ""            ""
  The return value is a tail of the given FILENAME; do NOT free() it!  */

/* This function was traditionally called 'basename', but we avoid this
  function name because
    * Various platforms have different functions in their libc.
      In particular, the glibc basename(), defined in <string.h>, does
      not consider trailing slashes to be part of the component:

 FILENAME      RESULT
          "foo/bar/"    ""
          "foo/bar//"   ""
    * The 'basename' command eliminates trailing slashes and for a root
      produces a non-empty result:
          FILENAME      RESULT
          "foo/bar/"    "bar"
          "foo/bar//"   "bar"
          "/"           "/"
          "//"          "/"
*/
```

```
extern char *last_component (char const *filename) _GL_ATTRIBUTE_PURE;

/* Return the length of the basename FILENAME.
  Typically FILENAME is the value returned by base_name or last_component.
  Act like strlen (FILENAME), except omit all trailing slashes.  */
extern size_t base_len (char const *filename) _GL_ATTRIBUTE_PURE;



#ifdef __cplusplus
} /* extern "C" */
#endif

#endif /* _BASENAME_LGPL_H */
```

```
=======================================================================
Written by Andy Polyakov <appro@fy.chalmers.se> for the OpenSSL
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Constant-time SSSE3 AES core implementation.
version 0.1
##
By Mike Hamburg (Stanford University), 2009
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c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

A "User Product" is either (1) a "consumer product", which means any tangible personal property which is normally used for personal, family, or household purposes, or (2) anything designed or sold for incorporation into a dwelling. In determining whether a product is a consumer product,

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1.553 prime 0.1.2

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1.554 fileutils 1.5.0

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1.558 io-wait 0.3.0

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1.561 libpkit 0.25.3-3.el9_5

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1.562 rubygems-update 3.3.26

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1.564 lvm2 1.02.202-6.el9

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OVERALL PROGRAM STRUCTURE

- The overall flow of the program is specified in main.cpp. First, global parameters are defined (given default values) in a single "globals" object. Then a handful of input arguments are processed - just those needed to locate the parameters.txt file. This file is then located and read in, and finally remaining command-line arguments are processed. Therefore the order of precedence is: default values, which are overwritten by parameters in the parameters.txt file, which are overwritten by parameters specified on the command line.
- The data.txt file is then read in, and certain checks are carried out to ensure that the combination of data and parameters makes sense.
- The main loop of the program over values of K then begins. In each iteration there are various
different sorts of analysis carried out (for example exhaustive analysis, thermodynamic integration etc.). The main MCMC loop is compulsory, but all other types of analysis are optional.
- For MCMC analyses (either the ordinary MCMC or the thermodynamic integral MCMC, and either with- or without-admixture) a distinct MCMCobject class is defined. This class contains all functions and variables needed to carry out the MCMC. Final results are saved back into the globals object.
- Where possible, outputs are produced at each iteration of the main loop (for each K) and results are flushed. This means that outputs can be viewed even when the program has not completely finished. Some outputs (such as normalised evidence values) can only be produced after the main loop.
- This code is designed to compile on both Windows and Mac. Operating-system specific functions are contained in the header file "OSfunctions.h"

EXHAUSTIVE APPROACH - SUMMING OVER
PARTITIONS ACCORDING TO RESTRICTED GROWTH FUNCTION

The exact model evidence can be obtained by summing over all possible assignments of n objects to K groups (n is the number of individuals in the case of the without-admixture model, or the number of gene copies in the case of

the admixture model). For example, if n=6 and K=3 then we would need to explore the following allocations...

1 1 1 1 1 1
1 1 1 1 1 2
1 1 1 1 1 3
1 1 1 1 2 1
1 1 1 1 2 2
1 1 1 1 2 3
1 1 1 1 3 1
...
3 3 3 3 3 2
3 3 3 3 3 3

The number of such allocations is K^n, which becomes prohibitively large for even moderate n and K.
We can make this more efficient by only summing over unique partitions. Mixture components are not uniquely defined, meaning allocations that correspond to the same partition have the same likelihood. For example, {1 1 1 2 2 2} is equivalent to {2 2 2 1 1 1} is equivalent to {3 3 3 2 2 2} etc. We can loop over all unique partitions using the restricted growth function, in which
the ith element can never be more than 1 higher than preceeding values (and no value can exceed K). This leads to the following list of allocations...

1 1 1 1 1 1
1 1 1 1 1 2
1 1 1 1 2 1
1 1 1 1 2 2
1 1 1 1 2 3
1 1 1 2 1 1
1 1 1 2 1 2
1 1 1 2 1 3
1 1 1 2 2 1
1 1 1 2 2 2
1 1 1 2 2 3
...
1 2 3 3 3 2
1 2 3 3 3 3

The number of such partitions is given by the sum from k=1 to k=K of S{n,k}, where S{n,k} denotes the Stirling numbers of the second kind (note that when K>=n this is equivalent to the Bell number B_n). This is often significantly smaller than K^n.
When calculating the overall likelihood, the likelihood of each partition must be multiplied by the number of unique group allocations that it encapsulates. For example, the partition {1 1 1 1 1 1} encapsulates K unique allocations, because the "1" values could be replaced with any value from 1 to K and still correspond to this partition.
Taking a more complex example, the partition {1 1 1 2 2 3} can be written as follows:
{o o o}{o o}{o}, where each dot "o" can be any unique number. There are K values that we can assign to the first cluster of three dots, then (K-1) values that we can assign to the second cluster of two dots, then (K-2) values that we can assign to the final cluster of a single dot. So, the number of unique allocations represented by this partition is K(K-1)(K-2).
More generally, let u denote the total number of elements in the partition (also the largest value in the restricted

growth function). So in the above example u=3. Then the number of allocation is given by K!/(K-u)!.
This is why log-likelihoods are weighted by lgamma(double(K)+1)-lgamma(double(K)-double(uniques)+1) in the exhaustive approach.

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Separate branches exist for each released version of the software. Select the branch corresponding to the version you

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Version History

12.12.2017 **Version1.0.5 released**
11.12.2017 Allow alternative data file format with multiple columns per locus.

07.12.2016 **Version1.0.4 released**
30.11.2016 Increased speed under admixture model via look-up table.

29.06.2016 **Version1.0.3 released**
29.06.2016 Improved cross-platform compatibility. Fixed clock time, which previously gave incorrect runtime estimates.

22.06.2016 **Version1.0.2 released**
21.06.2016 Added Metropolis-Hastings step to improve mixing under admixture model

20.05.2016 **Version1.0.1 released**
20.05.2016 Fixed bug causing Q-matrix probabilities to sum to >1

20.05.2016 **Version1.0.0 released**

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* /maverick-master-zip/MavericK-master/mainMCMC.h
* /maverick-master-zip/MavericK-master/TI.cpp
* /maverick-master-zip/MavericK-master/readIn.cpp
* /maverick-master-zip/MavericK-master/globals.cpp
* /maverick-master-zip/MavericK-master/readIn.h
* /maverick-master-zip/MavericK-master/Hungarian.h
* /maverick-master-zip/MavericK-master/writeOut.h
* /maverick-master-zip/MavericK-master/probability.h
* /maverick-master-zip/MavericK-master/exhaustive.cpp
* /maverick-master-zip/MavericK-master/MCMCobject_noAdmixture.h
* /maverick-master-zip/MavericK-master/MCMCobject_noAdmixture.cpp
* /maverick-master-zip/MavericK-master/mainMCMC.cpp
* /maverick-master-zip/MavericK-master/misc.h
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*

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* /maverick-master-zip/MavericK-master/writeOut.cpp
* /maverick-master-zip/MavericK-master/exhaustive.h
* /maverick-master-zip/MavericK-master/TI.h
* /maverick-master-zip/MavericK-master/EM_algorithm.h
* /maverick-master-zip/MavericK-master/OSfunctions.h
* /maverick-master-zip/MavericK-master/globals.h
* /maverick-master-zip/MavericK-master/EM_algorithm.cpp
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1.569 multierr 1.6.0

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1.574 did-you-mean 1.6.1

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1.575 jackson-databind 2.16.1

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==========

HLSL legalization for SPIR-V is a fuzzy topic; it cannot be expressed precisely
using some grammar. Instead, we've collected a few simple examples here to
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These examples are all variants to `0-copy-sbuf.hlsl`. And `*-ok.hlsl` are
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Also keep in mind that legalization is an ongoing effort; support has evolved
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```
// RUN: %dxc -T ps_6_2 -E main %s -verify

// expected-no-diagnostics
float compute(nointerpolation float3 value) {
 return GetAttributeAtVertex(value, 0)[0];
}

float middle(nointerpolation float3 value) {
 return compute(value);
}

float4 main(nointerpolation float3 a : A) : SV_Target
{
 float v1 = middle(a);
 return float4(v1.xxxx);
}
```

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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Thus, it is not the intent of this section to claim rights or contest

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of
the Library even though the source code is not.
Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with
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work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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"work that
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c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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will operate properly
with a modified version of the library, if
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* /joni-2-2-1-sources-4-jar/org/joni/UnsetAddrList.java
* /joni-2-2-1-sources-4-jar/org/joni/ast/ListNode.java
* /joni-2-2-1-sources-4-jar/org/joni/constants/SyntaxProperties.java
* /joni-2-2-1-sources-4-jar/org/joni/WarnCallback.java
* /joni-2-2-1-sources-4-jar/org/joni/Option.java
* /joni-2-2-1-sources-4-jar/org/joni/constants/internal/OPCode.java
* /joni-2-2-1-sources-4-jar/org/joni/exception/SyntaxException.java
* /joni-2-2-1-sources-4-jar/org/joni/constants/internal/TokenType.java
* /joni-2-2-1-sources-4-jar/org/joni/Compiler.java
* /joni-2-2-1-sources-4-jar/org/joni/constants/MetaChar.java
* /joni-2-2-1-sources-4-jar/org/joni/constants/internal/StringType.java
*
/joni-2-2-1-sources-4-jar/org/joni/constants/internal/TargetInfo.java
* /joni-2-2-1-sources-4-jar/org/joni/Parser.java
* /joni-2-2-1-sources-4-jar/org/joni/constants/internal/NodeStatus.java
* /joni-2-2-1-sources-4-jar/org/joni/Token.java

1.582 io-console 0.5.7

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1.583 alsa 1.2.13-2.el9

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1.584 forwardable 1.3.2

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table of data to be supplied by an application program that uses
the facility, other than as an argument passed when the facility
is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
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1.596 invokebinder 1.13

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* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/SmartBinder.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/SmartHandle.java
```

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*/
```

Found in path(s):

```
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Insert.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Cast.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/InvalidTransformException.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Transform.java
*
 /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Drop.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Fold.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Catch.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/Binder.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/TryFinally.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Varargs.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/FilterReturn.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Collect.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Permute.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Convert.java
```

* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Spread.java
* /invokebinder-1-13-sources-4-jar/com/headius/invokebinder/transform/Filter.java

1.597 zap 1.23.0

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1.598 tempfile 0.1.1

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1.599 tomcat-coyote-ffm 9.0.108

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1.600 rake 13.0.6

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1.601 aws-java-sdk 1.4.3

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Found in path(s):
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/Md5Utils.java
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*/
/**
* Interface for providing AWS credentials. Implementations are free to use any
* strategy for providing AWS credentials, such as simply providing static
* credentials that don't change, or more complicated implementations, such as
* integrating with existing key management systems.
*/
```

Found in path(s):
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/AWSCredentialsProvider.java
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*/
/**
   * Gets the optional <code>versionIdMarker</code> parameter indicating where in the
   * sorted list of all versions in the specified bucket to begin returning
   * results. Results are always ordered first lexicographically (i.e.
   * alphabetically) and then from most recent version to least recent
   * version.
   * <p>

   * A key marker must be specified when specifying a <code>versionIdMarker</code>.
   * Results begin immediately after the version with the specified key and
   * version ID.
   * </p>
   *
   * @return The optional <code>versionIdMarker</code> parameter indicating where in the
   *         sorted list of all versions in the specified bucket to begin
   *         returning results.
   *
   * @see ListVersionsRequest#setVersionIdMarker(String)
   * @see ListVersionsRequest#withVersionIdMarker(String)
   */
```

Found in path(s):
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/ListVersionsRequest.java
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```

```
* permissions and limitations under the License.
*/
/**
    * <p>
    * Registers a new <i>workflow type</i> and its configuration settings
    * in the specified domain.
    * </p>
    * <p>
    * The retention period for the workflow history is set by the
    * RegisterDomain action.
    * </p>
    * <p>
    * <b>IMPORTANT:</b> If the type already exists, then a TypeAlreadyExists
     * fault is returned. You cannot change the configuration settings of a
    * workflow type once it is registered and it must be registered as a new
    * version.
    * </p>
    * <p>
    * <b>Access Control</b>
    * </p>
    * <p>
    * You can use IAM policies to control this action's access to Amazon SWF
    * resources as follows:
    * </p>
    *
    * <ul>
    * <li>Use a <code>Resource</code> element with the domain name to limit
    * the action to only specified domains.</li>
    * <li>Use an <code>Action</code> element to allow or deny permission to
    * call this action.</li>
    * <li>Constrain the following parameters by using a
    * <code>Condition</code> element with the appropriate keys.
    * <ul>
    * <li> <code>defaultTaskList</code> : String constraint. The key is
    * <code>swf:defaultTaskList.name</code> .</li>
    * <li> <code>name</code> : String constraint. The key is
    * <code>swf:name</code> .</li>
    * <li> <code>version</code>
  : String constraint. The key is
    * <code>swf:version</code> .</li>
    *
    * </ul>
    * </li>
    *
    * </ul>
    * <p>
    * If the caller does not have sufficient permissions to invoke the
    * action, or the parameter values fall outside the specified
```

```
     * constraints, the action fails by throwing
     * <code>OperationNotPermitted</code> . For details and example IAM
     * policies, see <a
     * docs.aws.amazon.com/amazonswf/latest/developerguide/swf-dev-iam.html">
     * Using IAM to Manage Access to Amazon SWF Workflows </a> .
     * </p>
     *
     * @param registerWorkflowTypeRequest Container for the necessary
     *          parameters to execute the RegisterWorkflowType operation on
     *          AmazonSimpleWorkflow.
     *
     * @return A Java Future object containing the response from the
     *         RegisterWorkflowType service method, as returned by
     *         AmazonSimpleWorkflow.
     *
     * @throws AmazonClientException
     *           If any internal errors are encountered inside the client while
     *           attempting to make the request or handle the response.  For example
     *           if a network connection is not available.
     * @throws AmazonServiceException
     *           If an error response is returned by AmazonSimpleWorkflow indicating
     *           either a problem with the data in the request, or a server side issue.
     */
    /**
     * <p>
     * Used by deciders to tell the service that the DecisionTask identified
     * by the <code>taskToken</code> has successfully completed. The
     * <code>decisions</code> argument specifies the list of decisions made
     * while processing the task.
     * </p>
     * <p>
     * A
     * <code>DecisionTaskCompleted</code> event is added
     * to the workflow history. The <code>executionContext</code> specified
     * is attached to the event in the workflow execution history.
     * </p>
     * <p>
     * <b>Access Control</b>
      * </p>
     * <p>
     * If an IAM policy grants permission to use
     * <code>RespondDecisionTaskCompleted</code> , it can express permissions
     * for the list of decisions in the <code>decisions</code> parameter in
     * the same way as for the regular API. This approach maintains a uniform
     * conceptual model and helps keep policies readable. For more
     * information, see <a
     * docs.aws.amazon.com/amazonswf/latest/developerguide/swf-dev-iam.html">
     * Using IAM to Manage Access to Amazon SWF Workflows </a> .
```

```
   * </p>
   *
   * @param respondDecisionTaskCompletedRequest Container for the necessary
   *          parameters to execute the RespondDecisionTaskCompleted operation on
   *          AmazonSimpleWorkflow.
   *
   * @return A Java Future object containing the response from the
   *         RespondDecisionTaskCompleted service method, as returned by
   *         AmazonSimpleWorkflow.
   *
   * @throws AmazonClientException
    *             If any internal errors are encountered inside the client while
   *             attempting to make the request or handle the response.  For example
   *             if a network connection is not available.
   * @throws AmazonServiceException
   *             If an error response is returned by AmazonSimpleWorkflow indicating
   *             either a problem with the data in the request, or a server side issue.
   */
```

Found in path(s):
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/AmazonSimpleWorkflowAsync.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/AmazonSimpleWorkflowAsyncClient.java
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*/
/**
* Interface for providing encryption materials.
* Implementations are free to use any strategy for providing encryption
* materials, such as simply providing static material that doesn't change,
* or more complicated implementations, such as integrating with existing
* key management systems.
*/
```

Found in path(s):

* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/EncryptionMaterialsProvider.java
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Found in path(s):
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/regions/ServiceAbbreviations.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/ScanResultPage.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/ScanResultPage.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/QueryResultPage.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/regions/RegionUtils.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/QueryResultPage.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/regions/Regions.java
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*/
/**
   * <p>
   * Registers a new <i>workflow type</i> and its configuration settings
   * in the specified domain.
   * </p>
```

```
   * <p>
   * The retention period for the workflow history is set by the
   * RegisterDomain action.
   * </p>
   * <p>
   * <b>IMPORTANT:</b> If the type already exists, then a TypeAlreadyExists
    * fault is returned. You cannot change the configuration settings of a
   * workflow type once it is registered and it must be registered as a new
   * version.
   * </p>
   * <p>
   * <b>Access Control</b>
   * </p>
   * <p>
   * You can use IAM policies to control this action's access to Amazon SWF
   * resources as follows:
   * </p>
   *
   * <ul>
   * <li>Use a <code>Resource</code> element with the domain name to limit
   * the action to only specified domains.</li>
   * <li>Use an <code>Action</code> element to allow or deny permission to
   * call this action.</li>
   * <li>Constrain the following parameters by using a
   * <code>Condition</code> element with the appropriate keys.
   * <ul>
   * <li> <code>defaultTaskList</code> : String constraint. The key is
   * <code>swf:defaultTaskList.name</code> .</li>
   * <li> <code>name</code> : String constraint. The key is
   * <code>swf:name</code> .</li>
   * <li> <code>version</code>
  : String constraint. The key is
   * <code>swf:version</code> .</li>
   *
   * </ul>
   * </li>
   *
   * </ul>
   * <p>
   * If the caller does not have sufficient permissions to invoke the
   * action, or the parameter values fall outside the specified
   * constraints, the action fails by throwing
   * <code>OperationNotPermitted</code> . For details and example IAM
   * policies, see <a
   * docs.aws.amazon.com/amazonswf/latest/developerguide/swf-dev-iam.html">
   * Using IAM to Manage Access to Amazon SWF Workflows </a> .
   * </p>
   *
```

```
    * @param registerWorkflowTypeRequest Container for the necessary
    *          parameters to execute the RegisterWorkflowType service method on
    *          AmazonSimpleWorkflow.
    *
    * @throws OperationNotPermittedException
    * @throws UnknownResourceException
    * @throws TypeAlreadyExistsException
    * @throws LimitExceededException
    *
    * @throws AmazonClientException
     *           If any internal errors are encountered inside the client while
    *           attempting to make the request or handle the response.  For example
    *           if a network connection is not available.
    * @throws AmazonServiceException
    *           If an error response is returned by AmazonSimpleWorkflow indicating
    *           either a problem with the data in the request, or a server side issue.
    */
  /**
    * <p>
    * Used by deciders to tell the service that the DecisionTask identified
    * by the <code>taskToken</code> has successfully completed. The
    * <code>decisions</code> argument specifies the list of decisions made
    * while processing the task.
    * </p>
    * <p>
    * A
    * <code>DecisionTaskCompleted</code> event is added
    * to the workflow history. The <code>executionContext</code> specified
    * is attached to the event in the workflow execution history.
    * </p>
    * <p>
    * <b>Access Control</b>
     * </p>
    * <p>
    * If an IAM policy grants permission to use
    * <code>RespondDecisionTaskCompleted</code> , it can express permissions
    * for the list of decisions in the <code>decisions</code> parameter in
    * the same way as for the regular API. This approach maintains a uniform
    * conceptual model and helps keep policies readable. For more
    * information, see <a
    * docs.aws.amazon.com/amazonswf/latest/developerguide/swf-dev-iam.html">
    * Using IAM to Manage Access to Amazon SWF Workflows </a> .
    * </p>
    *
    * @param respondDecisionTaskCompletedRequest Container for the necessary
    *          parameters to execute the RespondDecisionTaskCompleted service method
    *          on AmazonSimpleWorkflow.
    *
```

```
   * @throws OperationNotPermittedException
   * @throws UnknownResourceException
   *
   * @throws AmazonClientException
   *             If any internal errors are encountered inside the client while

 *           attempting to make the request or handle the response.  For example
   *             if a network connection is not available.
   * @throws AmazonServiceException
   *             If an error response is returned by AmazonSimpleWorkflow indicating
   *             either a problem with the data in the request, or a server side issue.
   */
```

Found in path(s):
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/AmazonSimpleWorkflowClient.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/AmazonSimpleWorkflow.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/S3Object.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/STSSessionCredentials.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/ArgumentUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/PaginatedQueryList.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBScanExpression.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBIgnore.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/MultiObjectDeleteException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/HeaderHandler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/SUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBRangeKey.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/ObjectRestoreResult.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/PaginatedList.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/JsonMarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBMarshalling.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/s3/model/SetBucketNotificationConfigurationRequest.java
*

/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/PaginatedScanList.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/KeyPair.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/ObjectExpirationResult.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/NUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/NSUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBMarshalling.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBIndexRangeKey.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBMapper.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/SSUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBMappingException.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/ArgumentMarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBMapperConfig.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBRangeKey.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBMapper.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/KeyPair.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/DeleteObjectsResponse.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketLifecycleConfiguration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBReflector.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/JsonMarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBIgnore.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/NUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/CopyPartResult.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/BUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBAutoGeneratedKey.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/ArgumentMarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/BSUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/SSUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBReflector.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBScanExpression.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/TransferMonitor.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/CORSRule.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/PaginatedScanList.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBHashKey.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBTable.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/ServerSideEncryptionHeaderHandler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/STSSessionCredentialsProvider.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/BUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBMarshaller.java

* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBHashKey.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/UploadMonitor.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/PaginatedList.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBAutoGeneratedKey.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/NoSessionSupportCredentials.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBMappingException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/GenericBucketRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/BSUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBVersionAttribute.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/ServerSideEncryptionResult.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBMarshaller.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/ArgumentUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/BasicSessionCredentials.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketCrossOriginConfiguration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/PaginatedQueryList.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBAttribute.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/DynamoDBTable.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBMapperConfig.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodbv2/datamodeling/SUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBAttribute.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/dynamodb/datamodeling/DynamoDBVersionAttribute.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/ResponseHeaderHandlerChain.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/dynamodb/datamodeling/NSUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/RequestClientOptions.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/ResponseHeaderOverrides.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/DeleteObjectsResult.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/CopyPartRequest.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/junit/WorkflowTest.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/GenericWorkflowClientImpl.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/WorkerBase.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/spring/SpringWorkflowWorker.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/TimerDecisionStateMachine.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/ManualActivityCompletionClientImpl.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/DynamicWorkflowClientImpl.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/interceptors/RetryPolicy.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/ActivityWorker.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/interceptors/RetryDecorator.java
*
/aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/GenericWorkflowClientExternalImpl.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/interceptors/ScheduleDecorator.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/GenericActivityClientImpl.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/core/TryCatchFinallyContext.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/AsyncScope.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/test/TestWorkflowClock.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/Wait.java
* /aws-java-sdk-1-4-3-sources-4-
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*
/aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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WorkflowClient.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/ActivityTaskTimedOutException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/NoWait.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/worker/AsyncDecider.java
* /aws-java-sdk-1-4-3-sources-4-
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*
/aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/Asynchronous.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/ActivityExecutionContext.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/Promise.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/AsyncScopeContext.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/Settable.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/TaskContext.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/junit/WorkflowTestBase.java
* /aws-java-sdk-1-4-3-sources-4-
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/WorkflowClientBase.java
* /aws-java-sdk-1-4-3-sources-4-
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*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/junit/AsyncAssert.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/WorkflowReplayer.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/worker/DecisionState.java
* /aws-java-sdk-1-4-3-sources-4-

jar/com/amazonaws/services/simpleworkflow/flow/test/TestPOJOActivityImplementationGenericActivityClient.jav
a
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/ExternalTaskContext.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/pojo/POJOActivityImplementationFactory.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/TryCatchFinally.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/Suspendable.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/AsyncContextBase.java
*
/aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/pojo/POJOWorkflowDefinitionFactoryFactory.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/junit/GenericWorkflowTest.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/DynamicWorkflowClientExternalImpl.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/Task.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/WorkflowClientFactoryBase.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/ActivityDecisionStateMachine.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/aspectj/ExponentialRetryAspect.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/ActivitiesClientBase.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/worker/HistoryHelper.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/worker/DecisionsHelper.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/WorkflowClockImpl.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/Region.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/SetBucketPolicyRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/SetBucketAclRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/GetBucketPolicyRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/GetBucketLocationRequest.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/s3/model/SetBucketWebsiteConfigurationRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/http/HttpClientFactory.java
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/**

*
* Specifies a decision made by the decider. A decision can be one of these types:
*
*
*
* CancelTimer cancels a previously started timer and records a <code>TimerCanceled</code> event in the history.
* CancelWorkflowExecution closes the workflow execution and records a <code>WorkflowExecutionCanceled</code>
event in the history.
* CompleteWorkflowExecution closes the workflow execution and records a <code>WorkflowExecutionCompleted</code> event in the history .
* ContinueAsNewWorkflowExecution closes the workflow execution and starts a new workflow execution of the same type using the same workflow
* id and a unique run Id. A <code>WorkflowExecutionContinuedAsNew</code> event is recorded in the history.
* FailWorkflowExecution closes the workflow execution and records a <code>WorkflowExecutionFailed</code> event in the history.
* RecordMarker records a <code>MarkerRecorded</code> event in the history. Markers can be used for adding custom information in the history
* for instance to let deciders know that they do not need to look at the history beyond the marker event.
* RequestCancelActivityTask attempts to cancel a previously scheduled activity task. If the activity task was
scheduled but has not been
* assigned to a worker, then it will be canceled. If the activity task was already assigned to a worker, then the worker will be informed that
* cancellation has been requested in the response to RecordActivityTaskHeartbeat.
* RequestCancelExternalWorkflowExecution requests that a request be made to cancel the specified external workflow execution and records a
* <code>RequestCancelExternalWorkflowExecutionInitiated</code> event in the history.
* ScheduleActivityTask schedules an activity task.
* SignalExternalWorkflowExecution requests a signal to be delivered to the specified external workflow execution and records a
* <code>SignalExternalWorkflowExecutionInitiated</code> event in the history.
* StartChildWorkflowExecution requests that a child workflow execution be started and records a
* <code>StartChildWorkflowExecutionInitiated</code> event in the history. The child workflow
execution is a separate workflow execution with its own
* history.
* StartTimer starts a timer for this workflow execution and records a <code>TimerStarted</code> event in the history. This timer will fire
* after the specified delay and record a <code>TimerFired</code> event.
*
*
*
* Access Control
*
*
* If you grant permission to use <code>RespondDecisionTaskCompleted</code> , you can use IAM policies to

express permissions for the list of decisions
* returned by this action as if they were members of the API. Treating decisions as a pseudo API maintains a uniform conceptual model and helps keep
* policies readable. For details and example IAM policies, see
* Using IAM to Manage Access to Amazon SWF Workflows .
*
*
* Decision Failure
*
*
* Decisions can fail for several reasons
*
*
*
*
The ordering of decisions should follow a logical flow. Some decisions might not make sense in the current context of the workflow execution and
* will therefore fail.
* A limit on your account was reached.
* The decision lacks sufficient permissions.
*
*
*
* One of the following events might be added to the history to indicate an error. The event attribute's cause parameter indicates the cause. If
* cause is set to OPERATION_NOT_PERMITTED, the decision failed because it lacked sufficient permissions.
*
*
*
* ScheduleActivityTaskFailed a ScheduleActivityTask decision failed. This could happen if the activity type specified in the decision is
* not registered, is in a deprecated state, or the decision is not properly configured.
* RequestCancelActivityTaskFailed a RequestCancelActivityTask decision failed. This could happen if there is no open activity task with the
* specified activityId.
* StartTimerFailed a StartTimer decision failed. This could happen if there is another open timer with the same timerId.
* CancelTimerFailed a CancelTimer decision failed. This could happen if there is no open timer with the specified timerId.
* StartChildWorkflowExecutionFailed a StartChildWorkflowExecution decision failed. This could happen if the workflow type specified is not
* registered, is deprecated, or the decision is not properly configured.
* SignalExternalWorkflowExecutionFailed a SignalExternalWorkflowExecution decision failed. This could happen if the <code>workflowID</code>
* specified in the decision was incorrect.
* RequestCancelExternalWorkflowExecutionFailed a RequestCancelExternalWorkflowExecution

decision failed. This could happen if the
* <code>workflowID</code> specified in the decision was incorrect.
* CancelWorkflowExecutionFailed a CancelWorkflowExecution
decision failed. This could happen if there is an unhandled decision task pending
* in the workflow execution.
* CompleteWorkflowExecutionFailed a CompleteWorkflowExecution decision failed. This could happen if there is an unhandled decision task
* pending in the workflow execution.
* ContinueAsNewWorkflowExecutionFailed a ContinueAsNewWorkflowExecution decision failed. This could happen if there is an unhandled
* decision task pending in the workflow execution or the ContinueAsNewWorkflowExecution decision was not configured correctly.
* FailWorkflowExecutionFailed a FailWorkflowExecution decision failed. This could happen if there is an unhandled decision task pending in
* the workflow execution.
*
*
*
* The preceding error events might occur due to an error in the decider logic, which might put the workflow execution in an unstable state The cause
* field in the event structure for the
error event indicates the cause of the error.
*
*
* NOTE: A workflow execution may be closed by the decider by returning one of the following decisions when completing a decision task:
* CompleteWorkflowExecution, FailWorkflowExecution, CancelWorkflowExecution and ContinueAsNewWorkflowExecution. An UnhandledDecision fault will be
* returned if a workflow closing decision is specified and a signal or activity event had been added to the history while the decision task was being
* performed by the decider. Unlike the above situations which are logic issues, this fault is always possible because of race conditions in a
* distributed system. The right action here is to call RespondDecisionTaskCompleted without any decisions. This would result in another decision task
* with these new events included in the history. The decider should handle the new events and may decide to close the workflow execution.
*
*
* How to Code a Decision
*
*
* You code a decision by first setting the decision type field to one of the above decision values, and then set the corresponding attributes field
* shown below:
*
*
*
* ScheduleActivityTaskDecisionAttributes

* RequestCancelActivityTaskDecisionAttributes
* CompleteWorkflowExecutionDecisionAttributes
* FailWorkflowExecutionDecisionAttributes
* CancelWorkflowExecutionDecisionAttributes
* ContinueAsNewWorkflowExecutionDecisionAttributes
* RecordMarkerDecisionAttributes
* StartTimerDecisionAttributes
* CancelTimerDecisionAttributes
* SignalExternalWorkflowExecutionDecisionAttributes
* RequestCancelExternalWorkflowExecutionDecisionAttributes
* StartChildWorkflowExecutionDecisionAttributes
*
*
*/

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/**
* Container for the parameters to the {@link
com.amazonaws.services.simpleworkflow.AmazonSimpleWorkflow#respondDecisionTaskCompleted(RespondDeci
sionTaskCompletedRequest) RespondDecisionTaskCompleted operation}.
*
* Used by deciders to tell the service that the DecisionTask identified by the <code>taskToken</code> has
successfully
 completed. The
* <code>decisions</code> argument specifies the list of decisions made while processing the task.
*
*
* A
* <code>DecisionTaskCompleted</code> event is added to the workflow history. The
<code>executionContext</code> specified is attached
* to the event in the workflow execution history.

*
*
* Access Control
*
*
* If an IAM policy grants permission to use <code>RespondDecisionTaskCompleted</code> , it can express permissions for the list of decisions in the
* <code>decisions</code> parameter in the same way as for the regular API. This approach maintains a uniform conceptual model and helps keep policies
* readable. For more information, see Using IAM to Manage Access
* to Amazon SWF Workflows .
*
*
* @see com.amazonaws.services.simpleworkflow.AmazonSimpleWorkflow#respondDecisionTaskCompleted(RespondDecisionTaskCompletedRequest)
*/

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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/json/JSONObject.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/json/JSONTokener.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/json/JSONException.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/RestUtils.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/S3StringResponseHandler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketLoggingConfiguration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/AccessControlList.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/BinaryUtils.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/DateUtils.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/ServiceUtils.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/Grantee.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/RepeatableInputStream.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketNotificationConfiguration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/Owner.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/EmailAddressGrantee.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketTaggingConfiguration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/Grant.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/TagSet.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/http/HttpMethodReleaseInputStream.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketPolicy.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/transform/XmlResponsesSaxParser.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/RepeatableFileInputStream.java
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jar/com/amazonaws/services/elasticache/model/CacheSecurityGroupNotFoundException.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/importexport/model/UpdateJobResult.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/worker/BackoffThrottler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/glacier/internal/AcceptJsonRequestHandler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/StaticEncryptionMaterialsProvider.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/ClasspathPropertiesFileCredentialsProvider.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/AsyncParentContext.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/DataConverterException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/AsyncContextAware.java
*
/aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/DecisionContextProviderImpl.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/Activity.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/annotations/SkipTypeRegistration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/iterable/S3Objects.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/SimpleWorkflowDefinitionFactoryFactory.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/annotations/ExponentialRetry.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/sqs/buffered/QueueBufferFuture.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/OrPromise.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/GetState.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/DynamicClient.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/pojo/MethodConverterPair.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/test/TestDecisionContext.java

* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/SynchronousWorkflowWorker.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/ActivityTypeExecutionOptions.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/transform/GlacierErrorUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/DecisionStateMachine.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/spring/SpringWorkflowDefinitionFactoryFactory.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/TryCatch.java
*
/aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/annotations/ActivityRegistrationOptions.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/ChildWorkflowFailedException.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/annotations/ManualActivityCompletion.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/DynamicWorkflowClient.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/internal/CustomBackoffStrategy.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/Cancelable.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/worker/SynchronousActivityTaskPoller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/Activities.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/generic/WorkflowDefinitionFactoryFactory.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/AndPromise.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/worker/Throttler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/policy/actions/CloudFormationActions.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/WorkflowClientExternal.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/AwsHostNameUtils.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/DataConverter.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/ChildWorkflowTimedOutException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/ActivityTaskException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/annotations/Signal.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/interceptors/RetryPolicyBase.java
*
/aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/simpleworkflow/flow/core/Promises.java
* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/simpleworkflow/flow/spring/WorkflowScopeBeanNames.java
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jar/com/amazonaws/services/s3/model/SetBucketVersioningConfigurationRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/XmlUtils.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/DeleteVersionRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/util/VersionInfoUtils.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/AWSCredentials.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/http/HttpMethodName.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/AmazonClientException.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/SignatureVersion.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/BucketVersioningConfiguration.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/Permission.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/transform/VoidUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/transform/LegacyErrorUnmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/package-info.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/VersionListing.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/DeleteObjectsRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/MD5DigestCalculatingInputStream.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/transform/Marshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/XmlWriter.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/package-info.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/SigningAlgorithm.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/GetObjectMetadataRequest.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/ObjectListing.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/MultiFactorAuthentication.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/GroupGrantee.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/S3ObjectSummary.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/CannedAccessControlList.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/DeleteObjectRequest.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/transform/SimpleTypeUnmarshallers.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/S3VersionSummary.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/http/HttpResponseHandler.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/transform/Unmarshaller.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/CopyObjectResult.java

* /aws-java-sdk-1-4-3-sources-4-
jar/com/amazonaws/services/s3/model/SetBucketLoggingConfigurationRequest.java
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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/AbstractRepeatableInputStream.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/internal/RepeatableCipherInputStream.java
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*/
// The Amazon S3 DevPay token header is a special exception and can be safely moved
```

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* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/glacier/transfer/UploadResult.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/glacier/transfer/JobStatusMonitor.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/glacier/transfer/ArchiveTransferManager.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/MultipleFileTransferMonitor.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/AbstractTransfer.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/MultipleFileUpload.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/model/S3ObjectInputStream.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/DownloadMonitor.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/MultipleFileUploadImpl.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/MultipleFileDownloadImpl.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/MultipleFileDownload.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/TransferStateChangeListener.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/s3/transfer/internal/MultipleFileTransfer.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/glacier/internal/TreeHashInputStream.java
*
 /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/auth/SessionCredentialsProviderFactory.java
* /aws-java-sdk-1-4-3-sources-4-jar/com/amazonaws/services/glacier/TreeHashGenerator.java

1.602 jline 2.14.6

1.603 aopalliance-repackaged 2.3.0-b10

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1.604 time 0.1.1

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patch was accepted into Ruby.

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 * net/pop
 * net/smtp
 * racc

* ripper
* strscan

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 * open3

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 (marshal.c, string.c)

Shin-ichiro HARA
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* a patch for numeric.c

Chris Heath (traumdeutung)
* a patch for proc.c

HIROKAWA Hisashi
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 * POP3S support

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* committer
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 * ri

 * rubygems

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Johan Holmberg
* a patch for dir.c
* documentation

Erik Huelsmann

Dae San Hwang
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"incorporate"
* a patch for sprintf.c

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* committer
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 * complex.rb (1.8)
 * e2mmap.rb
 * forwardable.rb
 * irb
 * mathn
 * matrix.rb
 * mutex_m.rb
 * rational.rb (1.8)
 * sync.rb
 * shell/*
 * thwait.rb
 * tracer.rb

== J

Curtis Jackson
* missing/dup2.c

Alan Johnson
* a patch for net/ftp

Lyle

Johnson
* patches for nkf, bigdecimal, numeric.c

== K

Yoshihiro Kambayashi
* a patch for enc/trans/single_byte.trans.
* He wrote supports for some encodings.

Yutaka Kanemoto
* patches for common.mk, AIX AF_INET6 support

Motoyuki Kasahara
* He wrote getoptlong.rb

Masahiro Kawato
* a patch for shellwords.rb

Wataru Kimura
* a patch for configure.in

Michael Klishin
* patch for make help.

Noritada Kobayashi
* a patch for optparse.rb

Shigeo Kobayashi (shigek)
* committer
* He is the maintainer of bigdecimal

KONISHI, Hiromasa (H_Konishi)
* committer
* He had maintained the bcc32 support in 2004.

Kornelius "murphy" Kalnbach
* documentation

K.Kosako (kosako)
* committer
* He wrote Oniguruma.

Takehiro Kubo
* patches for dl 64bit support.

== L

Marc-Andre Lafortune (marcandre)

* committer
* patches for hash.c, array.c, thread.c, enumc, string.c, range.c and rdoc documentation.

Hongli Lai
* improved pstore.rb
* patch for tool/file2lastrev.rb.

raspberry lemon
* a patch for
 webrick/httpproxy.rb.

Christian Loew
* a patch for fileutils.rb

== M

Shugo Maeda (shugo)
* committer
* A system administrator of ruby-lang.org servers.
* He wrote and is the maintainer of:
 * monitor.rb
 * net/ftp
 * net/imap

Stephan Maka (mathew)
* documentation

Yukihiro Matsumoto (matz)
* Matz -- the founder, language designer of Ruby.
* committer
* Ruby itself, most of Ruby.
* He is the maintainer of:
 * singleton
 * timeout
 * gdbm
 * sdbm

Konrad Meyer
* documentation

Mib Software
* missing/vsnprintf.c

Todd C. Miller
* missing/strlcat.c
* missing/strlcpy.c

MIYASAKA, Masaru

* a patch for cgi.rb

Stefan Monnier
* regex.c was fixed with based on his Emacs21 patch.

Marcel Moolenaar
* patches for eval.c and gc.c.

moonwolf
* a patch for REXML, xmlrpc

Hiroshi Moriyama
* a patch for yaml.

Kyosuke Morohashi
* a patch for gem_prelude.rb

Kenta Murata
* patches for json, bignum.c

Akinori MUSHA (knu)
* committer
* He wrote and is the maintainer of:
 * abbrev.rb
 * generator
 (1.8)
 * enumerator (1.8)
 * set
 * ipaddr.rb
 * digest/*
 * syslog
* He is the branch maintainer of ruby_1_8, the release manager of 1.8 series.

== N

Hidetoshi NAGAI (nagai)
* committer
* He is the maintainer of tk/*

Nobuyoshi Nakada (nobu)
* committer
* a.k.a. the "patch monster"
* He wrote and is the maintainer of:
 * optparse
 * stringio
 * io/wait
 * iconv

Satoshi Nakagawa
* patches for util.c

Narihiro Nakamura (nari)
* committer
* a.k.a. authorNari
* working at GC

NAKAMURA, Hiroshi (nahi)
* committer
* He is the maintainer of:
 * csv.rb (1.8)
 * logger.rb
 * soap/* (1.8)
 * wsdl/* (1.8)
 * xsd/* (1.8)

NAKAMURA, Usaku (usa)
* committer
* a.k.a. unak
* He is the maintainer of mswin32 and mswin64 support.

NARUSE, Yui (naruse)
* committer
* a.k.a. "nurse"
* Did much upon m17n.
* He is the maintainer of:
 * json
 * nkf

Christian Neukirchen
* a patch for webrick/httputils

Michael Neumann (mneumann)
* committer
* He is the maintainer of
 * xmlrpc (1.8)
  * gserver (1.8)

NISHIO Hirokazu
* wrote a patch for CVE-2010-0541

Kazuhiro NISHIYAMA (kazu)
* committer
* a.k.a. znz

Go Noguchi

Martin Nordholts
* misc/rdebug.el

nmu
* a patch for socket

== O

okkez
* He is a sysop of the Ruby Reference Manual Renewal Project.
* fixed ipaddr.rb, ext/etc

Haruhiko Okumura
* some of missing/* is based on his book:
 * missing/erf.c
 * missing/lgamma_r.c
 * missing/tgamma.c

OMAE, jun
* a patch for debug.rb

Eugene Ossintsev
* documentation

== P

Heesob Park
* a patch for win32/win32.c.

pegacorn
* a patch for instruby.rb

== Q

== R

Gaston Ramos
* documentation

The Regents of the University of California
* missing/crypt.c
* missing/vsnprintf.c

Sam Roberts
* patch for socket
* documentation

Michal Rokos (michal)

* committer
* He was the maintainer of DJGPP support.

rubikitch
* a patch for io.c

Marcus Rueckert
* a patch for mkconfig.rb.

Run Paint Run Run
* patch for enc/unicode.c
* documentation

Sean Russell (ser)
* committer
* He wrote and
 is the maintainer of REXML.

== S

Kazuo Saito (ksaito)
* committer
* M17N

Tadashi Saito
* patches for test/ruby/test_math.rb, thread_*.c, bignum.c
* working upon BigDecimal.
* did much upon documentation

Masahiro Sakai
* a patch for io.c

Laurent Sansonetti
* a patch for tool/ytab.sed

Jeff Saracco
* documentation

Koichi Sasada (ko1)
* committer
* He wrote YARV.

Hugh Sasse
* a patch for net/http
* documentation

Charlie Savage
* a patch for win32/Makefile.sub

Michael Scholz
* a patch for ruby-mode.el

Arthur Schreiber
* patch for net/http and rdoc.

Masatoshi SEKI (seki)
* committer
* He wrote and is the maintainer of:
 * drb/*
 * erb
 * rinda

Roman Shterenzon
* a patch for open-uri.

Kent Sibilev

Gavin Sinclair (gsinclair)
* committer

John W. Small
* He wrote gserver.rb

Yuki Sonoda (yugui)
* committer
* She is the maintainer of man/* manual pages and is the release manager of 1.9 series.
* She wrote prime.rb.
* A developer and a sysop of redmine.ruby-lang.org.

SOUMA,
 Yutaka
* a patch for pack.c.

Tatsuki Sugiura
* WebDAV support for net/http

Masaki Suketa (suke)
* committer
* He is the maintainer of win32ole

sheepman
* patches for ruby.c, thread.c, stringio, enum.c, webrick, net/http

Siena. (siena)
* committer

Kirill A. Shutemov
* a patch for parse.y

Darren Smith
* a patch for golf_prelude.rb

Richard M. Stallman
* missing/alloca.c

Robin Stocker
* documentation

Joshua Stowers
* a patch for array.c

Marcus Stollsteimer (stomar)
* committer
* a maintainer of www.ruby-lang.org
* patches for cgi (HTML5 tag maker), numeric.c, bigdecimal, ostruct.rb, prime.rb, and others
* documentation

Adam Strzelecki
* a patch for compile.c

Masashi Sumi
* improved net/pop.rb

Eric Sunshine
* NeXT OpenStep, Rhapsody support

Kouhei Sutou (kou)
* committer
* He wrote and is the maintainer of rss/*

David Symonds
* documentation

== T

TAKANO Mitsuhiro (takano32)
* committer
* He is the maintainer of IA-64 support.
* BigDecimal

TAKAO, Kouji (kouji)
* committer
*

He is the maintainer of readline.

Nathaniel Talbott (ntalbott)
* committer
* He was the maintainer of test/unit, runit, rubyunit.

TANAKA, Akira (akr)
* committer
* Did much upon m17n.
* And he is the maintainer of:
 * open-uri
 * pathname
 * pp
 * resolv-replace
 * resolv
 * time
 * tsort

Takaaki Tateishi (ttate)
* committer
* He was the maintainer of dl

Technorama Ltd. (technoroma)
* committer
* openssl

Andrew Thompson
* a patch for socket.c IRIX support.

Dave Thomas (dave)
* committer
* a.k.a. the Pragmatic Programmer.
* He wrote rdoc.

Tietew
* patches for win32 support

Masahiro Tomita
* a patch for cgi.rb

Jakub Travnik
* a patch for eval.c

Tom Truscott
* missing/crypt.c

== U

UEDA, Satoshi
* a patch for uri

Takaaki Uematsu (uema2)
* committer
* He was the maintainer of WinCE support.

UENO, Katsuhiro (katsu)
* committer
* He is the maintainer of zlib

Hajimu UMEMOTO
* He wrote ipaddr.rb

URABE, Shyouhei (shyouhei)
* committer
* a.k.a. mput.
* He is the branch maintainer
 of ruby_1_8_6 and ruby_1_8_7
* and is the release manager of 1.8.x-pXXX.

== V

Joel VanderWerf
* a patch for numeric.c

Peter Vanbroekhoven

Corinna Vinschen

== W

wanabe (wanabe)
* committer
* fixed YARV and Oniguruma.

Chun Wang
* a patch for time.rb

WATANABE, Hirofumi (eban)
* committer
* He is the maintainer of
 * ftools (1.8)
 * tmpdir
 * un
 * Win32API

WATANABE, Tetsuya

* a patch for ruby.c

William Webber (wew)
* committer

Jim Weirich (jim)
* committer
* He wrote Rake.

Nathan Weizenbaum
* fixed misc/ruby-mode.el.

why the lukky stiff (why)
* committer
* He is the maintainer of syck

Caley Woods
* documentation

Gary Wright
* documentation

== X

== Y

Akira Yamada (akira)
* committer
* He is the maintainer of ruby related packages at Debian project.

Keita Yamaguchi
* patches for enum.c, parse.y
* documentation

Hirokazu Yamamoto (ocean)
* committer

Hirotaka Yoshioka
* a patch for improving SEGV handling

== Z

Aristarkh A Zagorodnikov
* a patch for io.c

Alexander
Zavorine
* committer

* He is the maintainer for Symbian OS.

Chiyuan Zhang
* a patch for misc/ruby-mode.el.

Dee Zsombor (zunda)
* a patch for thread_pthread.c

Dan Zwell
* a patch for net/pop
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include/ruby/oniguruma.h:
regcomp.c:
regenc.[ch]:
regerror.c:
regexec.c:
regint.h:
regparse.[ch]:
enc/ascii.c
enc/big5.c
enc/cp949.c
enc/emacs_mule.c
enc/encdb.c
enc/euc_jp.c
enc/euc_kr.c
enc/euc_tw.c
enc/gb18030.c
enc/gb2312.c
enc/gbk.c
enc/iso_8859_1.c
enc/iso_8859_10.c
enc/iso_8859_11.c
enc/iso_8859_13.c
enc/iso_8859_14.c
enc/iso_8859_15.c
enc/iso_8859_16.c

enc/iso_8859_2.c
enc/iso_8859_3.c
enc/iso_8859_4.c
enc/iso_8859_5.c
enc/iso_8859_6.c
enc/iso_8859_7.c
enc/iso_8859_8.c
enc/iso_8859_9.c
enc/koi8_r.c
enc/koi8_u.c
enc/shift_jis.c
enc/unicode.c
enc/us_ascii.c
enc/utf_16be.c
enc/utf_16le.c
enc/utf_32be.c
enc/utf_32le.c
enc/utf_8.c
enc/windows_1251.c

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enc/trans/JIS/UCS%JISX0201-KANA.src:
enc/trans/JIS/UCS%JISX0208@1990.src:
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enc/trans/JIS/UCS@SIP%JISX0213-2.src:

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random.c

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A C-program for MT19937, with initialization improved 2002/2/10.
Coded by Takuji Nishimura and Makoto Matsumoto.
This is a faster version by taking Shawn Cokus's optimization,
Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using init_genrand(seed)
or init_by_array(init_key, key_length).

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vm_dump.c:procstat_vm

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missing/alloca.c:
missing/dup2.c:
missing/erf.c:
missing/finite.c:
missing/hypot.c:
missing/isinf.c:
missing/isnan.c:
missing/lgamma_r.c:
missing/memcmp.c:
missing/memmove.c:
missing/strchr.c:
missing/strerror.c:
missing/strstr.c:
missing/tgamma.c:
ext/date/date_strftime.c:
ext/digest/sha1/sha1.[ch]:
ext/sdbm/_sdbm.c:
ext/sdbm/sdbm.h:

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(Arranged
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DATE: 1 March 1996

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ext/nkf/nkf-utf8/nkf.c:
ext/nkf/nkf-utf8/utf8tbl.c:

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ext/socket/getaddrinfo.c:
ext/socket/getnameinfo.c:

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1.617 did-you-mean 1.5.0

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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/Java5MongoConnectionPoolMBean.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/ConnectionStatus.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/management/jmx/JMXMBeanServer.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/tools/ConnectionPoolStat.java
*
/mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/management/NullMBeanServer.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/management/MBeanServer.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/management/MBeanServerFactory.java
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* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/util/annotations/NotThreadSafe.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/io/ByteBufferOutputStream.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/io/ByteBufferInputStream.java
*
 /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/io/ByteBufferHolder.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/types/Code.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/io/ByteStream.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/OptionMap.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/ThreadUtil.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/Hash.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/StringParseUtil.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/ThreadPool.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBObject.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/types/CodeWScope.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/UniqueList.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/NativeAuthenticationHelper.java
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* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/BSONEncoder.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/MongoConnectionPool.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/MongoInterruptedException.java
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 /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/Java5MongoConnectionPool.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/MongoClientOptions.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/JSONSerializers.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/ClassMapBasedObjectSerializer.java
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Found in path(s):

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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/BasicDBList.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/BasicBSONCallback.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DB.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/ReflectionDBObject.java
*
 /mongo-java-driver-2-11-3-sources-2-jar/org/bson/BSONLazyDecoder.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/LazyWriteableDBDecoder.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/io/BasicOutputBuffer.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBCallback.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBDecoderFactory.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DefaultDBDecoder.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/types/BasicBSONList.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/JSONParseException.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/Bytes.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/Response.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBPortPool.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/MapReduceOutput.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/types/MaxKey.java
*
 /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/LazyDBObject.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/OutMessage.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/MongoOptions.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBTCPConnector.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/LazyDBCallback.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/BasicBSONDecoder.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/BasicBSONEncoder.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/util/SimplePool.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/io/PoolOutputBuffer.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/JSONCallback.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/util/ClassMap.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/types/MinKey.java
*
 /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DefaultDBCallback.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/CommandResult.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/LazyWriteableDBCallback.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/gridfs/GridFSDBFile.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/WriteResult.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBPointer.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBPort.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBCollection.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBConnector.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBEncoderFactory.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/io/BSONInput.java

* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/util/JSON.java
*
/mongo-java-driver-2-11-3-sources-2-jar/org/bson/BSONObject.java
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* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/Mongo.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/MongoInternalException.java
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*
/mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/LazyWriteableDBObject.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBRef.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/LazyBSONCallback.java
* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/LazyBSONDecoder.java
* /mongo-java-driver-2-11-3-sources-2-jar/com/mongodb/DBCursor.java
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* /mongo-java-driver-2-11-3-sources-2-jar/org/bson/BSON.java
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1.619 jffi 1.3.13

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1.622 jnr-netdb 1.2.0

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1.623 error_highlight 0.3.0

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1.624 go-hclog 1.2.0

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1.625 bcutil-jdk18on 1.74

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* https://github.com/eclipse-ee4j/mail

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1.627 mailapi 1.4.2

1.628 datanucleus-core 4.1.17

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1.629 minitest 5.15.0

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Found in path(s):
* /minitest-5-15-0-gem/data-tar-gz/lib/minitest/benchmark.rb
No license file was found, but licenses were detected in source scan.

= minitest/{test,spec,mock,benchmark}

home :: https://github.com/seattlerb/minitest
bugs :: https://github.com/seattlerb/minitest/issues
rdoc :: http://docs.seattlerb.org/minitest
vim :: https://github.com/sunaku/vim-ruby-minitest
emacs:: https://github.com/arthurnn/minitest-emacs

== DESCRIPTION:

minitest provides a complete suite of testing facilities supporting
TDD, BDD, mocking, and benchmarking.

  "I had a class with Jim Weirich on testing last week and we were
   allowed to choose our testing frameworks. Kirk Haines and I were
   paired up and we cracked open the code for a few test
   frameworks...

   I MUST say that minitest is *very* readable / understandable
   compared to the 'other two' options we looked at. Nicely done and
   thank you for helping us keep our mental sanity."

  -- Wayne E. Seguin

minitest/test is a small and incredibly fast unit testing framework.
It provides a rich
set of assertions to make your tests clean and
readable.

minitest/spec is a functionally complete spec engine. It hooks onto
minitest/test and seamlessly bridges test assertions over to spec
expectations.

minitest/benchmark is an awesome way to assert the performance of your
algorithms in a repeatable manner. Now you can assert that your newb
co-worker doesn't replace your linear algorithm with an exponential
one!

minitest/mock by Steven Baker, is a beautifully tiny mock (and stub)
object framework.

minitest/pride shows pride in testing and adds coloring to your test
output. I guess it is an example of how to write IO pipes too. :P

minitest/test is meant to have a clean implementation for language
implementors that need a minimal set of methods to bootstrap a working
test suite. For example, there is no magic involved for test-case
discovery.

"Again, I can't praise enough the idea of a testing/specing
framework that I can actually read in full in one sitting!"

-- Piotr
Szotkowski

Comparing to rspec:

rspec is a testing DSL. minitest is ruby.

-- Adam Hawkins, "Bow Before MiniTest"

minitest doesn't reinvent anything that ruby already provides, like:
classes, modules, inheritance, methods. This means you only have to
learn ruby to use minitest and all of your regular OO practices like
extract-method refactorings still apply.

== FEATURES/PROBLEMS:

* minitest/autorun - the easy and explicit way to run all your tests.
* minitest/test - a very fast, simple, and clean test system.
* minitest/spec - a very fast, simple, and clean spec system.
* minitest/mock - a simple and clean mock/stub system.

* minitest/benchmark - an awesome way to assert your algorithm's performance.
* minitest/pride - show your pride in testing!
* Incredibly small and fast runner, but no bells and whistles.
* Written by squishy human beings. Software can never be perfect. We will all eventually die.

== RATIONALE:

See design_rationale.rb to see how specs and tests work
in minitest.

== SYNOPSIS:

Given that you'd like to test the following class:

```
class Meme
  def i_can_has_cheezburger?
    "OHAI!"
  end

  def will_it_blend?
    "YES!"
  end
end
```

=== Unit tests

Define your tests as methods beginning with +test_+.

```
require "minitest/autorun"

class TestMeme < Minitest::Test
  def setup
    @meme = Meme.new
  end

  def test_that_kitty_can_eat
    assert_equal "OHAI!", @meme.i_can_has_cheezburger?
  end

  def test_that_it_will_not_blend
    refute_match /^no/i, @meme.will_it_blend?
  end

  def test_that_will_be_skipped
    skip "test this later"
  end
end
```

=== Specs

```
require "minitest/autorun"

describe Meme do
  before do
    @meme = Meme.new
  end

  describe "when asked about cheeseburgers" do
    it "must respond positively" do
      _(@meme.i_can_has_cheezburger?).must_equal "OHAI!"
    end
  end

  describe "when asked about blending possibilities" do
    it "won't say no"
do
      _(@meme.will_it_blend?).wont_match /^no/i
    end
  end
end
```

For matchers support check out:

* https://github.com/wojtekmach/minitest-matchers
* https://github.com/rmm5t/minitest-matchers_vaccine

=== Benchmarks

Add benchmarks to your tests.

```
# optionally run benchmarks, good for CI-only work!
require "minitest/benchmark" if ENV["BENCH"]

class TestMeme < Minitest::Benchmark
  # Override self.bench_range or default range is [1, 10, 100, 1_000, 10_000]
  def bench_my_algorithm
    assert_performance_linear 0.9999 do |n| # n is a range value
      @obj.my_algorithm(n)
    end
  end
end
```

Or add them to your specs. If you make benchmarks optional, you'll
need to wrap your benchmarks in a conditional since the methods won't
be defined. In minitest 5, the describe name needs to match
<tt>/Bench(mark)?$/</tt>.

```
describe "Meme Benchmark" do
  if ENV["BENCH"] then
    bench_performance_linear "my_algorithm", 0.9999 do |n|
      100.times do

      @obj.my_algorithm(n)
      end
    end
  end
end
```

outputs something like:

```
# Running benchmarks:

TestBlah 100 1000 10000
bench_my_algorithm  0.006167  0.079279  0.786993
bench_other_algorithm  0.061679  0.792797  7.869932
```

Output is tab-delimited to make it easy to paste into a spreadsheet.

=== Mocks

Mocks and stubs defined using terminology by Fowler & Meszaros at
http://www.martinfowler.com/bliki/TestDouble.html:

"Mocks are pre-programmed with expectations which form a specification
of the calls they are expected to receive. They can throw an exception
if they receive a call they don't expect and are checked during
verification to ensure they got all the calls they were expecting."

```
class MemeAsker
  def initialize(meme)
    @meme = meme
  end

  def ask(question)
    method = question.tr(" ", "_") + "?"
    @meme.__send__(method)
  end
end

require "minitest/autorun"

describe MemeAsker, :ask do
  describe "when passed an unpunctuated
```

```
question" do
    it "should invoke the appropriate predicate method on the meme" do
      @meme = Minitest::Mock.new
      @meme_asker = MemeAsker.new @meme
      @meme.expect :will_it_blend?, :return_value

      @meme_asker.ask "will it blend"

      @meme.verify
    end
  end
end
```

==== Multi-threading and Mocks

Minitest mocks do not support multi-threading. If it works, fine, if it doesn't you can use regular ruby patterns and facilities like local variables. Here's an example of asserting that code inside a thread is run:

```
def test_called_inside_thread
  called = false
  pr = Proc.new { called = true }
  thread = Thread.new(&pr)
  thread.join
  assert called, "proc not called"
end
```

=== Stubs

Mocks and stubs are defined using terminology by Fowler & Meszaros at http://www.martinfowler.com/bliki/TestDouble.html:

"Stubs provide canned answers to calls made during the test".

Minitest's stub method overrides a single method for the duration of the block.

```
 def test_stale_eh
  obj_under_test = Something.new

  refute obj_under_test.stale?

  Time.stub :now, Time.at(0) do   # stub goes away once the block is done
    assert obj_under_test.stale?
  end
end
```

A note on stubbing: In order to stub a method, the method must

actually exist prior to stubbing. Use a singleton method to create a
new non-existing method:

```
 def obj_under_test.fake_method
   ...
 end
```

=== Running Your Tests

Ideally, you'll use a rake task to run your tests, either piecemeal or
all at once. Both rake and rails ship with rake tasks for running your
tests. BUT! You don't have to:

```
  % ruby -Ilib:test test/minitest/test_minitest_test.rb
  Run options: --seed 37685

  # Running:

  ..... (etc)

  Finished in 0.107130s, 1446.8403 runs/s, 2959.0217 assertions/s.

  155 runs, 317 assertions, 0 failures, 0 errors, 0 skips
```

There are runtime options available, both from minitest
 itself, and also
provided via plugins. To see them, simply run with +--help+:

```
  % ruby -Ilib:test test/minitest/test_minitest_test.rb --help
  minitest options:
    -h, --help                  Display this help.
    -s, --seed SEED               Sets random seed. Also via env. Eg: SEED=n rake
    -v, --verbose                 Verbose. Show progress processing files.
    -n, --name PATTERN              Filter run on /regexp/ or string.
    -e, --exclude PATTERN           Exclude /regexp/ or string from run.

  Known extensions: pride, autotest
    -p, --pride                 Pride. Show your testing pride!
    -a, --autotest                Connect to autotest server.
```

You can set up a rake task to run all your tests by adding this to your Rakefile:

```
  require "rake/testtask"

  Rake::TestTask.new(:test) do |t|
    t.libs << "test"
    t.libs << "lib"
    t.test_files = FileList["test/**/test_*.rb"]
```

```
  end

  task :default
 => :test
```

== Writing Extensions

To define a plugin, add a file named minitest/XXX_plugin.rb to your
project/gem. That file must be discoverable via ruby's LOAD_PATH (via
rubygems or otherwise). Minitest will find and require that file using
Gem.find_files. It will then try to call +plugin_XXX_init+ during
startup. The option processor will also try to call +plugin_XXX_options+
passing the OptionParser instance and the current options hash. This
lets you register your own command-line options. Here's a totally
bogus example:

```
  # minitest/bogus_plugin.rb:

  module Minitest
    def self.plugin_bogus_options(opts, options)
      opts.on "--myci", "Report results to my CI" do
        options[:myci] = true
        options[:myci_addr] = get_myci_addr
        options[:myci_port] = get_myci_port
      end
    end

    def self.plugin_bogus_init(options)
      self.reporter << MyCI.new(options) if options[:myci]
    end
  end
```

=== Adding custom reporters

Minitest
 uses composite reporter to output test results using multiple
reporter instances. You can add new reporters to the composite during
the init_plugins phase. As we saw in +plugin_bogus_init+ above, you
simply add your reporter instance to the composite via <tt><<</tt>.

+AbstractReporter+ defines the API for reporters. You may subclass it
and override any method you want to achieve your desired behavior.

start   :: Called when the run has started.
record :: Called for each result, passed or otherwise.
report :: Called at the end of the run.
passed? :: Called to see if you detected any problems.

Using our example above, here is how we might implement MyCI:

```
  # minitest/bogus_plugin.rb

  module Minitest
    class MyCI < AbstractReporter
      attr_accessor :results, :addr, :port

      def initialize options
        self.results = []
        self.addr = options[:myci_addr]
        self.port = options[:myci_port]
      end

      def record result
        self.results
 << result
      end

      def report
        CI.connect(addr, port).send_results self.results
      end
    end

    # code from above...
  end
```

== FAQ

=== What versions are compatible with what? Or what versions are supported?

Minitest is a dependency of rails, which until fairly recently had an overzealous backwards compatibility policy. As such, I'm stuck supporting versions of ruby that are long past EOL. Once rails 5.2 is dropped (hopefully April 2021), I get to drop a bunch of versions of ruby that I have to currently test against.

(As of 2021-01-31)

Current versions of rails: (https://endoflife.date/rails)

rails	min ruby	rec ruby	minitest	status
7.0	>= 2.7	3.0	>= 5.1	Future
6.1	>= 2.5	3.0	>= 5.1	Current
6.0	>= 2.5	2.6	>= 5.1	Security
5.2	>= 2.2.2	2.5	~> 5.1	Security	EOL @railsconf

2021?

Current versions of ruby: (https://endoflife.date/ruby)

ruby	Status	EOL Date
3.0	Current	2024-03-31
2.7	Maint	2023-03-31
2.6	Maint*	2022-03-31
2.5	EOL	2021-03-31
2.4	EOL	2020-03-31
2.3	EOL	2019-03-31
2.2	EOL	2018-03-31

See also:

* https://www.fastruby.io/blog/ruby/rails/versions/compatibility-table.html
* https://jamesjeffersconsulting.com/ruby-rails-version-matrix/

=== How to test SimpleDelegates?

The following implementation and test:

```
  class Worker < SimpleDelegator
    def work
    end
  end

  describe Worker do
    before do
      @worker = Worker.new(Object.new)
    end

    it "must respond to work" do
      _(@worker).must_respond_to :work
    end
  end
```

outputs a failure:

```
    1) Failure:
  Worker#test_0001_must respond to work [bug11.rb:16]:
  Expected #<Object:0x007f9e7184f0a0> (Object) to
 respond to #work.
```

Worker is a SimpleDelegate which in 1.9+ is a subclass of BasicObject.
Expectations are put on Object (one level down) so the Worker
(SimpleDelegate) hits +method_missing+ and delegates down to the

+Object.new+ instance. That object doesn't respond to work so the test
fails.

You can bypass <tt>SimpleDelegate#method_missing</tt> by extending the worker
with <tt>Minitest::Expectations</tt>. You can either do that in your setup at
the instance level, like:

```
before do
  @worker = Worker.new(Object.new)
  @worker.extend Minitest::Expectations
end
```

or you can extend the Worker class (within the test file!), like:

```
class Worker
  include ::Minitest::Expectations
end
```

=== How to share code across test classes?

Use a module. That's exactly what they're for:

```
module UsefulStuff
  def useful_method
    # ...
  end
end

describe Blah do
  include UsefulStuff

  def test_whatever
    # useful_method available here
   end
end
```

Remember, +describe+ simply creates test classes. It's just ruby at
the end of the day and all your normal Good Ruby Rules (tm) apply. If
you want to extend your test using setup/teardown via a module, just
make sure you ALWAYS call super. before/after automatically call super
for you, so make sure you don't do it twice.

=== How to run code before a group of tests?

Use a constant with begin...end like this:

```
describe Blah do
  SETUP = begin
```

```
    # ... this runs once when describe Blah starts
  end
  # ...
end
```

This can be useful for expensive initializations or sharing state.
Remember, this is just ruby code, so you need to make sure this
technique and sharing state doesn't interfere with your tests.

=== Why am I seeing <tt>uninitialized constant MiniTest::Test (NameError)</tt>?

Are you running the test with Bundler (e.g. via <tt>bundle exec</tt>)? If so,
in order to require minitest, you must first add the <tt>gem 'minitest'</tt>
to your Gemfile and
 run +bundle+. Once it's installed, you should be
able to require minitest and run your tests.

== Prominent Projects using Minitest:

* arel
* journey
* mime-types
* nokogiri
* rails (active_support et al)
* rake
* rdoc
* ...and of course, everything from seattle.rb...

== Developing Minitest:

Minitest requires {Hoe}[https://rubygems.org/gems/hoe].

=== Minitest's own tests require UTF-8 external encoding.

This is a common problem in Windows, where the default external Encoding is
often CP850, but can affect any platform.
Minitest can run test suites using any Encoding, but to run Minitest's
own tests you must have a default external Encoding of UTF-8.

If your encoding is wrong, you'll see errors like:

```
  --- expected
  +++ actual
  @@ -1,2 +1,3 @@
   # encoding: UTF-8
   -"Expected /\\w+/ to not match \"blah blah blah\"."
   +"Expected /\\w+/ to not match # encoding: UTF-8
   +\"blah blah blah\"."
```

To check your current encoding, run:

  ruby -e 'puts Encoding.default_external'

If
 your output is something other than UTF-8, you can set the RUBYOPTS
env variable to a value of '-Eutf-8'. Something like:

  RUBYOPT='-Eutf-8' ruby -e 'puts Encoding.default_external'

Check your OS/shell documentation for the precise syntax (the above
will not work on a basic Windows CMD prompt, look for the SET command).
Once you've got it successfully outputing UTF-8, use the same setting
when running rake in Minitest.

=== Minitest's own tests require GNU (or similar) diff.

This is also a problem primarily affecting Windows developers. PowerShell
has a command called diff, but it is not suitable for use with Minitest.

If you see failures like either of these, you are probably missing diff tool:

    4) Failure:
  TestMinitestUnitTestCase#test_assert_equal_different_long
[D:/ruby/seattlerb/minitest/test/minitest/test_minitest_test.rb:936]:
  Expected: "--- expected\n+++ actual\n@@ -1 +1 @@\n-
\"hahahahahahahahahahahahahahahahahahahaha\"\n+\"blahblahblahblahblahblahblahblahblahblah\"\n"
    Actual: "Expected: \"hahahahahahahahahahahahahahahahahahahaha\"\n Actual:
\"blahblahblahblahblahblahblahblahblah\""

    5) Failure:
  TestMinitestUnitTestCase#test_assert_equal_different_collection_hash_hex_invisible
[D:/ruby/seattlerb/minitest/test/minitest/test_minitest_test.rb:845]:
  Expected: "No visible difference in the Hash#inspect output.\nYou should look at the implementation of #== on
Hash or its members.\n
  {1=>#<Object:0xXXXXXX>}"
    Actual: "Expected: {1=>#<Object:0x00000003ba0470>}\n Actual: {1=>#<Object:0x00000003ba0448>}"

If you use Cygwin or MSYS2 or similar there are packages that include a
GNU diff for Windows. If you don't, you can download GNU diffutils from
http://gnuwin32.sourceforge.net/packages/diffutils.htm
(make sure to add it to your PATH).

You can make sure it's installed and path is configured properly with:

diff.exe -v

There are multiple lines of output, the first should be something like:

diff (GNU diffutils) 2.8.1

If you are using PowerShell make sure you run diff.exe, not just diff,
which will invoke the PowerShell built in function.

== Known Extensions:

capybara_minitest_spec :: Bridge between Capybara RSpec matchers and
Minitest::Spec expectations (e.g.
<tt>page.must_have_content("Title")</tt>).
color_pound_spec_reporter :: Test names print Ruby Object types in color with
your Minitest Spec style tests.
minispec-metadata :: Metadata for describe/it blocks & CLI tag filter.
E.g. <tt>it "requires JS driver", js: true do</tt> &
<tt>ruby test.rb --tag js</tt> runs tests tagged :js.
minispec-rails :: Minimal support to use Spec style in Rails 5+.
mini-apivore :: for swagger based automated API testing.
minitest-around :: Around block for minitest. An alternative
to
setup/teardown dance.
minitest-assert_errors :: Adds Minitest assertions to test for errors raised
or not raised by Minitest itself.
minitest-autotest :: autotest is a continuous testing facility meant to
be used during development.
minitest-bacon :: minitest-bacon extends minitest with bacon-like
functionality.
minitest-bang :: Adds support for RSpec-style let! to immediately
invoke let statements before each test.
minitest-bisect :: Helps you isolate and debug random test failures.
minitest-blink1_reporter :: Display test results with a Blink1.
minitest-capistrano :: Assertions and expectations for testing
Capistrano recipes.
minitest-capybara :: Capybara matchers support for minitest unit and

spec.
minitest-chef-handler :: Run Minitest suites as Chef report handlers
minitest-ci :: CI reporter plugin for Minitest.
minitest-context :: Defines contexts for code reuse in Minitest
specs that share common expectations.
minitest-debugger :: Wraps assert so failed assertions drop into
the ruby debugger.
minitest-display :: Patches Minitest to allow for an easily
configurable output.
minitest-documentation :: Minimal documentation format inspired by rspec's.

minitest-doc_reporter :: Detailed output inspired by rspec's documentation format.
minitest-emoji :: Print out emoji for your test passes, fails, and skips.
minitest-english :: Semantically symmetric aliases for assertions and expectations.
minitest-excludes :: Clean API for excluding certain tests you don't want to run under certain conditions.
minitest-fail-fast :: Reimplements RSpec's "fail fast" feature
minitest-filecontent :: Support unit tests with expectation results in files. Differing results will be stored again in files.
minitest-filesystem :: Adds assertion and expectation to help testing filesystem contents.
minitest-firemock :: Makes your Minitest mocks more resilient.
minitest-focus :: Focus on one test at a time.
minitest-gcstats :: A minitest plugin that adds a report of the top tests by number of objects allocated.
minitest-global_expectations:: Support minitest expectation methods for all objects
minitest-great_expectations :: Generally useful additions to minitest's assertions and expectations.
minitest-growl :: Test notifier for minitest via growl.
minitest-happy :: GLOBALLY ACTIVATE MINITEST PRIDE! RAWR!
minitest-have_tag :: Adds Minitest assertions to test for the existence of HTML tags, including contents, within a provided string.
minitest-heat :: Reporting that builds a heat map of failure locations
minitest-hooks :: Around and before_all/after_all/around_all hooks
minitest-hyper :: Pretty, single-page HTML reports for your Minitest runs
minitest-implicit-subject :: Implicit declaration of the test subject.
minitest-instrument :: Instrument ActiveSupport::Notifications when test method is executed.
minitest-instrument-db :: Store information about speed of test execution provided by minitest-instrument in database.
minitest-junit :: JUnit-style XML reporter for minitest.
minitest-keyword :: Use Minitest assertions with keyword arguments.
minitest-libnotify :: Test notifier for minitest via libnotify.
minitest-line :: Run test at line number.
minitest-logger :: Define assert_log and enable minitest to test log messages. Supports Logger and Log4r::Logger.
minitest-macruby :: Provides extensions to minitest for macruby UI testing.
minitest-matchers :: Adds support for RSpec-style matchers to minitest.
minitest-matchers_vaccine :: Adds assertions that adhere to the matcher spec, but without any expectation infections.
minitest-metadata :: Annotate tests with metadata (key-value).

minitest-mock_expectations :: Provides method call assertions for minitest.
minitest-mongoid :: Mongoid assertion matchers for Minitest.
minitest-must_not :: Provides
must_not as an alias for wont in
Minitest.
minitest-optional_retry :: Automatically retry failed test to help with flakiness.
minitest-osx :: Reporter for the Mac OS X notification center.
minitest-parallel_fork :: Fork-based parallelization
minitest-parallel-db :: Run tests in parallel with a single database.
minitest-power_assert :: PowerAssert for Minitest.
minitest-predicates :: Adds support for .predicate? methods.
minitest-profile :: List the 10 slowest tests in your suite.
minitest-rails :: Minitest integration for Rails 3.x.
minitest-rails-capybara :: Capybara integration for Minitest::Rails.
minitest-reporters :: Create customizable Minitest output formats.
minitest-rg :: Colored red/green output for Minitest.
minitest-rspec_mocks :: Use RSpec Mocks with Minitest.
minitest-server :: minitest-server provides a client/server setup
with your minitest process, allowing your test
run to send its results directly to a handler.
minitest-sequel :: Minitest assertions to speed-up development and
testing of Ruby Sequel database setups.
minitest-shared_description :: Support for shared specs and shared spec
subclasses
minitest-should_syntax :: RSpec-style <tt>x.should == y</tt> assertions for
Minitest.
minitest-shouldify :: Adding all manner of shoulds to Minitest (bad
idea)
minitest-snail :: Print a list of tests that take too long
minitest-spec-context :: Provides rspec-ish context method to
Minitest::Spec.
minitest-spec-expect :: Expect syntax for Minitest::Spec (e.g.
expect(sequences).to_include :celery_man).
minitest-spec-magic
:: Minitest::Spec extensions for Rails and beyond.
minitest-spec-rails :: Drop in Minitest::Spec superclass for
ActiveSupport::TestCase.
minitest-sprint :: Runs (Get it? It's fast!) your tests and makes
it easier to rerun individual failures.
minitest-stately :: Find leaking state between tests
minitest-stub_any_instance :: Stub any instance of a method on the given class
for the duration of a block.
minitest-stub-const :: Stub constants for the duration of a block.
minitest-tags :: Add tags for minitest.
minitest-unordered :: Adds a new assertion to minitest for checking the
contents of a collection, ignoring element order.
minitest-vcr :: Automatic cassette managment with Minitest::Spec
and VCR.

minitest_log :: Adds structured
logging, data explication, and verdicts.
minitest_owrapper :: Get tests results as a TestResult object.
minitest_should :: Shoulda style syntax for minitest test::unit.
minitest_tu_shim :: Bridges between test/unit and minitest.
mongoid-minitest :: Minitest matchers for Mongoid.
mutant-minitest :: Minitest integration for mutant.
pry-rescue :: A pry plugin w/ minitest support. See
pry-rescue/minitest.rb.
rematch :: Declutter your test files from large hardcoded data
and update them automatically when your code changes.
rspec2minitest :: Easily translate any RSpec matchers to Minitest
assertions and expectations.

== Unknown Extensions:

Authors... Please send me a pull request with a description of your minitest extension.

* assay-minitest
* detroit-minitest
* em-minitest-spec
* flexmock-minitest
*
guard-minitest
* guard-minitest-decisiv
* minitest-activemodel
* minitest-ar-assertions
* minitest-capybara-unit
* minitest-colorer
* minitest-deluxe
* minitest-extra-assertions
* minitest-rails-shoulda
* minitest-spec
* minitest-spec-should
* minitest-sugar
* spork-minitest

== Minitest related goods

* minitest/pride fabric: http://www.spoonflower.com/fabric/3928730-again-by-katie_allen

== REQUIREMENTS:

* Ruby 2.3+. No magic is involved. I hope.

== INSTALL:

sudo gem install minitest

On 1.9, you already have it. To get newer candy you can still install
the gem, and then requiring "minitest/autorun" should automatically
pull it in. If not, you'll need to do it yourself:

```
gem "minitest"     # ensures you"re using the gem, and not the built-in MT
require "minitest/autorun"

# ... usual testing stuffs ...
```

DO NOTE: There is a serious problem with the way that ruby 1.9/2.0
packages their own gems. They install a gem specification file, but
don't install the gem
contents in the gem path. This messes up
Gem.find_files and many other things (gem which, gem contents, etc).

Just install minitest as a gem for real and you'll be happier.

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Found in path(s):
* /minitest-5-15-0-gem/data-tar-gz/README.rdoc

1.630 x-sys 0.13.0

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1.631 ikvm.msbuild 8.8.0

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* /jnr-constants-0-10-4-sources-1-jar/jnr/constants/Constant.java
* /jnr-constants-0-10-4-sources-1-jar/jnr/constants/ConstantSet.java
* /jnr-constants-0-10-4-sources-1-jar/jnr/constants/platform/ConstantResolver.java
* /jnr-constants-0-10-4-sources-1-jar/jnr/constants/PlatformConstants.java

1.639 activemq 5.16.7

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Version 2.1, February 1999

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1.645 ant 1.8.4

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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/Type.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/AbstractReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/Runtime.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/AbstractNumberReference.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/Pointer.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AllocatedDirectMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/FromNativeContext.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/MethodResultContext.java
*
 /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/NullTypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ArrayMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ByteBufferMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Transient.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/AbstractRuntime.java

* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/CallingConvention.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AsmClassLoader.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/DirectMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/MemoryUtil.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/StdCall.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/ByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/X86_64StubCompiler.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Direct.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/IdentityFunctionMapper.java
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/jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Clear.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Meta.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/InvocationSession.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/NulTerminate.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/ByteByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/X86_32StubCompiler.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/SaveError.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/NativeLibrary.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/NullMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/ParameterFlags.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/LongLongByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/DelegatingMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/BadType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AbstractX86StubCompiler.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/LibraryOption.java
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/jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/LoadedLibrary.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/Platform.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/Library.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ARM_64StubCompiler.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Pinned.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/NativeRuntime.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/BoundedMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/AddressByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/InAccessibleMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/util/EnumMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/ToNativeConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/MemoryManager.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/MethodParameterContext.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/NativeLong.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/IgnoreError.java
*
/jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/PointerByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/AbstractBufferMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/Invoker.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AsmUtil.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/AbstractArrayMemoryIO.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/In.java

* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/JNINativeInterface.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/StubCompiler.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/SymbolNotFoundError.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AsmLibraryLoader.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AsmRuntime.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/DoubleByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/NumberUtil.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/AbstractMemoryIO.java
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 /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/JNIInvokeInterface.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/TypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/Address.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/DataConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/CodegenUtils.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Synchronized.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/IntByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/NativeMemoryManager.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/ToNativeContext.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/SkinnyMethodAdapter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/util/BufferUtil.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/FunctionMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/NativeLongByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/Provider.java
*
 /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/LibraryLoader.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/FromNativeConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Out.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/ShortByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/InvokerUtil.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/FloatByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AbstractA64StubCompiler.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/SimpleNativeContext.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/BoxedLong32ArrayParameterConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/EnumConverter.java
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/jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/mips/linux/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/VariableAccessorGenerator.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/i386/windows/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/i386/openbsd/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/mips64el/linux/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/int64_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/BufferParameterStrategy.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/BoxedFloatArrayParameterConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ToNativeOp.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/SignatureTypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/s390x/linux/TypeAliases.java
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/jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/HeapBufferParameterStrategy.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/CachingTypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/gid_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/NotImplMethodGenerator.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/InvalidRuntime.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/clock_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/i386/solaris/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/TypeAlias.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/x86_64/darwin/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/arm/linux/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/size_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/BoxedByteArrayParameterConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ClosureTypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/DefaultTypeMapper.java
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/jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/StringResultConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/fsfilcnt_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/nlink_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/uintptr_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/AbstractToNativeType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/caddr_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/InvokerTypeMapper.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/socklen_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/util/Annotations.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/sparc/solaris/TypeAliases.java

* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/s390/linux/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/ByReferenceParameterConverter.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/NullObjectParameterStrategy.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/dev_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/int32_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/TransientNativeMemory.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/fsblkcnt_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/ToNativeTypes.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/byref/NumberByReference.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/EnumSetConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/BoxedLong64ArrayParameterConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/StructByReferenceToNativeConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/i386/darwin/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/ppc64/ibmi/TypeAliases.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/sparcv9/solaris/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/mips64/linux/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/StructByReferenceFromNativeConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/AbstractFromNativeType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/aarch64/darwin/TypeAliases.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/annotations/Encoding.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/uid_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/mode_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/u_int16_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/BoxedIntegerArrayParameterConverter.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/off_t.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/ino_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ReflectionLibraryLoader.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/int16_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/aarch64/freebsd/TypeAliases.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/InvalidProvider.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/converters/CharSequenceArrayParameterConverter.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/LibraryLoader.java
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* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/blkcnt_t.java
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 /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/pid_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/types/u_int32_t.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/platform/x86_64/solaris/TypeAliases.java
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 /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/ResultType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/Types.java

* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/mapper/SimpleTypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/FromNativeType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/InterfaceScanner.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/AnnotationTypeMapper.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/StructByReferenceResultConverterFactory.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/ParameterType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/SigType.java
* /jnr-ffi-2-2-16-sources-4-jar/jnr/ffi/provider/jffi/ConverterMetaData.java

1.654 numcpus 0.6.1

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1.660 protobuf 1.5.3

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1.664 html5lib 1.1

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1.665 mail 1.4.2

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* /vt-dictionary-3-0-sources-6-jar/edu/vt/middleware/dictionary/FileWordList.java
* /vt-dictionary-3-0-sources-6-jar/edu/vt/middleware/dictionary/WordListDictionary.java
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* /vt-dictionary-3-0-sources-6-jar/edu/vt/middleware/dictionary/sort/ArraySorter.java
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* /vt-dictionary-3-0-sources-6-jar/edu/vt/middleware/dictionary/sort/ArraysSort.java
* /vt-dictionary-3-0-sources-6-jar/edu/vt/middleware/dictionary/sort/BubbleSort.java
*

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* /javassist-3-21-0-ga-sources-7-jar/javassist/bytecode/annotation/CharMemberValue.java
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* /javassist-3-21-0-ga-sources-7-jar/javassist/bytecode/annotation/StringMemberValue.java
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* /javassist-3-21-0-ga-sources-7-jar/javassist/runtime/Cflow.java
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* /javassist-3-21-0-ga-sources-7-jar/javassist/bytecode/InstructionPrinter.java
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* /javassist-3-21-0-ga-sources-7-jar/javassist/CtNewMethod.java
* /javassist-3-21-0-ga-sources-7-jar/javassist/URLClassPath.java
* /javassist-3-21-0-ga-sources-7-jar/javassist/compiler/AccessorMaker.java

* /javassist-3-21-0-ga-sources-7-jar/javassist/scopedpool/ScopedClassPoolRepository.java
* /javassist-3-21-0-ga-sources-7-jar/javassist/CtField.java
* /javassist-3-21-0-ga-sources-7-jar/javassist/expr/NewArray.java
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/javassist-3-21-0-ga-sources-7-jar/javassist/bytecode/analysis/FramePrinter.java
* /javassist-3-21-0-ga-sources-7-jar/javassist/tools/Callback.java
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* /dom4j-2-1-3-sources-jar/org/dom4j/io/XPP3Reader.java
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1.673 jdo-api 2.3-eb

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* /validation-api-1-1-0-final-sources-jar/javax/validation/spi/package-info.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/executable/package-info.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/constraints/package-info.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/package-info.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/metadata/package-info.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/constraintvalidation/ValidationTarget.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/constraintvalidation/package-info.java
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*
/validation-api-1-1-0-final-sources-jar/javax/validation/metadata/ReturnValueDescriptor.java
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* /validation-api-1-1-0-final-sources-jar/javax/validation/constraints/AssertFalse.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/TraversableResolver.java
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* /validation-api-1-1-0-final-sources-jar/javax/validation/groups/Default.java
* /validation-api-1-1-0-final-sources-jar/javax/validation/OverridesAttribute.java
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* /validation-api-1-1-0-final-sources-jar/javax/validation/UnexpectedTypeException.java
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* /validation-api-1-1-0-final-sources-jar/javax/validation/bootstrap/ProviderSpecificBootstrap.java
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* /validation-api-1-1-0-final-sources-jar/javax/validation/constraints/Max.java
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*
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1.690 activemq-protobuf 1.1

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1.691 activemq-kahadb-store 5.16.7

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1.692 stringio 3.0.8

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```
/**********************************************************************

 stringio.c -

 $Author$
 $RoughId: stringio.c,v 1.13 2002/03/14 03:24:18 nobu Exp $
 created at: Tue Feb 19 04:10:38 JST 2002

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```

Found in path(s):
* /stringio-3-0-8-gem/data-tar-gz/ext/stringio/stringio.c
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Pseudo `IO` class from/to `String`.

This library is based on MoonWolf version written in Ruby.  Thanks a lot.

## Differences to `IO`

* `fileno` raises `NotImplementedError`.
* encoding conversion is not implemented, and ignored silently.
* there is no `#to_io` method because this is not an `IO.

## Installation

Add this line to your application's Gemfile:

```ruby
gem 'stringio'
```
```

And then execute:

```
$ bundle
```

Or install it yourself as:

```
$ gem install stringio
```

Development

Run `bundle install` to install dependencies and then `bundle exec rake test` to run the tests.

To install this gem onto your local
machine, run `bundle exec rake install`. To release a new version, update the version number in `version.rb`, and then run `bundle exec rake release`, which will create a git tag for the version, push git commits and tags, and push the `.gem` file to rubygems.org.

Contributing

Bug reports and pull requests are welcome on GitHub at https://github.com/ruby/stringio.

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Found in path(s):
* /stringio-3-0-8-gem/data-tar-gz/README.md

1.693 commons-logging 1.2

1.693.1 Available under license :

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* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/LogConfigurationException.java
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* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/LogFactoryImpl.java
* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/Jdk13LumberjackLogger.java
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* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/Jdk14Logger.java
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* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/NoOpLog.java
* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/LogKitLogger.java
* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/Log.java

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* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/package.html
* /commons-logging-1-1-sources-9-jar/org/apache/commons/logging/impl/package.html

1.694 asm 5.0.3

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/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-jar/org/objectweb/asm/optimizer/AnnotationConstantsCollector.java/***

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* </body>
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/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/util/TraceMethodVisitor.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/util/TraceSignatureVisitor.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/util/package.html<html>
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/ASMContentHandler.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/Processor.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/SAXAdapter.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/SAXClassAdapter.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/SAXCodeAdapter.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/SAXFieldAdapter.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-jar/org/objectweb/asm/xml/asm-
xml.dtd<!--
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/xml/package.html<html>

Found
in path(s):
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/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/optimizer/shrink-
signatures.properties##
###

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* org/objectweb/asm/MethodWriter.resizeInstructions()V=-

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/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-jar/org/objectweb/asm/Edge.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/commons/Remapper.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/optimizer/FieldConstantsCollector.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/signature/package.html<html>
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/InnerClassNode.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/JumpInsnNode.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
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jar/org/objectweb/asm/tree/LineNumberNode.java/***
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/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/ParameterNode.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/TryCatchBlockNode.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/VarInsnNode.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/analysis/Analyzer.java/***
/opt/cola/permits/2735916537_1762226817.6472983/0/asm-5-0-3-sources-16-
jar/org/objectweb/asm/tree/analysis/Value.java/***

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1.695 xml-commons-resolver 1.1

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1.696 isc 2.0

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* configure.ac, Makefile.am: The original versions were derived from the
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* atf-c/ui.c: The format_paragraph and format_text functions were
 derived form the ones in the Monotone project, revision
 3a0982da308228d796df35f98d787c5cff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp:
These files were derived from the file_handle, systembuf, pipe and pistream
classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* admin/check-style.sh,
admin/check-style-common.awk,
admin/check-style-cpp.awk, admin/check-style-shell.awk: These files,
except the first one, were first implemented in the Buildtool project.
They were later adapted to be part of Boost.Process and, during that
process, the shell script was created.

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   <xsl:param name="text"/>
   <xsl:value-of select="$isc.copyright.leader"/>
   <xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
   <xsl:text>&#10;</xsl:text>
   <xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
   <xsl:if test="translate($rest, '&#9;&#32;', '')">
     <xsl:call-template name="isc.copyright.format">
       <xsl:with-param
 name="text" select="$rest"/>
     </xsl:call-template>
   </xsl:if>
 </xsl:template>

 <xsl:variable name="isc.copyright.text">
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  <xsl:call-template name="isc.copyright.format">
   <xsl:with-param name="text">
    <xsl:for-each select="book/info/copyright | refentry/docinfo/copyright">
  <xsl:text>Copyright (C) </xsl:text>
  <xsl:call-template name="copyright.years">
   <xsl:with-param name="years" select="year"/>
  </xsl:call-template>
  <xsl:text> </xsl:text>
  <xsl:value-of select="holder"/>
      <xsl:value-of select="$isc.copyright.breakline"/>
  <xsl:text>&#10;</xsl:text>
 </xsl:for-each>
 <xsl:value-of select="$isc.copyright.text"/>
   </xsl:with-param>
  </xsl:call-template>
 </xsl:variable>
</xsl:stylesheet>

<!--
 - Local variables:
 - mode: sgml
 - End:
-->
```

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1.698 lz4 2.6.1+incompatible

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