Open Source Used In ThousandEyes BrowserBot for Cisco Application Hosting 1.0

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In your requests please include the following reference number 78EE117C99-1155871198

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1.335 lumberjack v2.0.0
1.335.1 Available under license
1.336 util-linux 2.34 0.1ubuntu9.1
1.336.1 Available under license

1.1 adduser 3.118ubuntu2
1.1.1 Available under license:

This package was first put together by Ian Murdock
<imurdock@debian.org> and was maintained by Steve Phillips
<sjp@cvfn.org> from sources written for the Debian Project by Ian
Murdock, Ted Hajek <tedhajek@boombox.micro.umn.edu>, and Sven Rudolph
<sr1@inf.tu-dresden.de>.

Since Nov 27 1996, it was maintained by Guy Maor <maor@debian.org>. He
rewrote most of it.

Since May 20 2000, it is maintained by Roland Bauerschmidt
<rb@debian.org>.

Since March 24 2004, it is maintained by Roland Bauerschmidt
<rb@debian.org>, and co-maintained by Marc Haber
<mh+debian-packages@zugschlus.de>
Since 23 Oct 2005, it has been maintained by Joerg Hoh <joerg@joerghoh.de>

Since June 2006, it has been maintained by Stephen Gran <sgran@debian.org>

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1.2 x11-xkb-utils 7.7+5
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The contents of this package were downloaded from
https://xorg.freedesktop.org/releases/individual/app/
It contains the setxkbmap, xkbcomp, xkbevd, xkbprint and xkbutils applications.

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1.4 libunistring 0.9.10 2

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1.9 python-defaults 3.8.2-0ubuntu2

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This is the Debian GNU/Linux prepackaged version of the Python programming language. Python was written by Guido van Rossum <guido@cwi.nl> and others.

This package was put together by Klee Dienes <klee@debian.org> from sources from ftp.python.org:/pub/python, based on the Debianization by the previous maintainers Bernd S. Brentrup <bsb@uni-muenster.de> and Bruce Perens.

Current maintainer is Matthias Klose <doko@debian.org> until the final 2.3 version is released.

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A. HISTORY OF THE SOFTWARE

================================================================================

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of
All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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py3compile, py3clean and debpython module:

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1.10 ini v1.51.0

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1.14 icu 65
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## Commands to generate dependency files

```
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)
```

## Flags for position independent code

```
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPI
```

## Additional flags when building libraries and with threads

```
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =
```

## Compiler switch to embed a runtime search path

```
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,
```

## Compiler switch to embed a library name

```
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))
```

## Shared library options

```
LD_SOOPTIONS= -Wl,-Bsymbolic
```

## Shared object suffix

```
SO = so
```

## Non-shared intermediate object suffix

```
STATIC_O = ao
```

## Compilation rules

```
%.$(STATIC_O): $(srcdir)/%.c
  $(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<

%.o: $(srcdir)/%.c
  $(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
  $(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<

%.o: $(srcdir)/%.cpp
  $(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
```

## Dependency rules

```
%.d: $(srcdir)/%.c
  @echo "generating dependency information for $<"
  $(SHELL) -ec "$GEN_DEPS.c $< \n   | sed \"$/s/\([^n]\)\./:\&/g\\\" > $@;\n   [ -s $@ ] || rm -f $@"

%.d: $(srcdir)/%.cpp
```

---

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that copy, so the ordinary GNU General Public License applies to all
subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or
link a "work that uses the Library" with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the
Library is used in it and that the Library and its use are covered by
this License. You must supply a copy of this License. If the work
during execution displays copyright notices, you must include the
copyright notice for the Library among them, as well as a reference
directing the user to the copy of this License. Also, you must do one
of these things:

a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

b) Give prominent notice with the combined library of the fact
that part of it is a work based on the Library, and explaining
where to find the accompanying uncombined form of the same work.

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Appendix: How to Apply These Terms to Your New Libraries

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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

1.16 base-files 11ubuntu5.3

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification").

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.
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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.
If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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// This file was generated by gir (https://github.com/gtk-rs/gir)
// from gir-files (https://github.com/gtk-rs/gir-files)

// DO NOT EDIT

use gio_sys;
use glib;
use glib::object::Cast;
use glib::object::IsA;
use glib::signal::connect_raw;
use glib::signal::SignalHandlerId;
use glib::translate::*;
use glib_sys;
use gobject_sys;
use std::boxed::Box as Box_;
use std::fmt;
use std::mem::transmute;
use std::pin::Pin;
use std::ptr;
use Cancellable;

// This file was generated by gir (https://github.com/gtk-rs/gir)
// from gir-files (https://github.com/gtk-rs/gir-files)

// DO NOT EDIT

use gio_sys;
use glib;
use glib::object::Cast;
use glib::object::IsA;
use glib::signal::connect_raw;
use glib::signal::SignalHandlerId;
use glib::translate::*;
use glib_sys;
use gobject_sys;
use std::boxed::Box as Box_;
use std::fmt;
use std::mem::transmute;
use std::pin::Pin;
use std::ptr;
use Cancellable;

glib_wrapper! {  
    pub struct Permission(Object<gio_sys::GPermission, gio_sys::GPermissionClass, PermissionClass>);

    match fn {
        get_type => || gio_sys::g_permission_get_type(),
    }
}

pub const NONE_PERMISSION: Option<&Permission> = None;

pub trait PermissionExt: 'static {
    fn acquire<P: IsA<Cancellable>>(&self, cancellable: Option<&P>) -> Result<(), glib::Error>;

    fn acquire_async<P: IsA<Cancellable>, Q: FnOnce(Result<(), glib::Error>) + Send + 'static>(
        &self,
        cancellable: Option<&P>,
        callback: Q,
    );

    fn acquire_async_future(
        &self,
    );
}
fn get_allowed(&self) -> bool;

fn get_can_acquire(&self) -> bool;

fn get_can_release(&self) -> bool;

fn impl_update(&self, allowed: bool, can_acquire: bool, can_release: bool);

fn release<P: IsA<Cancellable>>(&self, cancellable: Option<&P>) -> Result<(), glib::Error>;;

fn release_async<P: IsA<Cancellable>, Q: FnOnce(Result<(), glib::Error>) + Send + 'static>(&self, cancellable: Option<&P>, callback: Q);

fn release_async_future(&self) -> Pin<Box<dyn std::future::Future<Output = Result<(), glib::Error>> + 'static>>;

fn connect_property_allowed_notify<F: Fn(&Self) + 'static>(&self, f: F) -> SignalHandlerId;

fn connect_property_can_acquire_notify<F: Fn(&Self) + 'static>(&self, f: F) -> SignalHandlerId;

fn connect_property_can_release_notify<F: Fn(&Self) + 'static>(&self, f: F) -> SignalHandlerId;

impl<O: IsA<Permission>> PermissionExt for O {
    fn acquire<P: IsA<Cancellable>, Q: FnOnce(Result<(), glib::Error>) + Send + 'static>(&self, cancellable: Option<&P>) -> Result<(), glib::Error> {
        unsafe {
            let mut error = ptr::null_mut();
            let _ = gio_sys::g_permission_acquire(
                self.as_ref().to_glib_none().0,
                cancellable.map(|p| p.as_ref()).to_glib_none().0,
                &mut error,
            );
            if error.is_null() { Ok(()).}
            } else { Err(from_glib_full(error))
        }
    }

    fn acquire_async<P: IsA<Cancellable>, Q: FnOnce(Result<(), glib::Error>) + Send + 'static>(&self,

    ) -> Pin<Box<dyn std::future::Future<Output = Result<(), glib::Error>> + 'static>>;

}
cancellable: Option<&P>,
callback: Q,

let user_data: Box_<Q> = Box::new(callback);
unsafe extern "C" fn acquire_async_trampoline<
    Q: FnOnce(Result<(), glib::Error>) + Send + 'static,
    >{
        _source_object: *mut gobject_sys::GObject,
        res: *mut gio_sys::GAsyncResult,
        user_data: glib_sys::gpointer,
    }

    let mut error = ptr::null_mut();
    let _ = gio_sys::g_permission_acquire_finish(_source_object as *mut _, res, &mut error);
    let result = if error.is_null() {
        Ok(())
    } else {
        Err(from_glib_full(error))
    };
    let callback: Box_<Q> = Box::from_raw(user_data as *mut _);
    callback(result);
}

let callback = acquire_async_trampoline::<Q>;
unsafe {
    gio_sys::g_permission_acquire_async(
        self.as_ref().to_glib_none().0,
        cancellable.map(|p| p.as_ref()).to_glib_none().0,
        Some(callback),
        Box_::into_raw(user_data as *mut _,
    ));
}

fn acquire_async_future(
    &self,
) -> Pin<Box<dyn std::future::Future<Output = Result<(), glib::Error>> + 'static>> {
    Box_::pin(crate::GioFuture::new(self, move |obj, send| {
        let cancellable = Cancellable::new();
        obj.acquire_async(Some(&cancellable), move |res| {
            send.resolve(res);
        });
    }));
    cancellable
})
}

fn get_allowed(&self) -> bool {
    unsafe {
        from_glib(gio_sys::g_permission_get_allowed(
fn get_can_acquire(&self) -> bool {
    unsafe {
        from_glib(gio_sys::g_permission_get_can_acquire(
            self.as_ref().to_glib_none().0,
        ));
    }
}

fn get_can_release(&self) -> bool {
    unsafe {
        from_glib(gio_sys::g_permission_get_can_release(
            self.as_ref().to_glib_none().0,
        ));
    }
}

fn impl_update(&self, allowed: bool, can_acquire: bool, can_release: bool) {
    unsafe {
        gio_sys::g_permission_impl_update(
            self.as_ref().to_glib_none().0,
            allowed.to_glib(),
            can_acquire.to_glib(),
            can_release.to_glib(),
        );
    }
}

fn release<P: IsA<Cancellable>>(&self, cancellable: Option<&P>) -> Result<(), glib::Error> {
    unsafe {
        let mut error = ptr::null_mut();
        let _ = gio_sys::g_permission_release(
            self.as_ref().to_glib_none().0,
            cancellable.map(|p| p.as_ref()).to_glib_none().0,
            &mut error,
        );
        if error.is_null() {
            Ok(())
        } else {
            Err(from_glib_full(error))
        }
    }
}

fn release_async<P: IsA<Cancellable>, Q: FnOnce(Result<(), glib::Error>) + Send + 'static>(
    &self,
    cancellable: Option<&P>,
    callback: Q,
) {
    let user_data: Box_<Q> = Box::new(callback);
    unsafe extern "C" fn release_async_trampoline<Q: FnOnce(Result<(), glib::Error>) + Send + 'static, >(
        _source_object: *mut gio::GObject,
        res: *mut gio::GAsyncResult,
        user_data: glib::gpointer,
    ) {
        let mut error = ptr::null_mut();
        let _ = gio::g_permission_release_finish(_source_object as *mut _, res, &mut error);
        let result = if error.is_null() {
            Ok::<Q>(())
        } else {
            Err(from_glib_full(error))
        };
        let callback: Box_<Q> = Box::from_raw(user_data as *mut _);
        callback(result);
    }
    let callback = release_async_trampoline::<Q>;
    unsafe {
        gio::g_permission_release_async(
            self.as_ref().to_glib_none().0,
            cancellable.map(|p| p.as_ref()).to_glib_none().0,
            Some(callback),
            Box::into_raw(user_data) as *mut _,
        );
    }
}

fn release_async_future(
    &self,
) -> Pin<Box<dyn std::future::Future<Output = Result<(), glib::Error>> + 'static>> {
    Box::pin(crate::GioFuture::new(self, move |obj, send| {
        let cancellable = Cancellable::new();
        obj.release_async(Some(&cancellable), move |res| {
            send.resolve(res);
        });
    }));
}

fn connect_property_allowed_notify<F: Fn(&Self) + 'static>(&self, f: F) -> SignalHandlerId {

}
unsafe extern "C" fn notify_allowed_trampoline<P, F: Fn(&P) + 'static>(
    this: *mut gio_sys::GPermission,
    _param_spec: glib_sys::gpointer,
    f: glib_sys::gpointer,
) where
    P: IsA<Permission>,
{
    let f: &F = &*(f as *const F);
    f(&Permission::from_glib_borrow(this).unsafe_cast())
}
unsafe {
    let f: Box_<F> = Box_::new(f);
    connect_raw(
        self.as_ptr() as *mut _,
        b"notify::allowed\".as_ptr() as *const _,
        Some(transmute(notify_allowed_trampoline::<Self, F> as usize)),
        Box_::into_raw(f),
    )
}
}

fn connect_property_can_acquire_notify<F: Fn(&Self) + 'static>(&self, f: F) -> SignalHandlerId {
unsafe extern "C" fn notify_can_acquire_trampoline<P, F: Fn(&P) + 'static>(
    this: *mut gio_sys::GPermission,
    _param_spec: glib_sys::gpointer,
    f: glib_sys::gpointer,
) where
    P: IsA<Permission>,
{
    let f: &F = &*(f as *const F);
    f(&Permission::from_glib_borrow(this).unsafe_cast())
}
unsafe {
    let f: Box_<F> = Box_::new(f);
    connect_raw(
        self.as_ptr() as *mut _,
        b"notify::can-acquire\".as_ptr() as *const _,
        Some(transmute(notify_can_acquire_trampoline::<Self, F> as usize)),
        Box_::into_raw(f),
    )
}
}

fn connect_property_can_release_notify<F: Fn(&Self) + 'static>(&self, f: F) -> SignalHandlerId {
unsafe extern "C" fn notify_can_release_trampoline<P, F: Fn(&P) + 'static>(
    this: *mut gio_sys::GPermission,
    _param_spec: glib_sys::gpointer,
    f: glib_sys::gpointer,
) where
    P: IsA<Permission>,
{...}
) where
P: IsA<Permission>,
{
  let f: &F = &*(f as *const F);
  f(&Permission::from_glib_borrow(this).unsafe_cast())
}
unsafe {
  let f: Box_<F> = Box_::new(f);
  connect_raw(
    self.as_ptr() as *mut _,
    b"notify::can-release\0".as_ptr as *const _,
    Some(transmute(notify_can_release_trampoline::<Self, F> as usize)),
    Box_::into_raw(f),
  )
}
}

impl fmt::Display for Permission {
  fn fmt(&self, f: &mut fmt::Formatter) -> fmt::Result {
    write!(f, "Permission")
  }
}

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Gnomovision version 69, Copyright (C) 19xx name of author
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<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!

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The End

1.19 libtext-iconv-perl 1.7-7

1.19.1 Available under license:

No license file was found, but licenses were detected in source scan.

Text::Iconv Version 1.6

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Description

This module provides a Perl interface to the iconv() codeset conversion function, as defined by the Single UNIX Specification. For more details see the POD documentation embedded in the file Iconv.pm, which will also be installed as Text::Iconv(3) man page.

Prerequisites

Your system should be compliant with the Single UNIX Specification or at least provide iconv_open(), iconv() and iconv_close() functions compliant with it.

Note that the possible conversions and the quality of the conversions depend on the available iconv conversion tables and algorithms, which
are in most cases supplied by the operating system vendor. Some systems also allow you to build your own tables (e.g., HP-UX, Tru64 UNIX, and AIX provide the genxlt(1) command).

It is also possible to use a separate iconv library such as GNU libiconv package, just make sure that the desired library is found (see <http://www.gnu.org/directory/localization/libiconv.html>).

Building the module
-------------------

Since the Single UNIX Specification only specifies an interface, several issues are left to the implementation to define, namely:

1. The supported codesets
2. The supported conversions
3. The names for the supported codesets

Please check your system documentation for the above points.

The module can be built using this sequence of commands:

```
perl Makefile.PL
make
make test
```

On some systems, the iconv functions are in libc; on others (and when you want to use a separate iconv library, such as GNU libiconv), you have to link with -liconv. Makefile.PL tries to automatically detect this. However, if your iconv library is in a non-standard path, you might have to help it. You can specify library and include paths like this:

```
perl Makefile.PL LIBS='-L/path/to/lib' INC='-I/path/to/include
```

You might also have to do this if you have multiple iconv implementations installed and want to use a specific one.

The purpose of the test scripts (run by "make test") is to check if an iconv library can be found, and if the iconv functions can be called. It is neither intended to find out which conversions are supported, nor to test the quality of the conversions provided by your iconv library. However, since the iconv functions can only be tested by calling them, which in turn requires the specification of codesets to be converted. The test scripts try some conversions which seem to be widely supported. Conversions not supported by your iconv implementation are skipped, but for the purpose outlined above this is no problem, as it shows that the iconv functions are working. There
is no standard way to find out which conversions are supported by an implementation. This is also the reason why Text::Iconv can't provide platform-independent support for codeset identifiers (even if it would be nice to have).

Installation
-------------

make install

Feedback
--------

Text::Iconv was tested on various platforms and with different iconv implementations.

If you built Text::Iconv on a platform where you needed to specify additional libraries, or where none of the codeset identifiers in the test scripts are supported, please drop me a note.

Thanks
-----

Thanks go to all those who reported bugs and suggested features.

Michael Piotrowski <mxp@dynalabs.de>

Found in path(s):
* /opt/cola/permits/1125674548_1611395382.31/0/libtext-iconv-perl-1-7-orig-2-tar-bz2/Text-Iconv-1.7/README

1.20 gsettings-desktop-schemas 3.36.0-1ubuntu1

1.20.1 Available under license:

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Version 2.1, February 1999

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1.21 harfbuzz 2.6.4-1ubuntu4

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1.23 debianutils 4.9.1
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1.24 gtk 3.24.18-1ubuntu1

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Version 2.1, February 1999

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1.25 xauth 1.1-0ubuntu1

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1.26 bash 5.0 6ubuntu1.1

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1.27 libencode-locale-perl 1.05-1

1.27.1 Available under license:
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Encode-Locale
The purpose of this Perl module is to determine what encodings should be used when interfacing to various external interfaces. You will need perl-5.8 or better to use this module.

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Found in path(s):
* /opt/cola/permits/1136700912_1613865105.39/0/libencode-locale-perl-1-05-orig-1-tar-gz/Encode-Locale-1.05/README
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---
abstract: 'Determine the locale encoding'
author:
- 'Gisle Aas <gisle@activestate.com>'
build_requires:
  ExtUtils::MakeMaker: '0'
  Test::More: '0'
configure_requires:
  ExtUtils::MakeMaker: '0'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.0401, CPAN::Meta::Converter version 2.15001'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: '1.4'
name: Encode-Locale
no_index:
directory:
  - t
  - inc
recommends:
  I18N::Langinfo: '0'
requires:
  Encode: '2'
  Encode::Alias: '0'
  perl: '5.008'
resources:
  repository: http://github.com/gisle/encode-locale
  version: '1.05'

Found in path(s):
* /opt/cola/permits/1136700912_1613865105.39/0/libencode-locale-perl-1-05-orig-1-tar-gz/Encode-Locale-
=head1 NAME

Encode::Locale - Determine the locale encoding

=head1 SYNOPSIS

use Encode::Locale;
use Encode;

$string = decode(locale => $bytes);
$bytes = encode(locale => $string);

if (-t) {
    binmode(STDIN, ':encoding(console_in)');
    binmode(STDOUT, ':encoding(console_out)');
    binmode(STDERR, ':encoding(console_out)');
}

# Processing file names passed in as arguments
my $uni_filename = decode(locale => $ARGV[0]);
open(my $fh, '<', encode(locale_fs => $uni_filename))
    || die "Can't open '$uni_filename': $!";
binmode($fh, ':encoding(locale)');
...

=head1 DESCRIPTION

In many applications it's wise to let Perl use Unicode for the strings it processes. Most of the interfaces Perl has to the outside world are still byte based. Programs therefore need to decode byte strings that enter the program from the outside and encode them again on the way out.

The POSIX locale system is used to specify both the language conventions requested by the user and the preferred character set to consume and output. The C<Encode::Locale> module looks up the charset and encoding (called a CODESET in the locale jargon) and arranges for the L<Encode> module to know this encoding under the name "locale". It means bytes obtained from the environment can be converted to Unicode strings by calling C<< Encode::encode(locale => $bytes) >> and converted back again with C<< Encode::decode(locale => $string) >>.

Where file systems interfaces pass file names in and out of the program we also need care. The trend is for operating systems to use a fixed file encoding that don't actually depend on the locale; and this module determines the most appropriate encoding for file names. The L<Encode> module will know this
encoding under the name "locale_fs". For traditional Unix systems this will be an alias to the same encoding as "locale".

For programs running in a terminal window (called a "Console" on some systems) the "locale" encoding is usually a good choice for what to expect as input and output. Some systems allows us to query the encoding set for the terminal and \texttt{C<Encode::Locale>} will do that if available and make these encodings known under the \texttt{C<Encode>} aliases "console_in" and "console_out". For systems where we can't determine the terminal encoding these will be aliased as the same encoding as "locale". The advice is to use "console_in" for input known to come from the terminal and "console_out" for output to the terminal.

In addition to arranging for various \texttt{Encode} aliases the following functions and variables are provided:

\begin{itemize}
\item \texttt{decode_argv( )}
\item \texttt{decode_argv( \texttt{Encode::FB_CROAK} )}
\end{itemize}

This will decode the command line arguments to perl (the \texttt{C<@ARGV>} array) in-place.

The function will by default replace characters that can't be decoded by "\texttt{x\{FFFD\}}", the Unicode replacement character.

Any argument provided is passed as \texttt{CHECK} to underlying \texttt{Encode::decode()} call. Pass the value \texttt{C<Encode::FB_CROAK>} to have the decoding croak if not all the command line arguments can be decoded. See \texttt{L<Encode/Handling Malformed Data> for details on other options for CHECK.}

\begin{itemize}
\item \texttt{env( \texttt{$uni_key} )}
\item \texttt{env( \texttt{$uni_key => $uni_value} )}
\end{itemize}

Interface to get/set environment variables. Returns the current value as a Unicode string. The \texttt{$uni_key} and \texttt{$uni_value} arguments are expected to be Unicode strings as well. Passing \texttt{C<undef>} as \texttt{$uni_value} deletes the environment variable named \texttt{$uni_key}.

The returned value will have the characters that can't be decoded replaced by "\texttt{x\{FFFD\}}", the Unicode replacement character.

There is no interface to request alternative \texttt{CHECK} behavior as for \texttt{decode_argv()}. If you need that you need to call \texttt{encode/decode} yourself. For example:

\begin{verbatim}
my $key = Encode::encode(locale => $uni_key, Encode::FB_CROAK);
\end{verbatim}
my $uni_value = Encode::decode(locale => $ENV{$key}, Encode::FB_CROAK);

=item reinit( )

=item reinit( $encoding )

Reinitialize the encodings from the locale. You want to call this function if you changed anything in the environment that might influence the locale.

This function will croak if the determined encoding isn't recognized by the Encode module.

With argument force $ENCODING_... variables to set to the given value.

=item $ENCODING_LOCALE

The encoding name determined to be suitable for the current locale. L<Encode> know this encoding as "locale".

=item $ENCODING_LOCALE_FS

The encoding name determined to be suitable for file system interfaces involving file names. L<Encode> know this encoding as "locale_fs".

=item $ENCODING_CONSOLE_IN

=item $ENCODING_CONSOLE_OUT

The encodings to be used for reading and writing output to the a console. L<Encode> know these encodings as "console_in" and "console_out".

=back

=head1 NOTES

This table summarizes the mapping of the encodings set up by the C<Encode::Locale> module:

<table>
<thead>
<tr>
<th>Encode</th>
<th>Windows</th>
<th>Mac OS X</th>
<th>POSIX</th>
</tr>
</thead>
<tbody>
<tr>
<td>Alias</td>
<td>ANSI</td>
<td>nl_langinfo</td>
<td>nl_langinfo</td>
</tr>
<tr>
<td>locale</td>
<td>ANSI</td>
<td>utf-8</td>
<td>nl_langinfo</td>
</tr>
<tr>
<td>locale_fs</td>
<td>ANSI</td>
<td>utf-8</td>
<td>nl_langinfo</td>
</tr>
<tr>
<td>console_in</td>
<td>OEM</td>
<td>nl_langinfo</td>
<td>nl_langinfo</td>
</tr>
<tr>
<td>console_out</td>
<td>OEM</td>
<td>nl_langinfo</td>
<td>nl_langinfo</td>
</tr>
</tbody>
</table>

=head2 Windows
Windows has basically 2 sets of APIs. A wide API (based on passing UTF-16 strings) and a byte based API based a character set called ANSI. The regular Perl interfaces to the OS currently only uses the ANSI APIs. Unfortunately ANSI is not a single character set.

The encoding that corresponds to ANSI varies between different editions of Windows. For many western editions of Windows ANSI corresponds to CP-1252 which is a character set similar to ISO-8859-1. Conceptually the ANSI character set is a similar concept to the POSIX locale CODESET so this module figures out what the ANSI code page is and make this available as $ENCODING_LOCALE and the "locale" Encoding alias.

Windows systems also operate with another byte based character set. It's called the OEM code page. This is the encoding that the Console takes as input and output. It's common for the OEM code page to differ from the ANSI code page.

=head2 Mac OS X

On Mac OS X the file system encoding is always UTF-8 while the locale can otherwise be set up as normal for POSIX systems.

File names on Mac OS X will at the OS-level be converted to NFD-form. A file created by passing a NFC-filename will come in NFD-form from readdir(). See L<Unicode::Normalize> for details of NFD/NFC.

Actually, Apple does not follow the Unicode NFD standard since not all character ranges are decomposed. The claim is that this avoids problems with round trip conversions from old Mac text encodings. See L<Encode::UTF8Mac> for details.

=head2 POSIX (Linux and other Unixes)

File systems might vary in what encoding is to be used for filenames. Since this module has no way to actually figure out what the is correct it goes with the best guess which is to assume filenames are encoding according to the current locale. Users are advised to always specify UTF-8 as the locale charset.

=head1 SEE ALSO

L<I18N::Langinfo>, L<Encode>, L<Term::Encoding>

=head1 AUTHOR

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=cut

Found in path(s):
* /opt/cola/permits/1136700912_1613865105.39/o/libencode-localperl-1-05-orig-1-tar-gz/Encode-Locale1.05/lib/Encode/Locale.pm

1.28 gotenv v1.2.0

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1.29 perl-openssl-defaults 4

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1.30 c-ares 1.40.0

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**1.31 glib-networking 2.64.2-1build1**

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1.33 cryptsetup 2.2.2-3ubuntu2.3
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We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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       (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
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of these things:

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machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable “work that
uses the Library”, as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
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that the user who changes the contents of definitions files in the
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b) Use a suitable shared library mechanism for linking with the
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the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

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e) Verify that the user has already received a copy of these
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components (compiler, kernel, and so on) of the operating system on
which the executable runs, unless that component itself accompanies
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1.41 openssl 1.1.1k

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1.45 libio-socket-ssl-perl 2.067-1

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---

abstract: 'Nearly transparent SSL encapsulation for IO::Socket::INET.'

author:
   - Steffen Ullrich <sullr@cpan.org>, Peter Behroozi, Marko Asplund

build_requires:
   ExtUtils::MakeMaker: '0'

configure_requires:
   ExtUtils::MakeMaker: '0'
   Net::SSLeay: '1.46'

dynamic_config: 1

generated_by: 'ExtUtils::MakeMaker version 7.24, CPAN::Meta::Converter version 2.150010'

license: perl

meta-spec:
   url: http://module-build.sourceforge.net/META-spec-v1.4.html

version: '1.4'

name: IO-Socket-SSL

no_index:

directory:
   - t
   - inc

requires:
   Mozilla::CA: '0'
   Net::SSLeay: '1.46'
=head1 NAME

IO::Socket::SSL - SSL sockets with IO::Socket interface

=head1 SYNOPSIS

use strict;
use IO::Socket::SSL;

# simple client
my $cl = IO::Socket::SSL->new('www.google.com:443');
print $cl "GET / HTTP/1.0\r\n\r\n";
print <$cl>;

# simple server
my $srv = IO::Socket::SSL->new(
    LocalAddr => '0.0.0.0:1234',
    Listen => 10,
    SSL_cert_file => 'server-cert.pem',
    SSL_key_file => 'server-key.pem',
);
$srv->accept;

=head1 DESCRIPTION

IO::Socket::SSL makes using SSL/TLS much easier by wrapping the necessary
functionality into the familiar L<IO::Socket> interface and providing secure
defaults whenever possible.
This way, existing applications can be made SSL-aware without much effort, at
least if you do blocking I/O and don't use select or poll.

But, under the hood, SSL is a complex beast.
So there are lots of methods to make it do what you need if the default
behavior is not adequate.
Because it is easy to inadvertently introduce critical security bugs or just hard to debug problems, I would recommend studying the following documentation carefully.

The documentation consists of the following parts:

=over 4

=item * L</"Essential Information About SSL/TLS">

=item * L</"Basic SSL Client">

=item * L</"Basic SSL Server">

=item * L</"Common Usage Errors">

=item * L</"Common Problems with SSL">

=item * L</"Using Non-Blocking Sockets">

=item * L</"Advanced Usage">

=item * L</"Integration Into Own Modules">

=item * L</"Description Of Methods">

=back

Additional documentation can be found in

=over 4

=item * L<IO::Socket::SSL::Intercept> - Doing Man-In-The-Middle with SSL

=item * L<IO::Socket::SSL::Utils> - Useful functions for certificates etc

=back

=head1 Essential Information About SSL/TLS

SSL (Secure Socket Layer) or its successor TLS (Transport Layer Security) are protocols to facilitate end-to-end security. These protocols are used when accessing web sites (https), delivering or retrieving email, and in lots of other use cases.

In the following documentation we will refer to both SSL and TLS as simply 'SSL'.

SSL enables end-to-end security by providing two essential functions:
=over 4

=item Encryption

This part encrypts the data for transit between the communicating parties, so that nobody in between can read them. It also provides tamper resistance so that nobody in between can manipulate the data.

=item Identification

This part makes sure that you talk to the right peer. If the identification is done incorrectly it is easy to mount man-in-the-middle attacks, e.g. if Alice wants to talk to Bob it would be possible for Mallory to put itself in the middle, so that Alice talks to Mallory and Mallory to Bob. All the data would still be encrypted, but not end-to-end between Alice and Bob, but only between Alice and Mallory and then between Mallory and Bob. Thus Mallory would be able to read and modify all traffic between Alice and Bob.

=back

Identification is the part which is the hardest to understand and the easiest to get wrong.

With SSL, the Identification is usually done with B<certificates> inside a B<PKI> (Public Key Infrastructure). These Certificates are comparable to an identity card, which contains information about the owner of the card. The card then is somehow B<signed> by the B<issuer> of the card, the B<CA> (Certificate Agency).

To verify the identity of the peer the following must be done inside SSL:

=over 4

=item *

Get the certificate from the peer. If the peer does not present a certificate we cannot verify it.

=item *

Check if we trust the certificate, e.g. make sure it's not a forgery.

We believe that a certificate is not a fake if we either know the certificate already or if we B<trust> the issuer (the CA) and can verify the issuers signature on the certificate. In reality there is often a hierarchy of certificate agencies and we only directly trust the root of this hierarchy.
In this case the peer not only sends his own certificate, but also all B<intermediate certificates>. Verification will be done by building a B<trust path> from the trusted root up to the peers certificate and checking in each step if the we can verify the issuer's signature.

This step often causes problems because the client does not know the necessary trusted root certificates. These are usually stored in a system dependent CA store, but often the browsers have their own CA store.

=item *

Check if the certificate is still valid. Each certificate has a lifetime and should not be used after that time because it might be compromised or the underlying cryptography got broken in the mean time.

=item *

Check if the subject of the certificate matches the peer. This is like comparing the picture on the identity card against the person representing the identity card.

When connecting to a server this is usually done by comparing the hostname used for connecting against the names represented in the certificate. A certificate might contain multiple names or wildcards, so that it can be used for multiple hosts (e.g. *.example.com and *.example.org).

Although nobody sane would accept an identity card where the picture does not match the person we see, it is a common implementation error with SSL to omit this check or get it wrong.

=item *

Check if the certificate was revoked by the issuer. This might be the case if the certificate was compromised somehow and now somebody else might use it to claim the wrong identity. Such revocations happened a lot after the heartbleed attack.

For SSL there are two ways to verify a revocation, CRL and OCSP. With CRLs (Certificate Revocation List) the CA provides a list of serial numbers for revoked certificates. The client somehow has to download the list (which can be huge) and keep it up to date. With OCSP (Online Certificate Status Protocol) the client can check a single certificate directly by asking the issuer.

Revocation is the hardest part of the verification and none of today's browsers get it fully correct. But, they are still better than most other implementations
which don't implement revocation checks or leave the hard parts to the
developer.

When accessing a web site with SSL or delivering mail in a secure way the
identity is usually only checked one way, e.g. the client wants to make sure it
talks to the right server, but the server usually does not care which client it
talks to.
But, sometimes the server wants to identify the client too and will request a
certificate from the client which the server must verify in a similar way.

=head1 Basic SSL Client

A basic SSL client is simple:

    my $client = IO::Socket::SSL->new('www.example.com:443')
or die "error=$!, ssl_error=$SSL_ERROR";

This will take the OpenSSL default CA store as the store for the trusted CA.
This usually works on UNIX systems.
If there are no certificates in the store it will try use L<Mozilla::CA> which
provides the default CAs of Firefox.

In the default settings, L<IO::Socket::SSL> will use a safer cipher set and SSL
version, do a proper hostname check against the certificate, and use SNI (server
name indication) to send the hostname inside the SSL handshake. This is
necessary to work with servers which have different certificates behind the
same IP address.
It will also check the revocation of the certificate with OCSP, but currently
only if the server provides OCSP stapling (for deeper checks see
C<ocsp_resolver> method).

Lots of options can be used to change ciphers, SSL version, location of CA and
much more. See documentation of methods for details.

With protocols like SMTP it is necessary to upgrade an existing socket to SSL.
This can be done like this:

    my $client = IO::Socket::INET->new('mx.example.com:25') or die $!;
    # .. read greeting from server
    # .. send EHLO and read response
    # .. send STARTTLS command and read response
    # .. if response was successful we can upgrade the socket to SSL now:
    IO::Socket::SSL->start_SSL($client,
    # explicitly set hostname we should use for SNI
    SSL_hostname => 'mx.example.com'
or die $SSL_ERROR;

A more complete example for a simple HTTP client:

```perl
my $client = IO::Socket::SSL->new(
  # where to connect
  PeerHost => "www.example.com",
  PeerPort => "https",

  # certificate verification - VERIFY_PEER is default
  SSL_verify_mode => SSL_VERIFY_PEER,

  # location of CA store
  # need only be given if default store should not be used
  SSL_ca_path => '/etc/ssl/certs', # typical CA path on Linux
  SSL_ca_file => '/etc/ssl/cert.pem', # typical CA file on BSD

  # or just use default path on system:
  IO::Socket::SSL::default_ca(), # either explicitly
  # or implicitly by not giving SSL_ca_*

  # easy hostname verification
  # It will use PeerHost as default name a verification
  # scheme as default, which is safe enough for most purposes.
  SSL_verifycn_name => 'foo.bar',
  SSL_verifycn_scheme => 'http',

  # SNI support - defaults to PeerHost
  SSL_hostname => 'foo.bar',

) or die "failed connect or ssl handshake: $!,$SSL_ERROR";

# send and receive over SSL connection
print $client "GET / HTTP/1.0\r\n\n";
print <$client>;

And to do revocation checks with OCSP (only available with OpenSSL 1.0.0 or higher and L<Net::SSLeay> 1.59 or higher):

```
# even better: make OCSP errors fatal
# (this will probably fail with lots of sites because of bad OCSP setups)
# also use common OCSP response cache
my $ocsp_cache = IO::Socket::SSL::OCSP_Cache->new;
my $client = IO::Socket::SSL->new(
  PeerAddr => $dst,
  SSL_ocsp_mode => SSL_OCSP_FULL_CHAIN|SSL_OCSP_FAIL_HARD,
  SSL_ocsp_cache => $ocsp_cache,
);  

# disable OCSP stapling in case server has problems with it
my $client = IO::Socket::SSL->new(
  PeerAddr => $dst,
  SSL_ocsp_mode => SSL_OCSP_NO_STAPLE,
);  

# check any certificates which are not yet checked by OCSP stapling or
# where we have already cached results. For your own resolving combine
# $ocsp->requests with $ocsp->add_response(uri, response).
my $ocsp = $client->ocsp_resolver();
my $errors = $ocsp->resolve_blocking();
if ($errors) {
  warn "OCSP verification failed: $errors"
  close($client);
}

=head1 Basic SSL Server

A basic SSL server looks similar to other L<IO::Socket> servers, only that it
also contains settings for certificate and key:

  # simple server
  my $server = IO::Socket::SSL->new(
    # where to listen
    LocalAddr => '127.0.0.1',
    LocalPort => 8080,
    Listen => 10,

    # which certificate to offer
    # with SNI support there can be different certificates per hostname
    SSL_cert_file => 'cert.pem',
    SSL_key_file => 'key.pem',
  ) or die "failed to listen: $!";

  # accept client
  my $client = $server->accept or die
  "failed to accept or ssl handshake: $!,SSL_ERROR";
This will automatically use a secure set of ciphers and SSL version and also supports Forward Secrecy with (Elliptic-Curve) Diffie-Hellmann Key Exchange.

If you are doing a forking or threading server, we recommend that you do the SSL handshake inside the new process/thread so that the master is free for new connections. We recommend this because a client with improper or slow SSL handshake could make the server block in the handshake which would be bad to do on the listening socket:

```
# inet server
my $server = IO::Socket::INET->new(
  # where to listen
  LocalAddr => '127.0.0.1',
  LocalPort => 8080,
  Listen => 10,
);

# accept client
my $client = $server->accept or die;

# SSL upgrade client (in new process/thread)
IO::Socket::SSL->start_SSL($client,
  SSL_server => 1,
  SSL_cert_file => 'cert.pem',
  SSL_key_file => 'key.pem',
) or die "failed to ssl handshake: $SSL_ERROR";
```

Like with normal sockets, neither forking nor threading servers scale well. It is recommended to use non-blocking sockets instead, see L</"Using Non-Blocking Sockets">

=head1 Common Usage Errors

This is a list of typical errors seen with the use of L<IO::Socket::SSL>:  

=over 4

=item *

Disabling verification with C<SSL_verify_mode>.

As described in L</"Essential Information About SSL/TLS">, a proper identification of the peer is essential and failing to verify makes Man-In-The-Middle attacks possible.

Nevertheless, lots of scripts and even public modules or applications disable verification, because it is probably the easiest way to make the thing work
and usually nobody notices any security problems anyway.

If the verification does not succeed with the default settings, one can do the following:

=over 8

=item *

Make sure the needed CAs are in the store, maybe use C<SSL_ca_file> or C<SSL_ca_path> to specify a different CA store.

=item *

If the validation fails because the certificate is self-signed and that's what you expect, you can use the C<SSL_fingerprint> option to accept specific leaf certificates by their certificate or pubkey fingerprint.

=item *

If the validation failed because the hostname does not match and you cannot access the host with the name given in the certificate, you can use C<SSL_verifycn_name> to specify the hostname you expect in the certificate.

=back

A common error pattern is also to disable verification if they found no CA store (different modules look at different "default" places). Because L<IO::Socket::SSL> is now able to provide a usable CA store on most platforms (UNIX, Mac OSX and Windows) it is better to use the defaults provided by L<IO::Socket::SSL>. If necessary these can be checked with the C<default_ca> method.

=item *

Polling of SSL sockets (e.g. select, poll and other event loops).

If you sysread one byte on a normal socket it will result in a syscall to read one byte. Thus, if more than one byte is available on the socket it will be kept in the network stack of your OS and the next select or poll call will return the socket as readable.

But, with SSL you don't deliver single bytes. Multiple data bytes are packaged and encrypted together in an SSL frame. Decryption can only be done on the whole frame, so a sysread for one byte actually reads the complete SSL frame from the socket, decrypts it and returns the first decrypted byte. Further sysreads will return more bytes from the same frame until all bytes are returned and the next SSL frame will be read from the socket.
Thus, in order to decide if you can read more data (e.g. if sysread will block) you must check if there are still data in the current SSL frame by calling `C<pending>` and if there are no data pending you might check the underlying socket with select or poll. Another way might be if you try to sysread at least 16kByte all the time. 16kByte is the maximum size of an SSL frame and because sysread returns data from only a single SSL frame you can guarantee that there are no pending data.

Additionally, contrary to plain sockets the data delivered on the socket are not necessarily application payload. It might be a TLS handshake, it might just be the beginning of a TLS record or it might be TLS session tickets which are send after the TLS handshake in TLS 1.3. In such situations select will return that data are available for read since it only looks at the plain socket. A sysread on the IO::Socket::SSL socket will not return any data though since it is an abstraction which only returns application data. This causes the sysread to hang in case the socket was blocking or to return an error with EAGAIN on non-blocking sockets. Applications using select or similar should therefore set the socket to non-blocking and also expect that the sysread might temporarily fail with EAGAIN.

See also L</"Using Non-Blocking Sockets">.

=item *

Expecting exactly the same behavior as plain sockets.

IO::Socket::SSL tries to emulate the usual socket behavior as good as possible, but full emulation can not be done. Specifically a read on the SSL socket might also result in a write on the TCP socket or a write on the SSL socket might result in a read on the TCP socket. Also C<accept> and B<close> on the SSL socket will result in writing and reading data to the TCP socket too.

Especially the hidden writes might result in a connection reset if the underlying TCP socket is already closed by the peer. Unless signal PIPE is explicitly handled by the application this will usually result in the application crashing. It is thus recommended to explicitly IGNORE signal PIPE so that the errors get propagated as EPIPE instead of causing a crash of the application.

=item *

Set 'SSL_version' or 'SSL_cipher_list' to a "better" value.

L<IO::Socket::SSL> tries to set these values to reasonable, secure values which
are compatible with the rest of the world.
But, there are some scripts or modules out there which tried to be smart and
get more secure or compatible settings.
Unfortunately, they did this years ago and never updated these values, so they
are still forced to do only 'TLSv1' (instead of also using TLSv12 or TLSv11).
Or they set 'HIGH' as the cipher list and thought they were secure, but did not
notice that 'HIGH' includes anonymous ciphers, e.g. without identification of
the peer.

So it is recommended to leave the settings at the secure defaults which
L<IO::Socket::SSL> sets and which get updated from time to time
to better fit the real world.

=item *

Make SSL settings inaccessible by the user, together with bad builtin settings.

Some modules use L<IO::Socket::SSL>, but don't make the SSL settings available
to the user. This is often combined with bad builtin settings or defaults (like
switching verification off).

Thus the user needs to hack around these restrictions by using
C<set_args_filter_hack> or similar.

=item *

Use of constants as strings.

Constants like C<SSL_VERIFY_PEER> or C<SSL_WANT_READ> should be used as
constants and not be put inside quotes, because they represent numerical values.

=item *

Forking and handling the socket in parent and child.

A B<fork> of the process will duplicate the internal user space SSL state of the
socket. If both master and child interact with the socket by using their own SSL
state strange error messages will happen. Such interaction includes explicit or
implicit B<close> of the SSL socket. To avoid this the socket should be explicitly
closed with B<SSL_no_shutdown>.

=item *

Forking and executing a new process.

Since the SSL state is stored in user space it will be duplicated by a B<fork> but
it will be lost when doing B<exec>. This means it is not possible to simply
redirect stdin and stdout for the new process to the SSL socket by duplicating
the relevant file handles. Instead explicitly exchanging plain data between child-process and SSL socket are needed.

=head1 Common Problems with SSL

SSL is a complex protocol with multiple implementations and each of these has their own quirks. While most of these implementations work together, it often gets problematic with older versions, minimal versions in load balancers, or plain wrong setups.

Unfortunately these problems are hard to debug. Helpful for debugging are a knowledge of SSL internals, wireshark and the use of the debug settings of L<IO::Socket::SSL> and L<Net::SSLeay>, which can both be set with C<$IO::Socket::SSL::DEBUG>.

The following debugs levels are defined, but used not in any consistent way:

=over 4

=item *

0 - No debugging (default).

=item *

1 - Print out errors from L<IO::Socket::SSL> and ciphers from L<Net::SSLeay>.

=item *

2 - Print also information about call flow from L<IO::Socket::SSL> and progress information from L<Net::SSLeay>.

=item *

3 - Print also some data dumps from L<IO::Socket::SSL> and from L<Net::SSLeay>.

=back

Also, C<analyze-ssl.pl> from the ssl-tools repository at L<https://github.com/noxxi/p5-ssl-tools> might be a helpful tool when debugging SSL problems, as do the C<openssl> command line tool and a check with a different SSL implementation (e.g. a web browser).

The following problems are not uncommon:
Bad server setup: missing intermediate certificates.

It is a regular problem that administrators fail to include all necessary certificates into their server setup, e.g. everything needed to build the trust chain from the trusted root. If they check the setup with the browser everything looks ok, because browsers work around these problems by caching any intermediate certificates and apply them to new connections if certificates are missing.

But, fresh browser profiles which have never seen these intermediates cannot fill in the missing certificates and fail to verify; the same is true with L<IO::Socket::SSL>.

Old versions of servers or load balancers which do not understand specific TLS versions or croak on specific data.

From time to time one encounters an SSL peer, which just closes the connection inside the SSL handshake. This can usually be worked around by downgrading the SSL version, e.g. by setting C<SSL_version>. Modern Browsers usually deal with such servers by automatically downgrading the SSL version and repeat the connection attempt until they succeed.

Worse servers do not close the underlying TCP connection but instead just drop the relevant packet. This is harder to detect because it looks like a stalled connection. But downgrading the SSL version often works here too.

A cause of such problems are often load balancers or security devices, which have hardware acceleration and only a minimal (and less robust) SSL stack. They can often be detected because they support much fewer ciphers than other implementations.

Bad or old OpenSSL versions.

L<IO::Socket::SSL> uses OpenSSL with the help of the L<Net::SSLeay> library. It is recommend to have a recent version of this library, because it has more features and usually fewer known bugs.

Validation of client certificates fail.
Make sure that the purpose of the certificate allows use as ssl client (check with C<< openssl x509 -purpose >>, that the necessary root certificate is in the path specified by C<SSL_ca*>) (or the default path) and that any intermediate certificates needed to build the trust chain are sent by the client.

Validation of self-signed certificate fails even if it is given with C<SSL_ca*> argument.

The C<SSL_ca*> arguments do not give a general trust store for arbitrary certificates but only specify a store for CA certificates which then can be used to verify other certificates. This especially means that certificates which are not a CA get simply ignored, notably self-signed certificates which do not also have the CA-flag set.

This behavior of OpenSSL differs from the more general trust-store concept which can be found in browsers and where it is possible to simply added arbitrary certificates (CA or not) as trusted.

Using Non-Blocking Sockets

If you have a non-blocking socket, the expected behavior on read, write, accept or connect is to set C<$!> to EWOULDBLOCK if the operation cannot be completed immediately. Note that EWOULDBLOCK is the same as EAGAIN on UNIX systems, but is different on Windows.

With SSL, handshakes might occur at any time, even within an established connection. In these cases it is necessary to finish the handshake before you can read or write data. This might result in situations where you want to read but must first finish the write of a handshake or where you want to write but must first finish a read.

In these cases C<$!> is set to EAGAIN like expected, and additionally C<$SSL_ERROR> is set to either SSL_WANT_READ or SSL_WANT_WRITE. Thus if you get EWOULDBLOCK on a SSL socket you must check C<$SSL_ERROR> for SSL_WANT_* and adapt your event mask accordingly.

Using readline on non-blocking sockets does not make much sense and I would advise against using it.

And, while the behavior is not documented for other L<IO::Socket> classes, it will try to emulate the behavior seen there, e.g. to return the received data instead of blocking, even if the line is not complete. If an unrecoverable error
occurs it will return nothing, even if it already received some data.

Also, I would advise against using C<accept> with a non-blocking SSL object because it might block and this is not what most would expect. The reason for this is that C<accept> on a non-blocking TCP socket (e.g. L<IO::Socket::IP>, L<IO::Socket::INET>..) results in a new TCP socket which does not inherit the non-blocking behavior of the master socket. And thus, the initial SSL handshake on the new socket inside C<IO::Socket::SSL::accept> will be done in a blocking way. To work around this you are safer by doing a TCP accept and later upgrade the TCP socket in a non-blocking way with C<start_SSL> and C<accept_SSL>.

my $cl = IO::Socket::SSL->new($dst);
$cl->blocking(0);
my $sel = IO::Select->new($cl);
while (1) {
  # with SSL a call for reading n bytes does not result in reading of n
  # bytes from the socket, but instead it must read at least one full SSL
  # frame. If the socket has no new bytes, but there are unprocessed data
  # from the SSL frame can_read will block!

  # wait for data on socket
  $sel->can_read();

  # new data on socket or eof
  READ:
  # this does not read only 1 byte from socket, but reads the complete SSL
  # frame and then just returns one byte. On subsequent calls it than
  # returns more byte of the same SSL frame until it needs to read the
  # next frame.
  my $n = sysread( $cl,my $buf,1);
  if ( ! defined $n ) {
    die $! if not ${EWOULDBLOCK};
    next if $SSL_ERROR == SSL_WANT_READ;
    if ( $SSL_ERROR == SSL_WANT_WRITE ) { # need to write data on renegotiation
      $sel->can_write;
      next;
    }
    die "something went wrong: $SSL_ERROR";
  } elsif ( ! $n ) {
    last; # eof
  } else {
    # read next bytes
    # we might have still data within the current SSL frame
    # thus first process these data instead of waiting on the underlying
    # socket object
    goto READ if $cl->pending;  # goto sysread
    next;  # goto $sel->can_read
  }

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Additionally there are differences to plain sockets when using select, poll, kqueue or similar technologies to get notified if data are available. Relying only on these calls is not sufficient in all cases since unread data might be internally buffered in the SSL stack. To detect such buffering `$pending()` need to be used. Alternatively the buffering can be avoided by using `$sysread` with the maximum size of an SSL frame. See L"Common Usage Errors" for details.

=head1 Advanced Usage

=head2 SNI Support

Newer extensions to SSL can distinguish between multiple hostnames on the same IP address using Server Name Indication (SNI).

Support for SNI on the client side was added somewhere in the OpenSSL 0.9.8 series, but with 1.0 a bug was fixed when the server could not decide about its hostname. Therefore client side SNI is only supported with OpenSSL 1.0 or higher in L<IO::Socket::SSL>.

With a supported version, SNI is used automatically on the client side, if it can determine the hostname from C<PeerAddr> or C<PeerHost> (which are synonyms in the underlying IO::Socket:: classes and thus should never be set both or at least not to different values).

On unsupported OpenSSL versions it will silently not use SNI.

The hostname can also be given explicitly given with C<SSL_hostname>, but in this case it will throw in error, if SNI is not supported.

To check for support you might call C<< IO::Socket::SSL->can_client_sni() >>.

On the server side, earlier versions of OpenSSL are supported, but only together with L<Net::SSLeay> version >= 1.50.

To check for support you might call C<< IO::Socket::SSL->can_server_sni() >>.

If server side SNI is supported, you might specify different certificates per host with C<SSL_cert*> and C<SSL_key*>, and check the requested name using C<get_servername>.

=head2 Talk Plain and SSL With The Same Socket

It is often required to first exchange some plain data and then upgrade the socket to SSL after some kind of STARTTLS command. Protocols like FTPS even need a way to downgrade the socket again back to plain.

The common way to do this would be to create a normal socket and use C<start_SSL> to upgrade and stop_SSL to downgrade:
my $sock = IO::Socket::INET->new(...) or die $!;
... exchange plain data on $sock until starttls command ...

IO::Socket::SSL->start_SSL($sock,%sslargs) or die $SSL_ERROR;
... now $sock is an IO::Socket::SSL object ...
... exchange data with SSL on $sock until stoptls command ...

$sock->stop_SSL or die $SSL_ERROR;
... now $sock is again an IO::Socket::INET object ...

But, lots of modules just derive directly from L<IO::Socket::INET>.
While this base class can be replaced with L<IO::Socket::SSL>, these modules cannot easily support different base classes for SSL and plain data and switch between these classes on a starttls command.

To help in this case, L<IO::Socket::SSL> can be reduced to a plain socket on startup, and connect_SSL/accept_SSL/start_SSL can be used to enable SSL and C<stop_SSL> to talk plain again:

    my $sock = IO::Socket::SSL->new(
      PeerAddr => ...
      SSL_startHandshake => 0,
      %sslargs
    ) or die $!;
    ... exchange plain data on $sock until starttls command ...
    $sock->connect_SSL or die $SSL_ERROR;
    ... now $sock is an IO::Socket::SSL object ...
    ... exchange data with SSL on $sock until stoptls command ...
    $sock->stop_SSL or die $SSL_ERROR;
    ... $sock is still an IO::Socket::SSL object ...
    ... but data exchanged again in plain ...

=head1 Integration Into Own Modules

L<IO::Socket::SSL> behaves similarly to other L<IO::Socket> modules and thus could be integrated in the same way, but you have to take special care when using non-blocking I/O (like for handling timeouts) or using select or poll.
Please study the documentation on how to deal with these differences.

Also, it is recommended to not set or touch most of the C<SSL_*> options, so that they keep their secure defaults. It is also recommended to let the user override these SSL specific settings without the need of global settings or hacks like C<set_args_filter_hack>.

The notable exception is C<SSL_verifycn_scheme>.
This should be set to the hostname verification scheme required by the module or protocol.
Description Of Methods

L<IO::Socket::SSL> inherits from another L<IO::Socket> module.

The choice of the super class depends on the installed modules:

-over 4

-item *

If L<IO::Socket::IP> with at least version 0.20 is installed it will use this module as super class, transparently providing IPv6 and IPv4 support.

-item *

If L<IO::Socket::INET6> is installed it will use this module as super class, transparently providing IPv6 and IPv4 support.

-item *

Otherwise it will fall back to L<IO::Socket::INET>, which is a perl core module. With L<IO::Socket::INET> you only get IPv4 support.

Please be aware that with the IPv6 capable super classes, it will look first for the IPv6 address of a given hostname. If the resolver provides an IPv6 address, but the host cannot be reached by IPv6, there will be no automatic fallback to IPv4.

To avoid these problems you can enforce IPv4 for a specific socket by using the C<Domain> or C<Family> option with the value AF_INET as described in L<IO::Socket::IP>. Alternatively you can enforce IPv4 globally by loading L<IO::Socket::SSL> with the option 'inet4', in which case it will use the IPv4 only class L<IO::Socket::INET> as the super class.

L<IO::Socket::SSL> will provide all of the methods of its super class, but sometimes it will override them to match the behavior expected from SSL or to provide additional arguments.

The new or changed methods are described below, but please also read the section about SSL specific error handling.

-over 4

-item Error Handling

If an SSL specific error occurs, the global variable C<SSL_ERROR> will be set.
If the error occurred on an existing SSL socket, the method $errstr$ will give access to the latest socket specific error.
Both $SSL_ERROR$ and the $errstr$ method give a dualvar similar to $S!$, e.g. providing an error number in numeric context or an error description in string context.

=item B$new(...)$

Creates a new L<IO::Socket::SSL> object. You may use all the friendly options that came bundled with the super class (e.g. L<IO::Socket::IP>, L<IO::Socket::INET>, ...) plus (optionally) the ones described below.
If you don't specify any SSL related options it will do its best in using secure defaults, e.g. choosing good ciphers, enabling proper verification, etc.

=over 2

=item SSL_server

Set this option to a true value if the socket should be used as a server.
If this is not explicitly set it is assumed if the C<Listen> parameter is given when creating the socket.

=item SSL_hostname

This can be given to specify the hostname used for SNI, which is needed if you have multiple SSL hostnames on the same IP address. If not given it will try to determine the hostname from C<PeerAddr>, which will fail if only an IP was given or if this argument is used within C<start_SSL>.
If you want to disable SNI, set this argument to "."
Currently only supported for the client side and will be ignored for the server side.
See section "SNI Support" for details of SNI the support.

=item SSL_startHandshake

If this option is set to false (defaults to true) it will not start the SSL handshake yet. This has to be done later with C<accept_SSL> or C<connect_SSL>.
Before the handshake is started read/write/etc. can be used to exchange plain data.

=item SSL_keepSocketOnError

If this option is set to true (defaults to false) it will not close the underlying TCP socket on errors. In most cases there is no real use for this
behavior since both sides of the TCP connection will probably have a
different idea of the current state of the connection.

=item SSL_ca | SSL_ca_file | SSL_ca_path

Usually you want to verify that the peer certificate has been signed by a
trusted certificate authority. In this case you should use this option to
specify the file (C<SSL_ca_file>) or directory (C<SSL_ca_path>) containing the
certificate(s) of the trusted certificate authorities.

C<SSL_ca_path> can also be an array or a string containing multiple path, where
the path are separated by the platform specific separator. This separator is
C<;> on DOS, Windows, Netware, C<,> on VMS and C<:> for all the other systems.
If multiple path are given at least one of these must be accessible.

You can also give a list of X509* certificate handles (like you get from
L<Net::SSLeay> or L<IO::Socket::SSL::Utils::PEM_xxx2cert>) with C<SSL_ca>. These
will be added to the CA store before path and file and thus take precedence.
If neither SSL_ca, nor SSL_ca_file or SSL_ca_path are set it will use
C<default_ca()> to determine the user-set or system defaults.
If you really don't want to set a CA set SSL_ca_file or SSL_ca_path to
C<undef> or SSL_ca to an empty list. (unfortunately C<''> is used by some
modules using L<IO::Socket::SSL> when CA is not explicitly given).

=item SSL_client_ca | SSL_client_ca_file

If verify_mode is VERIFY_PEER on the server side these options can be used to
set the list of acceptable CAs for the client. This way the client can select
they required certificate from a list of certificates.
The value for these options is similar to C<SSL_ca> and C<SSL_ca_file>.

=item SSL_fingerprint

Sometimes you have a self-signed certificate or a certificate issued by an
unknown CA and you really want to accept it, but don't want to disable
verification at all. In this case you can specify the fingerprint of the
certificate as C<'algo$hex_fingerprint'>. C<algo> is a fingerprint algorithm
supported by OpenSSL, e.g. 'sha1','sha256'... and C<hex_fingerprint> is the
hexadecimal representation of the binary fingerprint. Any colons inside the
hex string will be ignored.

If you want to use the fingerprint of the pubkey inside the certificate instead
of the certificate use the syntax C<'algo$pub$hex_fingerprint'> instead.
To get the fingerprint of an established connection you can use
C<get_fingerprint>.

It is also possible to skip C<algo$>, i.e. only specify the fingerprint. In
this case the likely algorithms will be automatically detected based on the
length of the digest string.

You can specify a list of fingerprints in case you have several acceptable certificates.
If a fingerprint matches the topmost (i.e. leaf) certificate no additional validations can make the verification fail.

=item SSL_cert_file | SSL_cert | SSL_key_file | SSL_key

If you create a server you usually need to specify a server certificate which should be verified by the client. Same is true for client certificates, which should be verified by the server.
The certificate can be given as a file with SSL_cert_file or as an internal representation of an X509* object (like you get from L<Net::SSLeay> or L<IO::Socket::SSL::Utils::PEM_xxx2cert>) with SSL_cert.
If given as a file it will automatically detect the format.
Supported file formats are PEM, DER and PKCS#12, where PEM and PKCS#12 can contain the certificate and the chain to use, while DER can only contain a single certificate.

If given as a list of X509* please note, that the all the chain certificates (e.g. all except the first) will be "consumed" by openssl and will be freed if the SSL context gets destroyed - so you should never free them yourself. But the servers certificate (e.g. the first) will not be consumed by openssl and thus must be freed by the application.

For each certificate a key is need, which can either be given as a file with SSL_key_file or as an internal representation of an EVP_PKEY* object with SSL_key (like you get from L<Net::SSLeay> or L<IO::Socket::SSL::Utils::PEM_xxx2key>).
If a key was already given within the PKCS#12 file specified by SSL_cert_file it will ignore any SSL_key or SSL_key_file.
If no SSL_key or SSL_key_file was given it will try to use the PEM file given with SSL_cert_file again, maybe it contains the key too.

If your SSL server should be able to use different certificates on the same IP address, depending on the name given by SNI, you can use a hash reference instead of a file with C<<hostname => cert_file>>.

If your SSL server should be able to use both RSA and ECDSA certificates for the same domain/IP a similar hash reference like with SNI is given. The domain names used to specify the additional certificates should be C<hostname%whatever>, i.e. C<hostname%ecc> or similar. This needs at least OpenSSL 1.0.2. To let the server pick the certificate based on the clients cipher preference C<SSL_honor_cipher_order> should be set to false.

In case certs and keys are needed but not given it might fall back to builtin defaults, see "Defaults for Cert, Key and CA".
Examples:

SSL_cert_file => 'mycert.pem',
SSL_key_file => 'mykey.pem',

SSL_cert_file => {
    "foo.example.org" => 'foo-cert.pem',
    "foo.example.org%ecc" => 'foo-ecc-cert.pem',
    "bar.example.org" => 'bar-cert.pem',
    # used when nothing matches or client does not support SNI
    "" => 'default-cert.pem',
    '%ecc' => 'default-ecc-cert.pem',
},
SSL_key_file => {
    "foo.example.org" => 'foo-key.pem',
    "foo.example.org%ecc" => 'foo-ecc-key.pem',
    "bar.example.org" => 'bar-key.pem',
    # used when nothing matches or client does not support SNI
    "" => 'default-key.pem',
    '%ecc' => 'default-ecc-key.pem',
}

=item SSL_passwd_cb

If your private key is encrypted, you might not want the default password prompt from Net::SSLeay. This option takes a reference to a subroutine that should return the password required to decrypt your private key.

=item SSL_use_cert

If this is true, it forces IO::Socket::SSL to use a certificate and key, even if you are setting up an SSL client. If this is set to 0 (the default), then you will only need a certificate and key if you are setting up a server.

SSL_use_cert will implicitly be set if SSL_server is set.
For convenience it is also set if it was not given but a cert was given for use (SSL_cert_file or similar).

=item SSL_version

Sets the version of the SSL protocol used to transmit data.
'SSLv23' uses a handshake compatible with SSL2.0, SSL3.0 and TLS1.x, while 'SSLv2', 'SSLv3', 'TLSv1', 'TLSv1_1', 'TLSv1_2', or 'TLSv1_3' restrict handshake and protocol to the specified version.
All values are case-insensitive. Instead of 'TLSv1_1', 'TLSv1_2', and 'TLSv1_3' one can also use 'TLSv11', 'TLSv12', and 'TLSv13'. Support for
'TLSv1_1', 'TLSv1_2', and 'TLSv1_3' requires recent versions of Net::SSLeay and openssl.

Independent from the handshake format you can limit to set of accepted SSL versions by adding !version separated by ':'.

The default SSL_version is 'SSLv23:!SSLv3:!SSLv2' which means, that the handshake format is compatible to SSL2.0 and higher, but that the successful handshake is limited to TLS1.0 and higher, that is no SSL2.0 or SSL3.0 because both of these versions have serious security issues and should not be used anymore.

You can also use !TLSv1_1 and !TLSv1_2 to disable TLS versions 1.1 and 1.2 while still allowing TLS version 1.0.

Setting the version instead to 'TLSv1' might break interaction with older clients, which need and SSL2.0 compatible handshake. On the other side some clients just close the connection when they receive a TLS version 1.1 request. In this case setting the version to 'SSLv23:!SSLv2:!SSLv3:!TLSv1_1:!TLSv1_2' might help.

=item SSL_cipher_list

If this option is set the cipher list for the connection will be set to the given value, e.g. something like 'ALL:!LOW:!EXP:!aNULL'. Look into the OpenSSL documentation (L<https://www.openssl.org/docs/manmaster/man1/openssl-ciphers.html#CIPHER-STRINGS>) for more details.

Unless you fail to contact your peer because of no shared ciphers it is recommended to leave this option at the default setting. The default setting prefers ciphers with forward secrecy, disables anonymous authentication and disables known insecure ciphers like MD5, DES etc. This gives a grade A result at the tests of SSL Labs.

To use the less secure OpenSSL builtin default (whatever this is) set SSL_cipher_list to ".

In case different cipher lists are needed for different SNI hosts a hash can be given with the host as key and the cipher suite as value, similar to B<SSL_cert*>.

=item SSL_honor_cipher_order

If this option is true the cipher order the server specified is used instead of the order proposed by the client. This option defaults to true to make use of our secure cipher list setting.

=item SSL_dh_file

To create a server which provides forward secrecy you need to either give the DH
parameters or (better, because faster) the ECDH curve. This setting cares about DH parameters.

To support non-elliptic Diffie-Hellman key exchange a suitable file needs to be given here or the SSL_dh should be used with an appropriate value. See dhparam command in openssl for more information.

If neither C<SSL_dh_file> nor C<SSL_dh> are set a builtin DH parameter with a length of 2048 bit is used to offer DH key exchange by default. If you don't want this (e.g. disable DH key exchange) explicitly set this or the C<SSL_dh> parameter to undef.

=item SSL_dh

Like SSL_dh_file, but instead of giving a file you use a preloaded or generated DH*.

=item SSL_ecdh_curve

To create a server which provides forward secrecy you need to either give the DH parameters or (better, because faster) the ECDH curve. This setting cares about the ECDH curve(s).

To support Elliptic Curve Diffie-Hellmann key exchange the OID or NID of at least one suitable curve needs to be provided here.

With OpenSSL 1.1.0+ this parameter defaults to C<auto>, which means that it lets OpenSSL pick the best settings. If support for CTX_set_ecdh_auto is implemented in Net::SSLeay (needs at least version 1.86) it will use this to implement the same default. Otherwise it will default to C<prime256v1> (builtin of OpenSSL) in order to offer ECDH key exchange by default.

If setting groups or curves is supported by Net::SSLeay (needs at least version 1.86) then multiple curves can be given here in the order of the preference, i.e. C<P-521:P-384:P-256>. When used at the client side this will include the supported curves as extension in the TLS handshake.

If you don't want to have ECDH key exchange this could be set to undef or set C<SSL_ciphers> to exclude all of these ciphers.

You can check if ECDH support is available by calling C<< IO::Socket::SSL->can_ecdh >>.

=item SSL_verify_mode

This option sets the verification mode for the peer certificate.
You may combine SSL_VERIFY_PEER (verify_peer), SSL_VERIFY_FAIL_IF_NO_PEER_CERT (fail verification if no peer certificate exists; ignored for clients),
SSL_VERIFY_CLIENT_ONCE (verify client once; ignored for clients). See OpenSSL man page for SSL_CTX_set_verify for more information.

The default is SSL_VERIFY_NONE for server (e.g. no check for client certificate) and SSL_VERIFY_PEER for client (check server certificate).

=item SSL_verify_callback

If you want to verify certificates yourself, you can pass a sub reference along with this parameter to do so. When the callback is called, it will be passed:

=over 4

=item 1.
a true/false value that indicates what OpenSSL thinks of the certificate,

=item 2.
a C-style memory address of the certificate store,

=item 3.
a string containing the certificate's issuer attributes and owner attributes, and

=item 4.
a string containing any errors encountered (0 if no errors).

=item 5.
a C-style memory address of the peer's own certificate (convertible to PEM form with Net::SSLeay::PEM_get_string_X509()).

=item 6.
The depth of the certificate in the chain. Depth 0 is the leaf certificate.

=back

The function should return 1 or 0, depending on whether it thinks the certificate is valid or invalid. The default is to let OpenSSL do all of the busy work.

The callback will be called for each element in the certificate chain.

See the OpenSSL documentation for SSL_CTX_set_verify for more information.

=item SSL_verifycn_scheme

The scheme is used to correctly verify the identity inside the certificate by using the hostname of the peer. See the information about the verification schemes in B<verify_hostname>.
If you don't specify a scheme it will use 'default', but only complain loudly if
the name verification fails instead of letting the whole certificate
verification fail. THIS WILL CHANGE, e.g. it will let the certificate
verification fail in the future if the hostname does not match the certificate
!!!! To override the name used in verification use B<SSL_verifycn_name>.

The scheme 'default' is a superset of the usual schemes, which will accept the
hostname in common name and subjectAltName and allow wildcards everywhere.
While using this scheme is way more secure than no name verification at all you
better should use the scheme specific to your application protocol, e.g. 'http',
'ftp'...

If you are really sure, that you don't want to verify the identity using the
hostname you can use 'none' as a scheme. In this case you'd better have
alternative forms of verification, like a certificate fingerprint or do a manual
verification later by calling B<verify_hostname> yourself.

=item SSL_verifycn_publicsuffix

This option is used to specify the behavior when checking wildcards certificates
for public suffixes, e.g. no wildcard certificates for *.com or *.co.uk should
be accepted, while *.example.com or *.example.co.uk is ok.

If not specified it will simply use the builtin default of
L<IO::Socket::SSL::PublicSuffix>, you can create another object with
from_string or from_file of this module.

To disable verification of public suffix set this option to C""

=item SSL_verifycn_name

Set the name which is used in verification of hostname. If SSL_verifycn_scheme
is set and no SSL_verifycn_name is given it will try to use SSL_hostname or
PeerHost and PeerAddr settings and fail if no name can be determined.
If SSL_verifycn_scheme is not set it will use a default scheme and warn if it
cannot determine a hostname, but it will not fail.

Using PeerHost or PeerAddr works only if you create the connection directly
with C<< IO::Socket::SSL->new >>, if an IO::Socket::INET object is upgraded
with B<start_SSL> the name has to be given in B<SSL_verifycn_name> or
B<SSL_hostname>.

=item SSL_check_crl

If you want to verify that the peer certificate has not been revoked
by the signing authority, set this value to true. OpenSSL will search
for the CRL in your SSL_ca_path, or use the file specified by
SSL_crl_file. See the Net::SSLeay documentation for more details. Note that this functionality appears to be broken with OpenSSL < v0.9.7b, so its use with lower versions will result in an error.

=item SSL_crl_file

If you want to specify the CRL file to be used, set this value to the pathname to be used. This must be used in addition to setting SSL_check_crl.

=item SSL_ocsp_mode

Defines how certificate revocation is done using OCSP (Online Status Revocation Protocol). The default is to send a request for OCSP stapling to the server and if the server sends an OCSP response back the result will be used.

Any other OCSP checking needs to be done manually with C<ocsp_resolver>.

The following flags can be combined with C<|>:

=over 8

=item SSL_OCSP_NO_STAPLE

Don't ask for OCSP stapling. This is the default if SSL_verify_mode is VERIFY_NONE.

=item SSL_OCSP_TRY_STAPLE

Try OCSP stapling, but don't complain if it gets no stapled response back. This is the default if SSL_verify_mode is VERIFY_PEER (the default).

=item SSL_OCSP_MUST_STAPLE

Consider it a hard error, if the server does not send a stapled OCSP response back. Most servers currently send no stapled OCSP response back.

=item SSL_OCSP_FAIL_HARD

Fail hard on response errors, default is to fail soft like the browsers do. Soft errors mean, that the OCSP response is not usable, e.g. no response, error response, no valid signature etc. Certificate revocations inside a verified response are considered hard errors in any case.

Soft errors inside a stapled response are never considered hard, e.g. it is expected that in this case an OCSP request will be send to the responsible OCSP responder.
=item SSL_OCSP_FULL_CHAIN

This will set up the C<ocsp_resolver> so that all certificates from the peer chain will be checked, otherwise only the leaf certificate will be checked against revocation.

=back

=item SSL_ocsp_staple_callback

If this callback is defined, it will be called with the SSL object and the OCSP response handle obtained from the peer, e.g. C<<$cb->($ssl,$resp)>>.
If the peer did not provide a stapled OCSP response the function will be called with C<$resp=undef>.
Because the OCSP response handle is no longer valid after leaving this function it should not by copied or freed. If access to the response is necessary after leaving this function it can be serialized with C<Net::SSLeay::i2d_OCSP_RESPONSE>.

If no such callback is provided, it will use the default one, which verifies the response and uses it to check if the certificate(s) of the connection got revoked.

=item SSL_ocsp_cache

With this option a cache can be given for caching OCSP responses, which could be shared between different SSL contexts. If not given a cache specific to the SSL context only will be used.

You can either create a new cache with C<< IO::Socket::SSL::OCSP_Cache->new([size]) >> or implement your own cache, which needs to have methods C<put($key,%entry)> and C<get($key)> (returning C<%entry>) where entry is the hash representation of the OCSP response with fields like C<nextUpdate>. The default implementation of the cache will consider responses valid as long as C<nextUpdate> is less then the current time.

=item SSL_reuse_ctx

If you have already set the above options for a previous instance of IO::Socket::SSL, then you can reuse the SSL context of that instance by passing it as the value for the SSL_reuse_ctx parameter. You may also create a new instance of the IO::Socket::SSL::SSL_Context class, using any context options that you desire without specifying connection options, and pass that here instead.

If you use this option, all other context-related options that you pass in the same call to new() will be ignored unless the context supplied was
invalid. Note that, contrary to versions of IO::Socket::SSL below v0.90, a global SSL context will not be implicitly used unless you use the set_default_context() function.

=item SSL_create_ctx_callback

With this callback you can make individual settings to the context after it got created and the default setup was done. The callback will be called with the CTX object from Net::SSLeay as the single argument.

Example for limiting the server session cache size:

SSL_create_ctx_callback => sub {
    my $ctx = shift;
    Net::SSLeay::CTX_sess_set_cache_size($ctx,128);
}

=item SSL_session_cache_size

If you make repeated connections to the same host/port and the SSL renegotiation time is an issue, you can turn on client-side session caching with this option by specifying a positive cache size. For successive connections, pass the SSL_reuse_ctx option to the new() calls (or use set_default_context()) to make use of the cached sessions. The session cache size refers to the number of unique host/port pairs that can be stored at one time; the oldest sessions in the cache will be removed if new ones are added.

This option does not effect the session cache a server has for it's clients, e.g. it does not affect SSL objects with SSL_server set.

Note that session caching with TLS 1.3 needs at least Net::SSLeay 1.86.

=item SSL_session_cache

Specifies session cache object which should be used instead of creating a new. Overrules SSL_session_cache_size. This option is useful if you want to reuse the cache, but not the rest of the context.

A session cache object can be created using
C<< IO::Socket::SSL::Session_Cache->new( cachesize ) >>.

Use set_default_session_cache() to set a global cache object.

=item SSL_session_key

Specifies a key to use for lookups and inserts into client-side session cache.
Per default ip:port of destination will be used, but sometimes you want to share the same session over multiple ports on the same server (like with FTPS).

=item SSL_session_id_context

This gives an id for the servers session cache. It's necessary if you want clients to connect with a client certificate. If not given but SSL_verify_mode specifies the need for client certificate a context unique id will be picked.

=item SSL_error_trap

When using the accept() or connect() methods, it may be the case that the actual socket connection works but the SSL negotiation fails, as in the case of an HTTP client connecting to an HTTPS server. Passing a subroutine ref attached to this parameter allows you to gain control of the orphaned socket instead of having it be closed forcibly.

The subroutine, if called, will be passed two parameters: a reference to the socket on which the SSL negotiation failed and the full text of the error message.

=item SSL_npn_protocols

If used on the server side it specifies list of protocols advertised by SSL server as an array ref, e.g. ['spdy/2', 'http1.1']. On the client side it specifies the protocols offered by the client for NPN as an array ref.

See also method C<next_proto_negotiated>.

Next Protocol Negotiation (NPN) is available with Net::SSLeay 1.46+ and openssl-1.0.1+. NPN is unavailable in TLSv1.3 protocol. To check support you might call C<< IO::Socket::SSL->can_npn() >>. If you use this option with an unsupported Net::SSLeay/OpenSSL it will throw an error.

=item SSL_alpn_protocols

If used on the server side it specifies list of protocols supported by the SSL server as an array ref, e.g. ['http/2.0', 'spdy/3.1', 'http/1.1']. On the client side it specifies the protocols advertised by the client for ALPN as an array ref.

See also method C<alpn_selected>.

Application-Layer Protocol Negotiation (ALPN) is available with Net::SSLeay 1.56+ and openssl-1.0.2+. More details about the extension are in RFC7301. To check support you might call C<< IO::Socket::SSL->can_alpn() >>. If you use this option with an unsupported Net::SSLeay/OpenSSL it will throw an error.

Note that some client implementations may encounter problems if both NPN and
ALPN are specified. Since ALPN is intended as a replacement for NPN, try providing ALPN protocols then fall back to NPN if that fails.

=item SSL_ticket_keycb => [$sub,$data] | $sub

This is a callback used for stateless session reuse (Session Tickets, RFC 5077).

This callback will be called as C<< $sub->($data,[{$key_name}]) >> where C<$data> is the argument given to SSL_ticket_keycb (or undef) and C<$key_name> depends on the mode:

=over 8

=item encrypt ticket

If a ticket needs to be encrypted the callback will be called without C<$key_name>. In this case it should return C<<($current_key,$current_key_name)> where C<$current_key> is the current key (32 byte random data) and C<$current_key_name> the name associated with this key (exactly 16 byte). This C<$current_key_name> will be incorporated into the ticket.

=item decrypt ticket

If a ticket needs to be decrypted the callback will be called with C<$key_name> as found in the ticket. It should return C<<($key,$current_key_name)> where C<$key> is the key associated with the given C<$key_name> and C<$current_key_name> the name associated with the currently active key. If C<$current_key_name> is different from the given C<$key_name> the callback will be called again to re-encrypt the ticket with the currently active key.

If no key can be found which matches the given C<$key_name> then this function should return nothing (empty list).

This mechanism should be used to limit the life time for each key encrypting the ticket. Compromise of a ticket encryption key might lead to decryption of SSL sessions which used session tickets protected by this key.

=back

Example:

Net::SSLeay::RAND_bytes(my $oldkey,32);
Net::SSLeay::RAND_bytes(my $newkey,32);
my $oldkey_name = pack("a16","oldsecret");
my $newkey_name = pack("a16","newsecret");

my @keys = ( [ $newkey_name, $newkey ], # current active key
[ $oldkey_name, $oldkey ], # already expired
);

my $keycb = [ sub {
    my ($mykeys,$name) = @_;

    # return (current_key, current_key_name) if no name given
    return ($mykeys->[0][1],$mykeys->[0][0]) if ! $name;

    # return (matching_key, current_key_name) if we find a key matching
    # the given name
    for(my $i = 0; $i<@$mykeys; $i++) {
        next if $name ne $mykeys->[Si][0];
        return ($mykeys->[Si][1],$mykeys->[0][0]);
    }

    # no matching key found
    return;
},
}@keys ];

my $srv = IO::Socket::SSL->new(..., SSL_ticket_keycb => $keycb);

=item SSL_mode_release_buffers 1|0

This enables or disables the SSL_MODE_RELEASE_BUFFERS option on the SSL object. With this option the read buffer will be released after each SSL_read but will need to be reallocated for each new SSL_read. If memory usage is a concern this might save lots of memory in the mean time though, about 34k per idle SSL connection according to the documentation in SSL_CTX_set_mode(3ssl).

=back

=item B<accept>

This behaves similar to the accept function of the underlying socket class, but additionally does the initial SSL handshake. But because the underlying socket class does return a blocking file handle even when accept is called on a non-blocking socket, the SSL handshake on the new file object will be done in a blocking way. Please see the section about non-blocking I/O for details. If you don't like this behavior you should do accept on the TCP socket and then upgrade it with C<start_SSL> later.

=item B<connect(...)>

This behaves similar to the connect function but also does an SSL handshake. Because you cannot give SSL specific arguments to this function, you should better either use C<new> to create a connect SSL socket or C<start_SSL> to upgrade an established TCP socket to SSL.
Contrary to a close for a simple INET socket a close in SSL also mandates a proper shutdown of the SSL part. This is done by sending a close notify message by both peers.

A naive implementation would thus wait until it receives the close notify message from the peer - which conflicts with the commonly expected semantic that a close will not block. The default behavior is thus to only send a close notify but not wait for the close notify of the peer. If this is required C<SSL_fast_shutdown> need to be explicitly set to false.

There are also cases where a SSL shutdown should not be done at all. This is true for example when forking to let a child deal with the socket and closing the socket in the parent process. A naive explicit C<close> or an implicit close when destroying the socket in the parent would send a close notify to the peer which would make the SSL socket in the client process unusable. In this case an explicit C<close> with C<SSL_no_shutdown> set to true should be done in the parent process.

For more details and other arguments see C<stop_SSL> which gets called from C<close> to shutdown the SSL state of the socket.

This function behaves from the outside the same as B<sysread> in other L<IO::Socket> objects, e.g. it returns at most LEN bytes of data. But in reality it reads not only LEN bytes from the underlying socket, but at a single SSL frame. It then returns up to LEN bytes it decrypted from this SSL frame. If the frame contained more data than requested it will return only LEN data, buffer the rest and return it on further read calls. This means, that it might be possible to read data, even if the underlying socket is not readable, so using poll or select might not be sufficient.

sysread will only return data from a single SSL frame, e.g. either the pending data from the already buffered frame or it will read a frame from the underlying socket and return the decrypted data. It will not return data spanning several SSL frames in a single call.

Also, calls to sysread might fail, because it must first finish an SSL handshake.

To understand these behaviors is essential, if you write applications which use event loops and/or non-blocking sockets. Please read the specific sections in this documentation.
This function behaves from the outside the same as B<syswrite> in other L<IO::Socket> objects, e.g. it will write at most LEN bytes to the socket, but there is no guarantee, that all LEN bytes are written. It will return the number of bytes written.

Because it basically just calls SSL_write from OpenSSL syswrite will write at most a single SSL frame. This means, that no more than 16.384 bytes, which is the maximum size of an SSL frame, will be written at once.

For non-blocking sockets SSL specific behavior applies. Please read the specific section in this documentation.

=item B<peek( BUF, LEN, [ OFFSET ])>  

This function has exactly the same syntax as B<sysread>, and performs nearly the same task but will not advance the read position so that successive calls to peek() with the same arguments will return the same results. This function requires OpenSSL 0.9.6a or later to work.

=item B<pending()>

This function gives you the number of bytes available without reading from the underlying socket object. This function is essential if you work with event loops, please see the section about polling SSL sockets.

=item B<get_fingerprint([algo,certificate,pubkey])>

This method returns the fingerprint of the given certificate in the form C<algo$digest_hex>, where C<algo> is the used algorithm, default 'sha256'. If no certificate is given the peer certificate of the connection is used. If C<pubkey> is true it will not return the fingerprint of the certificate but instead the fingerprint of the pubkey inside the certificate as C<algo$pub$digest_hex>.

=item B<get_fingerprint_bin([algo,certificate,pubkey])>

This method returns the binary fingerprint of the given certificate by using the algorithm C<algo>, default 'sha256'. If no certificate is given the peer certificate of the connection is used. If C<pubkey> is true it will not return the fingerprint of the certificate but instead the fingerprint of the pubkey inside the certificate.

=item B<get_cipher()>

Returns the string form of the cipher that the IO::Socket::SSL object is using.

=item B<get_sslversion()>
Returns the string representation of the SSL version of an established connection.

=item B<get_sslversion_int()>

Returns the integer representation of the SSL version of an established connection.

=item B<get_session_reused()>

This returns true if the session got reused and false otherwise. Note that with a reused session no certificates are send within the handshake and no ciphers are offered and thus functions which rely on this might not work.

=item B<dump_peer_certificate()>

Returns a parsable string with select fields from the peer SSL certificate. This method directly returns the result of the dump_peer_certificate() method of Net::SSLeay.

=item B<peer_certificate($field;[$refresh])>

If a peer certificate exists, this function can retrieve values from it. If no field is given the internal representation of certificate from Net::SSLeay is returned. If refresh is true it will not used a cached version, but check again in case the certificate of the connection has changed due to renegotiation.

The following fields can be queried:

=over 8

=item authority (alias issuer)

The certificate authority which signed the certificate.

=item owner (alias subject)

The owner of the certificate.

=item commonName (alias cn) - only for Net::SSLeay version >=1.30

The common name, usually the server name for SSL certificates.

=item subjectAltNames - only for Net::SSLeay version >=1.33

Alternative names for the subject, usually different names for the same server, like example.org, example.com, *.example.com.
It returns a list of (typ,value) with typ GEN_DNS, GEN_IPADD etc (these constants are exported from IO::Socket::SSL).
See Net::SSLeay::X509_get_subjectAltNames.

=back

=item B<sock_certificate($field)>

This is similar to C<peer_certificate> but will return the sites own certificate. The same arguments for B<$field> can be used.
If no B<$field> is given the certificate handle from the underlying OpenSSL will be returned. This handle will only be valid as long as the SSL connection exists and if used afterwards it might result in strange crashes of the application.

=item B<peer_certificates>

This returns all the certificates send by the peer, e.g. first the peers own certificate and then the rest of the chain. You might use B<CERT_asHash> from L<IO::Socket::SSL::Utils> to inspect each of the certificates.

This function depends on a version of Net::SSLeay >= 1.58.

=item B<get_servername>

This gives the name requested by the client if Server Name Indication (SNI) was used.

=item B<verify_hostname($hostname,$scheme,$publicsuffix)>

This verifies the given hostname against the peer certificate using the given scheme. Hostname is usually what you specify within the PeerAddr.
See the C<SSL_verifycn_publicsuffix> parameter for an explanation of suffix checking and for the possible values.

Verification of hostname against a certificate is different between various applications and RFCs. Some scheme allow wildcards for hostnames, some only in subjectAltNames, and even their different wildcard schemes are possible.
RFC 6125 provides a good overview.

To ease the verification the following schemes are predefined (both protocol name and rfcXXXX name can be used):

=over 8

=item rfc2818, xmpp (rfc3920), ftp (rfc4217)

Extended wildcards in subjectAltNames and common name are possible, e.g.
*.example.org or even www*.example.org. The common name will be only checked if no DNS names are given in subjectAltNames.

=item http (alias www)

While name checking is defined in rfc2818 the current browsers usually accept also an IP address (w/o wildcards) within the common name as long as no subjectAltNames are defined. Thus this is rfc2818 extended with this feature.

=item smtp (rfc2595), imap, pop3, acap (rfc4642), netconf (rfc5538), syslog (rfc5425), snmp (rfc5953)

Simple wildcards in subjectAltNames are possible, e.g. *.example.org matches www.example.org but not lala.www.example.org. If nothing from subjectAltNames match it checks against the common name, where wildcards are also allowed to match the full leftmost label.

=item ldap (rfc4513)

Simple wildcards are allowed in subjectAltNames, but not in common name. Common name will be checked even if subjectAltNames exist.

=item sip (rfc5922)

No wildcards are allowed and common name is checked even if subjectAltNames exist.

=item gist (rfc5971)

Simple wildcards are allowed in subjectAltNames and common name, but common name will only be checked if their are no DNS names in subjectAltNames.

=item default

This is a superset of all the rules and is automatically used if no scheme is given but a hostname (instead of IP) is known. Extended wildcards are allowed in subjectAltNames and common name and common name is checked always.

=item none

No verification will be done. Actually is does not make any sense to call verify_hostname in this case.

=back

The scheme can be given either by specifying the name for one of the above predefined schemes, or by using a hash which can have the following keys and values:
Determine if the common name gets checked. If 'always' it will always be checked (like in ldap), if 'when_only' it will only be checked if no names are given in subjectAltNames (like in http), for any other values the common name will not be checked.

Determines if and where wildcards in subjectAltNames are possible. If 'full_label' only cases like *.example.org will be possible (like in ldap), for 'anywhere' www*.example.org is possible too (like http), dangerous things like but www.*.org or even '*' will not be allowed.
For compatibility with older versions 'leftmost' can be given instead of 'full_label'.

Similar to wildcards_in_alt, but checks the common name. There is no predefined scheme which allows wildcards in common names.

Determines if an IP address is allowed in the common name (no wildcards are allowed). If set to 4 or 6 it only allows IPv4 or IPv6 addresses, any other true value allows both.

If you give a subroutine for verification it will be called with the arguments ($hostname,$commonName,@subjectAltNames), where hostname is the name given for verification, commonName is the result from peer_certificate('cn') and subjectAltNames is the result from peer_certificate('subjectAltNames').

All other arguments for the verification scheme will be ignored in this case.

This method returns the name of negotiated protocol - e.g. 'http/1.1'. It works for both client and server side of SSL connection.

NPN support is available with Net::SSLeay 1.46+ and openssl-1.0.1+. To check support you might call C<< IO::Socket::SSL->can_npn() >>.
=item B<alpn_selected()>

Returns the protocol negotiated via ALPN as a string, e.g. 'http/1.1', 'http/2.0' or 'spdy/3.1'.

ALPN support is available with Net::SSLeay 1.56+ and openssl-1.0.2+.
To check support, use C<< IO::Socket::SSL->can_alpn() >>.

=item B<errstr()>

Returns the last error (in string form) that occurred. If you do not have a real object to perform this method on, call IO::Socket::SSL::errstr() instead.

For read and write errors on non-blocking sockets, this method may include the string C<SSL wants a read first!> or C<SSL wants a write first!> meaning that the other side is expecting to read from or write to the socket and wants to be satisfied before you get to do anything. But with version 0.98 you are better comparing the global exported variable $SSL_ERROR against the exported symbols SSL_WANT_READ and SSL_WANT_WRITE.

=item B<opened()>

This returns false if the socket could not be opened, 1 if the socket could be opened and the SSL handshake was successful done and -1 if the underlying IO::Handle is open, but the SSL handshake failed.

=item B<< IO::Socket::SSL->start_SSL($socket, ... ) >>

This will convert a glob reference or a socket that you provide to an IO::Socket::SSL object. You may also pass parameters to specify context or connection options as with a call to new(). If you are using this function on an accepted socket, you must set the parameter "SSL_server" to 1, i.e. IO::Socket::SSL->start_SSL($socket, SSL_server => 1). If you have a class that inherits from IO::Socket::SSL and you want the $socket to be blessed into your own class instead, use MyClass->start_SSL($socket) to achieve the desired effect.

Note that if start_SSL() fails in SSL negotiation, $socket will remain blessed in its original class. For non-blocking sockets you better just upgrade the socket to IO::Socket::SSL and call accept_SSL or connect_SSL and the upgraded object. To just upgrade the socket set B<SSL_startHandshake> explicitly to 0. If you call start_SSL w/o this parameter it will revert to blocking behavior for accept_SSL and connect_SSL.

If given the parameter "Timeout" it will stop if after the timeout no SSL connection was established. This parameter is only used for blocking sockets, if it is not given the default Timeout from the underlying IO::Socket will be
used.

=item B<stop_SSL(...)>

This is the opposite of start_SSL(), connect_SSL() and accept_SSL(), e.g. it will shutdown the SSL connection and return to the class before start_SSL(). It gets the same arguments as close(), in fact close() calls stop_SSL() (but without downgrading the class).

Will return true if it succeeded and undef if failed. This might be the case for non-blocking sockets. In this case $! is set to EWOULDBLOCK and the ssl error to SSL_WANT_READ or SSL_WANT_WRITE. In this case the call should be retried again with the same arguments once the socket is ready.

For calling from C<stop_SSL> C<SSL_fast_shutdown> default to false, e.g. it waits for the close_notify of the peer. This is necessary in case you want to downgrade the socket and continue to use it as a plain socket.

After stop_SSL the socket can again be used to exchange plain data.

=item B<connect_SSL>, B<accept_SSL>

These functions should be used to do the relevant handshake, if the socket got created with C<new> or upgraded with C<start_SSL> and C<SSL_startHandshake> was set to false.
They will return undef until the handshake succeeded or an error got thrown.
As long as the function returns undef and $! is set to EWOULDBLOCK one could retry the call after the socket got readable (SSL_WANT_READ) or writeable (SSL_WANT_WRITE).

=item B<ocsp_resolver>

This will create an OCSP resolver object, which can be used to create OCSP requests for the certificates of the SSL connection. Which certificates are verified depends on the setting of C<SSL_ocsp_mode>: by default only the leaf certificate will be checked, but with SSL_OCSP_FULL_CHAIN all chain certificates will be checked.

Because to create an OCSP request the certificate and its issuer certificate need to be known it is not possible to check certificates when the trust chain is incomplete or if the certificate is self-signed.

The OCSP resolver gets created by calling C<< $ssl->ocsp_resovler >> and provides the following methods:

=over 8

=item hard_error
This returns the hard error when checking the OCSP response.
Hard errors are certificate revocations. With the C<SSL_ocsp_mode> of
SSL_OCSP_FAIL_HARD any soft error (e.g. failures to get signed information
about the certificates) will be considered a hard error too.

The OCSP resolving will stop on the first hard error.

The method will return undef as long as no hard errors occurred and still
requests to be resolved. If all requests got resolved and no hard errors
occurred the method will return C<"">.

=item soft_error

This returns the soft error(s) which occurred when asking the OCSP responders.

=item requests

This will return a hash consisting of C<(url,request)>-tuples, e.g. which
contain the OCSP request string and the URL where it should be sent too. The
usual way to send such a request is as HTTP POST request with a content-type
of C<application/ocsp-request> or as a GET request with the base64 and
url-encoded request is added to the path of the URL.

After you've handled all these requests and added the response with
C<add_response> you should better call this method again to make sure, that no
more requests are outstanding. IO::Socket::SSL will combine multiple OCSP
requests for the same server inside a single request, but some server don't
give a response to all these requests, so that one has to ask again with the
remaining requests.

=item add_response($uri,$response)

This method takes the HTTP body of the response which got received when sending
the OCSP request to C<$uri>. If no response was received or an error occurred
one should either retry or consider C<$response> as empty which will trigger a
soft error.

The method returns the current value of C<hard_error>, e.g. a defined value
when no more requests need to be done.

=item resolve_blocking(%args)

This combines C<requests> and C<add_response> which L<HTTP::Tiny> to do all
necessary requests in a blocking way. C<%args> will be given to L<HTTP::Tiny>
so that you can put proxy settings etc here. L<HTTP::Tiny> will be called with
C<verify_SSL> of false, because the OCSP responses have their own signatures so
no extra SSL verification is needed.
If you don't want to use blocking requests you need to roll your own user agent with `requests` and `add_response`.

=back

=item B<< IO::Socket::SSL->new_from_fd($fd, [mode], %sslargs) >>

This will convert a socket identified via a file descriptor into an SSL socket. Note that the argument list does not include a "MODE" argument; if you supply one, it will be thoughtfully ignored (for compatibility with IO::Socket::INET). Instead, a mode of `:+<` is assumed, and the file descriptor passed must be able to handle such I/O because the initial SSL handshake requires bidirectional communication.

Internally the given $fd will be upgraded to a socket object using the C`<new_from_fd>` method of the super class (L<IO::Socket::INET> or similar) and then C`<start_SSL>` will be called using the given C`<%sslargs>`. If C<$fd> is already an IO::Socket object you should better call C`<start_SSL>` directly.

=item B<IO::Socket::SSL::default_ca([ path|dir| SSL_ca_file => ..., SSL_ca_path => ... ])>

Determines or sets the default CA path. If existing path or dir or a hash is given it will set the default CA path to this value and never try to detect it automatically. If C<undef> is given it will forget any stored defaults and continue with detection of system defaults. If no arguments are given it will start detection of system defaults, unless it has already stored user-set or previously detected values.

The detection of system defaults works similar to OpenSSL, e.g. it will check the directory specified in environment variable SSL_CERT_DIR or the path OPENSSLDIR/certs (SSLCERTS: on VMS) and the file specified in environment variable SSL_CERT_FILE or the path OPENSSLDIR/cert.pem (SSLCERTS:cert.pem on VMS). Contrary to OpenSSL it will check if the SSL_ca_path contains PEM files with the hash as file name and if the SSL_ca_file looks like PEM. If no usable system default can be found it will try to load and use L<Mozilla::CA> and if not available give up detection. The result of the detection will be saved to speed up future calls.

The function returns the saved default CA as hash with SSL_ca_file and SSL_ca_path.

=item B<IO::Socket::SSL::set_default_context(...)>

You may use this to make IO::Socket::SSL automatically re-use a given context (unless specifically overridden in a call to new()).
It accepts one argument, which should be either an IO::Socket::SSL object or an
IO::Socket::SSL::SSL_Context object.
See the SSL_reuse_ctx option of new() for more details.
Note that this sets the default context globally, so use with caution (esp. in
mod_perl scripts).
=item B<IO::Socket::SSL::set_default_session_cache(...)>
You may use this to make IO::Socket::SSL automatically re-use a given session
cache (unless specifically overridden in a call to new()).
It accepts one argument, which should be an IO::Socket::SSL::Session_Cache
object or similar (e.g. something which implements get_session, add_session and
del_session like IO::Socket::SSL::Session_Cache does).
See the SSL_session_cache option of new() for more details.
Note that this sets the default cache globally, so use with caution.
=item B<IO::Socket::SSL::set_defaults(%args)>
With this function one can set defaults for all SSL_* parameter used for
creation of the context, like the SSL_verify* parameter. Any SSL_* parameter can
be given or the following short versions:
=over 8
=item mode - SSL_verify_mode
=item callback - SSL_verify_callback
=item scheme - SSL_verifycn_scheme
=item name - SSL_verifycn_name
=back
=item B<IO::Socket::SSL::set_client_defaults(%args)>
Similar to C<set_defaults>, but only sets the defaults for client mode.
=item B<IO::Socket::SSL::set_server_defaults(%args)>
Similar to C<set_defaults>, but only sets the defaults for server mode.
=item B<IO::Socket::SSL::set_args_filter_hack(\&code|'use_defaults')>
Sometimes one has to use code which uses unwanted or invalid arguments for SSL,
typically disabling SSL verification or setting wrong ciphers or SSL versions.
With this hack it is possible to override these settings and restore sanity.
Example:

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IO::Socket::SSL::set_args_filter_hack( sub {
    my ($is_server,$args) = @_;
    if ( ! $is_server ) {
        # client settings - enable verification with default CA
        # and fallback hostname verification etc
        delete @{$args}{qw(
            SSL_verify_mode
            SSL_ca_file
            SSL_ca_path
            SSL_verifycn_scheme
            SSL_version
        )};
        # and add some fingerprints for known certs which are signed by
        # unknown CAs or are self-signed
        $args->{SSL_fingerprint} = ...
    }
});

With the short setting C<set_args_filter_hack('use_defaults')> it will prefer
the default settings in all cases. These default settings can be modified with
C<set_defaults>, C<set_client_defaults> and C<set_server_defaults>.

=back

The following methods are unsupported (not to mention futile!) and
IO::Socket::SSL will emit a large CROAK() if you are silly enough to use them:

=over 4
=item truncate
=item stat
=item ungetc
=item setbuf
=item setvbuf
=item fdopen
=item send/recv

Note that send() and recv() cannot be reliably trapped by a tied filehandle
(such as that used by IO::Socket::SSL) and so may send unencrypted data over the
socket. Object-oriented calls to these functions will fail, telling you to use
the print/printf/syswrite and read/sysread families instead.
DEPRECATIONS

The following functions are deprecated and are only retained for compatibility:

context_init()

use the SSL_reuse_ctx option if you want to re-use a context

socketToSSL() and socket_to_SSL()

use IO::Socket::SSL->start_SSL() instead

kill_socket()

use close() instead

get_peer_certificate()

use the peer_certificate() function instead. Used to return X509_Certificate with methods subject_name and issuer_name. Now simply returns $self which has these methods (although deprecated).

issuer_name()

use peer_certificate( 'issuer' ) instead

subject_name()

use peer_certificate( 'subject' ) instead

EXAMPLES

See the 'example' directory, the tests in 't' and also the tools in 'util'.

BUGS

If you use IO::Socket::SSL together with threads you should load it (e.g. use or require) inside the main thread before creating any other threads which use it. This way it is much faster because it will be initialized only once. Also there
are reports that it might crash the other way.

Creating an IO::Socket::SSL object in one thread and closing it in another thread will not work.

IO::Socket::SSL does not work together with Storable::fd_retrieve/fd_store. See BUGS file for more information and how to work around the problem.

Non-blocking and timeouts (which are based on non-blocking) are not supported on Win32, because the underlying IO::Socket::INET does not support non-blocking on this platform.

If you have a server and it looks like you have a memory leak you might check the size of your session cache. Default for Net::SSLeay seems to be 20480, see the example for SSL_create_ctx_callback for how to limit it.

TLS 1.3 support regarding session reuse is incomplete.

=head1 SEE ALSO

IO::Socket::INET, IO::Socket::INET6, IO::Socket::IP, Net::SSLeay.

=head1 THANKS

Many thanks to all who added patches or reported bugs or helped IO::Socket::SSL another way. Please keep reporting bugs and help with patches, even if they just fix the documentation.

Special thanks to the team of Net::SSLeay for the good cooperation.

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Patches incorporated from various people, see file Changes.

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* /opt/cola/permits/1146929071_1616836857.87/0/libio-socket-ssl-perl-2-067-orig-tar-gz/IO-Socket-SSL-2.067/lib/IO/Socket/SSL.pod
No license file was found, but licenses were detected in source scan.

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Found in path(s):
* /opt/cola/permits/1146929071_1616836857.87/0/libio-socket-ssl-perl-2-067-orig-tar-gz/IO-Socket-SSL-2.067/lib/IO/Socket/SSL/PublicSuffix.pm

1.46 libx11 1.6.9-2ubuntu1.1

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1.52 zlib 1.2.11

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/* zlib.h -- interface of the 'zlib' general purpose compression library  
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1.53 libxcrypt 4.4.10-10ubuntu4
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1.60 libxkbfile 1.1.0-1

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Client application for querying drivers' configuration information
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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1.66 libxtst 1.2.3-1

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1.67 libmailtools-perl 2.21-1

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=encoding utf8

=head1 NAME

Mail::Address - parse mail addresses

=head1 SYNOPSIS

use Mail::Address;
my @addrs = Mail::Address->parse($line);

foreach $addr (@addrs) {
    print $addr->format,"\n";
}

=head1 DESCRIPTION

C<Mail::Address> extracts and manipulates email addresses from a message
header. It cannot be used to extract addresses from some random text.
You can use this module to create RFC822 compliant fields.

Although C<Mail::Address> is a very popular subject for books, and is
used in many applications, it does a very poor job on the more complex
message fields. It does only handle simple address formats (which
covers about 95% of what can be found). Problems are with

=over 4

=item *

no support for address groups, even not with the semi-colon as
separator between addresses;

=item *


limited support for escapes in phrases and comments. There are
cases where it can get wrong; and

you have to take care of most escaping when you create an address yourself:
C<Mail::Address> does not do that for you.

Often requests are made to the maintainers of this code improve this
situation, but this is not a good idea, where it will break zillions
of existing applications. If you wish for a fully RFC2822 compliant
implementation you may take a look at L<Mail::Message::Field::Full>,
part of MailBox.

B<. Example>

my $s = Mail::Message::Field::Full->new($from_header);
# ref $s isa Mail::Message::Field::Addresses;

my @$g = $s->groups;       # all groups, at least one
# ref @$g[0] isa Mail::Message::Field::AddrGroup;
my @$ga = @$g[0]->addresses; # group addresses

my @$a = @$s->addresses;   # all addresses
# ref @$a[0] isa Mail::Message::Field::Address;

=head1 METHODS

=head2 Constructors

=over 4

=item Mail::Address-E<gt>B<new>( $phrase, $address, [ $comment ] )

Create a new C<Mail::Address> object which represents an address with the
elements given. In a message these 3 elements would be seen like:

PHRASE <ADDRESS> (COMMENT)
ADDRESS (COMMENT)

example:

Mail::Address->new("Perl5 Porters", "perl5-porters@africa.nicoh.com");

=item $obj-E<gt>B<parse>($line)
Parse the given line a return a list of extracted `Mail::Address` objects. The line would normally be one taken from a To, Cc or Bcc line in a message.

Example:

```
my @addr = Mail::Address->parse($line);
```

=head2 Accessors

=item `$obj->address()`

Return the address part of the object.

=item `$obj->comment()`

Return the comment part of the object.

=item `$obj->format(@addresses)`

Return a string representing the address in a suitable form to be placed on a `To`, `Cc`, or `Bcc` line of a message. This method is called on the first address to be used; other specified addresses will be appended, separated by commas.

=item `$obj->phrase()`

Return the phrase part of the object.

=head2 Smart accessors

=item `$obj->host()`

Return the address excluding the user id and '@'

=item `$obj->name()`

Using the information contained within the object attempt to identify what the person or group's name is.
This function tries to be smart with the "phrase" of the email address, which is probably a very bad idea. Consider to use L<phrase()|Mail::Address/"Accessors"> itself.

Return the address excluding the '@' and the mail domain

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<lt>aas@osloneE<gt>.  
Mail::Field::AddrList by Peter Orbaek E<lt>poe@cit.dkE<gt>.  
Mail::Mailer and Mail::Send by Tim Bunce E<lt>Tim.Bunce@ig.co.ukE<gt>.  
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my $filter = Mail::Filter->new( \&filter1, \&filter2 );
my $mail = Mail::Internet->new( ['<'] );
my $mail = $filter->filter($mail);
my $folder = Mail::Folder->new( '...' );
my $filter->filter($folder);

=head1 DESCRIPTION

C-Mail::Filter> provides an interface to filtering Email through multiple subroutines.

C-Mail::Filter> filters mail by calling each filter subroutine in turn. Each filter subroutine is called with two arguments, the first is the filter object and the second is the mail or folder object being filtered.

The result from each filter sub is passed to the next filter as the mail object. If a filter subroutine returns undef, then C-Mail::Filter> will abort and return immediately.

The function returns the result from the last subroutine to operate on the mail object.

=head1 METHODS

=head2 Constructors

=over 4

=item Mail::Filter-E<gt>B<new>(@filters)

Create a new C-Mail::Filter> object with the given filter subroutines. Each filter may be either a code reference or the name of a method to call on the <Mail::Filter> object.

=back

=head2 Accessors

=over 4

=item $obj-E<gt>B<add>(@filters)

Add the given @filters to the end of the filter list.

=back
=head2 Processing

=over 4

=item $obj-E<gt>B<filter>(Mail|$folder)

If the first argument is a Mail::Internet object, then this object will be passed through the filter list. If the first argument is a Mail::Folder object, then each message in turn will be passed through the filter list.

=item $obj-E<gt>B<folder>()

While the filter method is called with a Mail::Folder object, these filter subroutines can call this method to obtain the folder object that is being processed.

=item $obj-E<gt>B<msgnum>()

If the filter method is called with a Mail::Folder object, then the filter subroutines may call this method to obtain the message number of the message that is being processed.

=back

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<aas@oslone.nt.no@gt>
Mail::Field::AddrList by Peter Orback E<poe@cit.dk@gt>
Mail::Mailer and Mail::Send by Tim Bunce E<Tim.Bunce@ig.co.uk@gt>
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(encoding utf8)

=head1 NAME

Mail::Internet - manipulate email messages

=head1 SYNOPSIS

use Mail::Internet;
my $msg = Mail::Internet->new(*STDIN);

=head1 DESCRIPTION

This package implements reading, creating, manipulating, and writing email messages. Sometimes, the implementation tries to be too smart, but in the general case it works as expected.

If you start writing a new application, you should use the Mail::Box distribution, which has more features and handles messages much better according to the RFCs. See http://perl.overmeer.net/mailbox/. You may also chose MIME::Entity, to get at least some multipart support in your application.

=head1 METHODS

=head2 Constructors

=over 4

=item $obj->dup()

Duplicate the message as a whole. Both header and body will be deep-copied: a new Mail::Internet object is returned.

=item $obj->extract(@lines)

Extract header and body from an ARRAY of message lines. Requires an object already created with Mail::Internet/'Constructors', which contents will get overwritten.

=item $obj->new( [arg], [options] )

=item Mail::Internet->new( [arg], [options] )
$arg is optional and may be either a file descriptor (reference to a GLOB) or a reference to an array. If given the new object will be initialized with headers and body either from the array or read from the file descriptor.

The \texttt{Mail::Header::new()} \texttt{Mail::Header/"Constructors"} \%options \texttt{C<Modify>}, \texttt{C<MailFrom>} and \texttt{C<FoldLength>} may also be given.

- **Option--Default**
  - Body \[\]
  - Header \texttt{undef}

=over 2

=item Body \=> ARRAY-of-LINES

The value of this option should be a reference to an array which contains the lines for the body of the message. Each line should be terminated with \texttt{C<\n>} (LF). If Body is given then \texttt{Mail::Internet} will not attempt to read the body from \texttt{$arg} (even if it is specified).

=item Header \=> Mail::Header

The value of this option should be a \texttt{Mail::Header} object. If given then \texttt{Mail::Internet} will not attempt to read a mail header from \texttt{$arg}, if it was specified.

=back

=item \$obj-\texttt{read}($fh)

Read a message from the \$fh into an already existing message object. Better use \texttt{Mail::Internet/"Constructors"} with the \$fh as first argument.

=back

=head2 Accessors

=over 4

=item \$obj-\texttt{body} \(
  \texttt{[}$body\texttt{]} \) \texttt{)}

Returns the body of the message. This is a reference to an array. Each entry in the array represents a single line in the message.

If I\$body is given, it can be a reference to an array or an array, then
the body will be replaced. If a reference is passed, it is used directly and not copied, so any subsequent changes to the array will change the contents of the body.

=item $obj->B<head>()

Returns the C<Mail::Header> object which holds the headers for the current message.

=back

=head2 Processing the message as a whole

=over 4

=item $obj->B<as_mbox_string>()

Returns the message as a string in mbox format. C<$already_escaped>, if given and true, indicates that L<escape_from()> has already been called on this object.

=item $obj->B<as_string>()

Returns the message as a single string.

=item $obj->B<print>()

Print the header, body or whole message to file descriptor I<$fh>. I<$fd> should be a reference to a GLOB. If I<$fh> is not given the output will be sent to STDOUT.

example:

   $mail->print( \*STDOUT );  # Print message to STDOUT

=item $obj->B<print_body>()

Print only the body to the $fh (default STDOUT).

=item $obj->B<print_header>()

Print only the header to the $fh (default STDOUT).

=back

=head2 Processing the header

Most of these methods are simply wrappers around methods provided
by L<Mail::Header::Mail::Header>. 

=item_obj-E<gt>B<add>(PAIRS)

The PAIRS are field-name and field-content. For each PAIR, L<Mail::Header::add()|Mail::Header/"Processing"> is called. All fields are added after existing fields. The last addition is returned.

=item_obj-E<gt>B<combine>( $tag, [$with] )

See L<Mail::Header::combine()|Mail::Header/"Processing">.

=item_obj-E<gt>B<delete>( $tag, [$tags] )

Delete all fields with the name $tag. L<Mail::Header::delete()|Mail::Header/"Processing"> is doing the work.

=item_obj-E<gt>B<fold>( [length] )

See L<Mail::Header::fold()|Mail::Header/"Processing">.

=item_obj-E<gt>B<fold_length>( [tag], [length] )

See L<Mail::Header::fold_length()|Mail::Header/"Accessors">.

=item_obj-E<gt>B<get>( $tag, [$tags] )

In LIST context, all fields with the name $tag are returned. In SCALAR context, only the first field which matches the earliest $tag is returned. L<Mail::Header::get()|Mail::Header/"Processing"> is called to collect the data.

=item_obj-E<gt>B<header>(\@lines)

See L<Mail::Header::header()|Mail::Header/"Fake constructors">.

=item_obj-E<gt>B<replace>(PAIRS)

The PAIRS are field-name and field-content. For each PAIR, L<Mail::Header::replace()|Mail::Header/"Processing"> is called with index 0. If a $field is already in the header, it will be removed first. Do not specified the same field-name twice.

=back

=head2 Processing the body

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Attempts to remove a user's signature from the body of a message. It does this by looking for a line equal to C<'-- '> within the last C<$nlines> of the message. If found then that line and all lines after it will be removed. If C<$nlines> is not given a default value of 10 will be used. This would be of most use in auto-reply scripts.

Add your signature to the body. L<remove_sig()|Mail::Internet/"Processing the body"> will strip existing signatures first.

-Option   --Default
File       undef
Signature  []

Take from the FILEHANDLE all lines starting from the first C<< -- >>.

Removes all leading and trailing lines from the body that only contain white spaces.

It can cause problems with some applications if a message contains a line starting with C<'From '> in particular when attempting to split a folder. This method inserts a leading C<' '> on any line that matches the regular expression C/<\/*/From/>
Post an article via NNTP. Requires Net::NNTP to be installed.

-Option --Default
  Debug <false>
  Host <required>
  Port 119

=back

=item Debug => BOOLEAN

Debug value to pass to Net::NNTP, see L<Net::NNTP>

=item Host => HOSTNAME|Net::NNTP object

Name of NNTP server to connect to, or a Net::NNTP object to use.

=item Port => INTEGER

Port number to connect to on remote host

=back

=item $obj-E<gt>B<reply>(%options)

Create a new object with header initialised for a reply to the current object. And the body will be a copy of the current message indented.

The C<.mailhdr> file in your home directory (if exists) will be read first, to provide defaults.

-Option --Default
  Exclude []
  Indent '>'
  Keep []
  ReplyAll false

=back

=item Exclude => ARRAY-of-FIELDS

Remove the listed FIELDS from the produced message.

=item Indent => STRING

Use as indentation string. The string may contain C<%>% to get a single C<%>, C<%f> to get the first from name, C<%F> is the first character of C<%f>,...
C<%l> is the last name, C<%L> its first character, C<%n> the whole from string, and C<%I> the first character of each of the names in the from string.

=item Keep => ARRAY-of-FIELDS

Copy the listed FIELDS from the original message.

=item ReplyAll => BOOLEAN

Automatically include all To and Cc addresses of the original mail, excluding those mentioned in the Bcc list.

=back

=item $obj-E<gt>B<send>( [type, [args...]] )

Send a Mail::Internet message using L<Mail::Mailer|Mail::Mailer>. $type and $args are passed on to L<Mail::Mailer::new()|Mail::Mailer/"Constructors").

=item $obj-E<gt>B<smtpsend>( [%options] )

Send a Mail::Internet message using direct SMTP to the given ADDRESSES, each can be either a string or a reference to a list of email addresses. If none of C<To>, <Cc> or C<Bcc> are given then the addresses are extracted from the message being sent.

The return value will be a list of email addresses that the message was sent to. If the message was not sent the list will be empty.

Requires Net::SMTP and Net::Domain to be installed.

-Option --Default
  Bcc        undef
  Cc         undef
  Debug      <false>
  Hello      localhost.localdomain
  Host       $ENV{SMTPHOSTS}
  MailFrom   Mail::Util::mailaddress()
  Port       25
  To         undef

=over 2

=item Bcc => ADDRESSES

=item Cc => ADDRESSES

=item Debug => BOOLEAN
Debug value to pass to Net::SMTP, see <Net::SMTP>

=item Hello => STRING

Send a HELO (or EHLO) command to the server with the given name.

=item Host => HOSTNAME

Name of the SMTP server to connect to, or a Net::SMTP object to use.

If C<Host> is not given then the SMTP host is found by attempting connections first to hosts specified in C<$ENV{SMTPHOSTS}>, a colon separated list, then C<mailhost> and C<localhost>.

=item MailFrom => ADDRESS

The e-mail address which is used as sender. By default, L-Mail::Util::mailaddress()|Mail::Util/"FUNCTIONS" provides the address of the sender.

=item Port => INTEGER

Port number to connect to on remote host.

=item To => ADDRESSES

=back

=item $obj-E<gt>B<unescape_from>((()))

Remove the escaping added by L-escape_from()|Mail::Internet/"High-level functionality".

=back

=head1 SEE ALSO

This module is part of the MailTools distribution, F-http://perl.overmeer.net/mailtools/.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

-Mail::Cap by Gisle Aas E<aas@oslonett.no>E.<gt>
-Mail::Field::AddrList by Peter Orbaek E<poe@cit.dk>E.<gt>
-Mail::Mailer and Mail::Send by Tim Bunce E<Tim.Bunce@ig.co.uk>E.<gt>
For other contributors see ChangeLog.
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This program is free software; you can redistribute it and/or modify it under the same terms as Perl itself.
See <http://www.perl.com/perl/misc/Artistic.html>

Found in path(s):
* /opt/cola/permits/1146929080_1616812141.62/0/libmail tools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Internet.pod

No license file was found, but licenses were detected in source scan.

---
abstract: 'Various ancient e-mail related modules'
author:
  - 'Mark Overmeer <markov@cpan.org>'
built requires:
  ExtUtils::MakeMaker: '0'
configure requires:
  ExtUtils::MakeMaker: '0'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.3, CPAN::Meta::Converter version 2.150010'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: '1.4'
name: MailTools
no_index:
directory:
  - t
  - inc
requires:
  Date::Format: '0'
  Date::Parse: '0'
  IO::Handle: '0'
  Net::Domain: '1.05'
  Net::SMTP: '1.03'
  Test::More: '0'
resources:
  homepage: http://perl.overmeer.net/CPAN/
  license: http://dev.perl.org/licenses/
  repository: https://github.com/markov2/perl5-MailTools.git
  version: '2.21'
x_serialization_backend: 'CPAN::Meta::YAML version 0.011'
Mail::Cap - understand mailcap files

my $mc   = Mail::Cap->new;

my $desc = $mc->description('image/gif');
print "GIF desc: $desc\n";

my $cmd  = $mc->viewCmd('text/plain; charset=iso-8859-1', 'file.txt');

Parse mailcap files as specified in "RFC 1524 --A User Agent Configuration Mechanism For Multimedia Mail Format Information>. In the description below C<$type> refers to the MIME type as specified in the C<Content-Type> header of mail or HTTP messages. Examples of types are:

  image/gif
  text/html
  text/plain; charset=iso-8859-1

You could also take a look at the File::MimeInfo distribution, which are accessing tables which are used by many applications on a system, and therefore have succeeded the mail-cap specifications on modern (UNIX) systems.

Create and initialize a new Mail::Cap object. If you give it an argument it will try to parse the specified file. Without any arguments it will search for the mailcap file using the standard mailcap path, or the MAILCAPS environment variable if it is defined.
-Option --Default
filename undef
take 'FIRST'

=over 2

=item filename => FILENAME

Add the specified file to the list to standard locations. This file
is tried first.

=item take => 'ALL'|'FIRST'

Include all mailcap files you can find. By default, only the first
file is parsed, however the RFC tells us to include ALL. To maintain
backwards compatibility, the default only takes the FIRST.

=back

eexample:

$mcap = new Mail::Cap;
$mcap = new Mail::Cap "/mydir/mailcap";
$mcap = new Mail::Cap filename => "/mydir/mailcap";
$mcap = new Mail::Cap take => 'ALL';
$mcap = Mail::Cap->new(take => 'ALL');

=back

=head2 Run commands

These methods invoke a suitable program presenting or manipulating the
media object in the specified file. They all return C<1> if a command
was found, and C<0> otherwise. You might test C<$?> for the outcome
of the command.

=over 4

=item $obj->B<compose>($type, $file)

=item $obj->B<edit>($type, $file)

=item $obj->B<print>($type, $file)

=item $obj->B&view>($type, $file)

=back
=head2 Command creator

These methods return a string that is suitable for feeding to system() in order to invoke a suitable program presenting or manipulating the media object in the specified file. It will return C<undef> if no suitable specification exists.

=over 4
=item $obj-E<gt>B<composeCmd>(type, $file)
=item $obj-E<gt>B<editCmd>(type, $file)
=item $obj-E<gt>B<printCmd>(type, $file)
=item $obj-E<gt>B<viewCmd>(type, $file)

=back

=head2 Look-up definitions

Methods return the corresponding mailcap field for the type.

=over 4
=item $obj-E<gt>B<description>(type)
=item $obj-E<gt>B<field>(type, $field)

Returns the specified field for the type. Returns undef if no specification exists.

=over 4
=item $obj-E<gt>B<nametemplate>(type)
=item $obj-E<gt>B<textualnewlines>(type)
=item $obj-E<gt>B<x11_bitmap>(type)

=back

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS
The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<lt>aas@oslonn.noE&lt/>.  
Mail::Field::AddrList by Peter Orbaek E<lt>poe@cit.dkE&lt/>.  
Mail::Mailer and Mail::Send by Tim Bunce E<lt>Tim.Bunce@ig.co.ukE&lt/>.  
For other contributors see ChangeLog.

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See F<http://www.perl.com/perl/misc/Artistic.html>

Found in path(s):  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Cap.pod  
No license file was found, but licenses were detected in source scan.

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Found in path(s):  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Filter.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Mailer/rfc822.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Util.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Send.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Field/AddrList.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Field/Date.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Mailer.qmail.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Address.pm  
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Field.pm
Mail::Field::Date - a date header field

Represents one "Date" header field.

Extends L","DESCRIPTION" in Mail::Field|Mail::Field","DESCRIPTION">

Methods

Extends L","METHODS" in Mail::Field|Mail::Field","METHODS">

Constructors

Extends L","Constructors" in Mail::Field|Mail::Field","Constructors">

over 4
=item Mail::Field::Date-E<gt>B<combine>( $fields )

Inherited, see L<Mail::Field/"Constructors">

=item Mail::Field::Date-E<gt>B<extract>( $tag, $head [, $index ] )

Inherited, see L<Mail::Field/"Constructors">

=item Mail::Field::Date-E<gt>B<new>( $tag [, STRING [ %options ] ] )

Inherited, see L<Mail::Field/"Constructors">

=back

=head2 "Fake" constructors

Extends L<""Fake" constructors" in Mail::Field|Mail::Field/""Fake" constructors">.

=over 4

=item $obj-E<gt>B<create>(%options)

Inherited, see L<Mail::Field/""Fake" constructors">

=item $obj-E<gt>B<parse>()

Inherited, see L<Mail::Field/""Fake" constructors">

=back

=head2 Accessors

Extends L<"Accessors" in Mail::Field|Mail::Field/"Accessors">.

=over 4

=item $obj-E<gt>B<set>(%options)

-Option --Default
  Time    undef
  TimeStr undef

=over 2

=item Time => SECONDS

=item TimeStr => STRING
A string acceptable to Date::Parse.

=back

=item $obj-B<stringify>()

Inherited, see L-Mail::Field/"Accessors"

=item $obj-B<tag>()

=item Mail::Field::Date-B<tag>()

Inherited, see L-Mail::Field/"Accessors"

=back

=head2 Smart accessors

Extends L-"Smart accessors" in Mail::Field|Mail::Field/"Smart accessors".

=over 4

=item $obj-B<text>( [STRING] )

Inherited, see L-Mail::Field/"Smart accessors"

=item $obj-B<time>( [$time] )

Query (or change) the $time (as stored in the field) in seconds.

=back

=head1 DETAILS

Extends L-"DETAILS" in Mail::Field|Mail::Field/"DETAILS".

=head1 DIAGNOSTICS

=over 4

=item Error: Undefined subroutine <method> called

Mail::Field objects use autoloading to compile new functionality. Apparently, the method called is not implemented for the specific class of the field object.

=back
=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<aas@oslone.no>.
Mail::Field::AddrList by Peter Orbaek E<poe@cit.dk>.
Mail::Mailer and Mail::Send by Tim Bunce E<Tim.Bunce@ig.co.uk>.
For other contributors see ChangeLog.

=head1 LICENSE

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Found in path(s):
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Field/Date.pod
No license file was found, but licenses were detected in source scan.

=encoding utf8

=head1 NAME

Mail::Field::AddrList - object representation of e-mail address lists

=head1 INHERITANCE

Mail::Field::AddrList
is a Mail::Field

=head1 SYNOPSIS

use Mail::Field::AddrList;

$to   = Mail::Field->new('To');
$from = Mail::Field->new('From', 'poe@daimi.aau.dk (Peter Orbaek)');
$from->create('foo@bar.com' => 'Mr. Foo', poe => 'Peter');
$from->parse('foo@bar.com (Mr Foo), Peter Orbaek <poe>');
# make a RFC822 header string
print $from->stringify(),"\n";
# extract e-mail addresses and names
@addresses = $from->addresses(); # strings
@names = $from->names(); # strings
@addr
= $from->addr_list(); # Mail::Address objects (v2.00)
# adjoin a new address to the list
$from->set_address('foo@bar.com', 'Mr. Foo');
=head1 DESCRIPTION
Defines parsing and formatting of address field, for the following
fields: C<To>, C<From>, C<Cc>, C<Reply-To>, and C<Sender>.
All the normally used features of the address field specification of
RFC2822 are implemented, but some complex (and therefore hardly ever used)
constructs will not be understood. Use Mail::Message::Field::Full
in MailBox if you need full RFC compliance.
Extends L<"DESCRIPTION" in Mail::Field|Mail::Field/"DESCRIPTION">.
=head1 METHODS
Extends L<"METHODS" in Mail::Field|Mail::Field/"METHODS">.
=head2 Constructors
Extends L<"Constructors" in Mail::Field|Mail::Field/"Constructors">.
=over 4
=item Mail::Field::AddrList-E<gt>B<combine>($fields)
Inherited, see L<Mail::Field/"Constructors">
=item Mail::Field::AddrList-E<gt>B<extract>( $tag, $head [, $index ] )
Inherited, see L<Mail::Field/"Constructors">
=item Mail::Field::AddrList-E<gt>B<new>( $tag [, STRING | %options] )
Inherited, see L<Mail::Field/"Constructors">

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Fake constructors

Extends L"Fake constructors" in Mail::Field|Mail::Field/"Fake constructors".

=item $obj-E<gt>B<create>(%options)
Inherited, see L-Mail::Field/"Fake constructors"

=item $obj-E<gt>B<parse>()
Inherited, see L-Mail::Field/"Fake constructors"

Accessors

Extends L"Accessors" in Mail::Field|Mail::Field/"Accessors".

=item $obj-E<gt>B<set>(%options)
Inherited, see L-Mail::Field/"Accessors"

=item $obj-E<gt>B<stringify>()
Inherited, see L-Mail::Field/"Accessors"

=item $obj-E<gt>B<tag>()
=Mail::Field::AddrList-E<gt>B<tag>()
Inherited, see L-Mail::Field/"Accessors"

=Smart accessors

Extends L"Smart accessors" in Mail::Field|Mail::Field/"Smart accessors".

=item $obj-E<gt>B<addr_list>()
Returns the collected L<Mail::Address|Mail::Address> objects.

=item $obj-E<gt>B<addresses>()

Returns a list if email addresses, found in the field content.

=item $obj-E<gt>B<names>()

Returns a list of nicely formatted named, for each of the addresses found in the content.

=item $obj-E<gt>B<set_address>($email, $name)

Add/replace an $email address to the field.

=item $obj-E<gt>B<text>( [STRING] )

Inherited, see L<Mail::Field/"Smart accessors">

=back

=head1 DETAILS

Extends L<"DETAILS" in Mail::Field|Mail::Field/"DETAILS">.

=head1 DIAGNOSTICS

=over 4

=item Error: Undefined subroutine <method> called

Mail::Field objects use autoloading to compile new functionality. Apparently, the method called is not implemented for the specific class of the field object.

=back

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<lt>aas@oslonett.noE<gt>.
Mail::Field::AddrList by Peter Orbaek E<lt>poe@cit.dkE&lt;/gt>.
Mail::Mailer and Mail::Send by Tim Bunce E<lt>Tim.Bunce@ig.co.ukE&lt;/gt>.
For other contributors see ChangeLog.

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Copyrights 1995-2000 Graham Barr E<lt>gbarr@pobox.comE&lt;/gt> and
2001-2017 Mark Overmeer E<lt>perl@overmeer.netE&lt;/gt>.

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under the same terms as Perl itself.
See F<http://www.perl.com/perl/misc/Artistic.html>

Found in path(s):
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-
  2.21/lib/Mail/Field/AddrList.pod
No license file was found, but licenses were detected in source scan.

#!/usr/bin/perl -w

# Note by Mark Overmeer:
#  This script does work, but Mail::Send may provide a nicer interface

# NAME
# mail-mailer.pl - Smtp client written in Perl
#
# SYNOPSIS
# mail-mailer.pl -s "Test" --smtp=my-smtp-server.com admin@net1.net
#
# INTRODUCTION
# This script can be an alternative to the 'mail' Unix command when
# sending e-mails.
# It reads the mail body from the standard input.
# If your system is Windows, use the '--smtp' option to send your
# e-mails.
#This script works in Linux, Unix and Windows environments.
#
# OPTIONS
#-f				From
#-s				Subject
#-c				Cc-address
#-b				bcc-address
#--sendmail		Use sendmail to send the e-mail
#--qmail			Use qmail-inject to send the e-mail
#--smtp=HOSTNAMEUse HOSTNAME as the SMTP server.
#--helpPrints the help info and exits
#
# EXAMPLES
use Mail::Mailer;
use Getopt::Long;
use strict;

# hash that'll receive the arguments and options
my %opt;
GetOptions ( \%opt, 'help', 'f=s', 's=s', 'c=s', 'b=s', 'sendmail',
'qmail', 'smtp=s' );

if ($opt{help}) { help(); exit 0; }

$opt{to} = $ARGV[\$#ARGV]; # the "To" address is the last argument
die "Error: You didn't specify a destination e-mail address." unless ( $opt{to} || $opt{c} || $opt{b} );

# Defining the method to send the message
my $mailer; # Mail::Mailer object
if ($opt{sendmail}) { $mailer = new Mail::Mailer 'sendmail';
} elsif ($opt{qmail}) { $mailer = new Mail::Mailer 'qmail';
} elsif ($opt{smtp}) { $mailer = new Mail::Mailer 'smtp', Server => $opt{smtp};
} else { die "Error: you didn't specify the delivery method. ".
"Possible methods are: 'n'--qmail', '--sendmail', and ".
"--smtp=HOSTNAME'n"; }

# Setting the headers
my %headers; # hash with the e-mail headers
$headers{To} =$opt{to};
$headers{From} =$opt{f} if defined $opt{f};
$headers{Cc} =$opt{c} if defined $opt{c};
$headers{Bcc} =$opt{b} if defined $opt{b};
$headers{Subject} =$opt{s} if defined $opt{s};
$mailer->open(\%headers);
# Reading and feeding the e-mail body
while (<STDIN>) {
last if ( $_ =~ /^\.$/ );
print $mailer $_;
}
# Finishing
$mailer->close();
# Subroutines
sub help {
print '
Example 1: Entering the e-mail body by hand:
mail-mailer.pl -s "Hy buddy" --qmail friend@domain.com
[ ENTER YOU MESSAGE BODY ]
[ A SINGLE . (dot sign) ALONE IN ONE LINE TO SAY ]
[ YOU FINISHED YOUR E-MAIL ]
.
Example 2: Using the output of another program as the body:
dir c:\ | perl mail-mailer.pl -f me@mydom.com -s "My c:\" admin@mydom.com --smtp=server1.mydom.com
OPTIONS
-f addrFrom address.
-s TEXTSubject.
-c addrCc-address.
-b addrbcc-address.
--sendmailUse sendmail to send the e-mail.
--qmailUse qmail-inject to send the e-mail.
--smtp HOSTNAMEUse HOSTNAME as the SMTP server.
--helpPrints this help text.
';
}
Found in path(s):
* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools2.21/examples/mail-mailer.pl
No license file was found, but licenses were detected in source scan.
=encoding utf8
=head1 NAME
Mail::Send - Simple electronic mail interface
=head1 SYNOPSIS

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require Mail::Send;

$msg = Mail::Send->new;
$msg = Mail::Send->new(Subject => 'example', To => 'timbo');

$msg->to('user@host');
$msg->to('user@host', 'user2@example.com');
$msg->subject('example subject');
$msg->cc('user@host');
$msg->bcc('someone@else');

$msg->set($header, @values);
$msg->add($header, @values);
$msg->delete($header);

# Launch mailer and set headers. The filehandle returned
# by open() is an instance of the Mail::Mailer class.
# Arguments to the open() method are passed to the Mail::Mailer
# constructor.

$fh = $msg->open;   # some default mailer
$fh = $msg->open('sendmail'); # explicit
print $fh "Body of message"
$fh->close          # complete the message and send it
    or die "couldn't send whole message: $!
";
=back

=head2 Header fields

=over 4

=item $obj-E<gt>B<add>($fieldname, @values)

Add values to the list of defined values for the $fieldname.

=item $obj-E<gt>B<bcc>(@values)

=item $obj-E<gt>B<cc>(@values)

=item $obj-E<gt>B<delete>($fieldname)

=item $obj-E<gt>B<set>($fieldname, @values)

The @values will replace the old values for the $fieldname. Returned is
the LIST of values after modification.

=item $obj-E<gt>B<subject>(@values)

=item $obj-E<gt>B<to>(@values)

=back

=head2 Sending

=over 4

=item $obj-E<gt>B<open>(%options)

The %options are used to initiate a mailer object via
L<Mail::Mailer::new()>. Then L<Mail::Mailer::open()> is called
with the knowledge collected in this C<Mail::Send> object.

Be warned: this module implements raw smtp, which means that you have
to escape lines which start with a dot, by adding one in front.

=back

=head1 SEE ALSO

This module is part of the MailTools distribution,
F<http://perl.overmeer.net/mailtools/>.

---

Open Source Used In ThousandEyes BrowserBot for Cisco Application Hosting 1.0 728
AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<lt>aas@oslonett.noE&lt;/gt>.
Mail::Field::AddrList by Peter Orbaek E<lt>poe@cit.dkE&lt;/gt>.
Mail::Mailer and Mail::Send by Tim Bunce E<lt>Tim.Bunce@ig.co.ukE&lt;/gt>.
For other contributors see ChangeLog.

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See F<http://www.perl.com/perl/misc/Artistic.html>

NAME

MailTools - bundle of ancient email modules

SYNOPSIS

# This is a place-holder for the distribution

DESCRIPTION

MailTools is a bundle: an ancient form of combining packages into one distribution. Gladly, it can be distributed as if it is a normal distribution as well.

B<Be warned:> The code you find here is very old. It works for simple emails, but when you start with new code then please use more sophisticated libraries. The main reason that you still find this code on CPAN, is because many books use it as example.

Component

In this distribution, you find
// Mail::Address
Parse email address from a header line.

// Mail::Cap
Interpret mailcap files: mappings of file-types to applications as used by many command-line email programs.

// Mail::Field
Simplifies access to (some) email header fields. Used by Mail::Header.

// Mail::Filter
Process Mail::Internet messages.

// Mail::Header
Collection of Mail::Field objects, representing the header of a Mail::Internet object.

// Mail::Internet
Represents a single email message, with header and body.

// Mail::Mailer
Send Mail::Internet emails via direct smtp or local MTA’s.

// Mail::Send
Build a Mail::Internet object, and then send it out using Mail::Mailer.

// Mail::Util
"Smart functions" you should not depend on.

SEE ALSO
This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.
AUTHORS

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See F<http://www.perl.com/perl/misc/Artistic.html>

NAME

Mail::Field::Generic - implementation for inspecific fields

INHERITANCE

Mail::Field::Generic
is a Mail::Field

SYNOPSIS

use Mail::Field;
my $field = Mail::Field->new('Subject', 'some subject text');
my $field = Mail::Field->new(subject => 'some subject text');

DESCRIPTION

A generic implementation for header fields without own implementation. This is fine for fields like C<Subject>, C<X-Mailer>, etc., where the field holds only a string of no particular importance/format.
Extends L<"DESCRIPTION" in Mail::Field::Field/"DESCRIPTION">.

=head1 METHODS

Extends L<"METHODS" in Mail::Field::Field/"METHODS">.

=head2 Constructors

Extends L<"Constructors" in Mail::Field::Field/"Constructors">.

=over 4

=item Mail::Field::Generic-E<gt>B<combine>( $fields )
Inherited, see L<Mail::Field/"Constructors">

=item Mail::Field::Generic-E<gt>B<extract>( $tag, $head [, $index ] )
Inherited, see L<Mail::Field/"Constructors">

=item Mail::Field::Generic-E<gt>B<new>( $tag [, STRING | %options] )
Inherited, see L<Mail::Field/"Constructors">

=back

=head2 "Fake" constructors

Extends L<"Fake" constructors" in Mail::Field::Field/"Fake" constructors">.

=over 4

=item $obj-E<gt>B<create>( %options )

-Option--Default
Text    ""

=over 2

=item Text => STRING

=back

=item $obj-E<gt>B<parse>( [STRING] )

Set the new text, which is empty when no STRING is provided.
Extends L<"Accessors" in Mail::Field|Mail::Field/"Accessors">.

=item $obj->B<set>(%options)
Inherited, see L<Mail::Field/"Accessors">

=item $obj->B<stringify>()
Inherited, see L<Mail::Field/"Accessors">

=item $obj->B<tag>()

=item Mail::Field::Generic->B<tag>()
Inherited, see L<Mail::Field/"Accessors">

Extends L<"Smart accessors" in Mail::Field|Mail::Field/"Smart accessors">.

=item $obj->B<text>( [STRING] )
Inherited, see L<Mail::Field/"Smart accessors">

Extends L<"DETAILS" in Mail::Field|Mail::Field/"DETAILS">.

=item Error: Undefined subroutine <method> called

Mail::Field objects use autoloading to compile new functionality. Apparently, the method called is not implemented for the specific
class of the field object.

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<lt>aas@oslone.noE<gt>.
Mail::Field::AddrList by Peter Orbaek E<lt>poe@cit.dkE<gt>.
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Found in path(s):
* /opt/cola/permits/1146929080_1616812141.62/0/lib/mailtools-perl-2.2.1-orig-1.tar.gz/MailTools-2.2.1/lib/Mail/Field/Generic.pod
No license file was found, but licenses were detected in source scan.

=encoding utf8

=head1 NAME

Mail::Field - base-class for manipulation of mail header fields

=head1 INHERITANCE

Mail::Field is extended by
Mail::Field::AddrList
Mail::Field::Date
Mail::Field::Generic

=head1 SYNOPSIS
use Mail::Field;

my $field = Mail::Field->new('Subject', 'some subject text');
my $field = Mail::Field->new(Subject => 'some subject text');
print $field->tag, ': ', $field->stringify, '
';

my $field = Mail::Field->subject('some subject text');

=head1 DESCRIPTION

C<Mail::Field> creates and manipulates fields in MIME headers, collected
within a L<Mail::Header|Mail::Header> object. Different field types have their
own sub-class (extension), defining additional useful accessors to the
field content.

People are invited to merge their implementation to special fields into
MailTools, to maintain a consistent set of packages and documentation.

=head1 METHODS

=head2 Constructors

Mail::Field (and it's sub-classes) define several methods which return
new objects. These can all be categorized as constructor.

=over 4

=item Mail::Field-E<gt>B<combine>( $fields)

Take a LIST of C<Mail::Field> objects (which should all be of the same
sub-class) and create a new object in that same class.

=item Mail::Field-E<gt>B<extract>( $tag, $head [, $index ] )

Takes as arguments the tag name, a C<Mail::Header> object
and optionally an index.

If the index argument is given then C<extract> will retrieve the given tag
from the C<Mail::Header> object and create a new C<Mail::Field> based object.
I<undef> will be returned in the field does not exist.

If the index argument is not given the result depends on the context
in which C<extract> is called. If called in a scalar context the result
will be as if C<extract> was called with an index value of zero. If called
in an array context then all tags will be retrieved and a list of
C<Mail::Field> objects will be returned.

=item Mail::Field-E<gt>B<new>( $tag [, STRING | %options ] )
Create an object in the class which defines the field specified by the $tag argument.

=back

=head2 "Fake" constructors

=over 4

=item $obj-E<gt>B<create>(%options)

This constructor is used internally with preprocessed field information. When called on an existing object, its original content will get replaced.

=item $obj-E<gt>B<parse>()

Parse a field line.

=back

=head2 Accessors

=over 4

=item $obj-E<gt>B<set>(%options)

Change the settings (the content, but then smart) of this field.

=item $obj-E<gt>B<stringify>()

Returns the field as a string.

=item $obj-E<gt>B<tag>()

=mail::Field-E<gt>B<tag>()

Return the tag (in the correct case) for this item. Well, actually any casing is OK, because the field tags are treated case-insensitive; however people have some preferences.

=back

=head2 Smart accessors

=over 4
Without arguments, the field is returned as L<stringify()|Mail::Field/"Accessors"> does. Otherwise, the STRING is parsed with L<parse()|Mail::Field/"Fake” constructors”> to replace the object’s content.

It is more clear to call either L<stringify()|Mail::Field/"Accessors”> or L<parse()|Mail::Field/”Fake” constructors”> directly, because this method does not add additional processing.

---

**DETAILS**

**SUB-CLASS PACKAGE NAMES**

All sub-classes should be called Mail::Field::I<name> where I<name> is derived from the tag using these rules.

Consider a tag as being made up of elements separated by ‘-’.

Convert all characters to lowercase except the first in each element, which should be uppercase.

I<name> is then created from these elements by using the first N characters from each element.

N is calculated by using the formula :-

$$\text{int}((7 + \#\text{elements}) / \#\text{elements})$$

I<name> is then limited to a maximum of 8 characters, keeping the first 8 characters.

For an example of this take a look at the definition of the
C<_header_pkg_name()> subroutine in C<Mail::Field>

=head1 DIAGNOSTICS

=over 4

=item Error: Undefined subroutine <method> called

Mail::Field objects use autoloading to compile new functionality.
Apparently, the method called is not implemented for the specific
class of the field object.

=back

=head1 SEE ALSO

This module is part of the MailTools distribution,
F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark
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2.21/lib/Mail/Field.pod
No license file was found, but licenses were detected in source scan.

=encoding utf8

=head1 NAME

Mail::Util - mail utility functions
INHERITANCE

Mail::Util
is a Exporter

SYNOPSIS

use Mail::Util qw( ... );

DESCRIPTION

This package provides several mail related utility functions. Any function required must be explicitly listed on the use line to be exported into the calling package.

FUNCTIONS

mailaddress( [address] )

Return a guess at the current users mail address. The user can force the return value by setting the MAILADDRESS environment variable. [2.10] You may set the $address via the parameter.

WARNING:
When not supplied via the environment variable, <mailaddress> looks at various configuration files and other environmental data. Although this seems to be smart behavior, this is not predictable enough (IMHO) to be used. Please set the MAILADDRESS explicitly, and do not trust on the "automatic detection", even when that produces a correct address (on the moment)

e.g:

# in your main script
$ENV{MAILADDRESS} = 'me@example.com';

# everywhere else
use Mail::Util 'mailaddress';
print mailaddress;

# since v2.10
mailaddress "me@example.com";

maildomain()
Attempt to determine the current user mail domain string via the following methods

=over 4

=item * Look for the MAILDOMAIN environment variable, which can be set from outside the program. This is by far the best way to configure the domain.

=item * Look for a sendmail.cf file and extract DH parameter

=item * Look for a smail config file and use the first host defined in hostname(s)

=item * Try an SMTP connect (if Net::SMTP exists) first to mailhost then localhost

=item * Use value from Net::Domain::domainname (if Net::Domain exists)

=back

WARNING:
On modern machines, there is only one good way to provide information to this method: the first; always explicitly configure the MAILDOMAIN.

example:

# in your main script
$ENV{MAILDOMAIN} = 'example.com';

# everywhere else
use Mail::Util 'maildomain';
print maildomain;

=item B<read_mbox>($file)

Read $file, a binmail mailbox file, and return a list of references. Each reference is a reference to an array containing one message.

WARNING:
This method does not quote lines which accidentally also start with the message separator C<From>, so this implementation can be considered broken. See Mail::Box::Mbox

=back

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.
AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

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See <http://www.perl.com/perl/misc/Artistic.html>

NAME

Mail::Header - manipulate MIME headers

SYNOPSIS

use Mail::Header;

my $head = Mail::Header->new;
my $head = Mail::Header->new( *STDIN );
my $head = Mail::Header->new( ['<'], Modify => 0);

DESCRIPTION

Read, write, create, and manipulate MIME headers, the leading part of each modern e-mail message, but also used in other protocols like HTTP. The fields are kept in Mail::Field objects.

Be aware that the header fields each have a name part, which shall be treated case-insensitive, and a content part, which may be folded over multiple lines.
Mail::Header does not always follow the RFCs strict enough, does not help you with character encodings. It does not use weak references where it could (because those did not exist when the module was written) which costs some performance and make the implementation a little more complicated. The Mail::Message::Head implementation is much newer and therefore better.

=head1 METHODS

=head2 Constructors

=over 4

=item $obj-B<gt>dup()

Create a duplicate of the current object.

=item $obj-B<gt>new( [$source], [%options] )

=item Mail::Header-B<gt>new( [$source], [%options] )

The $source may be either a file descriptor (reference to a GLOB) or a reference to an array. If given the new object will be initialized with headers either from the array or read from the file descriptor.

%options is a list of options given in the form of key-value pairs, just like a hash table. Valid options are

-Option     --Default
FoldLength   79
MailFrom     'KEEP'
Modify       false

=over 2

=item FoldLength => INTEGER

The default length of line to be used when folding header lines. See L<fold_length()>.

=item MailFrom => 'IGNORE'|'COERCE'|'KEEP'|'ERROR'

See method L<mail_from()>.

=item Modify => BOOLEAN

If this value is I<true> then the headers will be re-formatted,
otherwise the format of the header lines will remain unchanged.

=back

=head2 "Fake" constructors

Be warned that the next constructors all require an already created header object, of which the original content will be destroyed.

=over 4

=item $obj-E<gt>B<empty>()

Empty an existing C<Mail::Header> object of all lines.

=item $obj-E<gt>B<extract>(ARRAY)

Extract a header from the given array into an existing Mail::Header object. C<extract> B<will modify> this array.
Returns the object that the method was called on.

=item $obj-E<gt>B<header>( [ARRAY] )

C<header> does multiple operations. First it will extract a header from the ARRAY, if given. It will then reformat the header (if reformatting is permitted), and finally return a reference to an array which contains the header in a printable form.

=item $obj-E<gt>B<header_hashref>( [HASH] )

As L<header()|Mail::Header/"Fake" constructors">, but it will eventually set headers from a hash reference, and it will return the headers as a hash reference.

example:

$fields->{From} = 'Tobias Brox <tobix@cpan.org>';  
$fields->{To} = ['you@somewhere', 'me@localhost'];  
$head->header_hashref($fields);

=item $obj-E<gt>B<read>($fh)

Read a header from the given file descriptor into an existing Mail::Header object.

=back
Accessors

Set the default fold length for all tags or just one. With no arguments the default fold length is returned. With two arguments it sets the fold length for the given tag and returns the previous value. If only \$length is given it sets the default fold length for the current object.

In the two argument form \$fold_length may be called as a static method, setting default fold lengths for tags that will be used by all \Mail::Header objects. See the \$fold method for a description on how \Mail::Header uses these values.

This specifies what to do when a \'From\' line is encountered. Valid values are \'IGNORE\' - ignore and discard the header, \'ERROR\' - invoke an error (call die), \'COERCE\' - rename them as Mail-From and \'KEEP\' - keep them.

If \$value is \'false\' then \Mail::Header will not do any automatic reformatting of the headers, other than to ensure that the line starts with the tags given.

Processing

Add a new line to the header. If \$tag is \'undef\' the tag will be extracted from the beginning of the given line. If \$index is given, the new line will be inserted into the header at the given point, otherwise the new line will be appended to the end of the header.

Returns the header as a single string.

Returns the header as a single string.
Remove any header line that, other than the tag, only contains whitespace

=item $obj-E<gt>B<combine>( $tag [, $with] )

Combine all instances of $tag into one. The lines will be joined together $with, or a single space if not given. The new item will be positioned in the header where the first instance was, all other instances of $tag will be removed.

=item $obj-E<gt>B<count>($tag)

Returns the number of times the given tag appears in the header

=item $obj-E<gt>B<delete>( $tag [, $index ] )

Delete a tag from the header. If an $index id is given, then the Nth instance of the tag will be removed. If no $index is given, then all instances of tag will be removed.

=item $obj-E<gt>B<fold>( [ $length ] )

Fold the header. If $length is not given, then C<Mail::Header> uses the following rules to determine what length to fold a line.

=item $obj-E<gt>B<get>( $tag [, $index ] )

Get the text from a line. If an $index is given, then the text of the Nth instance will be returned. If it is not given the return value depends on the context in which C<get> was called. In an array context a list of all the text from all the instances of the $tag will be returned. In a scalar context the text for the first instance will be returned.

The lines are unfolded, but still terminated with a new-line (see C<chomp>)

=item $obj-E<gt>B<print>( [$fh] )

Print the header to the given file descriptor, or C<STDOUT> if no file descriptor is given.

=item $obj-E<gt>B<replace>( $tag, $line [, $index ] )

Replace a line in the header. If $tag is C<undef> the tag will be extracted from the beginning of the given line. If $index is given the new line will replace the Nth instance of that tag, otherwise the first instance of the tag is replaced. If the tag does not appear in the header then a new line will be appended to the header.

=item $obj-E<gt>B<tags>()
Returns an array of all the tags that exist in the header. Each tag will only appear in the list once. The order of the tags is not specified.

=item $obj->B<unfold>( [$tag] )

Unfold all instances of the given tag so that they do not spread across multiple lines. If C<$tag> is not given then all lines are unfolded.

The unfolding process is wrong but (for compatibility reasons) will not be repaired: only one blank at the start of the line should be removed, not all of them.

=back

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

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* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/lib/Mail/Header.pod
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# MailTools

* My extended documentation: <http://perl.overmeer.net/CPAN/>
* Development via GitHub: <https://github.com/markov2/perl5-MailTools>
MailTools is one of the oldest modules on CPAN. It started off in 1995, the year Perl 5.0 got released! That’s just in the beginning of the MIME standards.

Email has become more complex over the years. The components of MailTools only understand very basic emails and fairly simple mime headers. Fixing some things will break existing applications.

The only reason for still maintaining it, is because it is used as example in many books and therefore applications still use it. Not because it is good. Sometimes it is good enough, though.

Later, this module got extended by the MIME::Entity modules to support multiparts and more smartness. But even those modules are quite limited, considering modern emails.

Please note: When you start all over, please consider Mail::Box! For a feature overview, take a look at the [MailBox overview](http://perl.overmeer.net/mailbox/html/Mail_Box-Overview/) and [MailBox features](http://perl.overmeer.net/mailbox/html/Mail_Box-Index/) pages.

## Development → Release

Important to know, is that I use an extension on POD to write the manuals. The "raw" unprocessed version is visible on GitHub. It will run without problems, but does not contain manual-pages.

Releases to CPAN are different: "raw" documentation gets removed from the code and translated into real POD and clean HTML. This reformatting is implemented with the OODoc distribution (A name I chose before OpenOffice existed, sorry for the confusion)

Clone from github for the "raw" version. For instance, when you want to contribute a new feature.

On github, you can find the processed version for each release. But the better source is CPAN; to get it installed simply run:

```sh
cpan -i MailTools
```
## Contributing

When you want to contribute to this module, you do not need to provide a perfect patch... actually: it is nearly impossible to create a patch which I will merge without modification. Usually, I need to adapt the style of code and documentation to my own strict rules.

When you submit an extension, please contribute a set with

1. code
2. code documentation
3. regression tests in t/

**Please note:**
When you contribute in any way, you agree to transfer the copyrights to Mark Overmeer (you will get the honors in the code and/or ChangeLog). You also automatically agree that your contribution is released under the same license as this project: licensed as perl itself.

## Copyright and License

Graham Barr wrote most of these components, since 2001 maintained by Mark Overmeer. Copyright is with these authors.

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* /opt/cola/permits/1146929080_1616812141.62/0/libmailtools-perl-2-21-orig-1-tar-gz/MailTools-2.21/README.md
No license file was found, but licenses were detected in source scan.

=head1 NAME

Mail::Mailer - send simple emails

=head1 INHERITANCE

Mail::Mailer
is an IO::Handle

=head1 SYNOPSIS
use Mail::Mailer;
use Mail::Mailer qw(mail);  # specifies default mailer

$mailer = Mail::Mailer->new;
$mailer = Mail::Mailer->new($type, @args);

$mailer->open(%headers);
print $mailer $body;
$mailer->close
  or die "couldn't send whole message: $!
";

=head1 DESCRIPTION

Sends mail using any of the built-in methods. As TYPE argument
to L[new()|Mail::Mailer/"Constructors">, you can specify any of

=over 4

=item C<sendmail>

Use the C<sendmail> program to deliver the mail.

=item C<smtp>

Use the C<smtp> protocol via Net::SMTP to deliver the mail. The server
to use can be specified in C<@args> with

$mailer = Mail::Mailer->new('smtp', Server => $server);

The smtp mailer does not handle C<Cc> and C<Bcc> lines, neither their
C<Resent-*> fellows. The C<Debug> options enables debugging output
from C<Net::SMTP>.

[added 2.21] You may also use the C<< StartTLS => 1 >> options to upgrade the
connection with STARTTLS. You need C<libnet> version 1.28 (2014) for this
to work.

You may also use the C<< Auth => [ $user, $password ] >> option for SASL
authentication. To make this work, you have to install the L<Authen::SASL>
distribution yourself: it is not automatically installed.

=item C<smtps>

This option is B<deprecated> when you have C<libnet> 1.28 (2014) and above.

Use the smtp over ssl protocol via L<Net::SMTP::SSL> to deliver the mail.
Usage is identical to C<smtp>. You have to install Authen::SASL as well.
$mailer = Mail::Mailer->new('smtps', Server => $server);

=item C<qmail>

Use qmail's qmail-inject program to deliver the mail.

=item C<testfile>

Used for debugging, this displays the data to the file named in
C<$Mail::Mailer::testfile::config{outfile}> which defaults to a file
named C<mailer.testfile>. No mail is ever sent.

=back

C<Mail::Mailer> will search for executables in the above order. The
default mailer will be the first one found.

=head1 METHODS

=head2 Constructors

=over 4

=item Mail::Mailer-E<gt>B<new>($type, %options)

The $type is one of the back-end sender implementations, as described in
the DESCRIPTION chapter of this manual page. The %options are passed to
that back-end.

=item $obj-E<gt>B<open>(HASH)

The HASH consists of key and value pairs, the key being the name of
the header field (eg, C<To>), and the value being the corresponding
contents of the header field. The value can either be a scalar
(eg, C<gnat@frii.com>) or a reference to an array of scalars
(C<< eg, ['gnat@frii.com', 'Tim.Bunce@ig.co.uk'] >>).

=back

=head1 DETAILS

=head2 ENVIRONMENT VARIABLES

=over 4

=item PERL_MAILERS
Augments/override the build in choice for binary used to send out our mail messages.

Format:

"type1:mailbinary1;mailbinary2;...:type2:mailbinaryX;...:" 

Example: assume you want you use private sendmail binary instead of mailx, one could set C<PERL_MAILERS> to:

"mail:/does/not/exists:sendmail:$HOME/test/bin/sendmail"

On systems which may include C::<> in file names, use C<|> as separator between type-groups.

"mail:c:/does/not/exists|sendmail:$HOME/test/bin/sendmail"

=head2 BUGS

Mail::Mailer does not help with folding, and does not protect against various web-script hacker attacks, for instance where a new-line is inserted in the content of the field.

=head1 SEE ALSO

This module is part of the MailTools distribution, F<http://perl.overmeer.net/mailtools/>.

=head1 AUTHORS

The MailTools bundle was developed by Graham Barr. Later, Mark Overmeer took over maintenance without commitment to further development.

Mail::Cap by Gisle Aas E<lt>aas@oslonett.noE<gt>.
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Mail::Mailer and Mail::Send by Tim Bunce E<lt>Tim.Bunce@ig.co.ukE<gt>.
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1.69 gzip 1.10 0ubuntu4

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---
abstract: 'Pretty printing of data structures'
author:
  - 'Gisle Aas <gisle@activestate.com>'
build_requires:
  Test: '0'
configure_requires:
  ExtUtils::MakeMaker: '0'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.0401, CPAN::Meta::Converter version 2.150001'
license: perl
NAME
Data::Dump - Pretty printing of data structures

SYNOPSIS
use Data::Dump qw(dump ddx);

$str = dump(@list);
@copy_of_list = eval $str;

# or use it for easy debug printout
ddx localtime;

DESCRIPTION
This module provides functions that takes a list of values as their argument and produces a string as its result. The string contains Perl code that, when "eval"ed, produces a deep copy of the original arguments.

The main feature of the module is that it strives to produce output that is easy to read. Example:

@a = (1, [2, 3], {4 => 5});
dump(@a);

Produces:
If you dump just a little data, it is output on a single line. If you
dump data that is more complex or there is a lot of it, line breaks are
automatically added to keep it easy to read.

AUTHORS

The "Data::Dump" module is written by Gisle Aas <gisle@aas.no>, based on
"Data::Dumper" by Gurusamy Sarathy <gsar@umich.edu>.

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=head1 NAME

Data::Dump - Pretty printing of data structures

=head1 SYNOPSIS

use Data::Dump qw(dump);

$str = dump(@list);
@copy_of_list = eval $str;

# or use it for easy debug printout
use Data::Dump; dd localtime;

=head1 DESCRIPTION

This module provide a few functions that traverse their
argument and produces a string as its result. The string contains
Perl code that, when C<eval>ed, produces a deep copy of the original
arguments.

The main feature of the module is that it strives to produce output
that is easy to read. Example:

@a = (1, [2, 3], {4 => 5});
dump(@a);
Produces:

"(1, [2, 3], { 4 => 5 })"

If you dump just a little data, it is output on a single line. If you dump data that is more complex or there is a lot of it, line breaks are automatically added to keep it easy to read.

The following functions are provided (only the dd* functions are exported by default):

=over
=item dump( ... )
=item pp( ... )

Returns a string containing a Perl expression. If you pass this string to Perl's built-in eval() function it should return a copy of the arguments you passed to dump().

If you call the function with multiple arguments then the output will be wrapped in parenthesis "( ..., ... )". If you call the function with a single argument the output will not have the wrapping. If you call the function with a single scalar (non-reference) argument it will just return the scalar quoted if needed, but never break it into multiple lines. If you pass multiple arguments or references to arrays of hashes then the return value might contain line breaks to format it for easier reading. The returned string will never be "\n" terminated, even if it contains multiple lines. This allows code like this to place the semicolon in the expected place:

    print '$obj = ', dump($obj), ";\n";

If dump() is called in void context, then the dump is printed on STDERR and then "\n" terminated. You might find this useful for quick debug printouts, but the dd*() functions might be better alternatives for this.

There is no difference between dump() and pp(), except that dump() shares its name with a not-so-useful perl builtin. Because of this some might want to avoid using that name.

=item quote( $string )

Returns a quoted version of the provided string.

It differs from C<dump($string)> in that it will quote even numbers and not try to come up with clever expressions that might shorten the
output. If a non-scalar argument is provided then it's just stringified instead of traversed.

=item dd(...)

=item ddx(...)

These functions will call dump() on their argument and print the result to STDOUT (actually, it's the currently selected output handle, but STDOUT is the default for that).

The difference between them is only that ddx() will prefix the lines it prints with "# " and mark the first line with the file and line number where it was called. This is meant to be useful for debug printouts of state within programs.

=item dumpf(..., \&filter)

Short hand for calling the dump_filtered() function of L<Data::Dump::Filtered>. This works like dump(), but the last argument should be a filter callback function. As objects are visited the filter callback is invoked and it can modify how the objects are dumped.

=back

=head1 CONFIGURATION

There are a few global variables that can be set to modify the output generated by the dump functions. It's wise to localize the setting of these.

=over

=item $Data::Dump::INDENT

This holds the string that's used for indenting multiline data structures. It's default value is "  " (two spaces). Set it to " " to suppress indentation. Setting it to "| " makes for nice visuals even if the dump output then fails to be valid Perl.

=item $Data::Dump::TRY_BASE64

How long must a binary string be before we try to use the base64 encoding for the dump output. The default is 50. Set it to 0 to disable base64 dumps.

=back
=head1 LIMITATIONS

Code references will be dumped as C<< sub { ... } >>. Thus, C<eval>ing them will not reproduce the original routine. The C<<...-operator used will also require perl-5.12 or better to be evaled.

If you forget to explicitly import the C<dump> function, your code will core dump. That's because you just called the builtin C<dump> function by accident, which intentionally dumps core. Because of this you can also import the same function as C<pp>, mnemonic for "pretty-print".

=head1 HISTORY

The C<Data::Dump> module grew out of frustration with Sarathy's in-most-cases-excellent C<Data::Dumper>. Basic ideas and some code are shared with Sarathy's module.

The C<Data::Dump> module provides a much simpler interface than C<Data::Dumper>. No OO interface is available and there are fewer configuration options to worry about. The other benefit is that the dump produced does not try to set any variables. It only returns what is needed to produce a copy of the arguments. This means that C<dump("foo")> simply returns C""foo"", and C<dump(1..3)> simply returns C<(1, 2, 3)>.

=head1 SEE ALSO

L<Data::Dump::Filtered>, L<Data::Dump::Trace>, L<Data::Dumper>, L<JSON>, L<Storable>

=head1 AUTHORS

The C<Data::Dump> module is written by Gisle Aas <gisle@aas.no>, based on C<Data::Dumper> by Gurusamy Sarathy <gsar@umich.edu>.

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=cut

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NAME

Data::Dump::Trace - Helpers to trace function and method calls

SYNOPSIS

use Data::Dump::Trace qw(autowrap mcall);

autowrap("LWP::UserAgent" => "ua", "HTTP::Response" => "res");

use LWP::UserAgent;
$ua = mcall(LWP::UserAgent => "new");  # instead of LWP::UserAgent->new;
$ua->get("http://www.example.com")->dump;

DESCRIPTION

The following functions are provided:

autowrap( $class )

autowrap( $class => $prefix )

autowrap( $class1 => $prefix1, $class2 => $prefix2, ... )

autowrap( $class1 => \%info1, $class2 => \%info2, ... )

Register classes whose objects are automatically wrapped when
returned by one of the call functions below. If $prefix is provided
it will be used as to name the objects.

Alternative is to pass an \%info hash for each class. The recognized keys are:

prefix => $string

The prefix string used to name objects of this type.

proto => \%hash

A hash of prototypes to use for the methods when an object is wrapped.
=item wrap( name => $str, obj => $obj, proto => \%hash )

Returns a wrapped function or object. When a wrapped function is invoked then a trace is printed after the underlying function has returned. When a method on a wrapped object is invoked then a trace is printed after the methods on the underlying objects has returned.

See L</"Prototypes"> for description of the C<proto> argument.

=item call( $name, \&func, $proto, @ARGS )

Calls the given function with the given arguments. The trace will use $name as the name of the function.

See L</"Prototypes"> for description of the $proto argument.

=item mcall( $class, $method, $proto, @ARGS )

=item mcall( $object, $method, $proto, @ARGS )

Calls the given method with the given arguments.

See L</"Prototypes"> for description of the $proto argument.

=item trace( $symbol, $prototype )

Replaces the function given by $symbol with a wrapped function.

=back

=head2 Prototypes

B<Note: The prototype string syntax described here is experimental and likely to change in revisions of this interface>.

The $proto argument to call() and mcall() can optionally provide a prototype for the function call. This give the tracer hints about how to best format the argument lists and if there are I<in/out> or I<out> arguments. The general form for the prototype string is:

<arguments> = <return_value>

The default prototype is "@ = @"; list of values as input and list of values as output.

The value '%%' can be used for both arguments and return value to say that key/value pair style lists are used.
Alternatively, individual positional arguments can be listed each represented by a letter:

=over

=item C<i>
input argument

=item C<o>
output argument

=item C<O>
both input and output argument

=back

If the return value prototype has C<!> appended, then it signals that this function sets errno ($!) when it returns a false value. The trace will display the current value of errno in that case.

If the return value prototype looks like a variable name (with C<$> prefix), and the function returns a blessed object, then the variable name will be used as prefix and the returned object automatically traced.

=head1 SEE ALSO
L<Data::Dump>

=head1 AUTHOR

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=cut

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* /opt/cola/permits/1144895079_1616101129.69/0/libdata-dump-perl-1-23-orig-1-tar-gz/Data-Dump-1.23/lib/Data/Dump/Trace.pm
1.71 libnet-ssleay-perl 1.88-2ubuntu1

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1.74 libxmu 1.1.3-0ubuntu1

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1.75 glib 2.64.6-1~ubuntu20.04.3

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1.78 nettle 3.5.1+really3.5.1-2

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1.85 e2fsprogs 1.45.5 2ubuntu1

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This is the Debian GNU/Linux prepackaged version of the static EXT2 file system consistency checker (e2fsck.static). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

Sources were obtained from http://sourceforge.net/projects/e2fsprogs

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Upstream Author: Theodore Ts'o <tytso@mit.edu>

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#
# This is a Makefile stub which handles the creation of BSD shared
# libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#

all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:$(BSD_LIB)

$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) `echo $(my_dir) | sed -e 's;lib/;;'"/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
/*@$(INSTALL_PROGRAM) $(BSD_LIB) \n$(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB) @-$(LDCONFIG)

install-strip: install

install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)

This package was added to the e2fsprogs debian source package by
Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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Index: tdbsa/tdb.c

---

--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
/*
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
-
- Copyright (C) Andrew Tridgell 2005
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Theodore Ts'o
23-June-2007

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```
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That's all there is to it!
This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

http://sourceforge.net/projects/e2fsprogs

Upstream Author: Theodore Ts'o <tytso@mit.edu>

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Gadi Oxman, August 1995

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```
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```
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1.89 libhtml-tagset-perl 3.20-4
1.89.1 Available under license:
No license file was found, but licenses were detected in source scan.

license: ~

Found in path(s):
* /opt/cola/permits/1146693911_1616601054.92/0/libhtml-tagset-perl-3-20-orig-1-tar-gz/HTML-Tagset-3.20/META.yml
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=head1 CAVEATS

You may find it useful to alter the behavior of modules (like C<HTML::Element> or C<HTML::TreeBuilder>) that use C<HTML::Tagset>'s data tables by altering the data tables themselves. You are welcome to try, but be careful; and be aware that different modules may or may react differently to the data tables being changed.

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ACKNOWLEDGEMENTS

Most of the code/data in this module was adapted from code written by Gisle Aas for C<HTML::Element>, C<HTML::TreeBuilder>, and C<HTML::LinkExtor>. Then it was maintained by Sean M. Burke.

AUTHOR

Current maintainer: Andy Lester, C<< <andy at petdance.com> >>

BUGS

Please report any bugs or feature requests to C<bug-html-tagset at rt.cpan.org>, or through the web interface at L<http://rt.cpan.org/NoAuth/ReportBug.html?Queue=HTML-Tagset>. I will be notified, and then you'll automatically be notified of progress on your bug as I make changes.

Found in path(s):
* /opt/cola/permits/1146693911_1616601054.92/0/libhtml-tagset-perl-3-20-orig-1-tar-gz/HTML-Tagset-3.20/Tagset.pm

No license file was found, but licenses were detected in source scan.

README for HTML::Tagset

HTML::Tagset

This module contains data tables useful in dealing with HTML.

It provides no functions or methods.
PREREQUISITES

This suite requires Perl 5.

HTML::Tagset doesn't use any nonstandard modules.

INSTALLATION

You install HTML::Tagset, as you would install any perl module library, by running these commands:

perl Makefile.PL
make
make test
make install

If you want to install a private copy of HTML::Tagset in your home directory, then you should try to produce the initial Makefile with something like this command:

perl Makefile.PL LIB=~/.perl

DOCUMENTATION

POD-format documentation is included in Tagset.pm. POD is readable with the 'perldoc' utility. See ChangeLog for recent changes.

MACPERL INSTALLATION NOTES

Don't bother with the makefiles. Just make a HTML directory in your MacPerl site_lib or lib directory, and move Tagset.pm into there.

SUPPORT

Questions, bug reports, and suggestions for HTML::Tagset should just be sent to me at sburke@cpan.org

AVAILABILITY

The latest version of HTML::Tagset is available from the Comprehensive Perl Archive Network (CPAN). Visit
<http://www.perl.com/CPAN/> to find a CPAN site near you.

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  * /opt/cola/permits/1146693911_1616601054.92/0/libhtml-tagset-perl-3.20-orig-1-tar-gz/HTML-Tagset-3.20/README

1.90 pygobject 3.36.0

1.90.1 Available under license:

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A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
 ; CHECK-LABEL: PR41535:
 ; CHECK: // %bb.0:
 ; CHECK-NEXT: ext v0.8b, v0.8b, v1.8b, #4
 ; CHECK-NEXT: mov v0.d[1], v0.d[0]
 ; CHECK-NEXT: ret
 %cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
 %cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
 %r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
 ret <4 x i32> %r
}

define void @autogen_SD4739(i8*) {
 ; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
 BB:
 %L34 = load i8, i8* %0
 %Cmp56 = icmp sgt i8 undef, %L34
 br label %CF246

 CF246: ; preds = %CF246, %BB
 %SI163 = select i1 %Cmp56, i8 %L34, i8 undef
 br i1 undef, label %CF246, label %CF248

 CF248: ; preds = %CF248, %CF246
 store i8 %SI163, i8* %0
 br label %CF248
}

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define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
; First, extend each i32 to i64
; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
; Next, convert each to double.
; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
; CHECK: ret
%flt = sitofp <16 x i32> %in to <16 x double>
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1
; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}

; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s

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Check that we don't end up with the "LLVM ERROR: Cannot select" error.
Additionally ensure that the output code actually put fp128 values in SSE registers.

```assembly
declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
  ; CHECK-LABEL:    TestSelect:
  ; CHECK:      # %bb.0:
  ; CHECK-NEXT:  pushq %rbx
  ; CHECK-NEXT:    .cfi_def_cfa_offset 16
  ; CHECK-NEXT:    subq $32, %rsp
  ; CHECK-NEXT:    .cfi_def_cfa_offset 48
  ; CHECK-NEXT:    .cfi_offset %rbx, -16
  ; CHECK-NEXT:    movaps %xmm1, {{[-0-9]+}}(%r{{[sb]}}p) # 16-byte Spill
  ; CHECK-NEXT:    movaps %xmm0, (%rsp) # 16-byte Spill
  ; CHECK-NEXT:    callq __gttf2
  ; CHECK-NEXT:    movl %eax, %ebx
  ; CHECK-NEXT:    movaps (%rsp), %xmm0 # 16-byte Reload
  ; CHECK-NEXT:    movaps {{[-0-9]+}}(%r{{[sb]}}p), %xmm1 # 16-byte Reload
  ; CHECK-NEXT:    callq __subtf3
  ; CHECK-NEXT:    testl %ebx, %ebx
  ; CHECK-NEXT:    jg .LBB0_2
  ; CHECK-NEXT:  # %bb.1:
  ; CHECK-NEXT:    xorps %xmm0, %xmm0
  ; CHECK-NEXT:  .LBB0_2:
  ; CHECK-NEXT:    addq $32, %rsp
  ; CHECK-NEXT:    .cfi_def_cfa_offset 16
  ; CHECK-NEXT:    popq %rbx
  ; CHECK-NEXT:    .cfi_def_cfa_offset 8
  ; CHECK-NEXT:    retq
  %cmp = fcmp ogt fp128 %a, %b
  %sub = fsub fp128 %a, %b
  %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
  ret fp128 %res
}

define fp128 @TestFabs(fp128 %a) {
  ; CHECK-LABEL:    TestFabs:
  ; CHECK:      # %bb.0:
  ; CHECK-NEXT:    andps {{.*}}(%rip), %xmm0
  ; CHECK-NEXT:    retq
  %res = call fp128 @llvm.fabs.f128(fp128 %a)
  ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
```
; CHECK-LABEL: TestCopysign:
; CHECK:    # %bb.0:
; CHECK-NEXT: andps {{.*}}(%rip), %xmm1
; CHECK-NEXT: andps {{.*}}(%rip), %xmm0
; CHECK-NEXT: orps %xmm1, %xmm0
; CHECK-NEXT: retq
%res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
ret fp128 %res
};

define fp128 @TestFneg(fp128 %a) {
; CHECK-LABEL: TestFneg:
; CHECK:    # %bb.0:
; CHECK-NEXT: pushq %rax
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: movaps %xmm0, %xmm1
; CHECK-NEXT: callq __multf3
; CHECK-NEXT: xorps {{.*}}(%rip), %xmm0
; CHECK-NEXT: popq %rax
; CHECK-NEXT: .cfi_def_cfa_offset 8
; CHECK-NEXT: retq
%mul = fmul fp128 %a, %a
%res = fsub fp128 0xL00000000000000008000000000000000, %mul
ret fp128 %res
}

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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
  ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
  ret void
}

define internal void @innerNoAttribute() {
  ret void
}
We should not add an attribute during inlining. No attribute means unknown.
Inlining doesn't change the fact that we don't know anything about this
function.

```c
define void @outerNoAttribute() {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
    call void @innerSmall()
    ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
    call void @innerNoAttribute()
    ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = [ "min-legal-vector-width"="512" ]
```

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RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG:  %\[C1:const[0-9]??\] = bitcast i32 805874720 to i32
; CHECK-DAG:  %\[C2:const[0-9]??\] = bitcast i32 805874688 to i32
; CHECK-DAG:  %\[C3:const[0-9]??\] = bitcast i32 805873720 to i32
; CHECK-DAG:  %\[C4:const[0-9]??\] = bitcast i32 805873688 to i32
; CHECK:  %0 = inttoptr i32 %\[C2\] to i8*
; CHECK-NEXT:  %1 = load volatile i8, i8* %0
; CHECK-NEXT:  %\[M1:const_mat[0-9]??\] = add i32 %\[C2\], 4
; CHECK-NEXT:  %2 = inttoptr i32 %\[M1\] to i8*
; CHECK-NEXT:  %3 = load volatile i8, i8* %2
; CHECK-NEXT:  %\[M2:const_mat[0-9]??\] = add i32 %\[C2\], 31
; CHECK-NEXT:  %4 = inttoptr i32 %\[M2\] to i8*
; CHECK-NEXT:  %5 = load volatile i8, i8* %4
; CHECK-NEXT:  %\[M3:const_mat[0-9]??\] = add i32 %\[C1\], 7
; CHECK-NEXT:  %6 = inttoptr i32 %\[C1\] to i8*
; CHECK-NEXT:  %7 = load volatile i8, i8* %6
; CHECK-NEXT:  %\[M4:const_mat[0-9]??\] = add i32 %\[C1\], 8
; CHECK-NEXT:  %8 = inttoptr i32 %\[M4\] to i8*
; CHECK-NEXT:  store i8 %9, i8* %10
; CHECK-NEXT:  %\[M5:const_mat[0-9]??\] = add i32 %\[C3\], 7
; CHECK-NEXT:  %11 = inttoptr i32 %\[M5\] to i8*
; CHECK-NEXT:  store i8 %12, i8* %11
; CHECK-NEXT:  %\[M6:const_mat[0-9]??\] = add i32 %\[C1\], 80
define void @foo_i8() {
  entry:
  %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
  %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
  %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
  %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
  %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
  store i8 %4, i8* inttoptr(i32 805873688 to i8*)
  store i8 %3, i8* inttoptr(i32 805873719 to i8*)
  store i8 %2, i8* inttoptr(i32 805873720 to i8*)
  store i8 %1, i8* inttoptr(i32 805873727 to i8*)
  store i8* inttoptr(i32 805874800 to i8*), i8** @goo
  ret void
}

define void @foo_i16() {
  entry:
  %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
  %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
  %2 = load volatile i16, i16* inttoptr (i32 805874719 to i16*), align 2
  %3 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
  %4 = load volatile i16, i16* inttoptr (i32 805874727 to i16*), align 2
  store i16 %4, i16* inttoptr(i32 805873688 to i16*)
  store i16 %3, i16* inttoptr(i32 805873719 to i16*)
  store i16 %2, i16* inttoptr(i32 805873720 to i16*)
  store i16 %1, i16* inttoptr(i32 805873727 to i16*)
  store i16* inttoptr(i32 805874800 to i16*), i16** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]?] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %[[M2:const_mat[0-9]?] = add i32 %[[C2]], 32
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %[[M3:const_mat[0-9]?] = add i32 %[[C2]], 62
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %[[M4:const_mat[0-9]?] = add i32 %[[C1]], 22
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

; Check that for i32 type, the maximum legal offset is 124.
; CHECK-DAG:  %[[C1:const[0-9]??]] = bitcast i32 805874816 to i32
; CHECK-DAG:  %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK:     %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %2 = inttoptr i32 %[[C2]], 4
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %4 = inttoptr i32 %[[C2]], 124
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]], 8
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %10 = inttoptr i32 %[[C1]], 12
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

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------------------------------------------------------------------------------

clang-tidy CERT Files

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de Minimis description of such content.
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {  
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])  
; CHECK-NEXT: bb:  
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]  
; CHECK-NEXT: ret void  
}  
bb:  
%tmp = load <8 x i64>, <8 x i64>* %arg1  
store <8 x i64> %tmp, <8 x i64>* %arg  
ret void  
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {  
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])  
; CHECK-NEXT: bb:  
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32  
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32  
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*  
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)  
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]  
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* [[ARG]])  
; CHECK-NEXT: %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32  
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32  
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2

; CHECK-NEXT: ret void  
}  
bb:  
%tmp = alloca <8 x i64>, align 32  
%tmp2 = alloca <8 x i64>, align 32  
%tmp3 = bitcast <8 x i64>* %tmp to i8*  
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)  
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)  
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32  
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2

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```c
ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ; CHECK-LABEL: define \{[^@]+\}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* \[ARG:%.*\], <8 x i64>* \[ARG1_VAL:%.*\])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> \[ARG1_VAL\], <8 x i64>* \[ARG\]
  ; CHECK-NEXT: ret void

  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
  ; CHECK-LABEL: define \{[^@]+\}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* \[ARG:%.*\])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: \[TMP:%.*\] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: \[TMP2:%.*\] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: \[TMP3:%.*\] = bitcast <8 x i64>* \[TMP\] to i8*
  ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 \[TMP3\], i8 0, i64 32, i1 false)
  ; CHECK-NEXT: \[TMP_VAL:%.*\] = load <8 x i64>, <8 x i64>* \[TMP\]
  ; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* \[TMP2\], <8 x i64>* \[TMP_VAL\])
  ; CHECK-NEXT: \[TMP4:%.*\] = load <8 x i64>, <8 x i64>* \[TMP2\], align 32
  ; CHECK-NEXT: store <8 x i64> \[TMP4\], <8 x i64>* \[ARG\], align 2
  ; CHECK-NEXT: ret void

  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
```
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{[^@]+}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(8 x i64)* %arg) #2 {
 ; CHECK-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
 ; CHECK-SAME: (8 x i64)* {{[ARG:%.*]}}
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
 ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
 ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
 ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
 ; CHECK-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(8 x i64)* [[TMP2]], <8 x i64>* [[TMP]]
 ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
 ; CHECK-NEXT: store <8 x i64> [[TMP4]],<8 x i64>* [[ARG]], align 2
 ; CHECK-NEXT: ret void
 }

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(8 x i64)* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(8 x i64)* %arg, <8 x i64>* readonly %arg1) #2 {
 ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
 ; CHECK-SAME: (8 x i64)* {ARG:%.*], <8 x i64>* readonly {ARG1:%.*})
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
 ; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
 ; CHECK-NEXT: ret void
 }

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(8 x i64)* %arg) #1 {
 ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
 ; CHECK-SAME: (8 x i64)* {{[ARG:%.*]}}
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
; CHECK-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT:  ret void

bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT:    ret void

  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  ; CHECK-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; CHECK-NEXT:    call void @llvm.memset.p0i8.i64(i8* [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  ; CHECK-NEXT:    call fastcc void @callee_avx2 Legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
  ; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT:    ret void

  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
; CHECK-LABEL: define
{{[^@]+}}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
: CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT:   [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT:   ret i32 [[C]]
;
%A = load i32, i32* %X
%B = load i32, i32* %Y
%C = add i32 %A, %B
ret i32 %C
}

define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
; CHECK-LABEL: define
{{[^@]+}}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
: CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT:   [[A:%.*]] = alloca i32
; CHECK-NEXT:   store i32 1, i32* [[A]]
; CHECK-NEXT:   [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:   [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:   [[C:%.*]] = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:   ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A
%C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
ret i32 %C
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
; CHECK-LABEL: define
{{[^@]+}}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
: CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT:   [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT:    [[A:%.*]] = alloca i32
; CHECK-NEXT:    store i32 1, i32* [[A]]
; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:    [[C:%.*]] = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:    ret i32 [[C]]
}

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT:    [[A:%.*]] = alloca i32
; CHECK-NEXT:    store i32 1, i32* [[A]]
; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:    [[C:%.*]] = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:    ret i32 [[C]]
}

; Function Attrs: argmemonly nounwound
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecursion nounwound uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecursion uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecursion uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecursion uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecursion uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwound }

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NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py

; RUN: llc %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=-prefer-256-bit | FileCheck %s
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mcpu=sky anderlace -mattr=prefer-256-bit,avx512vnni | FileCheck %s
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for vpmaddwd+vpaddd

; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-client | FileCheck %s
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-server | FileCheck %s
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=tigerlake | FileCheck %s

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdq a (%rdi), %ymm0
; CHECK-NEXT:  vmovdq 32(%rdi), %ymm1
; CHECK-NEXT:  vpaddd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vmovdq %ymm0, (%rdx)
; CHECK-NEXT:  vmovdq %ymm1, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
; %d = load <16 x i32>, <16 x i32>* %a
; %e = load <16 x i32>, <16 x i32>* %b
; %f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void

}
define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: avg_v64i8_512:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
    ; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %1 = load <64 x i8>, <64 x i8>* %a
    %2 = load <64 x i8>, <64 x i8>* %b
    %3 = zext <64 x i8> %1 to <64 x i32>
    %4 = zext <64 x i8> %2 to <64 x i32>
    %5 = add nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, %6 = add nsw <64 x i32> %5, %4
    %7 = lshr <64 x i32> %6, %8 = trunc <64 x i32> %7 to <64 x i8>
    store <64 x i8> %8, <64 x i8>* undef, align 4
    ret void
}

define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: pmaddwd_32_256:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> %i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> %i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31
}
define void @pmaddwd_32_512(\*<32 x i16> %APtr, \*<32 x i16> %BPtr, \*<16 x i32> %CPtr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: pmaddwd_32_512:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpmaddwd (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}

define void @psubus_64i8_max_256(\*<64 x i8> %xptr, \*<64 x i8> %yptr, \*<64 x i8> %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: psubus_64i8_max_256:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT: vmovdqa %ymm0, 32(%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %x = load <64 x i8>, <64 x i8>* %xptr
    %y = load <64 x i8>, <64 x i8>* %yptr
    %cmp = icmp ult <64 x i8> %x, %y
    %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
    %res = sub <64 x i8> %max, %y
    store <64 x i8> %res, <64 x i8>* %zptr
    ret void
}
define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: psubus_64i8_max_512:
    ; CHECK:      # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: _Z9test_charPcS_i_256:
    ; CHECK:      # %bb.0: # %entry
    ; CHECK-NEXT: movl %edx, %eax
    ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT: xorl %ecx, %ecx
    ; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
    ; CHECK-NEXT: .p2align 4, 0x90
    ; CHECK-NEXT: .LBB8_1: # %vector.body
    ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm3
    ; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm4
    ; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpaddd %ymm2, %ymm3, %ymm2
    ; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm4, %ymm3, %ymm3
    ; CHECK-NEXT: vpaddd %ymm1, %ymm3, %ymm1
    ; CHECK-NEXT: addq $32, %rcx
    ; CHECK-NEXT: cmpq %rcx, %rax
    ; CHECK-NEXT: jne .LBB8_1
    ; CHECK-NEXT: # %bb.2: # %middle.block
    ; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm1
    ; CHECK-NEXT: vpaddd %ymm0, %ymm2, %ymm0
    ; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm0
    ; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
    ; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0

; CHECK-NEXT:   vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:   vpaddd %xmm1, %xmm0
; CHECK-NEXT:   vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:   vpaddd %xmm1, %xmm0
; CHECK-NEXT:   vmovd %xmm0, %eax
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq

entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

middle.block:
%rdx.shuf = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf
%rdx.shuf1 = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sad_16i8_512:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT:    movq $-1024, %rax # imm = 0xFC00
    ; CHECK-NEXT:    .p2align 4, 0x90
    ; CHECK-NEXT:  .LBB11_1: # %vector.body
    ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm1
    ; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
    ; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
    ; CHECK-NEXT:    addq $4, %rax
    ; CHECK-NEXT:    jne .LBB11_1
    ; CHECK-NEXT:  # %bb.2: # %middle.block
    ; CHECK-NEXT:    vextracti64x4 %xmm1, %zmm0, %xmm1
    ; CHECK-NEXT:    vpadd %zmm1, %zmm0, %zmm0
    ; CHECK-NEXT:    vextracti128 %xmm0, %zmm0, %xmm1
    ; CHECK-NEXT:    vpadd %xmm1, %zmm0, %xmm0
    ; CHECK-NEXT:    vpsdf {{{.*#}}} %zmm1 = %xmm0[2,3,0,1]
    ; CHECK-NEXT:    vpadd %zmm1, %zmm0, %zmm0
    ; CHECK-NEXT:    vpsdf {{{.*#}}} %zmm1 = %xmm0[1,1,2,3]
    ; CHECK-NEXT:    vpadd %zmm1, %zmm0, %zmm0
    ; CHECK-NEXT:    vmovd %zmm0, %eax
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    entry:
    br label %vector.body
vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %10 = add <16 x i32> %10, %rdx.shuf

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
 ; CHECK-LABEL:  spto16f32_256:
 ; CHECK:    # %bb.0:
 ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
 ; CHECK-NEXT:  kshiftrw $8, %k0, %k1
 ; CHECK-NEXT:  vpmovm2d %k1, %ymm0
 ; CHECK-NEXT:  vctdq2ps %ymm0, %ymm0
}
define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {  
; CHECK-LABEL: sbto16f32_512:  
; CHECK:       # %bb.0:  
; CHECK-NEXT: vpmovw2m %ymm0, %k0  
; CHECK-NEXT: vpmovm2d %k0, %zmm0  
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0  
; CHECK-NEXT: vmovaps %zmm0, (%rdi)  
; CHECK-NEXT: vzeroupper  
; CHECK-NEXT: retq  
  
%mask = icmp slt <16 x i16> %a, zeroinitializer  
%1 = sitofp <16 x i1> %mask to <16 x float>  
store <16 x float> %1, <16 x float>* %res  
ret void  
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {  
; CHECK-LABEL: sbto16f64_256:  
; CHECK:       # %bb.0:  
; CHECK-NEXT: vpmovw2m %ymm0, %k0  
; CHECK-NEXT: vpmovm2d %k0, %zmm0  
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0  
; CHECK-NEXT: vextracti128 $1, %zmm0, %xmm0  
; CHECK-NEXT: vcvtdq2pd %xmm0, %xmm0  
; CHECK-NEXT: vpmovm2d %k0, %yìnmm2  
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3  
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2  
; CHECK-NEXT: vcvtdq2pd %xmm2, %xmm2  
; CHECK-NEXT: vmovaps %ymm2, 32(%rđi)  
; CHECK-NEXT: vmovaps %ymm3, (%rđi)  
; CHECK-NEXT: vmovaps %ymm0, 96(%rđi)  
; CHECK-NEXT: vmovaps %ymm1, 64(%rđi)  
; CHECK-NEXT: vzeroupper  
; CHECK-NEXT: retq  
  
%mask = icmp slt <16 x i16> %a, zeroinitializer  
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f64_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:   vcvtq2pd %ymm0, %zmm1
    ; CHECK-NEXT:   vextractl64x4 $1, %zmm0, %ymm0
    ; CHECK-NEXT:   vcvtq2pd %ymm0, %zmm0
    ; CHECK-NEXT:   vmovaps %zmm0, 64(%rdi)
    ; CHECK-NEXT:   vmovaps %zmm1, (%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitialize
    %1 = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: ubto16f32_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:   vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:   vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:   vcvtq2ps %ymm0, %ymm0
    ; CHECK-NEXT:   vpmovm2d %k0, %ymm1
    ; CHECK-NEXT:   vpsrld $31, %ymm1, %ymm1
    ; CHECK-NEXT:   vcvtq2ps %ymm1, %ymm1
    ; CHECK-NEXT:   vmovaps %ymm1, (%rdi)
    ; CHECK-NEXT:   vmovaps %ymm0, 32(%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitialize
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: ubto16f32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:   vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:   vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:   vcvtq2ps %ymm0, %ymm0
    ; CHECK-NEXT:   vpmovm2d %k0, %ymm1
    ; CHECK-NEXT:   vpsrld $31, %ymm1, %ymm1
    ; CHECK-NEXT:   vcvtq2ps %ymm1, %ymm1
    ; CHECK-NEXT:   vmovaps %ymm1, (%rdi)
    ; CHECK-NEXT:   vmovaps %ymm0, 32(%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitialize
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}
define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: ubto16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:  kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:  vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:  vcvtq2pd %xmm0, %ymm1
    ; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm0
    ; CHECK-NEXT:  vcvtq2pd %xmm0, %ymm0
    ; CHECK-NEXT:  vpmovm2d %k0, %ymm2
    ; CHECK-NEXT:  vpsrld $31, %ymm2, %ymm2
    ; CHECK-NEXT:  vcvtq2pd %xmm0, %ymm3
    ; CHECK-NEXT:  vextracti128 $1, %ymm2, %xmm2
    ; CHECK-NEXT:  vcvtq2pd %xmm2, %ymm2
    ; CHECK-NEXT:  vmovaps %ymm2, 32(%rdi)
    ; CHECK-NEXT:  vmovaps %ymm3, 64(%rdi)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: ubto16f64_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:  vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:  vcvtq2pd %xmm0, %ymm1
    ; CHECK-NEXT:  vextracti64x4 $1, %ymm0, %ymm0
    ; CHECK-NEXT:  vcvtq2pd %ymm0, %ymm1
    ; CHECK-NEXT:  vextracti64x4 $1, %ymm0, %ymm0
    ; CHECK-NEXT:  vcvtq2pd %ymm0, %ymm3
    ; CHECK-NEXT:  vmovaps %ymm3, 96(%rdi)
    ; CHECK-NEXT:  vmovaps %ymm1, 64(%rdi)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: ubto16f64_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:  vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:  vcvtq2pd %ymm0, %ymm0
    ; CHECK-NEXT:  vcvtq2pd %ymm0, %ymm0
    ; CHECK-NEXT:  vmovaps %ymm0, 64(%rdi)
; CHECK-NEXT:   vpmovd2m %ymm1, %k0
; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k1
; CHECK-NEXT:   kunpckbw %k0, %k1
; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
; CHECK-LABEL: test_16f32tosb_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:   vpmovd2m %zmm1, %k1
; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: mul256:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:   vmovdqa (%rdi), %ymm0
; CHECK-AVX512-NEXT:   vmovdqa 32(%rdi), %ymm1
; CHECK-AVX512-NEXT:   vmovdqa (%rsi), %ymm2
; CHECK-AVX512-NEXT:   vmovdqa 32(%rsi), %ymm3
; CHECK-AVX512-NEXT:   vpunpckhbw {{.*#+}} ymm4 =
  ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:   vpunpckhbw {{.*#+}} ymm5 =
  ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT:   vpnullw %ymm4, %ymm5, %ymm4
; CHECK-AVX512-NEXT:   vmovdqa {{.*#+}} ymm5 =
; CHECK-AVX512-NEXT:   vpand %ymm5, %ymm4, %ymm4
; CHECK-AVX512-NEXT:   vpunpcklbw {{.*#+}} ymm3 =
  ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:   vpunpcklbw {{.*#+}} ymm1 =
  ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:   vpnullw %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:   vpand %ymm5, %ymm1, %ymm1
; CHECK-AVX512-NEXT:   vpckuswb %ymm4, %ymm1, %ymm1

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; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm3 =
  ymm2[8,8,9,10,10,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,31,31]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm4 =
  ymm0[8,8,9,10,10,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,31,31]
; CHECK-AVX512-NEXT: vpmullw %ymm3, %ymm4, %ymm3
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} ymm2 =
  ymm2[0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} ymm0 =
  ymm0[0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpmullw %ymm2, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-AVX512-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-AVX512-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-AVX512-NEXT: vzeroupper
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: mul256:
; CHECK-VBMI: # %bb.0:
; CHECK-VBMI-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-VBMI-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-VBMI-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-VBMI-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm4 =
  ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,31,31]
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm5 =
  ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,31,31]
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT: vpackuswb %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT: vperm2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT: vperm2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT: vperm2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT: vperm2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT: vperm2b %ymm4, %ymm3, %ymm1

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define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {

zmm0[8, 8, 9, 9, 10, 10, 11, 11, 12, 12, 13, 13, 14, 14, 15, 15, 24, 24, 25, 25, 26, 26, 27, 27, 28, 28, 29, 29, 30, 30, 31, 31, 40, 40, 41, 41, 42, 42, 43, 43, 44, 44, 45, 45, 46, 46, 47, 47, 56, 56, 57, 57, 58, 58, 59, 59, 60, 60, 61, 61, 62, 62, 63, 63]
; CHECK-VBMI-NEXT: vpmullw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT: vpunpcklbw {%.*#+} zmm1 = zmm1[0, 0, 1, 1, 2, 2, 3, 3, 4, 4, 5, 5, 6, 6, 7, 7, 16, 16, 17, 17, 18, 18, 19, 19, 20, 20, 20, 20, 21, 21, 22, 22, 23, 23, 24, 24, 32, 32, 33, 33, 34, 34, 35, 35, 36, 36, 37, 37, 38, 38, 39, 39, 48, 48, 49, 49, 50, 50, 51, 51, 52, 52, 53, 53, 54, 54, 55, 55]
; CHECK-VBMI-NEXT: vpunpcklbw {%.*#+} zmm0 = zmm0[0, 0, 1, 1, 2, 2, 3, 3, 4, 4, 5, 5, 6, 6, 7, 7, 16, 16, 17, 17, 18, 18, 19, 19, 20, 20, 20, 20, 21, 21, 22, 22, 23, 23, 24, 24, 32, 32, 33, 33, 34, 34, 35, 35, 36, 36, 37, 37, 38, 38, 39, 39, 48, 48, 49, 49, 50, 50, 51, 51, 52, 52, 53, 53, 54, 54, 55, 55]
; CHECK-VBMI-NEXT: vpmullw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT: vmovdqa64 {%.*#+} zmm1 = [0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38, 40, 42, 44, 46, 96, 98, 100, 102, 104, 106, 108, 110, 48, 50, 52, 54, 56, 58, 60, 62, 112, 114, 116, 118, 120, 122, 124, 126]
; CHECK-VBMI-NEXT: vpermib2b {%zmm2}, %zmm0, %zmm0
; CHECK-VBMI-NEXT: vmovdqa64 %zmm1, (%rdx)
; CHECK-VBMI-NEXT: vzeroupper
; CHECK-VBMI-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {  
; CHECK-LABEL: mload_v4i32:
; CHECK: # %bb.0:
; CHECK-NEXT: vptestnmd %xmm0, %xmm0, %k1
; CHECK-NEXT: vpbldem (%rdi), %xmm1, %xmm0 {%k1}
; CHECK-NEXT: retq
%mask = icmp eq <4 x i32> %trigger, zeroinitializer
%res = call <4 x i32> @llvm.masked.load.v4i32(<4 x i32>*, i32 4, <4 x i32> %mask, %dst)
ret <4 x i32> %res
}  
declare <4 x i32> @llvm.masked.load.v4i32(<4 x i32>* %addr, i32 4, <4 x i32> %mask, %dst) nounwind "min-legal-vector-width"="256"

define <16 x i32> @trunc_v16i64_v16i32(<16 x i64> %x) nounwind "min-legal-vector-width"="256" {  
; CHECK-LABEL: trunc_v16i64_v16i32:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm1
; CHECK-NEXT: vmovdqa4 (%rdi), %ymm2
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm3
; CHECK-NEXT: vpmovqd %ymm0, %xmm0
; CHECK-NEXT: vpmovqd %ymm1, %xmm1
define <16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
  CHECK-LABEL: trunc_v16i64_v16i8:
  CHECK:  
  CHECK-NEXT:  vmovdqa (%rdi), %ymm0
  CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
  CHECK-NEXT:  vmovdqa 64(%rdi), %ymm2
  CHECK-NEXT:  vmovdqa 96(%rdi), %ymm3
  CHECK-NEXT:  vpmovq dq %ymm0, %xmm0
  CHECK-NEXT:  vpmovq dq %ymm1, %xmm1
  CHECK-NEXT:  vpmovq dq %ymm2, %xmm2
  CHECK-NEXT:  vpmovq dq %ymm3, %xmm3
  CHECK-NEXT:  vzeroupper
  CHECK-NEXT:  retq
  %a = load <16 x i64>, <16 x i64>* %x
  %b = trunc <16 x i64> %a to <16 x i32>
  ret <16 x i8> %b
}

define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {  
  CHECK-LABEL: trunc_v16i32_v16i8:
  CHECK:  
  CHECK-NEXT:  vmovdqa (%rdi), %ymm0
  CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
  CHECK-NEXT:  vpmovqdb %ymm0, %xmm0
  CHECK-NEXT:  vpmovqdb %ymm1, %xmm1
  CHECK-NEXT:  vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
  CHECK-NEXT:  vzeroupper
  CHECK-NEXT:  retq
  %a = load <16 x i32>, <16 x i32>* %x
  %b = trunc <16 x i32> %a to <16 x i8>
  ret <16 x i8> %b
}

define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
  CHECK-LABEL: trunc_v8i64_v8i8:
  CHECK:  
  CHECK-NEXT:  vpmovq dq %ymm0, %xmm0
  CHECK-NEXT:  vpmovq dq %ymm1, %xmm1
  CHECK-NEXT:  vzeroupper
  CHECK-NEXT:  retq
  %a = load <16 x i32>, <16 x i32>* %x
  %b = trunc <16 x i32> %a to <16 x i8>
  ret <16 x i8> %b
}
define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i16:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:    vpmovqb %ymm1, %xmm1
    ; CHECK-NEXT:    vpmovqb %ymm0, %xmm0
    ; CHECK-NEXT:    vpunpckldq \{ \*\#\} %xmm0 = %xmm0[0],%xmm1[0],%xmm0[1],%xmm1[1]
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = trunc <8 x i64> %a to <8 x i8>
    ret <8 x i8> %b
}

define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpsrlq $48, 32(%rdi), %ymm1
    ; CHECK-NEXT:    vpsrlq $48, (%rdi), %ymm2
    ; CHECK-NEXT:    vmovdqa \{ \*\#\} ymm0 = [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30]
    ; CHECK-NEXT:    vpermilw %ymm1, %ymm2, %ymm0
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = lshr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
    %c = trunc <8 x i64> %b to <8 x i32>
    ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rdi), %ymm1
    ; CHECK-NEXT:    vmovdqa \{ \*\#\} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
    ; CHECK-NEXT:    vpermilw 32(%rdi), %ymm1, %ymm0
    }
define <32 x i8> @trunc_v32i16_v32i16_zeroses(@trunc_v32i16_v32i16_zeroses)
define <8 x i32> @trunc_v8i64_v8i32_sign(@trunc_v8i64_v8i32_sign)
define <16 x i16> @trunc_v16i32_v16i16_sign(@trunc_v16i32_v16i16_sign)
; CHECK:    # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-NEXT:  vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT:  vpermil2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT:  retq
%a = load <16 x i32>, <16 x i32>* %x
%b = ashr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>
%c = truncu <16 x i32> %b to <16 x i16>
ret <16 x i16> %c

define <32 x i8> @trunc_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {  
; CHECK-AVX512-LABEL:  trunc_v32i16_v32i8_sign:
; CHECK-AVX512:    # %bb.0:
; CHECK-AVX512-NEXT:  vpsraw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT:  vpsraw $8, (%rdi), %ymm1
; CHECK-AVX512-NEXT:  vpacksswb %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-AVX512-NEXT:  retq
%
; CHECK-VBMI-LABEL:  trunc_v32i16_v32i8_sign:
; CHECK-VBMI:    # %bb.0:
; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT:  vpermi2b 32(%rdi), %ymm1, %ymm0
; CHECK-VBMI-NEXT:  retq
%a = load <32 x i16>, <32 x i16>* %x
%b = ashr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = truncu <32 x i16> %b to <32 x i8>
ret <32 x i8> %c

define void @zext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {  
; CHECK-LABEL:  zext_v16i8_v16i64:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm1 = xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
; CHECK-NEXT:  vpmovzxwq {{.*#+}} xmm2 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} xmm2 = xmm2[0],zero,zero,xmm2[1],zero,zero,xmm2[2],zero,zero,xmm2[3],zero,zero
; CHECK-NEXT:  vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT:  vpmovufd {{.*#+}} xmm3 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm3 =
xmm3[0], zero, zero, zero, xmm3[1], zero, zero, zero, xmm3[2], zero, zero, zero, xmm3[3], zero, zero, zero
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm1 =
xmm1[0], zero, zero, xmm1[1], zero, zero, zero, xmm1[2], zero, zero, zero, xmm1[3], zero, zero, zero
; CHECK-NEXT:  vpmovzxbq {{.*#+}} ymm0 =
xmm0[0], zero, zero, zero, zero, zero, zero, zero, xmm0[1], zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero, zero,
; CHECK-NEXT:  vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:  vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT:  vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT:  vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}
define void @s sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vpmovsxbw %xmm0, %ymm0
; CHECK-NEXT:  vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpmovsxwq %xmm1, %ymm1
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT:  vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]
; CHECK-NEXT:  vpmovsxwq %xmm3, %ymm3
; CHECK-NEXT:  vpmovsxwq %xmm0, %ymm0
; CHECK-NEXT:  vpmovsxwq %xmm2, %ymm2
; CHECK-NEXT:  vmovdqa %ymm2, 64(%rdi)
; CHECK-NEXT:  vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:  vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = sext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}
define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i16_setcc:
; CHECK:      # %bb.0:
; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:  vpcmpeqw %xmm1, %xmm0, %k1
; CHECK-NEXT:  kshiftrb $4, %k1, %k2
; CHECK-NEXT:  vmovdqa64 32(%rdi), %ymm3 [%k2]
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; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i16> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r)  "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i32_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqd %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrr $4, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 [%k2]
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 [%k1]
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i32> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r)  "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i8_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqb %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrw $8, %k1, %k2
; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 [%k2]
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 [%k1]
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT:  retq
  %x = load <16 x i32>, <16 x i32>* %p
  %y = load <16 x i32>, <16 x i32>* %q
  %a = icmp eq <16 x i8> %s, %t
  %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
  store <16 x i32> %b, <16 x i32>* %r
  ret void
)

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL:  vselect_split_v16i16_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT:  vpcmpeqw %ymm1, %ymm0, %k1
; CHECK-NEXT:  kshiftrw $8, %k1, %k2
; CHECK-NEXT:  vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT:  vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT:  vmovdqa %ymm2, (%rdx)
; CHECK-NEXT:  vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
  %x = load <16 x i32>, <16 x i32>* %p
  %y = load <16 x i32>, <16 x i32>* %q
  %a = icmp eq <16 x i16> %s, %t
  %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
  store <16 x i32> %b, <16 x i32>* %r
  ret void
}

define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {
; CHECK-LABEL:  trunc_packus_v16i32_v16i8:
; CHECK: # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT:  vpmovuswb %ymm0, %xmm0
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
  %a = load <16 x i32>, <16 x i32>* %p
  %b = icmp slt <16 x i32> %a, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255>
  %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255, %i32 255>
  %d = icmp sgt <16 x i32> %c, zeroinitializer
  %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
  %f = trunc <16 x i32> %e to <16 x i8>
define void @trunc_packus_v16i32_v16i8_store(<16 x i32>* %p, <16 x i8>* %q) "min-legal-vector-width"="256" {  
  ; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:  
  ; CHECK:  
  ; CHECK-NEXT:  
  ; CHECK-NEXT: vmovdqa (%rdi), %ymm0  
  ; CHECK-NEXT: vpackusdw 32(%rdi), %ymm0, %ymm0  
  ; CHECK-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]  
  ; CHECK-NEXT: vpmovuswb %ymm0, (%rsi)  
  ; CHECK-NEXT: vzeroupper  
  ; CHECK-NEXT: retq  
  %a = load <16 x i32>, <16 x i32>* %p  
  %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>  
  %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> %c  
  %d = icmp sgt <16 x i32> %c, zeroinitialzier  
  %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitialzier  
  %f = trunc <16 x i32> %e to <16 x i8>  
  store <16 x i8> %f, <16 x i8>* %q  
  ret void  
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {  
  ; CHECK-LABEL: v64i1_argument_return:  
  ; CHECK:  
  ; CHECK-NEXT: retq  
  ret <64 x i1> %x  
}

define void @v64i1_shuffle(<64 x i8>* %x, <64 x i8>* %y) "min-legal-vector-width"="256" {  
  ; CHECK-LABEL: v64i1_shuffle:  
  ; CHECK:  
  ; CHECK-NEXT: vmovdqa (%rdi), %ymm1  
  ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm0  
  ; CHECK-NEXT: vptestnmb %ymm1, %ymm1, %k0  
  ; CHECK-NEXT: kshiftrd $1, %k0, %k1  
  ; CHECK-NEXT: movq $-3, %rax  
  ; CHECK-NEXT: kmovq %rax, %k2  
  ; CHECK-NEXT: kandq %k2, %k1, %k1  
  ; CHECK-NEXT: kshiftlq $63, %k0, %k2  
  ; CHECK-NEXT: kshiftrq $62, %k2, %k2  
  ; CHECK-NEXT: korq %k2, %k1, %k1  
  ; CHECK-NEXT: movq $-5, %rax  
  ; CHECK-NEXT: kmovq %rax, %k2  
  ; CHECK-NEXT: kandq %k2, %k1, %k1  

; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $17, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $47, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-131073, %rax # imm = 0xFFFDFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $16, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $46, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-262145, %rax # imm = 0xFFFFBFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $19, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $45, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-524289, %rax # imm = 0xFFF7FFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $18, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $44, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1048577, %rax # imm = 0xFFEFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $21, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $43, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-2097153, %rax # imm = 0xFFDFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $23, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $41, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-4194305, %rax # imm = 0xFFFBFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $20, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $42, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-4194305, %rax # imm = 0xFFFBFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $23, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $41, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT: movq $-8388609, %rax # imm = 0xFF7FFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $22, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $40, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-16777217, %rax # imm = 0xFEFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $25, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $39, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-33554433, %rax # imm = 0xFDFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $24, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $38, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-67108865, %rax # imm = 0xFBFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $27, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $37, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-134217729, %rax # imm = 0xF7FFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $26, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $36, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-268435457, %rax # imm = 0xEFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $29, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $35, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-536870913, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $28, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $34, %k2, %k2
; CHECK-NEXT:  kandq  %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $24, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $6, %k2, %k2
; CHECK-NEXT:  korq  %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-288230376151711745, %rax # imm = 0xFBFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq  %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $27, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $5, %k2, %k2
; CHECK-NEXT:  korq  %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq  %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $26, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $4, %k2, %k2
; CHECK-NEXT:  korq  %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-1152921504606846977, %rax # imm = 0xEFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq  %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $29, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $3, %k2, %k2
; CHECK-NEXT:  korq  %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-2305843009213693953, %rax # imm = 0xDFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq  %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $31, %k1, %k2
; CHECK-NEXT:  kshiftlq $62, %k2, %k2
; CHECK-NEXT:  kshiftrq $2, %k2, %k2
; CHECK-NEXT:  korq  %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-4611686018427387905, %rax # imm = 0xBFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq  %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $30, %k1, %k1
; CHECK-NEXT:  kshiftlq $61, %k1, %k1
; CHECK-NEXT:  kshiftrq $1, %k1, %k1
; CHECK-NEXT:  korq  %k1, %k0, %k1
; CHECK-NEXT:  vmovdqu8 %ymm1, (%rsi) {%k1}
; CHECK-NEXT:  kshiftrq $32, %k1, %k1
; CHECK-NEXT:  vmovdqu8 %ymm0, 32(%rsi) {%k1}
; CHECK-NEXT:  vzeroupper
entry:
%a = load <64 x i8>, <64 x i8>* %x
%b = icmp eq <64 x i8> %a, zeroinitializer
%shuf = shufflevector <64 x i1> %b, undef, <64 x i32> <i32 1, i32 0, i32 3, i32 2, i32 5, i32 4, i32 7, i32 6, i32 9, i32 8, i32 11, i32 10, i32 13, i32 12, i32 15, i32 14, i32 17, i32 16, i32 19, i32 18, i32 21, i32 20, i32 23, i32 22, i32 25, i32 24, i32 27, i32 26, i32 29, i32 28, i32 31, i32 30, i32 33, i32 32, i32 35, i32 34, i32 37, i32 36, i32 39, i32 38, i32 41, i32 40, i32 43, i32 42, i32 45, i32 44, i32 47, i32 46, i32 49, i32 48, i32 51, i32 50, i32 53, i32 52, i32 55, i32 54, i32 57, i32 56, i32 59, i32 58, i32 61, i32 60, i32 63, i32 62>
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, i32 1, <64 x i1> %shuf)
ret void
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>* , i32, <64 x i1>)

@mem64_dst = global i64 0, align 8
@mem64_src = global i64 0, align 8
define i32 @v64i1_inline_asm() "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_inline_asm:
; CHECK: # %bb.0:
; CHECK-NEXT: kmovq {{.*}}(%rip), %k0
; CHECK-NEXT: #APP
; CHECK-NEXT: #NO_APP
; CHECK-NEXT: kmovq %k0, {{.*}}(%rip)
; CHECK-NEXT: movl -{{[0-9]+}}(%rsp), %eax
; CHECK-NEXT: retq
%1 = alloca i32, align 4
%2 = load i64, i64* @mem64_src, align 8
%3 = call i64 asm ",","=k,k,~{dirflag},~{fpsr},~{flags}"(i64 %2)
store i64 %3, i64* @mem64_dst, align 8
%4 = load i32, i32* %1, align 4
ret i32 %4
}
declare void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = sext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: cmp_v8i64_zext:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vmovdqa (%rsi), %ymm0
 ; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
 ; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
 ; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
 ; CHECK-NEXT: vpsrlq $63, %ymm1, %ymm1
 ; CHECK-NEXT: vpsrlq $63, %ymm0, %ymm0
 ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
 ; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %x = load <8 x i64>, <8 x i64>* %xptr
 %y = load <8 x i64>, <8 x i64>* %yptr
 %cmp = icmp slt <8 x i64> %x, %y
 %ext = zext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
 ; CHECK-LABEL: var_rotate_v16i8:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
 ; CHECK-NEXT: vpsubb %xmm1, %xmm2, %xmm2
 ; CHECK-NEXT: vmovzxwb { {.*+#} } ymm1 =
 xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zero,
 xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xmm1[15],zero
 ; CHECK-NEXT: vmovzxwb { {.*+#} } ymm0 =
 xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,
 xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
 ; CHECK-NEXT: vpsllvw %ymm1, %ymm0, %ymm1
 ; CHECK-NEXT: vmovzxwb { {.*+#} } ymm2 =
 xmm2[0],zero,xmm2[1],zero,xmm2[2],zero,xmm2[3],zero,xmm2[4],zero,xmm2[5],zero,xmm2[6],zero,xmm2[7],zero,
 xmm2[8],zero,xmm2[9],zero,xmm2[10],zero,xmm2[11],zero,xmm2[12],zero,xmm2[13],zero,xmm2[14],zero,xmm2[15],zero
 ; CHECK-NEXT: vpsllvw %ymm2, %ymm0, %ymm0
 ; CHECK-NEXT: vpor %ymm0, %ymm1, %ymm0
 ; CHECK-NEXT: vmovwb %ymm0, %xmm0

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {  
CHECK-LABEL: var_rotate_v32i8:
CHECK:  # %bb.0:
CHECK-NEXT: vpsllw $4, %ymm0, %ymm2
CHECK-NEXT: vpsrlw $4, %ymm0, %ymm3
CHECK-NEXT: vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
CHECK-NEXT: vpsllw $5, %ymm1, %ymm1
CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
CHECK-NEXT: vpsllw $2, %ymm0, %ymm2
CHECK-NEXT: vpsrlw $6, %ymm0, %ymm3
CHECK-NEXT: vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
CHECK-NEXT: vpaddb %ymm1, %ymm1, %ymm1
CHECK-NEXT: vpsllw $7, %ymm0, %ymm2
CHECK-NEXT: vpaddb %ymm0, %ymm0, %ymm0
CHECK-NEXT: vpternlogq $248, {{.*}}(%rip), %ymm2, %ymm3
CHECK-NEXT: vpaddb %ymm1, %ymm1, %ymm1
CHECK-NEXT: vpsllw $2, %ymm0, %ymm2
CHECK-NEXT: vpsrlw $6, %ymm0, %ymm3
CHECK-NEXT: vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
CHECK-NEXT: vpaddb %ymm1, %ymm1, %ymm1
CHECK-NEXT: vpsllw $7, %ymm0, %ymm2
CHECK-NEXT: vpaddb %ymm0, %ymm0, %ymm0
CHECK-NEXT: vpternlogq $248, {{.*}}(%rip), %ymm2, %ymm3
CHECK-NEXT: vpaddb %ymm1, %ymm1, %ymm1
CHECK-NEXT: retq
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define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: constant_rotate_v32i8:
    ; CHECK-AVX512: # %bb.0:
    ; CHECK-AVX512-NEXT: vpsllw $4, %ymm0, %ymm1
    ; CHECK-AVX512-NEXT: vpand {{.*}}(%rip), %ymm1, %ymm1
    ; CHECK-AVX512-NEXT: vbroadcasti128 {{.*#+}} ymm2 = [8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]
    ; CHECK-AVX512-NEXT: vpblendvb %ymm2, %ymm2, %ymm1
    ; CHECK-AVX512-NEXT: vpaddb %ymm2, %ymm2, %ymm2
    ; CHECK-AVX512-NEXT: vpblendvb %ymm2, %ymm2, %ymm2
    ; CHECK-AVX512-NEXT: vpxor %xmm2, %xmm2, %xmm2
    ; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm3 = ymm0[8],ymm2[8],ymm0[9],ymm2[9],ymm0[10],ymm2[10],ymm0[11],ymm2[11],ymm0[12],ymm2[12],ymm0[13],ymm2[13],ymm0[14],ymm2[14],ymm0[15],ymm2[15],ymm0[24],ymm2[24],ymm0[25],ymm2[25],ymm0[26],ymm2[26],ymm0[27],ymm2[27],ymm0[28],ymm2[28],ymm0[29],ymm2[29],ymm0[30],ymm2[30],ymm0[31],ymm2[31]
    ; CHECK-AVX512-NEXT: vpsllw {{.*}}(%rip), %ymm3, %ymm3
    ; CHECK-AVX512-NEXT: vpsrlw $8, %ymm3, %ymm3
    ; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} ymm0 = ymm0[0],ymm2[0],ymm0[1],ymm2[1],ymm0[2],ymm2[2],ymm0[3],ymm2[3],ymm0[4],ymm2[4],ymm0[5],ymm2[5],ymm0[6],ymm2[6],ymm0[7],ymm2[7],ymm0[16],ymm2[16],ymm0[17],ymm2[17],ymm0[18],ymm2[18],ymm0[19],ymm2[19],ymm0[20],ymm2[20],ymm0[21],ymm2[21],ymm0[22],ymm2[22],ymm0[23],ymm2[23]
}
define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: splatconstant_rotate_v32i8:
  ; CHECK-AVX512-NEXT:  vpsllw {{.*}}(%rip), %ymm0, %ymm0
  ; CHECK-AVX512-NEXT:  vpsllw $8, %ymm0, %ymm0
  ; CHECK-AVX512-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0
  ; CHECK-AVX512-NEXT:  vpor %ymm0, %ymm1, %ymm0
  ; CHECK-AVX512-NEXT:  retq
  ;
  ; CHECK-VBMI-LABEL: constant_rotate_v32i8:
  ; CHECK-VBMI:    # %bb.0:
  ; CHECK-VBMI-NEXT:  vpsllw $4, %ymm0, %ymm1
  ; CHECK-VBMI-NEXT:  vpsllw $2, %ymm1, %ymm3
  ; CHECK-VBMI-NEXT:  vpsllw {{.*}}(%rip), %ymm1, %ymm1
  ; CHECK-VBMI-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0
  ; CHECK-VBMI-NEXT:  vpor %ymm0, %ymm1, %ymm0
  ; CHECK-VBMI-NEXT:  vblendlwb %ymm2, %ymm0, %ymm2, %ymm2, %ymm2, %ymm2
  ; CHECK-VBMI-NEXT:  vpsllw {{.*}}(%rip), %ymm0, %ymm0
  ; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm2 = [1,3,5,7,9,11,13,15,33,35,37,39,41,43,45,47,17,19,21,23,25,27,29,31,49,51,53,55,57,59,61,63]
  ; CHECK-VBMI-NEXT:  vpermi2b %ymm3, %ymm0, %ymm2
  ; CHECK-VBMI-NEXT:  vpor %ymm2, %ymm1, %ymm0
  ; CHECK-VBMI-NEXT:  retq
  ;
  %shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31, i8 32>
  %lshr = lshr <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31, i8 32>
  %or = or <32 x i8> %shl, %lshr
  ret <32 x i8> %or
}

define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: splatconstant_rotate_v32i8:

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define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {  
; CHECK-LABEL: splatconstant_rotate_mask_v32i8:  
; CHECK:       # %bb.0:  
; CHECK-NEXT:    vpsllw $4, %ymm0, %ymm1  
; CHECK-NEXT:    vpsrlw $4, %ymm0, %ymm0  
; CHECK-NEXT:    vpternlogq $216, {{.*}}(%rip), %ymm1, %ymm0  
; CHECK-NEXT:    vpand {{.*}}(%rip), %ymm0, %ymm0  
; CHECK-NEXT:    retq  
%shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4,  
%lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4,  
%rmask = and <32 x i8> %lshr, <i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55,  
%lmask = and <32 x i8> %shl, <i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33,  
%or = or <32 x i8> %shl, %lshr  
ret <32 x i8> %or  
}  

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Developed by:

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University of Illinois at Urbana-Champaign

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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp

LINK_LIBS
   llubCore
   llubHost
   llubSymbol

LINK_COMPONENTS
   Support

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Zbigniew Skowron <zbychs@gmail.com>
; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 { entry:
br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph:
  ; preds = %entry
  br label %while.body

while.body:
  ; preds = %exit.2, %while.body.lr.ph
  %lsr.iv = phi %struct.0* [%cgep22, %exit.2], [undef, %while.body.lr.ph]
  switch i32 undef, label %exit [
    i32 1, label %sw.bb.i
    i32 2, label %sw.bb3.i
  ]

sw.bb.i:
  ; preds = %while.body
  unreachable

sw.bb3.i:
  ; preds = %while.body
  unreachable

exit:
  ; preds = %while.body
  switch i32 undef, label %exit.2 [
    i32 1, label %sw.bb.i17
    i32 2, label %sw.bb3.i20
  ]

sw.bb.i17:
  ; preds = %exit
  %0 = bitcast %struct.0* %lsr.iv to i32*
  %1 = load i32, i32* %0, align 4
  unreachable

sw.bb3.i20:
  ; preds = %exit
  %2 = bitcast %struct.0* %lsr.iv to i8**
  %3 = load i8*, i8** %2, align 4
  unreachable
exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
  ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }

; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=4 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS________OP M,IS__TUNIT_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=3 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_OPM,NOT_CGSCC_NPM,NOT_TUNIT_OPM,IS__TUNIT____,IS________OP M,IS__TUNIT_NPM
; RUN: opt -attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_NPM,IS__CGSCC____,IS________OP M,IS__CGSCC_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS________NP M,IS__CGSCC_NPM

; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* %arg1) #0 {

; NOT_TUNIT_NPM-LABEL: define
  {{[^@]+}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.%]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.%]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.%]] = load <8 x i64>, <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define
  {{[^@]+}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {

  bbc:
  %tmp = load <8 x i64>, <8 x i64> * %arg
  store <8 x i64> %tmp, <8 x i64> * %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {

  bbc:
  %tmp = load <8 x i64>, <8 x i64> * %arg
  store <8 x i64> %tmp, <8 x i64> * %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg) #1 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg) {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg) {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}
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bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

; NOT_TUNIT_NPM-LABEL: define
{ /*[@]+ */}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{ /*[@]+ */}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {

; IS__TUNIT_OPM-LABEL: define { /*[@]+ */}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* noalias writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 32
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)

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; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* %arg1) #0 { 
  
  ; NOT_TUNIT_NPM-LABEL: define
  ({{[^@]+}}) @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
  ; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
  ; NOT_TUNIT_NPM-NEXT: bb:
    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
    store <8 x i64> [[TMP0:%.*]], <8 x i64>* [[ARG1_PRIV]], align 64
    [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
    store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
    ret void
  }

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
    ; IS__TUNIT_OPM-LABEL: define ([[@@]+]) @avx512_legal512_prefer256_call_avx512_legal512_prefer512
    ; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
    ; IS__TUNIT_OPM-NEXT: bb:
    ; IS__TUNIT_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapturenonnull writeonlyalign 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture noreferrer writeonlyalign 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture noreferrer readonlyalign 64 dereferenceable(64) [[TMP]])
    ; IS__TUNIT_OPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
    ; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    ; IS__TUNIT_OPM-NEXT: ret void
    ;
    ; IS__TUNIT_NPM-LABEL: define ([[@@]+]) @avx512_legal512_prefer256_call_avx512_legal512_prefer512
    ; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
    ; IS__TUNIT_NPM-NEXT: bb:
    ; IS__TUNIT_NPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_NPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_NPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapturenonnull writeonlyalign 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__TUNIT_NPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture noreferrer writeonlyalign 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture noreferrer readonlyalign 64 dereferenceable(64) [[TMP0]])
    ; IS__TUNIT_NPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
    ; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    ; IS__TUNIT_NPM-NEXT: ret void
    ;
    ; IS__CGSCC_OPM-LABEL: define ([[@@]+]) @avx512_legal512_prefer256_call_avx512_legal512_prefer512
    ; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture writeonlyalign 2 dereferenceable(64) [[ARG:%.*]])
    ; IS__CGSCC_OPM-NEXT: bb:
    ; IS__CGSCC_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
    ; IS__CGSCC_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
    ; IS__CGSCC_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapturenull writeonlyalign 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture noreferrer writeonlyalign 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture noreferrer readonlyalign 64 dereferenceable(64) [[TMP]])
: IS__CGSCC_OPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
: IS__CGSCC_OPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
: IS__CGSCC_OPM-NEXT:    ret void
;
: IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512

; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
: IS__CGSCC_NPM-NEXT:    bb:
: IS__CGSCC_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
: IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
: IS__CGSCC_NPM-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
: IS__CGSCC_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
: IS__CGSCC_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
: IS__CGSCC_NPM-NEXT:    ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
: IS__CGSCC_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
: IS__CGSCC_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
: IS__CGSCC_NPM-NEXT:    ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
: IS__CGSCC_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
: IS__CGSCC_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
: IS__CGSCC_NPM-NEXT:    ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1) #1 {
;
: NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
: NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
: NOT_TUNIT_NPM-NEXT:    bb:
: NOT_TUNIT_NPM-NEXT:    [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG]], align 64
: NOT_TUNIT_NPM-NEXT:    store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
: NOT_TUNIT_NPM-NEXT:    ret void
;
: IS__TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
: IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64))
: IS__TUNIT_NPM-NEXT:    ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}
define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
  ; IS__UNIT_OPM-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
  ; IS__UNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
  ; IS__UNIT_OPM-NEXT: bb:
  ; IS__UNIT_OPM-NEXT: [TMP:%.*] = alloca <8 x i64>, align 32
  ; IS__UNIT_OPM-NEXT: [TMP2:%.*] = alloca <8 x i64>, align 32
  ; IS__UNIT_OPM-NEXT: [TMP3:%.*] = bitcast <8 x i64>* [TMP] to i8*
  ; IS__UNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
  ; IS__UNIT_OPM-NEXT: call fastcc void
  @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
  ; IS__UNIT_OPM-NEXT: [TMP4:%.*] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
  ; IS__UNIT_OPM-NEXT: store <8 x i64> %tmp, <8 x i64>* %arg
  ; IS__UNIT_OPM-NEXT: ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
  ; IS__UNIT_OPM-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
  ; IS__UNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
  ; IS__UNIT_OPM-NEXT: bb:
  ; IS__UNIT_OPM-NEXT: [TMP:%.*] = alloca <8 x i64>, align 32
  ; IS__UNIT_OPM-NEXT: [TMP2:%.*] = alloca <8 x i64>, align 32
  ; IS__UNIT_OPM-NEXT: [TMP3:%.*] = bitcast <8 x i64>* [TMP] to i8*
  ; IS__UNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
  ; IS__UNIT_OPM-NEXT: call fastcc void
  @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
  ; IS__UNIT_OPM-NEXT: [TMP4:%.*] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
  ; IS__UNIT_OPM-NEXT: store <8 x i64> %tmp, <8 x i64>* %arg
  ; IS__UNIT_OPM-NEXT: ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
  ; IS__UNIT_OPM-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
  ; IS__UNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
  ; IS__UNIT_OPM-NEXT: bb:
  ; IS__UNIT_OPM-NEXT: [TMP:%.*] = alloca <8 x i64>, align 32
  ; IS__UNIT_OPM-NEXT: [TMP2:%.*] = alloca <8 x i64>, align 32
  ; IS__UNIT_OPM-NEXT: [TMP3:%.*] = bitcast <8 x i64>* [TMP] to i8*
  ; IS__UNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
  ; IS__UNIT_OPM-NEXT: call fastcc void
  @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
  ; IS__UNIT_OPM-NEXT: [TMP4:%.*] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
  ; IS__UNIT_OPM-NEXT: store <8 x i64> %tmp, <8 x i64>* %arg
  ; IS__UNIT_OPM-NEXT: ret void
}
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP3]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx512_legal256_prefer256 CALL_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonnull readable align 64 dereferenceable(64) [[TMP3]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal256_prefer256 CALL_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP3]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx512_legal256_prefer256 CALL_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readable align 64 dereferenceable(64) [[TMP3]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256 CALL_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp3)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

This should not promote

define internal fastcc void @callee_avx512_legal512_prefer256 CALL_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {
;

; NOT_TUNIT_NPM-LABEL: define
{([^@]+)} @callee_avx512_legal512_prefer256 CALL_avx512_legal512_prefer256
define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 { 
  
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 { 
  
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void

; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT: [[TMP4:.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* noalias nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* noalias nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;b:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
;
; NOT_TUNIT_NPM-LABEL: define {
{{[^@]+}} @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {
{{[^@]+}} @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [TMP0:%.*])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [TMP0], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [TMP], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
;
; IS__TUNIT_OPM-LABEL: define {
{{[^@]+}} @avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64> [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define {{[^@]+}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: ( <8 x i64> * nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64> [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256( <8 x i64> * noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_OPM-SAME: ( <8 x i64> * nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64> [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256( <8 x i64> * noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_NPM-SAME: ( <8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64> [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256( <8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64

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; is__cgcnc npm-next:  store <8 x i64> [[tmp4]], <8 x i64>* [[arg]], align 2
; is__cgcnc npm-next:  ret void

bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void

; this should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
  bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}
define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}
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```assembly
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP%.%]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2%.%]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3%.%]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4%.%]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define {{[^@]+}} @avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG%.%]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP%.%]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2%.%]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3%.%]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0%.%]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4%.%]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}} @avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG%.%]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP%.%]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2%.%]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3%.%]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: [[TMP0%.%]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__CGSCC_OPM-NEXT: [[TMP4%.%]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}} @avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG%.%]])
```

; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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RUN: llc -O3 -mtriple=powerpc-unknown-linux-gnu -mcpu=e500 -mattr=spe < %s | FileCheck %s

PowerPC SPE is a rare in-tree target that has the FP_TO_SINT node marked as Legal.

Verify that fptosi(42.1) isn't simplified when the rounding mode is unknown.
Verify that no gross errors happen.

CHECK-LABEL: @f20
COMMON: cfdctsz
define i32 @f20(double %a) strictfp {
  entry:
  %result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1,
  metadata !"fpexcept.strict")
  strictfp
  ret i32 %result
}

@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)

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define void @zext256() "min-legal-vector-width"="256" {  
  ; VEC256-LABEL: 'zext256'  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void  
  ;  
  ; AVX512VL512-LABEL: 'zext256'  
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>  
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>  
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>  
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>  
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>  
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void  
  ;  
  ; SKX512-LABEL: 'zext256'  
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>  
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>  
  ;
define void @zext512() "min-legal-vector-width"="512" {
    ; AVX-LABEL: 'zext512'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ret void
}

; AVX512VL256-LABEL: 'zext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL512-LABEL: 'zext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL512-LABEL: 'zext512'
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; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x
define void @sext512() "min-legal-vector-width"="512" { 
    ; AVX512VL512-LABEL: 'sext512'
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; SKX512-LABEL: 'sext256'
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; AVX512VL512-LABEL: 'sext256'
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; SKX512-LABEL: 'sext256'
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    %A = sext <8 x i8> undef to <8 x i64>
    %B = sext <8 x i16> undef to <8 x i64>
    %C = sext <8 x i32> undef to <8 x i64>
    %D = sext <16 x i8> undef to <16 x i32>
    %E = sext <16 x i16> undef to <16 x i32>
    %F = sext <32 x i8> undef to <32 x i16>
    ret void
}

define void @sext512() "min-legal-vector-width"="512" { 
    ; AVX512VL512-LABEL: 'sext512'
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %G = sext <64 x i8> undef to <64 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %H = sext <64 x i16> undef to <64 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %I = sext <64 x i32> undef to <64 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %J = sext <128 x i8> undef to <128 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %K = sext <128 x i16> undef to <128 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %L = sext <128 x i32> undef to <128 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %M = sext <256 x i8> undef to <256 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %N = sext <256 x i16> undef to <256 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %O = sext <256 x i32> undef to <256 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %P = sext <512 x i8> undef to <512 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %Q = sext <512 x i16> undef to <512 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %R = sext <512 x i32> undef to <512 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %S = sext <1024 x i8> undef to <1024 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %T = sext <1024 x i16> undef to <1024 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %U = sext <1024 x i32> undef to <1024 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V = sext <2048 x i8> undef to <2048 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %W = sext <2048 x i16> undef to <2048 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %X = sext <2048 x i32> undef to <2048 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %Y = sext <4096 x i8> unde
; SKX512-LABEL: 'sext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

define void @trunc256() "min-legal-vector-width"="256" {
    ; AVX-LABEL: 'trunc256'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; AVX512VL256-LABEL: 'trunc256'
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
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; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'trunc256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'trunc256'
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'trunc256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = trunc <8 x i64> undef to <8 x i32>
%B = trunc <8 x i64> undef to <8 x i16>
%C = trunc <8 x i64> undef to <8 x i8>
%D = trunc <16 x i32> undef to <16 x i16>
%E = trunc <16 x i32> undef to <16 x i8>
%F = trunc <32 x i16> undef to <32 x i8>
ret void
}
define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
    AVX-LABEL: 'zext256_vXi1'
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
    AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
    AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
    AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
    AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
    AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
    AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
    AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
    AVX512VL256-LABEL: 'zext256_vXi1'
    AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
to <8 x i32>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>

; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; SKX256-LABEL: 'zext256_vXi1'

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>

; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>

; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>

; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
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to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 42 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'sext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i64 = sext <16 x i1> undef to <16 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i64 = sext <32 x i1> undef to <32 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i64 = sext <64 x i1> undef to <64 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i64 = sext <16 x i1> undef to <16 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i64 = sext <32 x i1> undef to <32 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i64 = sext <64 x i1> undef to <64 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i64 = sext <16 x i1> undef to <16 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i64 = sext <32 x i1> undef to <32 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i64 = sext <64 x i1> undef to <64 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; SKX256-LABEL: 'sext256_vXii'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 = sext <8 x i1> undef to <8 x i64>

%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>

%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>

%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; AVX512VL256-LABEL: 'trunc_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
; SKX512-LABEL: 'trunc_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2
x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x il>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
%V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>
%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>
%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to <16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>
%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>
ret i32 undef
}
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1.95 sqlite 3.30.1

1.96 tzdata 2021a-0ubuntu0.20.04
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The End
NAME
Net::SMTP::SSL - SSL support for Net::SMTP

SYNOPSIS
use Net::SMTP::SSL;

my $smtps = Net::SMTP::SSL->new("example.com", Port => 465);

DESCRIPTION
Implements the same API as Net::SMTP, but uses IO::Socket::SSL for its
network operations. Due to the nature of "Net::SMTP"'s "new" method, it
is not overridden to make use of a default port for the SMTPS service.
Perhaps future versions will be smart like that. Port 465 is usually
what you want, and it's not a pain to specify that.

For interface documentation, please see Net::SMTP.

SEE ALSO
Net::SMTP, IO::Socket::SSL, perl.

AUTHOR
Casey West, <casey@geeknest.com>.
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=head1 NAME

Net::SMTP::SSL - SSL support for Net::SMTP

=head1 SYNOPSIS

use Net::SMTP::SSL;

my $smtps = Net::SMTP::SSL->new("example.com", Port => 465);

=head1 DEPRECATED

B<Hey!  Read this!>

Since Net::SMTP v1.28 (2014-10-08), Net::SMTP itself has support for SMTP over
SSL, and also for STARTTLS. Use Net::SMTP, not Net::SMTP::SSL.

=head1 DESCRIPTION

Implements the same API as L<Net::SMTP|Net::SMTP>, but uses
L<IO::Socket::SSL|IO::Socket::SSL> for its network operations. Due to
the nature of C<Net::SMTP>'s C<new> method, it is not overridden to make
use of a default port for the SMTPS service. Perhaps future versions
will be smart like that. Port C<465> is usually what you want, and it's
not a pain to specify that.

For interface documentation, please see L<Net::SMTP|Net::SMTP>.

=head1 SEE ALSO

L<Net::SMTP>,
L<IO::Socket::SSL>,
L<perl>.

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=cut

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We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free
library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.
Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)
These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:
a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.
7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

   a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

   b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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1.103 gtk 2.24.32-4ubuntu4

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

The licenses for most software are designed to take away your freedom to share and change it. By contrast, the GNU General Public
Licenses are intended to guarantee your freedom to share and change free software—to make sure the software is free for all its users.

This license, the Library General Public License, applies to some specially designated Free Software Foundation software, and to any other libraries whose authors decide to use it. You can use it for your libraries, too.

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For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This
license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data
prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no
charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a
table of data to be supplied by an application program that uses
the facility, other than as an argument passed when the facility
is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
root function must still compute square roots.)

These requirements apply to the modified work as a whole. If
identifiable sections of that work are not derived from the Library,
and can be reasonably considered independent and separate works in
themselves, then this License, and its terms, do not apply to those
sections when you distribute them as separate works. But when you
distribute the same sections as part of a whole which is a work based
on the Library, the distribution of the whole must be on the terms of
this License, whose permissions for other licensees extend to the
entire whole, and thus to each and every part regardless of who wrote
it.

Thus, it is not the intent of this section to claim rights or contest
your rights to work written entirely by you; rather, the intent is to
exercise the right to control the distribution of derivative or
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In addition, mere aggregation of another work not based on the Library
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Once this change is made in a given copy, it is irreversible for
that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies
the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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#
# Original Author: Harlan Stenn <harlan@mumps.pfcs.com>
#
# $Log: ernolist.a,v $
# Revision 3.0 1993/08/18 12:04:35 ram
# Baseline for dist 3.0 netwide release.
#
# This is a simple-minded awk script to generate an initialization for
# sys_ernolist on systems that don't have it.
# This file now depends only on sys/errno.h error numbers under maxerr being
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1.108 libfontenc 1.1.4-0ubuntu1

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
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distribute the object code for the work under the terms of Section 6.
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whether or not they are linked directly with the Library itself.

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with the Library, with the complete machine-readable "work that
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that the user who changes the contents of definitions files in the
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to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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1.122.1 Available under license :

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=head1 NAME

HTML::Parser - HTML parser class

=head1 SYNOPSIS

use HTML::Parser ();

# Create parser object
$p = HTML::Parser->new( api_version => 3,
  start_h => \&start, "tagname, attr"],
  end_h => \&end,  "tagname"],
  marked_sections => 1,
);

# Parse document text chunk by chunk
$p->parse($chunk1);
$p->parse($chunk2);
#...
$p->eof;                 # signal end of document

# Parse directly from file
$p->parse_file("foo.html");
# or
Objects of the `HTML::Parser` class will recognize markup and separate it from plain text (alias data content) in HTML documents. As different kinds of markup and text are recognized, the corresponding event handlers are invoked.

`HTML::Parser` is not a generic SGML parser. We have tried to make it able to deal with the HTML that is actually "out there", and it normally parses as closely as possible to the way the popular web browsers do it instead of strictly following one of the many HTML specifications from W3C. Where there is disagreement, there is often an option that you can enable to get the official behaviour.

The document to be parsed may be supplied in arbitrary chunks. This makes on-the-fly parsing as documents are received from the network possible.

If event driven parsing does not feel right for your application, you might want to use `HTML::PullParser`. This is an `HTML::Parser` subclass that allows a more conventional program structure.

The following method is used to construct a new `HTML::Parser` object:

```perl
=over
=item $p = HTML::Parser->new( %options_and_handlers )
=over
```

This class method creates a new `HTML::Parser` object and returns it. Key/value argument pairs may be provided to assign event handlers or initialize parser options. The handlers and parser options can also be set or modified later by the method calls described below.

If a top level key is in the form "<event>_h" (e.g., "text_h") then it assigns a handler to that event, otherwise it initializes a parser option. The event handler specification value must be an array reference. Multiple handlers may also be assigned with the 'handlers => [%handlers]' option. See examples below.

If `new()` is called without any arguments, it will create a parser that uses callback methods compatible with version 2 of `HTML::Parser`. See the section on "version 2 compatibility" below for details.
The special constructor option 'api_version => 2' can be used to initialize version 2 callbacks while still setting other options and handlers. The 'api_version => 3' option can be used if you don't want to set any options and don't want to fall back to v2 compatible mode.

Examples:

```perl
$p = HTML::Parser->new(api_version => 3,
    text_h => [ sub {...}, "dtext" ]);  
```

This creates a new parser object with a text event handler subroutine that receives the original text with general entities decoded.

```perl
$p = HTML::Parser->new(api_version => 3,
    start_h => [ 'my_start', "self,tokens" ]);  
```

This creates a new parser object with a start event handler method that receives the $p and the tokens array.

```perl
$p = HTML::Parser->new(api_version => 3,
    handlers => { text => [@array, "event,text"],
                  comment => [@array, "event,text"],
                       });  
```

This creates a new parser object that stores the event type and the original text in @array for text and comment events.

=back

The following methods feed the HTML document to the C<HTML::Parser> object:

=over

=item $p->parse( $string )

Parse $string as the next chunk of the HTML document. Handlers invoked should not attempt to modify the $string in-place until $p->parse returns.

If an invoked event handler aborts parsing by calling $p->eof, then $p->parse() will return a FALSE value. Otherwise the return value is a reference to the parser object ($p).

=item $p->parse( $code_ref )

If a code reference is passed as the argument to be parsed, then the
chunks to be parsed are obtained by invoking this function repeatedly. Parsing continues until the function returns an empty (or undefined) result. When this happens $p->eof is automatically signaled.

Parsing will also abort if one of the event handlers calls $p->eof.

The effect of this is the same as:

```perl
while (1) {
    my $chunk = &$code_ref();
    if (!defined($chunk) || !length($chunk)) {
        $p->eof;
        return $p;
    }
    $p->parse($chunk) || return undef;
}
```

But it is more efficient as this loop runs internally in XS code.

=item $p->parse_file( $file )

Parse text directly from a file. The $file argument can be a filename, an open file handle, or a reference to an open file handle.

If $file contains a filename and the file can't be opened, then the method returns an undefined value and $! tells why it failed. Otherwise the return value is a reference to the parser object.

If a file handle is passed as the $file argument, then the file will normally be read until EOF, but not closed.

If an invoked event handler aborts parsing by calling $p->eof, then $p->parse_file() may not have read the entire file.

On systems with multi-byte line terminators, the values passed for the offset and length argspecs may be too low if parse_file() is called on a file handle that is not in binary mode.

If a filename is passed in, then parse_file() will open the file in binary mode.

=item $p->eof

Signals the end of the HTML document. Calling the $p->eof method outside a handler callback will flush any remaining buffered text (which triggers the C<text> event if there is any remaining text).
Calling $p->eof inside a handler will terminate parsing at that point and cause $p->parse to return a FALSE value. This also terminates parsing by $p->parse_file().

After $p->eof has been called, the parse() and parse_file() methods can be invoked to feed new documents with the parser object.

The return value from eof() is a reference to the parser object.

Most parser options are controlled by boolean attributes. Each boolean attribute is enabled by calling the corresponding method with a TRUE argument and disabled with a FALSE argument. The attribute value is left unchanged if no argument is given. The return value from each method is the old attribute value.

Methods that can be used to get and/or set parser options are:

=item $p->attr_encoded
=item $p->attr_encoded( $bool )

By default, the C<attr> and C:@attr> argspecs will have general entities for attribute values decoded. Enabling this attribute leaves entities alone.

=item $p->backquote
=item $p->backquote( $bool )

By default, only ' and " are recognized as quote characters around attribute values. MSIE also recognizes backquotes for some reason. Enabling this attribute provides compatibility with this behaviour.

=item $p->boolean_attribute_value( $val )

This method sets the value reported for boolean attributes inside HTML start tags. By default, the name of the attribute is also used as its value. This affects the values reported for C<tokens> and C<attr> argspecs.

=item $p->case_sensitive
=item $p->case_sensitive( $bool )
By default, tag names and attribute names are down-cased. Enabling this attribute leaves them as found in the HTML source document.

=item $p->closing_plaintext

=item $p->closing_plaintext( $bool )

By default, "plaintext" element can never be closed. Everything up to the end of the document is parsed in CDATA mode. This historical behaviour is what at least MSIE does. Enabling this attribute makes closing "</plaintext>" tag effective and the parsing process will resume after seeing this tag. This emulates early gecko-based browsers.

=item $p->empty_element_tags

=item $p->empty_element_tags( $bool )

By default, empty element tags are not recognized as such and the "/" before "">" is just treated like a normal name character (unless C<strict_names> is enabled). Enabling this attribute make C<HTML::Parser> recognize these tags.

Empty element tags look like start tags, but end with the character sequence "/>" instead of "">". When recognized by C<HTML::Parser> they cause an artificial end event in addition to the start event. The C<text> for the artificial end event will be empty and the C<tokenpos> array will be undefined even though the token array will have one element containing the tag name.

=item $p->marked_sections

=item $p->marked_sections( $bool )

By default, section markings like <![CDATA[...]]> are treated like ordinary text. When this attribute is enabled section markings are honoured.

There are currently no events associated with the marked section markup, but the text can be returned as C<skipped_text>.

=item $p->strict_comment

=item $p->strict_comment( $bool )

By default, comments are terminated by the first occurrence of "-->". This is the behaviour of most popular browsers (like Mozilla, Opera and MSIE), but it is not correct according to the official HTML
standard. Officially, you need an even number of "--" tokens before
the closing ">" is recognized and there may not be anything but
whitespace between an even and an odd "--".

The official behaviour is enabled by enabling this attribute.

Enabling of 'strict_comment' also disables recognizing these forms as
comments:

</comment>
<!comment>

=item $p->strict_end

=item $p->strict_end( $bool )

By default, attributes and other junk are allowed to be present on end tags in a
manner that emulates MSIE's behaviour.

The official behaviour is enabled with this attribute. If enabled,
only whitespace is allowed between the tagname and the final ">".

=item $p->strict_names

=item $p->strict_names( $bool )

By default, almost anything is allowed in tag and attribute names.
This is the behaviour of most popular browsers and allows us to parse
some broken tags with invalid attribute values like:

<IMG SRC=newprevlstGr.gif ALT=[PREV LIST] BORDER=0>

By default, "[LIST]" is parsed as a boolean attribute, not as
part of the ALT value as was clearly intended. This is also what
Mozilla sees.

The official behaviour is enabled by enabling this attribute. If
enabled, it will cause the tag above to be reported as text
since "[LIST]" is not a legal attribute name.

=item $p->unbroken_text

=item $p->unbroken_text( $bool )

By default, blocks of text are given to the text handler as soon as
possible (but the parser takes care always to break text at a
boundary between whitespace and non-whitespace so single words and
entities can always be decoded safely. This might create breaks that make it hard to do transformations on the text. When this attribute is enabled, blocks of text are always reported in one piece. This will delay the text event until the following (non-text) event has been recognized by the parser.

Note that the C<offset> argspec will give you the offset of the first segment of text and C<length> is the combined length of the segments. Since there might be ignored tags in between, these numbers can't be used to directly index in the original document file.

=item $p->utf8_mode

=item $p->utf8_mode( $bool )

Enable this option when parsing raw undecoded UTF-8. This tells the parser that the entities expanded for strings reported by C<attr>, C:@attr> and C<dtext> should be expanded as decoded UTF-8 so they end up compatible with the surrounding text.

If C<utf8_mode> is enabled then it is an error to pass strings containing characters with code above 255 to the parse() method, and the parse() method will croak if you try.

Example: The Unicode character "\x{2665}" is "\xE2\x99\xA5" when UTF-8 encoded. The character can also be represented by the entity "&hearts;" or "&#x2665". If we feed the parser:

$p->parse("\xE2\x99\xA5&hearts;";);

then C<dtext> will be reported as "\xE2\x99\xA5\x{2665}" without C<utf8_mode> enabled, but as "\xE2\x99\xA5\xE2\x99\xA5" when enabled. The later string is what you want.

This option is only available with perl-5.8 or better.

=item $p->xml_mode

=item $p->xml_mode( $bool )

Enabling this attribute changes the parser to allow some XML constructs. This enables the behaviour controlled by individually by the C<case_sensitive>, C<empty_element_tags>, C<strict_names> and C<xml_pic> attributes and also suppresses special treatment of elements that are parsed as CDATA for HTML.

=item $p->xml_pic
By default, I-processing instructions are terminated by "">

As markup and text is recognized, handlers are invoked. The following method is used to set up handlers for different events:

This method assigns a subroutine, method, or array to handle an event.

Event is one of text, start, end, declaration, comment, process, start_document, end_document or default.

The \subroutine is a reference to a subroutine which is called to handle the event.

The \method_name is the name of a method of $p which is called to handle the event.

The @accum is an array that will hold the event information as sub-arrays.

If the second argument is "", the event is ignored.
If it is undef, the default handler is invoked for the event.

The $argspec is a string that describes the information to be reported for the event. Any requested information that does not apply to a specific event is passed as undef. If $argspec is omitted, then it is left unchanged.

The return value from $p->handler is the old callback routine or a
reference to the accumulator array.

Any return values from handler callback routines/methods are always ignored. A handler callback can request parsing to be aborted by invoking the $p->eof method. A handler callback is not allowed to invoke the $p->parse() or $p->parse_file() method. An exception will be raised if it tries.

Examples:

$p->handler(start => "start", 'self, attr, attrseq, text');

This causes the "start" method of object $p to be called for 'start' events. The callback signature is $p->start(%attr, @attr_seq, $text).

$p->handler(start => &start, 'attr, attrseq, text');

This causes subroutine start() to be called for 'start' events. The callback signature is start(%attr, @attr_seq, $text).

$p->handler(start => @accum, "S", attr, attrseq, text');

This causes 'start' event information to be saved in @accum. The array elements will be ['S', %attr, @attr_seq, $text].

$p->handler(start => "");

This causes 'start' events to be ignored. It also suppresses invocations of any default handler for start events. It is in most cases equivalent to $p->handler(start => sub {}), but is more efficient. It is different from the empty-sub-handler in that C<skipped_text> is not reset by it.

$p->handler(start => undef);

This causes no handler to be associated with start events. If there is a default handler it will be invoked.

=back

Filters based on tags can be set up to limit the number of events reported. The main bottleneck during parsing is often the huge number of callbacks made from the parser. Applying filters can improve performance significantly.

The following methods control filters:

=over
Both the C\texttt{start} event and the C\texttt{end} event as well as any events that would be reported in between are suppressed. The ignored elements can contain nested occurrences of itself. Example:

\begin{verbatim}
$p->ignore_elements(qw(script style));
\end{verbatim}

The C\texttt{script} and C\texttt{style} tags will always nest properly since their content is parsed in CDATA mode. For most other tags C\texttt{ignore_elements} must be used with caution since HTML is often not well formed.

\begin{verbatim}
$p->ignore_tags(qw(style));
\end{verbatim}

Any C\texttt{start} and C\texttt{end} events involving any of the tags given are suppressed. To reset the filter (i.e. don't suppress any C\texttt{start} and C\texttt{end} events), call C\texttt{ignore_tags} without an argument.

\begin{verbatim}
$p->report_tags(qw(script style));
\end{verbatim}

Any C\texttt{start} and C\texttt{end} events involving any of the tags I\texttt{not} given are suppressed. To reset the filter (i.e. report all C\texttt{start} and C\texttt{end} events), call C\texttt{report_tags} without an argument.

Internally, the system has two filter lists, one for C\texttt{report_tags} and one for C\texttt{ignore_tags}, and both filters are applied. This effectively gives C\texttt{ignore_tags} precedence over C\texttt{report_tags}.

Examples:

\begin{verbatim}
$p->ignore_tags(qw(style));
$p->report_tags(qw(script style));
\end{verbatim}

results in only C\texttt{script} events being reported.

Argspec is a string containing a comma-separated list that describes the information reported by the event. The following argspec identifier names can be used:

\begin{verbatim}
=over
=attr
\end{verbatim}
Attr causes a reference to a hash of attribute name/value pairs to be passed.

Boolean attributes' values are either the value set by $p->boolean_attribute_value, or the attribute name if no value has been set by $p->boolean_attribute_value.

This passes undef except for C<start> events.

Unless C<xml_mode> or C<case_sensitive> is enabled, the attribute names are forced to lower case.

General entities are decoded in the attribute values and one layer of matching quotes enclosing the attribute values is removed.

The Unicode character set is assumed for entity decoding.

=item C<@attr>

Basically the same as C<attr>, but keys and values are passed as individual arguments and the original sequence of the attributes is kept. The parameters passed will be the same as the @attr calculated here:

```perl
    @attr = map { $_ => $attr->{$_} } @$attrseq;
```

assuming $attr and $attrseq here are the hash and array passed as the result of C<attr> and C<attrseq> argspecs.

This passes no values for events besides C<start>.

=item C<attrseq>

Attrseq causes a reference to an array of attribute names to be passed. This can be useful if you want to walk the C<attr> hash in the original sequence.

This passes undef except for C<start> events.

Unless C<xml_mode> or C<case_sensitive> is enabled, the attribute names are forced to lower case.

=item C<column>

Column causes the column number of the start of the event to be passed. The first column on a line is 0.
Dtext causes the decoded text to be passed. General entities are automatically decoded unless the event was inside a CDATA section or was between literal start and end tags (C<script>, C<style>, C<xmp>, C<iframe>, C<title>, C<textarea> and C<plaintext>).

The Unicode character set is assumed for entity decoding. With Perl version 5.6 or earlier only the Latin-1 range is supported, and entities for characters outside the range 0..255 are left unchanged.

This passes undef except for C<text> events.

Event causes the event name to be passed.

The event name is one of C<text>, C<start>, C<end>, C<declaration>, C<comment>, C<process>, C<start_document> or C<end_document>.

Is_cdata causes a TRUE value to be passed if the event is inside a CDATA section or between literal start and end tags (C<script>, C<style>, C<xmp>, C<iframe>, C<title>, C<textarea> and C<plaintext>).

if the flag is FALSE for a text event, then you should normally either use C<dtext> or decode the entities yourself before the text is processed further.

Length causes the number of bytes of the source text of the event to be passed.

Line causes the line number of the start of the event to be passed. The first line in the document is 1. Line counting doesn't start until at least one handler requests this value to be reported.

Offset causes the byte position in the HTML document of the start of the event to be passed. The first byte in the document has offset 0.
Offset_end causes the byte position in the HTML document of the end of the event to be passed. This is the same as C<offset> + C<length>.

=item C<self>

Self causes the current object to be passed to the handler. If the handler is a method, this must be the first element in the argspec.

An alternative to passing self as an argspec is to register closures that capture $self by themselves as handlers. Unfortunately this creates circular references which prevent the HTML::Parser object from being garbage collected. Using the C<self> argspec avoids this problem.

=item C<skipped_text>

Skipped_text returns the concatenated text of all the events that have been skipped since the last time an event was reported. Events might be skipped because no handler is registered for them or because some filter applies. Skipped text also includes marked section markup, since there are no events that can catch it.

If an C"">-handler is registered for an event, then the text for this event is not included in C<skipped_text>. Skipped text both before and after the C"">-event is included in the next reported C<skipped_text>.

=item C<tag>

Same as C<tagname>, but prefixed with "/'" if it belongs to an C<end> event and "!'" for a declaration. The C<tag> does not have any prefix for C<start> events, and is in this case identical to C<tagname>.

=item C<tagname>

This is the element name (or I<generic identifier> in SGML jargon) for start and end tags. Since HTML is case insensitive, this name is forced to lower case to ease string matching.

Since XML is case sensitive, the tagname case is not changed when C<xml_mode> is enabled. The same happens if the C<case_sensitive> attribute is set.

The declaration type of declaration elements is also passed as a tagname, even if that is a bit strange.

In fact, in the current implementation tagname is identical to C<token0> except that the name may be forced to lower case.
=item C<token0>

Token0 causes the original text of the first token string to be passed. This should always be the same as $tokens->[0].

For C<declaration> events, this is the declaration type.

For C<start> and C<end> events, this is the tag name.

For C<process> and non-strict C<comment> events, this is everything inside the tag.

This passes undef if there are no tokens in the event.

=item C<tokenpos>

Tokenpos causes a reference to an array of token positions to be passed. For each string that appears in C<tokens>, this array contains two numbers. The first number is the offset of the start of the token in the original C<text> and the second number is the length of the token.

Boolean attributes in a C<start> event will have (0,0) for the attribute value offset and length.

This passes undef if there are no tokens in the event (e.g., C<text>) and for artificial C<end> events triggered by empty element tags.

If you are using these offsets and lengths to modify C<text>, you should either work from right to left, or be very careful to calculate the changes to the offsets.

=item C<tokens>

Tokens causes a reference to an array of token strings to be passed. The strings are exactly as they were found in the original text, no decoding or case changes are applied.

For C<declaration> events, the array contains each word, comment, and delimited string starting with the declaration type.

For C<comment> events, this contains each sub-comment. If $p->strict_comments is disabled, there will be only one sub-comment.

For C<start> events, this contains the original tag name followed by the attribute name/value pairs. The values of boolean attributes will be either the value set by $p->boolean_attribute_value, or the attribute name if no value has been set by
$p->boolean_attribute_value.

For C<end> events, this contains the original tag name (always one token).

For C<process> events, this contains the process instructions (always one token).

This passes C<undef> for C<text> events.

=item C<text>

Text causes the source text (including markup element delimiters) to be passed.

=item C<undef>

Pass an undefined value. Useful as padding where the same handler routine is registered for multiple events.

=item C<'...'>

A literal string of 0 to 255 characters enclosed in single (') or double (") quotes is passed as entered.

=back

The whole argspec string can be wrapped up in C<@{...}> to signal that the resulting event array should be flattened. This only makes a difference if an array reference is used as the handler target. Consider this example:

    $p->handler(text => [], 'text');
    $p->handler(text => [], '@{text}');

With two text events; C<"foo">, C<"bar">; then the first example will end up with [["foo"], ["bar"]]} and the second with ["foo", "bar"] in the handler target array.

=head2 Events

Handlers for the following events can be registered:

=over

=item C<comment>

This event is triggered when a markup comment is recognized.
Example:

<!-- This is a comment -- -- So is this -->

=item C<declaration>

This event is triggered when a I<markup declaration> is recognized.

For typical HTML documents, the only declaration you are likely to find is <!DOCTYPE ...>.

Example:

<!DOCTYPE HTML PUBLIC "-//W3C//DTD HTML 4.01//EN"
 "http://www.w3.org/TR/html4/strict.dtd">

DTDs inside <!DOCTYPE ...> will confuse HTML::Parser.

=item C<default>

This event is triggered for events that do not have a specific handler. You can set up a handler for this event to catch stuff you did not want to catch explicitly.

=item C<end>

This event is triggered when an end tag is recognized.

Example:

</A>

=item C<end_document>

This event is triggered when $p->eof is called and after any remaining text is flushed. There is no document text associated with this event.

=item C<process>

This event is triggered when a processing instructions markup is recognized.

The format and content of processing instructions are system and application dependent.

Examples:
This event is triggered when a start tag is recognized.

Example:

```html
<A HREF="http://www.perl.com/">
```

This event is triggered before any other events for a new document. A handler for it can be used to initialize stuff. There is no document text associated with this event.

This event is triggered when plain text (characters) is recognized. The text may contain multiple lines. A sequence of text may be broken between several text events unless $p->unbroken_text is enabled.

The parser will make sure that it does not break a word or a sequence of whitespace between two text events.

C<HTML::Parser> can parse Unicode strings when running under perl-5.8 or better. If Unicode is passed to $p->parse() then chunks of Unicode will be reported to the handlers. The offset and length argspecs will also report their position in terms of characters.

It is safe to parse raw undecoded UTF-8 if you either avoid decoding entities and make sure to not use I<argspecs> that do, or enable the C<utf8_mode> for the parser. Parsing of undecoded UTF-8 might be useful when parsing from a file where you need the reported offsets and lengths to match the byte offsets in the file.

If a filename is passed to $p->parse_file() then the file will be read in binary mode. This will be fine if the file contains only ASCII or Latin-1 characters. If the file contains UTF-8 encoded text then care must be taken when decoding entities as described in the previous paragraph, but better is to open the file with the UTF-8 layer so that it is decoded properly:
open(my $fh, "<:utf8", "index.html") || die "...: $!";
$p->parse_file($fh);

If the file contains text encoded in a charset besides ASCII, Latin-1 or UTF-8 then decoding will always be needed.

=head1 VERSION 2 COMPATIBILITY

When an HTML::Parser object is constructed with no arguments, a set of handlers is automatically provided that is compatible with the old HTML::Parser version 2 callback methods.

This is equivalent to the following method calls:

$p->handler(start => "start", "self, tagname, attr, attrseq, text");
$p->handler(end => "end", "self, tagname, text");
$p->handler(text => "text", "self, text, is_cdata");
$p->handler(process => "process", "self, token0, text");
$p->handler(comment =>
sub { my($self, $tokens) = @_;
  for (@$tokens) {$self->comment($_);},
  "self, tokens");
$p->handler(declaration =>
sub { my $self = shift;
  $self->declaration(substr($_[0], 2, -1));},
  "self, text");

Setting up these handlers can also be requested with the "api_version => 2" constructor option.

=head1 SUBCLASSING

The HTML::Parser class is subclassable. Parser objects are plain hashes and HTML::Parser reserves only hash keys that start with "_hparser". The parser state can be set up by invoking the init() method, which takes the same arguments as new().

=head1 EXAMPLES

The first simple example shows how you might strip out comments from an HTML document. We achieve this by setting up a comment handler that does nothing and a default handler that will print out anything else:

use HTML::Parser;
HTML::Parser->new(default_h => [sub { print shift }, 'text'],
  comment_h => ['']);
An alternative implementation is:

```perl
use HTML::Parser;
HTML::Parser->new(
    end_document_h => 
        [sub { print shift }, 'skipped_text'],
    comment_h                               => [""],
    )->parse_file(shift || die) || die $!;

This will in most cases be much more efficient since only a single
callback will be made.

The next example prints out the text that is inside the <title>
element of an HTML document. Here we start by setting up a start
handler. When it sees the title start tag it enables a text handler
that prints any text found and an end handler that will terminate
parsing as soon as the title end tag is seen:

```perl
use HTML::Parser ();

sub start_handler
{
    return if shift ne "title";
    my $self = shift;
    $self->handler(text => sub { print shift }, "dtext");
    $self->handler(end => sub { $self->eof if shift eq "title"; },
    "tagname,self");
}

my $p = HTML::Parser->new(api_version => 3);
$p->handler( start => \
        \&start_handler, "tagname,self");
$p->parse_file(shift || die) || die $!;
print "\n";

More examples are found in the F<eg/> directory of the C<HTML-Parser>
distribution: the program C<hrefsub> shows how you can edit all links
found in a document; the program C<htextsub> shows how to edit the text only; the
program C<hstrip> shows how you can strip out certain tags/elements
and/or attributes; and the program C<htext> show how to obtain the
plain text, but not any script/style content.

You can browse the F<eg/> directory online from the I<[Browse]> link on
the http://search.cpan.org/~gaas/HTML-Parser/ page.
```

=head1 BUGS

The <style> and <script> sections do not end with the first "</", but
need the complete corresponding end tag. The standard behaviour is not really practical.

When the I<strict_comment> option is enabled, we still recognize comments where there is something other than whitespace between even and odd "--" markers.

Once $p->boolean_attribute_value has been set, there is no way to restore the default behaviour.

There is currently no way to get both quote characters into the same literal argspec.

Empty tags, e.g. "<>" and "</>" are not recognized. SGML allows them to repeat the previous start tag or close the previous start tag respectively.

NET tags, e.g. "code/.../" are not recognized. This is SGML shorthand for "<code>...</code>".

Unclosed start or end tags, e.g. "<tt<b>...</b</tt>" are not recognized.

=head1 DIAGNOSTICS

The following messages may be produced by HTML::Parser. The notation in this listing is the same as used in L<perldiag>:

=over

=item Not a reference to a hash

(F) The object blessed into or subclassed from HTML::Parser is not a hash as required by the HTML::Parser methods.

=item Bad signature in parser state object at %p

(F) The _hparsers_x_state element does not refer to a valid state structure. Something must have changed the internal value stored in this hash element, or the memory has been overwritten.

=item _hparsers_x_state element is not a reference

(F) The _hparsers_x_state element has been destroyed.

=item Can't find '_hparsers_x_state' element in HTML::Parser hash

(F) The _hparsers_x_state element is missing from the parser hash.
It was either deleted, or not created when the object was created.

=item API version %s not supported by HTML::Parser %s

(F) The constructor option 'api_version' with an argument greater than or equal to 4 is reserved for future extensions.

=item Bad constructor option '%s'

(F) An unknown constructor option key was passed to the new() or init() methods.

=item Parse loop not allowed

(F) A handler invoked the parse() or parse_file() method. This is not permitted.

=item marked sections not supported

(F) The $p->marked_sections() method was invoked in a HTML::Parser module that was compiled without support for marked sections.

=item Unknown boolean attribute (%d)

(F) Something is wrong with the internal logic that set up aliases for boolean attributes.

=item Only code or array references allowed as handler

(F) The second argument for $p->handler must be either a subroutine reference, then name of a subroutine or method, or a reference to an array.

=item No handler for %s events

(F) The first argument to $p->handler must be a valid event name; i.e. one of "start", "end", "text", "process", "declaration" or "comment".

=item Unrecognized identifier %s in argspec

(F) The identifier is not a known argspec name. Use one of the names mentioned in the argspec section above.

=item Literal string is longer than 255 chars in argspec

(F) The current implementation limits the length of literals in an argspec to 255 characters. Make the literal shorter.
=item Backslash reserved for literal string in argspec

(F) The backslash character "\" is not allowed in argspec literals. It is reserved to permit quoting inside a literal in a later version.

=item Unterminated literal string in argspec

(F) The terminating quote character for a literal was not found.

=item Bad argspec (%s)

(F) Only identifier names, literals, spaces and commas are allowed in argspecs.

=item Missing comma separator in argspec

(F) Identifiers in an argspec must be separated with ",,\n.

=item Parsing of undecoded UTF-8 will give garbage when decoding entities

(W) The first chunk parsed appears to contain undecoded UTF-8 and one or more argspecs that decode entities are used for the callback handlers.

The result of decoding will be a mix of encoded and decoded characters for any entities that expand to characters with code above 127. This is not a good thing.

The recommended solution is to apply Encode::decode_utf8() on the data before feeding it to the $p->parse(). For $p->parse_file() pass a file that has been opened in ":utf8" mode.

The alternative solution is to enable the C<utf8_mode> and not decode before passing strings to $p->parse(). The parser can process raw undecoded UTF-8 sanely if the C<utf8_mode> is enabled, or if the "attr", "@attr" or "dtext" argspecs are avoided.

=item Parsing string decoded with wrong endianness

(W) The first character in the document is U+FFFE. This is not a legal Unicode character but a byte swapped BOM. The result of parsing will likely be garbage.

=item Parsing of undecoded UTF-32

(W) The parser found the Unicode UTF-32 BOM signature at the start of the document. The result of parsing will likely be garbage.
Parsing of undecoded UTF-16

(W) The parser found the Unicode UTF-16 BOM signature at the start of the document. The result of parsing will likely be garbage.

SEE ALSO
L<HTML::Entities>, L<HTML::PullParser>, L<HTML::TokeParser>, L<HTML::HeadParser>, L<HTML::LinkExtor>, L<HTML::Form>
L<HTML::TreeBuilder> (part of the I<HTML-Tree> distribution)
L<http://www.w3.org/TR/html4/>

More information about marked sections and processing instructions may be found at L<http://www.is-thought.co.uk/book/sgml-8.htm>.

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OVERVIEW

The HTML-Parser distribution is a collection of modules that parse and extract information from HTML documents. The modules present in this collection are:

HTML::Parser - The parser base class. It receives arbitrary sized chunks of the HTML text, recognizes markup elements, and separates them from the plain text. As different kinds of markup and text are recognized, the corresponding event handlers are invoked.

HTML::Entities - Provides functions to encode and decode text with embedded HTML. &lt;entities&gt;.
HTML::HeadParser - A lightweight HTML::Parser subclass that extracts information from the <HEAD> section of an HTML document.

HTML::LinkExtor - An HTML::Parser subclass that extracts links from an HTML document.

HTML::PullParser - An alternative interface to the basic parser that does not require event driven programming.

HTML::TokeParser - An HTML::PullParser subclass with fixed token setup and methods for extracting text. Many simple parsing needs are probably best attacked with this module.

In addition take a look at the HTML::Tree package that build on HTML::Parser to create and extract information from HTML syntax trees (similar to HTML DOM).

PREREQUISITES

In order to install and use this package you will need Perl version 5.8 or better. The HTML::Tagset module should be installed.

If you intend to use the HTML::HeadParser you probably want to install libwww-perl too.

INSTALLATION

Just follow the usual procedure:

    perl Makefile.PL
    make
    make test
    make install

REPORTING BUGS

Bug reports and issues for discussion about these modules can be sent to the <libwww@perl.org> mailing list.

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Found in path(s):
* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/README
No license file was found, but licenses were detected in source scan.

=encoding utf8

=head1 NAME

HTML::Entities - Encode or decode strings with HTML entities

=head1 SYNOPSIS

use HTML::Entities;

$a = "Våre norske tegn b&oslash;r &#230res";
decode_entities($a);
encode_entities($a, "\200-\377");

For example, this:

$input = "vis-à-vis Beyoncé's naïve\npapier-mâché résumé";
print encode_entities($input), "\n"

Prints this out:

vis-\&agrave;-vis Beyonc\'s naïve\npapier-m\&acirc;ch\&eacute;r; r\&eacute;sumé;

=head1 DESCRIPTION

This module deals with encoding and decoding of strings with HTML character entities. The module provides the following functions:

=over 4

=item decode_entities( $string, ... )

This routine replaces HTML entities found in the $string with the corresponding Unicode character. Unrecognized entities are left alone.

If multiple strings are provided as argument they are each decoded separately and the same number of strings are returned.
If called in void context the arguments are decoded in-place.
This routine is exported by default.
=item _decode_entities( $string, \%entity2char )
=item _decode_entities( $string, \%entity2char, $expand_prefix )
This will in-place replace HTML entities in $string. The %entity2char
hash must be provided. Named entities not found in the %entity2char
hash are left alone. Numeric entities are expanded unless their value
overflow.
The keys in %entity2char are the entity names to be expanded and their
values are what they should expand into. The values do not have to be
single character strings. If a key has ";" as suffix,
then occurrences in $string are only expanded if properly terminated
with ";". Entities without ";" will be expanded regardless of how
they are terminated for compatibility with how common browsers treat
entities in the Latin-1 range.
If $expand_prefix is TRUE then entities without trailing ";" in
%entity2char will even be expanded as a prefix of a longer
unrecognized name. The longest matching name in %entity2char will be
used. This is mainly present for compatibility with an MSIE
misfeature.
$string = "foo&nbspbar";
_decode_entities($string, { nb => "@", nbsp => "\xA0" }, 1);
print $string; # will print "foo bar"
This routine is exported by default.
=item encode_entities( $string )
=item encode_entities( $string, $unsafe_chars )
This routine replaces unsafe characters in $string with their entity
representation. A second argument can be given to specify which characters to
consider unsafe. The unsafe characters is specified using the regular
expression character class syntax (what you find within brackets in regular
expressions).
The default set of characters to encode are control chars, high-bit chars, and
the C<< < >>, C<< & >>, C<< > >>, C<< ' >> and C<< " >> characters. But this,
for example, would encode I<just> the C<< < >>, C<< & >>, C<< > >>, and C<< "
>> characters:

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$encoded = encode_entities($input, '<>&"');

and this would only encode non-plain ascii:

$encoded = encode_entities($input, '^\n\x20-\x25\x27-\x7e');

This routine is exported by default.

=item encode_entities_numeric( $string )

=item encode_entities_numeric( $string, $unsafe_chars )

This routine works just like encode_entities, except that the replacement entities are always C<&amp;#xI<hexnum>;> and never C<&entname>;. For example, C<encode_entities("r\xF4le")> returns "r&ocirc;le", but C<encode_entities_numeric("r\xF4le")> returns "r&#xF4;le".

This routine is I<not> exported by default. But you can always export it with C<use HTML::Entities qw(encode_entities_numeric)> or even C<use HTML::Entities qw(:DEFAULT encode_entities_numeric)>.

=back

All these routines modify the string passed as the first argument, if called in a void context. In scalar and array contexts, the encoded or decoded string is returned (without changing the input string).

If you prefer not to import these routines into your namespace, you can call them as:

use HTML::Entities ();
$decoded = HTML::Entities::decode($a);
$encoded = HTML::Entities::encode($a);
$encoded = HTML::Entities::encode_numeric($a);

The module can also export the %char2entity and the %entity2char hashes, which contain the mapping from all characters to the corresponding entities (and vice versa, respectively).

=head1 COPYRIGHT

Copyright 1995-2006 Gisle Aas. All rights reserved.

This library is free software; you can redistribute it and/or modify it under the same terms as Perl itself.
Found in path(s):
* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/lib/HTML/Entities.pm
No license file was found, but licenses were detected in source scan.

=head1 NAME

HTML::PullParser - Alternative HTML::Parser interface

=head1 SYNOPSIS

use HTML::PullParser;

$p = HTML::PullParser->new(file => "index.html",
   start => 'event, tagname, @attr',
   end => 'event, tagname',
   ignore_elements => [qw(script style)],
) || die "Can't open: $!";
while (my $token = $p->get_token) {
    #...do something with $token
}

=head1 DESCRIPTION

The HTML::PullParser is an alternative interface to the HTML::Parser class. It basically turns the HTML::Parser inside out. You associate a file (or any IO::Handle object or string) with the parser at construction time and then repeatedly call $parser->get_token to obtain the tags and text found in the parsed document.

The following methods are provided:

=over 4

=item $p = HTML::PullParser->new( file => $file, %options )

=item $p = HTML::PullParser->new( doc => \$doc, %options )

A C<HTML::PullParser> can be made to parse from either a file or a literal document based on whether the C<file> or C<doc> option is passed to the parser's constructor.

The C<file> passed in can either be a file name or a file handle object. If a file name is passed, and it can't be opened for reading, then the constructor will return an undefined value and $! will tell you why it failed. Otherwise the argument is taken to be some object that the C<HTML::PullParser> can read() from when it needs more data. The stream will be read() until EOF, but not closed.
A \texttt{doc} can be passed plain or as a reference to a scalar. If a reference is passed then the value of this scalar should not be changed before all tokens have been extracted.

Next the information to be returned for the different token types must be set up. This is done by simply associating an argspec (as defined in \texttt{HTML::Parser}) with the events you have an interest in. For instance, if you want \texttt{start} tokens to be reported as the string \texttt{S} followed by the tagname and the attributes you might pass an \texttt{start}-option like this:

\begin{verbatim}
$p = HTML::PullParser->new(
   doc => $document_to_parse,
   start => "S", tagname, @attr,
   end => "E", tagname,
);
\end{verbatim}

At last other \texttt{HTML::Parser} options, like \texttt{ignore_tags}, and \texttt{unbroken_text}, can be passed in. Note that you should not use the I<event>_h options to set up parser handlers. That would confuse the inner logic of \texttt{HTML::PullParser}.

\begin{verbatim}
=item $token = $p->get_token

This method will return the next \texttt{token} found in the HTML document, or \texttt{undef} at the end of the document. The token is returned as an array reference. The content of this array match the argspec set up during \texttt{HTML::PullParser} construction.

=item $p->unget_token( @tokens )

If you find out you have read too many tokens you can push them back, so that they are returned again the next time $p->get_token is called.

=back
\end{verbatim}

=head1 EXAMPLES

The 'eg/hform' script shows how we might parse the form section of HTML::Documents using HTML::PullParser.

=head1 SEE ALSO

L<HTML::Parser>, L<HTML::Toker>

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=cut

Found in path(s):
* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1.tar.gz/HTML-Parser-3.72/lib/HTML/PullParser.pm
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* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1.tar.gz/HTML-Parser-3.72/util.c
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=head1 NAME

HTML::TokeParser - Alternative HTML::Parser interface

=head1 SYNOPSIS

require HTML::TokeParser;
$p = HTML::TokeParser->new("index.html") ||
  die "Can't open: $!";
$p->empty_element_tags(1); # configure its behaviour

while (my $token = $p->get_token) {
  #...
}

=head1 DESCRIPTION

The C<HTML::TokeParser> is an alternative interface to the C<HTML::Parser> class. It is an C<HTML::PullParser> subclass with a predeclared set of token types. If you wish the tokens to be reported differently you probably want to use the C<HTML::PullParser> directly.

The following methods are available:

=over 4
The object constructor argument is either a file name, a file handle object, or the complete document to be parsed. Extra options can be provided as key/value pairs and are processed as documented by the base classes.

If the argument is a plain scalar, then it is taken as the name of a file to be opened and parsed. If the file can't be opened for reading, then the constructor will return C<undef> and $! will tell you why it failed.

If the argument is a reference to a plain scalar, then this scalar is taken to be the literal document to parse. The value of this scalar should not be changed before all tokens have been extracted.

Otherwise the argument is taken to be some object that the C<HTML::TokeParser> can read() from when it needs more data. Typically it will be a filehandle of some kind. The stream will be read() until EOF, but not closed.

A newly constructed C<HTML::TokeParser> differ from its base classes by having the C<unbroken_text> attribute enabled by default. See L<HTML::Parser> for a description of this and other attributes that influence how the document is parsed. It is often a good idea to enable C<empty_element_tags> behaviour.

Note that the parsing result will likely not be valid if raw undecoded UTF-8 is used as a source. When parsing UTF-8 encoded files turn on UTF-8 decoding:

```perl
code
open(my $fh, "<:utf8", "index.html") || die "Can't open 'index.html': $!";
my $p = HTML::TokeParser->new( $fh );
# ...
```

If a $filename is passed to the constructor the file will be opened in raw mode and the parsing result will only be valid if its content is Latin-1 or pure ASCII.

If parsing from an UTF-8 encoded string buffer decode it first:

```perl
code
utf8::decode($document);
my $p = HTML::TokeParser->new( \$document );
```
=item $p->get_token

This method will return the next I<token> found in the HTML document, or C<undef> at the end of the document. The token is returned as an array reference. The first element of the array will be a string denoting the type of this token: "S" for start tag, "E" for end tag, "T" for text, "C" for comment, "D" for declaration, and "PI" for process instructions. The rest of the token array depend on the type like this:

["S", $tag, $attr, $attrseq, $text]
["E", $tag, $text]
["T", $text, $is_data]
["C", $text]
["D", $text]
["PI", $token0, $text]

where $attr is a hash reference, $attrseq is an array reference and the rest are plain scalars. The L<HTML::Parser/Argspec> explains the details.

=item $p->unget_token( @tokens )

If you find you have read too many tokens you can push them back, so that they are returned the next time $p->get_token is called.

=item $p->get_tag

=item $p->get_tag( @tags )

This method returns the next start or end tag (skipping any other tokens), or C<undef> if there are no more tags in the document. If one or more arguments are given, then we skip tokens until one of the specified tag types is found. For example:

$p->get_tag("font", "/font");

will find the next start or end tag for a font-element.

The tag information is returned as an array reference in the same form as for $p->get_token above, but the type code (first element) is missing. A start tag will be returned like this:

[$tag, $attr, $attrseq, $text]

The tagname of end tags are prefixed with "/", i.e. end tag is
This method returns all text found at the current position. It will return a zero length string if the next token is not text. Any entities will be converted to their corresponding character.

If one or more arguments are given, then we return all text occurring before the first of the specified tags found. For example:

$p->get_text("p", "br");

will return the text up to either a paragraph of linebreak element.

The text might span tags that should be I<textified>. This is controlled by the $p->{textify} attribute, which is a hash that defines how certain tags can be treated as text. If the name of a start tag matches a key in this hash then this tag is converted to text. The hash value is used to specify which tag attribute to obtain the text from. If this tag attribute is missing, then the upper case name of the tag enclosed in brackets is returned, e.g. "[IMG]". The hash value can also be a subroutine reference. In this case the routine is called with the start tag token content as its argument and the return value is treated as the text.

The default $p->{textify} value is:

{img => "alt", applet => "alt"}

This means that <IMG> and <APPLET> tags are treated as text, and that the text to substitute can be found in the ALT attribute.

Same as $p->get_text above, but will collapse any sequences of white space to a single space character. Leading and trailing white space is removed.

This will return all text found at the current position ignoring any
phrasal-level tags. Text is extracted until the first non phrasal-level tag. Textification of tags is the same as for get_text(). This method will collapse white space in the same way as get_trimmed_text() does.

The definition of <i>phrasal-level tags</i> is obtained from the HTML::Tagset module.

=back

=head1 EXAMPLES

This example extracts all links from a document. It will print one line for each link, containing the URL and the textual description between the <A>...</A> tags:

use HTML::TokeParser;
$p = HTML::TokeParser->new(shift||"index.html");

while (my $token = $p->get_tag("a")) {
  my $url = $token->[1]{href} || ";
  my $text = $p->get_trimmed_text("/a");
  print "$url\t$text\n";
}

This example extract the <TITLE> from the document:

use HTML::TokeParser;
$p = HTML::TokeParser->new(shift||"index.html");
if ($p->get_tag("title")) {
  my $title = $p->get_trimmed_text;
  print "Title: $title\n";
}

=head1 SEE ALSO

L<HTML::PullParser>, L<HTML::Parser>

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=cut

Found in path(s):
This is an example showing how you can extract links from a document received using LWP:

```
use LWP::UserAgent;
use HTML::LinkExtor;
use URI::URL;

$url = "http://www.perl.org/";  # for instance
$ua = LWP::UserAgent->new;

# Set up a callback that collect image links
my @imgs = ();
sub callback {
    my($tag, %attr) = @_;  
    return if $tag ne 'img';  # we only look closer at <img ...
    push(@imgs, values %attr);
}

# Make the parser.  Unfortunately, we don't know the base yet
# (it might be different from $url)
$p = HTML::LinkExtor->new(&callback);

# Request document and parse it as it arrives
$res = $ua->request(HTTP::Request->new(GET => $url),
    sub { $p->parse($_[0]) });

# Expand all image URLs to absolute ones
my $base = $res->base;
@imgs = map { $_ = url($_, $base)->abs; } @imgs;

# Print them out
print join("n", @imgs), "n";
```

=head1 SEE ALSO

L<HTML::Parser>, L<HTML::Tagset>, L<LWP>, L<URI::URL>

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* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/lib/HTML/LinkExtor.pm
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* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/hparser.h
* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/hparser.c
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=head1 NAME

HTML::Filter - Filter HTML text through the parser

=head1 NOTE

B<This module is deprecated.> The C<HTML::Parser> now provides the functionality of C<HTML::Filter> much more efficiently with the C<default> handler.

=head1 SYNOPSIS

require HTML::Filter;
$Sp = HTML::Filter->new->parse_file("index.html");

=head1 DESCRIPTION

C<HTML::Filter> is an HTML parser that by default prints the original text of each HTML element (a slow version of cat(1) basically). The callback methods may be overridden to modify the filtering for some HTML elements and you can override output() method which is called to print the HTML text.
C<HTML::Filter> is a subclass of C<HTML::Parser>. This means that the document should be given to the parser by calling the $p->parse() or $p->parse_file() methods.

=head1 EXAMPLES

The first example is a filter that will remove all comments from an HTML file. This is achieved by simply overriding the comment method to do nothing.

    package CommentStripper;
    require HTML::Filter;
    @ISA=qw(HTML::Filter);
    sub comment { }  # ignore comments

The second example shows a filter that will remove any E<lt>TABLE>s found in the HTML file. We specialize the start() and end() methods to count table tags and then make output not happen when inside a table.

    package TableStripper;
    require HTML::Filter;
    @ISA=qw(HTML::Filter);
    sub start
    {
        my $self = shift;
        $self->{table_seen}++ if $_[0] eq "table";
        $self->SUPER::start(@_);
    }

    sub end
    {
        my $self = shift;
        $self->SUPER::end(@_);
        $self->{table_seen}-- if $_[0] eq "table";
    }

    sub output
    {
        my $self = shift;
        unless ($self->{table_seen}) {
            $self->SUPER::output(@_);
        }
    }

If you want to collect the parsed text internally you might want to do something like this:
package FilterIntoString;
require HTML::Filter;
@ISA=qw(HTML::Filter);
sub output { push(@{$_[0]->{fhtml}}, $_[1]) }
sub filtered_html { join("", @{$_[0]->{fhtml}}) }

=head1 SEE ALSO
L<HTML::Parser>

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* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/lib/HTML/Filter.pm
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* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/Parser.xs
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=back

=head1 EXAMPLE

$H = HTTP::Headers->new;
$p = HTML::HeadParser->new($H);
$p->parse(<EO T>);
<title>Stupid example</title>
<br base hret="http://www.linpro.no/lwp"/> Normal text starts here.
EOT

undef $p;
print $h->title;  # should print "Stupid example"

=head1 SEE ALSO

L<HTML::Parser>, L<HTTP::Headers>

The C<HTTP::Headers> class is distributed as part of the
I<libwww-perl> package. If you don't have that distribution installed
you need to provide the $header argument to the C<HTTP::HeadParser>
constructor with your own object that implements the documented
protocol.

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---
abstract: 'HTML parser class'
author:
  - 'Gisle Aas <gisle@activestate.com>'
buildRequires:
  ExtUtils::MakeMaker: 0
  Test::More: 0
configure_requires:
  ExtUtils::MakeMaker: 0
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 6.66, CPAN::Meta::Converter version 2.133380'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: 1.4
name: HTML-Parser
no_index:
directory:
  - t
  - inc
recommends:
HTTP::Headers: 0
requires:
HTML::Tagset: 3
XSLoader: 0
perl: 5.008
resources:
MailingList: mailto:libwww@perl.org
repository: http://github.com/gisle/html-parser
version: 3.72

Found in path(s):
* /opt/cola/permits/1135169270_1613462977.8/0/libhtml-parser-perl-3-72-orig-1-tar-gz/HTML-Parser-3.72/META.yml

1.125 diffutils 3.7-3
1.125.1 Available under license:

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1.126 libtimedate-perl 2.3200-1
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=encoding utf8

=head1 NAME

Date::Language::Bulgarian - localization for Date::Format

=head1 DESCRIPTION

This is Bulgarian localization for Date::Format. It is important to note that this module source code is in utf8. All strings which it outputs are in utf8, so it is safe to use it currently only with English. You are left alone to try and convert the output when using different Date::Language::* in the same application. This should be addressed in the future.

=head1 SYNOPSIS

use strict;
use warnings;
use Date::Language;
local $\texttt{\$\textasciitilde}$;
my $\texttt{\$template =\%a \%b \%e \%T \%Y (\%Y-\%m-\%d \%H:\%M:\%S \textendline}$;
my $\texttt{\$time=1290883821; # or just use time();}$
my $\texttt{\$lt = localtime($time);}$
my $\texttt{\%languages = qw(English GMT German EEST Bulgarian EET);}$
binmode(select,':utf8');

foreach my $l(keys \%languages){
  my $\texttt{\$lang = Date::Language->new($l);}$
  my $\texttt{\$zone = \%languages{$l};}$
  print $\texttt{\$/ "$l $zone\textendline};$
  print $\texttt{\$lang->time2str($template, $time);}$
  print $\texttt{\$lang->time2str($template, $time, $zone);}$
  print $\texttt{\$lang->strftime($template, \@lt);}$
}

=head1 AUTHOR

Krasimir Berov (berov@cpan.org)

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=cut

Found in path(s):
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  2.32/lib/Date/Language/Bulgarian.pm
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=head1 NAME

Date::Language::Hungarian - Magyar format for Date::Format

=head1 SYNOPSIS

my $\texttt{\$lang = Date::Language->new('Hungarian');}$
print $\texttt{\$lang->time2str("%a %b %e %T %Y", time);}$

@\texttt{\$lt = localtime(time);}$
print $\texttt{\$lang->time2str($template, time);}$
print $\texttt{\$lang->strftime($template, \@lt);}$

print $\texttt{\$lang->time2str($template, time, $zone);}$
print $lang->strftime($template, @lt, $zone);

print $lang->ctime(time);
print $lang->asctime(@lt);

print $lang->ctime(time, $zone);
print $lang->asctime(@lt, $zone);

See L<Date::Format>.

=head1 AUTHOR

Paula Goddard (paula -at- paulacska -dot- com)

=head1 LICENCE

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=cut

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---
abstract: unknown
author:
  - 'Graham Barr <gbarr@pobox.com>
build_requires:
  ExtUtils::MakeMaker: '0'
configure_requires:
  ExtUtils::MakeMaker: '0'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.44, CPAN::Meta::Converter version 2.150010'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: '1.4'
name: TimeDate
no_index: 
directory:
  - t
  - inc
resources:
  bugtracker: https://github.com/atoomic/perl-TimeDate/issues
  repository: https://github.com/atoomic/perl-TimeDate
  version: '2.32'
x_serialization_backend: 'CPAN::Meta::YAML version 0.018'

Found in path(s):
* /opt/cola/permits/1146928942_1616812368.9/0/libtimedate-perl-2-3200-orig-1-tar-gz/TimeDate-2.32/META.yml
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* /opt/cola/permits/1146928942_1616812368.9/0/libtimedate-perl-2-3200-orig-1-tar-gz/TimeDate-2.32/lib/Date/Format.pm
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This is the perl5 TimeDate distribution. It requires perl version 5.003 or later

This distribution replaces my earlier GetDate distribution, which was only a date parser. The date parser contained in this distribution is far superior to the yacc based parser, and a *lot* faster.

The parser contained here will only parse absolute dates, if you want a date parser that can parse relative dates then take a look at the Time modules by David Muir on CPAN.

You install the library by running these commands:

    perl Makefile.PL
    make
    make test
    make install

Please report any bugs/suggestions to <gbarr@pobox.com>

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1.127 libxv 1.0.11-1

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1.128 icu 66.1-2ubuntu2

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## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
```

Open Source Used In ThousandEyes BrowserBot for Cisco Application Hosting 1.0 1465
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\(GEN_{DEPS}.c\) $< \n | sed '"s/\(.*\)\.o[ :]*\$\(\@\)o : /g\" > $@; \
 [ -s $@ ] \| rm -f $@"

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\(GEN_{DEPS}.cc\) $< \n | sed '"s/\(.*\)\.o[ :]*\$\(\@\)o : /g\" > $@; \
 [ -s $@ ] \| rm -f $@"

## Versioned libraries rules
%.SO..$(SO..TARGET_VERSION_MAJOR): %.SO..$(SO..TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@

%.SO: %.SO..$(SO..TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}.$(SO)..$(SO..TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup
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1.145 libxcomposite 0.4.5-1

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1.146 liblwp-protocol-https-perl 6.07-2ubuntu2

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=head1 NAME

LWP::Protocol::https - Provide https support for LWP::UserAgent

=head1 SYNOPSIS

use LWP::UserAgent;

$ua = LWP::UserAgent->new(ssl_opts => { verify_hostname => 1 });
$res = $ua->get("https://www.example.com");

# specify a CA path
$ua = LWP::UserAgent->new(}
ssl_opts => {
    SSL_ca_path => '/etc/ssl/certs',
    verify_hostname => 1,
};

=head1 DESCRIPTION

The LWP::Protocol::https module provides support for using https schemed URLs with LWP. This module is a plug-in to the LWP protocol handling, so you don't use it directly. Once the module is installed LWP is able to access sites using HTTP over SSL/TLS.

If hostname verification is requested by LWP::UserAgent's C<ssl_opts>, and neither C<SSL_ca_file> nor C<SSL_ca_path> is set, then C<SSL_ca_file> is implied to be the one provided by Mozilla::CA. If the Mozilla::CA module isn't available SSL requests will fail. Either install this module, set up an alternative C<SSL_ca_file> or disable hostname verification.

This module used to be bundled with the libwww-perl, but it was unbundled in v6.02 in order to be able to declare its dependencies properly for the CPAN tool-chain. Applications that need https support can just declare their dependency on LWP::Protocol::https and will no longer need to know what underlying modules to install.

=head1 SEE ALSO

L<IO::Socket::SSL>, L<Crypt::SSLeay>, L<Mozilla::CA>

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Found in path(s):
* /opt/cola/permits/1109180166_1606268542.5/0/liblwp-protocol-https-perl-6-07-orig-1-tar-gz/LWP-Protocol-https-6.07/lib/LWP/Protocol/https.pm

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abstract: 'Provide https support for LWP::UserAgent'

author:
    - 'Gisle Aas <gisle@activestate.com>'

build_requires:
    Test::More: '0'
    Test::RequiresInternet: '0'
NAME
LWP::Protocol::https - Provide https support for LWP::UserAgent

SYNOPSIS
use LWP::UserAgent;

$ua = LWP::UserAgent->new(ssl_opts => { verify_hostname => 1 });
$res = $ua->get("https://www.example.com");

DESCRIPTION
The LWP::Protocol::https module provides support for using https schemed
URLs with LWP. This module is a plug-in to the LWP protocol handling, so
you don't use it directly. Once the module is installed LWP is able to
access sites using HTTP over SSL/TLS.
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SEE ALSO
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A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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</table>

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1.151 libfile-desktopentry-perl 0.22-1
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---
abstract: 'Module to handle .desktop files'
author:
- 'Jaap Karssenberg <pardus@cpan.org>'
build_requires:
ExtUtils::MakeMaker: '0'
configure_requires:
=head1 LIMITATIONS

If you try to exec a remote file with an application that can only handle files on the local file system we should -according to the spec- download the file to a temp location. This is not supported. Use the C<wants_uris()> method to check if an application supports urls.

The values of the various Desktop Entry keys are not parsed (except for the Exec key). This means that booleans will be returned as the strings "true" and "false" and lists will still be ; ; separated.

If the icon is given as name and not as path it should be resolved for the C<%i> code in the Exec key. We need a separate module for the icon spec to deal with this.

According to the spec comments can contain any encoding. However since this module read files as utf8, invalid UTF-8 characters in a comment will cause
an error.

There is no support for Legacy-Mixed Encoding. Everybody is using utf8 now ... right?

=head1 AUTHOR

Jaap Karssenberg (Pardus) E<lt>pardus@cpan.orgE<gt>

Maintained by Michiel Beijen E<lt>michielb@cpan.orgE<gt>

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=head1 SEE ALSO

L<http://standards.freedesktop.org/desktop-entry-spec/desktop-entry-spec-latest.html>

L<File::BaseDir> and L<File::MimeInfo::Applications>

L<X11::FreeDesktop::DesktopEntry>

cut

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# File-DesktopEntry

You can use this module to work with `.desktop` files as specified by the Freedesktop.org specification.

## INSTALLATION

If you are on Linux, the most convenient way to install this module is via your package manager. On Debian or Ubuntu:

    sudo apt-get install libfile-desktopentry-perl

on Fedora, RHEL or CentOS:

    sudo yum install perl-File-DesktopEntry
note: on RHEL or CentOS you'd need to first install [EPEL](https://fedoraproject.org/wiki/EPEL):

```
sudo yum install epel-release
```

If you can't install from a package manager, the best solution is installation using your cpan client. This will take care of installing any dependencies:

```
cpan File::DesktopEntry
```

To install this module manually, type the following:

```
perl Makefile.PL
    make
    make test
    make install
```

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Found in path(s):
* /opt/cola/permits/1144894847_161612232.05/0/libfile-desktopentry-perl-0-22-orig-1-tar-gz/File-DesktopEntry-0.22/README.md

1.152 coreutils 8.30 3ubuntu2

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1.154 mitchellh-mapstructure v1.1.2

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KCM credential cache.
HDB LDAP backend.

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AES in libhcrypto

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@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
@author Paulo Barreto <paulo.barreto@terra.com.br>

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Ferguson, Eric Young and Dana How for comparing notes; and Ray Lau,
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 * command :
 * $ ./checkTag tag
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 * They are compatible if first 3 digits are identical.
 * Anything beyond that is free, and doesn't impact validation.
 * Example : tag v1.8.1.2 is compatible with version 1.8.1
 * When tag and version are not compatible, program exits with error code 1.
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 */

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1.160 avahi 0.7-4ubuntu7

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1.165 atk 2.35.1-1ubuntu2

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)
Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally
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1.167 pango 1.44.7-2ubuntu4

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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1.173 glib-networking 2.64.2-1ubuntu0.1

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.
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-------------------------------------------------------------------------
1.175 libtie-ixhash-perl 1.23-2

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=head1 NAME

Tie::IxHash - ordered associative arrays for Perl

=head1 SYNOPSIS

# simple usage
use Tie::IxHash;
tie HASHVARIABLE, 'Tie::IxHash' [, LIST];

# OO interface with more powerful features
use Tie::IxHash;
TIEOBJECT = Tie::IxHash->new( [LIST] );
TIEOBJECT->Splice( OFFSET [, LENGTH [, LIST]] );
TIEOBJECT->Push( LIST );
TIEOBJECT->Pop;
TIEOBJECT->Shift;
TIEOBJECT->Unshift( LIST );
TIEOBJECT->Keys( [LIST] );
TIEOBJECT->Values( [LIST] );
TIEOBJECT->Indices( LIST );
TIEOBJECT->Delete( [LIST] );
TIEOBJECT->Replace( OFFSET, VALUE, [KEY] );
TIEOBJECT->Reorder( LIST );
TIEOBJECT->SortByKey;
TIEOBJECT->SortByValue;
TIEOBJECT->Length;

=head1 DESCRIPTION

This Perl module implements Perl hashes that preserve the order in which the
hash elements were added. The order is not affected when values
corresponding to existing keys in the IxHash are changed. The elements can
also be set to any arbitrary supplied order. The familiar perl array
operations can also be performed on the IxHash.

=head2 Standard C<TIEHASH> Interface
The standard \texttt{tie} mechanism is available. This interface is recommended for simple uses, since the usage is exactly the same as regular Perl hashes after the \texttt{tie} is declared.

=head2 Object Interface

This module also provides an extended object-oriented interface that can be used for more powerful operations with the \texttt{IxHash}. The following methods are available:

=over 8

=item FETCH, STORE, DELETE, EXISTS

These standard \texttt{tie} methods mandated by Perl can be used directly. See the \texttt{tie} entry in perlfunc(1) for details.

=item Push, Pop, Shift, Unshift, Splice

These additional methods resembling Perl functions are available for operating on key-value pairs in the \texttt{IxHash}. The behavior is the same as the corresponding perl functions, except when a supplied hash key already exists in the hash. In that case, the existing value is updated but its order is not affected. To unconditionally alter the order of a supplied key-value pair, first \texttt{DELETE} the \texttt{IxHash} element.

=item Keys

Returns an array of \texttt{IxHash} element keys corresponding to the list of supplied indices. Returns an array of all the keys if called without arguments. Note the return value is mostly only useful when used in a list context (since perl will convert it to the number of elements in the array when used in a scalar context, and that may not be very useful).

If a single argument is given, returns the single key corresponding to the index. This is usable in either scalar or list context.

=item Values

Returns an array of \texttt{IxHash} element values corresponding to the list of supplied indices. Returns an array of all the values if called without arguments. Note the return value is mostly only useful when used in a list context (since perl will convert it to the number of elements in the array when used in a scalar context, and that may not be very useful).

If a single argument is given, returns the single value corresponding to the index. This is usable in either scalar or list context.
=item Indices

Returns an array of indices corresponding to the supplied list of keys. Note the return value is mostly only useful when used in a list context (since Perl will convert it to the number of elements in the array when used in a scalar context, and that may not be very useful).

If a single argument is given, returns the single index corresponding to the key. This is usable in either scalar or list context.

=item Delete

Removes elements with the supplied keys from the IxHash.

=item Replace

Substitutes the IxHash element at the specified index with the supplied value-key pair. If a key is not supplied, simply substitutes the value at index with the supplied value. If an element with the supplied key already exists, it will be removed from the IxHash first.

=item Reorder

This method can be used to manipulate the internal order of the IxHash elements by supplying a list of keys in the desired order. Note however, that any IxHash elements whose keys are not in the list will be removed from the IxHash.

=item Length

Returns the number of IxHash elements.

=item SortByKey

Reorders the IxHash elements by textual comparison of the keys.

=item SortByValue

Reorders the IxHash elements by textual comparison of the values.

=item Clear

Resets the IxHash to its pristine state: with no elements at all.
EXAMPLE

use Tie::IxHash;

# simple interface
$t = tie(%myhash, 'Tie::IxHash', 'a' => 1, 'b' => 2);
%myhash = (first => 1, second => 2, third => 3);
$smyhash{fourth} = 4;
@keys = keys %myhash;
@values = values %myhash;
print("y") if exists $myhash{third};

# OO interface
$t = Tie::IxHash->new(first => 1, second => 2, third => 3);
$t->Push(fourth => 4);  # same as $myhash{‘fourth’} = 4;
($k, $v) = $t->Pop;     # $k is ‘fourth’, $v is 4
$t->Unshift(neg => -1, zeroth => 0);
($k, $v) = $t->Shift;   # $k is ‘neg’, $v is -1
@oneandtwo = $t->Splice(1, 2, foo => 100, bar => 101);

@keys = $t->Keys;
@values = $t->Values;
@indices = $t->Indices(‘foo’, ’zeroth’);
@itemkeys = $t->Keys(@indices);
@itemvals = $t->Values(@indices);
$t->Replace(2, 0.3, ’other’);
$t->Delete(‘second’, ’zeroth’);
$len = $t->Length;   # number of key-value pairs
$t->Reorder(reverse @keys);
$t->SortByKey;
$t->SortByValue;

BUGS

You cannot specify a negative length to C<Splice>. Negative indexes are OK, though.

NOTE

Indexing always begins at 0 (despite the current C<$[> setting) for all the functions.

TODO
Addition of elements with keys that already exist to the end of the IxHash must be controlled by a switch.

Provide C<TIEARRAY> interface when it stabilizes in Perl.

Rewrite using XSUBs for efficiency.

=head1 AUTHOR

Gurusamy Sarathy        gsar@umich.edu

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=head1 VERSION

Version 1.23

=head1 SEE ALSO

perl(1)

=cut

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---
abstract: ordered associative arrays for Perl
author:
- Gurusamy Sarathy        gsar@umich.edu
build_requires:
  Test::More: 0
dynamic_config: 1
generated_by: 'Module::Build version 0.4003, CPAN::Meta::Converter version 2.112621'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: 1.4
name: Tie-IxHash
provides:
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file: lib/Tie/IxHash.pm
version: 1.23
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1.176 pcre2 10.34-7

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Written by: Philip Hazel
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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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---

abstract: 'A perl module for parsing XML documents'
author:
- 'Clark Cooper (coopercc@netheaven.com)'
buildRequires:
  ExtUtils::MakeMaker: '0'
Test::More: '0'
warnings: '0'
configureRequires:

1.181 libxml-parser-perl 2.46-1
1.181.1 Available under license:

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Found in path(s):
* /opt/cola/permits/1155871549_1619073653.5/0/libxml-parser-perl-2-46-orig-tar-gz/XML-Parser-2.46/samples/REC-xml-19980210.xml
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---
ExtUtils::MakeMaker: '0'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.34, CPAN::Meta::Converter version 2.150010'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: '1.4'
name: XML-Parser
no_index:
directory:
  - t
  - inc
requires:
  LWP::UserAgent: '0'
  perl: '5.00405'
resources:
  bugtracker: https://github.com/toddr/XML-Parser/issues
  repository: http://github.com/toddr/XML-Parser
  version: '2.46'
x_serialization_backend: 'CPAN::Meta::YAML version 0.018'

Found in path(s):
* /opt/cola/permits/1155871549_1619073653.5/0/libxml-parser-perl-2-46-orig-tar-gz/XML-Parser-2.46/META.yml
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Found in path(s):
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  * /opt/cola/permits/1155871549_1619073653.5/0/libxml-parser-perl-2-46-orig-tar-gz/XML-Parser-2.46/samples/canonical
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```perl
# LWPExternEnt.pl
#
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package XML::Parser;

use URI;
use URI::file;
use LWP::UserAgent;

##
## Note that this external entity handler reads the entire entity into
## memory, so it will choke on huge ones. It would be really nice if
## LWP::UserAgent optionally returned us an IO::Handle.
##

sub lwp_ext_ent_handler {
  my ($xp, $base, $sys) = @_;  # We don't use public id

  my $uri;

  if (defined $base) {
    # Base may have been set by parsefile, which is agnostic about
    # whether it's a file or URI.
    my $base_uri = new URI($base);
    unless (defined $base_uri->scheme) {
      $base_uri = URI->new_abs($base_uri, URI::file->cwd);
    }

    $uri = URI->new_abs($sys, $base_uri);
  }
  else {
    $uri = new URI($sys);
    unless (defined $uri->scheme) {
      $uri = URI->new_abs($uri, URI::file->cwd);
    }
  }

  my $ua = $xp->{_lwpagent};
  unless (defined $ua) {
    $ua = $xp->{_lwpagent} = new LWP::UserAgent();
    $ua->env_proxy();
  }

  $ua->data($uri);  # Read the entity into $uri
  $ua->get($base_uri);  # Send the file
  my $result = $ua->response;
  if (defined $result) {
    $xp->{_lwpdata} = $result;
  } else {  # Handle error
    die q{Error getting external entity: $base_uri}

  }

  my $prefix = $sys->prefix;
  my $us = $sys->userstring;
  unless (defined $prefix) {
    $prefix = "
```
my $req = new HTTP::Request('GET', $uri);

my $res = $ua->request($req);
if ($res->is_error) {
    $xp->{ErrorMessage} .= "n" . $res->status_line . "$uri"
    return undef;
}

$xp->{_BaseStack} ||= [];
push(@( $xp->{_BaseStack} ), $base);

$xp->base($uri);
return $res->content;
}  # End lwp_ext_ent_handler

sub lwp_ext_ent_cleanup {
    my ($xp) = @_;
    $xp->base(pop(@( $xp->{_BaseStack} )));
}  # End lwp_ext_ent_cleanup

1;

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1.182 mpdecimal 2.4.2-3

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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That's all there is to it!
DRuntime: Runtime Library for the D Programming Language

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GCC RUNTIME LIBRARY EXCEPTION

Version 3.1, 31 March 2009

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1.197 keyutils 1.6-6ubuntu1

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set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
set xtics rotate by -45
set style histogram errorbars gap 2 lw 1
set style fill solid border -1
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* DNS resolver (dirmngr/dns.c)

dns.c - Recursive, Reentrant DNS Resolver.

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program `Gnomovision' (a program to direct compilers to make passes
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<signature of Ty Coon>, 1 April 1989
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The End

1.209 gir1.2-gdkpixbuf-2.0 2.40.0+dfsg-3ubuntu0.2

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1.210 libxml
a7fe7ee45938c53a8dd028dd40bbaa461191a2fd

2

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src/base/md5.c  
src/base/md5.h

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src/bdf/bdf.h  
src/bdf/bdfdrivr.c  
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src/gzip/infblock.c  
src/gzip/infblock.h  
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* Optimize Java serialization code when writing a small message to a stream.
* Optimize Java serialization of strings so that UTF-8 encoding happens only once per string per serialization call.
* Clean up some Java warnings.
* Fix bug with permanent callbacks that delete themselves when run.
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* Fixed detection of sched_yield on Solaris.
* Added atomicops for Solaris

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* Fixed minor IBM xlC compiler build issues
* Added atomicops for AIX (POWER)

1.218 systemd 245.4-4ubuntu3.5
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1.223 networkd-dispatcher 2.0.1-1

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*/

// Original can be found at:
// https://bitbucket.org/lindenlab/llsd
// Modifications by Joshua Bell inexorabletash@gmail.com
// https://github.com/inexorabletash/polyfill
// ES3/ES5 implementation of the Khronos Typed Array Specification  
// Ref: http://www.khronos.org/registry/typedarray/specs/latest/  
// Date: 2011-02-01  
//  
// Variations:  
// * Allows typed_array.get/set() as alias for subscripts (typed_array[])  

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Written by: Philip Hazel
Email local part: ph10
Email domain: cam.ac.uk

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'openssl/crypto/x509v3/v3_ssl.c',
'openssl/crypto/x509v3/v3_ssl.c',
'openssl/crypto/x509v3/v3_tlfs.c',
'openssl/crypto/x509v3/v3_utl.c',
'openssl/crypto/x509v3/v3_utils.c',
'openssl/engines/e_capi.c',
'openssl/engines/e_padlock.c',
],
'openssl_sources_BSD-x86': [
'./config/archs/BSD-x86/asm/crypto/aes/aesni-x86.s',
'./config/archs/BSD-x86/asm/crypto/aes/vpaes-x86.s',
'./config/archs/BSD-x86/asm/crypto/bf/bf-586.s',
'./config/archs/BSD-x86/asm/crypto/bn/bn-586.s',
'./config/archs/BSD-x86/asm/crypto/bn/co-586.s',
'./config/archs/BSD-x86/asm/crypto/bn/x86-gf2m.s',
'./config/archs/BSD-x86/asm/crypto/bn/x86-mont.s',
]
'/config/archs/BSD-x86/asm/crypto/camellia/cml1l-x86.s',
'/config/archs/BSD-x86/asm/crypto/chacha/chacha-x86.s',
'/config/archs/BSD-x86/asm/crypto/des/crypt586.s',
'/config/archs/BSD-x86/asm/crypto/des/des-586.s',
'/config/archs/BSD-x86/asm/crypto/ec/ecp_nistz256-x86.s',
'/config/archs/BSD-x86/asm/crypto/md5/md5-586.s',
'/config/archs/BSD-x86/asm/crypto/modes/ghash-x86.s',
'/config/archs/BSD-x86/asm/crypto/poly1305/poly1305-x86.s',
'/config/archs/BSD-x86/asm/crypto/rc4/rc4-586.s',
'/config/archs/BSD-x86/asm/crypto/ripemd/rmd-586.s',
'/config/archs/BSD-x86/asm/crypto/sha/sha1-586.s',
'/config/archs/BSD-x86/asm/crypto/sha/sha256-586.s',
'/config/archs/BSD-x86/asm/crypto/sha/sha512-586.s',
'/config/archs/BSD-x86/asm/crypto/whirlpool/wp-mmx.s',
'/config/archs/BSD-x86/asm/crypto/x86cpuid.s',
'/config/archs/BSD-x86/asm/engines/e_padlock-x86.s',
]

'openssl_defines_BSD-x86': [
  'NDEBUG',
  'L_ENDIAN',
  'OPENSSL_CNIC',
  'OPENSSL_CPUL_OBJ',
  'OPENSSL_BN_ASM_PART_WORDS',
  'OPENSSL_IA32_SSE2',
  'OPENSSL_BN_ASM_MONT',
  'OPENSSL_BN_ASM_GF2m',
  'SHA1_ASM',
  'SHA256_ASM',
  'SHA512_ASM',
  'RC4_ASM',
  'MD5_ASM',
  'RMD160_ASM',
  'AESNI_ASM',
  'VPAES_ASM',
  'WHIRLPOOL_ASM',
  'GHASH_ASM',
  'ECP_NISTZ256_ASM',
  'POLY1305_ASM',
],

'openssl_cflags_BSD-x86': [
  '-Wa,-noexecstack',
  '-Wall -O3 -fomit-frame-pointer',
  '-pthread',
  '-Wall -O3 -fomit-frame-pointer',
],

'openssl_ex_libs_BSD-x86': [
  '-pthread',
],

'openssl_libs_BSD-x86': [
  ],

'openssl_libs_BSD-x86': [
  ],

'openssl_libs_BSD-x86': [
  ],

'openssl_libs_BSD-x86': [
  ]
]
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## Installation

[Download JSON directly](https://raw.githubusercontent.com/shinnn/spdx-license-ids/master/index.json), or [use](https://docs.npmjs.com/cli/install) [npm](https://docs.npmjs.com/about-npm):

```javascript
const ids = require('spdx-license-ids');
//=> ['0BSD', 'AAL', 'ADSL', 'AFL-1.1', 'AFL-1.2', 'AFL-2.0', 'AFL-2.1', 'AFL-3.0', 'AGPL-1.0-only', ...]
ids.includes('BSD-3-Clause'); //=> true
```
ids.includes('CC-BY-1.0'); //=> true

ids.includes('GPL-3.0'); //=> false

### require('spdx-license-ids/deprecated')

Type: `string[]`

Deprecated license IDs.

```javascript
const deprecatedIds = require('spdx-license-ids/deprecated');
//=> ['AGPL-1.0', 'AGPL-3.0', 'GFDL-1.1', 'GFDL-1.2', 'GFDL-1.3', 'GPL-1.0', 'GPL-2.0', ...]

deprecatedIds.includes('BSD-3-Clause'); //=> false
deprecatedIds.includes('CC-BY-1.0'); //=> false

deprecatedIds.includes('GPL-3.0'); //=> true
```

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name: license update
on:
schedule:
  # 00:00:00 every Monday
  # https://crontab.guru/#0_0_*_*_1
  - cron: "0 0 * * 1"
workflow_dispatch:

jobs:
  update_routes:
    runs-on: ubuntu-latest
    steps:
    - uses: actions/checkout@v2
    - run: "./tools/license-builder.sh"  # run the license builder tool
    - uses: gr2m/create-or-update-pull-request-action@v1.x  # create a PR or update the Action's existing PR
    env:
      GITHUB_TOKEN: ${{ secrets.GITHUB_TOKEN }}
    with:
      title: "doc: run license-builder"
      body: "License is likely out of date. This is an automatically generated PR by the `license-builder.yml` GitHub Action, which runs `license-builder.sh` and submits a new PR or updates an existing PR."
      commit-message: 'doc: run license-builder'
      labels: meta

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A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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<thead>
<tr>
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0.9.0 thru 1.2  1991-1995  CWI      yes
1.3 thru 1.5.2  1.2  1995-1999  CNRI    yes
1.6         1.5.2       2000  CNRI    no
2.0         1.6       2000  BeOpen.com   no
1.6.1       1.6       2001  CNRI    yes (2)
2.1     2.0+1.6.1  2001  PSF      no
2.0.1     2.0+1.6.1  2001  PSF      yes
2.1.1   2.1+2.0.1   2001  PSF      yes
2.1.2   2.1.1  2002  PSF      yes
2.1.3   2.1.2  2002  PSF      yes
2.2 and above  2.1.1  2001-now  PSF    yes

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wcwidth.js: JavaScript Portng of Markus Kuhn's wcwidth() Implementation
=======================================================================
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This package is a JavaScript porting of `wcwidth()` implementation
[by Markus Kuhn](http://www.cl.cam.ac.uk/~mgk25/ucs/wcwidth.c).

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Tests included here are based on build output generated by the six-speed benchmark suite.

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'openssl/crypto/poly1305/poly1305_pmeth.c',
'openssl/crypto/rand/drbg_ctr.c',
'openssl/crypto/rand/drbg_lib.c',
'openssl/crypto/rand/rand_egd.c',
'openssl/crypto/rand/rand_err.c',
'openssl/crypto/rand/rand_lib.c',
'openssl/crypto/rand/rand_unix.c',
'openssl/crypto/rand/rand_vms.c',
'openssl/crypto/rand/rand_win.c',
'openssl/crypto/rand/randfile.c',
'openssl/crypto/rc2/rc2_cbc.c',
'openssl/crypto/rc2/rc2_ecb.c',
'openssl/crypto/rc2/rc2_skey.c',
'openssl/crypto/rc2/rc2ofb64.c',
'openssl/crypto/ripemd/rmd_dgst.c',
'openssl/crypto/ripemd/rmd_one.c',
'openssl/crypto/rsa/rsa_ameth.c',
'openssl/crypto/rsa/rsa_asn1.c',
'openssl/crypto/rsa/rsa_chk.c',
},

'openssl_sources_BSD-x86_64': [  
    './config/archs/BD-x86_64/asm_avx/2crypto/aesni-mb-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/aesni-sha1-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/aesni-sha256-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/aesni-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/cryptovpaes-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/cryptobnrsaz-avx2.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/cryptobnrsaz-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/cryptobnx64-64mont.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/cryptobnx64-64mont5.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/cryptocamellia/cmxll-x86_64.s',  
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    './config/archs/BD-x86_64/asm_avx/2crypto/cryptopoly1305/poly1305-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/crecrc4-md5-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/crecrc4-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/sha/sha1-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/sha/sha1-mb-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/sha/sha256-md8-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/sha/sha256-segmented-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/sha/wrirlpool/wp-x86_64.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/x86_64cpuid.s',  
    './config/archs/BD-x86_64/asm_avx/2crypto/x86_64secds.s',  
    ],

'openssl_defines_BSD-x86_64': [  
    'NDEBUG',  
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    'OPENSSL_PIC',  
    'OPENSSL_CPUID_OBJ',  
    'OPENSSL_IA32_SSE2',  
    'OPENSSL_BN_ASM_MONT',  
    'OPENSSL_BN_ASM_MONT5',  
    'OPENSSL_BN_ASM_GF2m',  
    'SHA1_ASM',  
    'SHA256_ASM',  
    'SHA512_ASM',  
    'KECCAK1600_ASM',  
    'RC4_ASM',  
    'MD5_ASM',  
    'AESNI_ASM',  
    ]
'VPAES_ASM',
'GHASH_ASM',
'ECP_NISTZ256_ASM',
'X25519_ASM',
'POLY1305_ASM',
],
'openssl_cflags_BSD-x86_64': [
'-Wa,-noexecstack',
'-Wall -O3',
'-pthread',
'-Wall -O3',
],
'openssl_ex_libs_BSD-x86_64': [
'-pthread',
],
'include_dirs': [
'./include',
'./crypto',
'./crypto/include/internal',
],
'defines': ['<@(openssl_defines_BSD-x86_64)]],
'cflags' : ['<@(openssl_cflags_BSD-x86_64)]],
'libraries': ['<@(openssl_ex_libs_BSD-x86_64)]],
'sources': ['<@(openssl_sources), '<@(openssl_sources_BSD-x86_64)]],
}
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    './config/archs/BSD-x86_64/asm/crypto/vpaes-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/bn/rsa-avx2.s',
    './config/archs/BSD-x86_64/asm/crypto/bn/rsa-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/camellia/cmll-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/chacha/chacha-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/ec/ecp_nistp256-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/ec/ecp_nistp256-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/md5/md5-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/modes/aesni-gcm-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/modes/ghash-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/poly1305/poly1305-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/rc4/rc4-md5-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/sha/keccak1600-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/sha/sha1-md5-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/sha/sha1-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/sha/sha256-md5-x86_64.s',
    './config/archs/BSD-x86_64/asm/crypto/sha/sha256-x86_64.s',
]
./config/archs/BSD-x86_64/asm/crypto/sha/sha512-x86_64.s',
'./config/archs/BSD-x86_64/asm/crypto/whirlpool/wp-x86_64.s',
'./config/archs/BSD-x86_64/asm/crypto/x86_64cpuid.s',
'./config/archs/BSD-x86_64/asm/engines/e_padlock-x86_64.s',
],
'openssl_defines_BSD-x86_64': [
'NDEBUG',
'L_ENDIAN',
'OPENSSLPIC',
'OPENSSL_CPUD_OBJ',
'OPENSSL_IA32_SSE2',
'OPENSSL_BN_ASM_MONT',
'OPENSSL_BN_ASM_MONT5',
'OPENSSL_BN_ASM_GF2m',
'SHA1_ASM',
'SHA256_ASM',
'SHA512_ASM',
'KECCAK1600_ASM',
'RC4_ASM',
'MD5_ASM',
'AESNI_ASM',
'VPAES_ASM',
'GHASH_ASM',
'ECPPINSTZ256_ASM',
'X25519_ASM',
'POLY1305_ASM',
],
'openssl_cflags_BSD-x86_64': [
'-Wa,--noexecstack',
'-Wall -O3',
'-pthread',
'-Wall -O3',
],
'openssl_ex_libs_BSD-x86_64': [
'-pthread',
],
'include_dirs': [
'./',
'./include',
'./crypto',
'./crypto/include/internal',
],
'defines': ['<@opensslDefines_BSD-x86_64>'],
'cflags': ['<@opensslCflags_BSD-x86_64>'],
'libraries': ['<@opensslLibs_BSD-x86_64>'],
'sources': ['<@opensslSources_BSD-x86_64>'],
}
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```javascript
validate-npm-package-license
```

Give me a string and I'll tell you if it's a valid npm package license string.

```javascript
var valid = require('validate-npm-package-license');
```

SPDX license identifiers are valid license strings:

```javascript
var assert = require('assert');
var validSPDXExpression = {
  validForNewPackages: true,
  validForOldPackages: true,
  spdx: true
};

assert.deepEqual(valid('MIT'), validSPDXExpression);
assert.deepEqual(valid('BSD-2-Clause'), validSPDXExpression);
assert.deepEqual(valid('Apache-2.0'), validSPDXExpression);
assert.deepEqual(valid('ISC'), validSPDXExpression);
```

The function will return a warning and suggestion for nearly-correct license identifiers:
```javascript
assert.deepEqual(
valid('Apache 2.0'),
{
  validForOldPackages: false,
  validForNewPackages: false,
  warnings: [ 
    'license should be ' + 
    'a valid SPDX license expression (without "LicenseRef"), ' + 
    '"UNLICENSED", or ' + 
    '"SEE LICENSE IN <filename>"'.
    'license is similar to the valid expression "Apache-2.0"'
  ]
};
```

SPDX expressions are valid, too ...

```javascript
// Simple SPDX license expression for dual licensing
assert.deepEqual(
valid('(GPL-3.0-only OR BSD-2-Clause)'),
validSPDXExpression
);
```

... except if they contain `LicenseRef`

```javascript
var warningAboutLicenseRef = {
  validForOldPackages: false,
  validForNewPackages: false,
  spdx: true,
  warnings: [ 
    'license should be ' + 
    'a valid SPDX license expression (without "LicenseRef"), ' + 
    '"UNLICENSED", or ' + 
    '"SEE LICENSE IN <filename>"'.
  ];
};

assert.deepEqual(
valid('LicenseRef-Made-Up'),
warningAboutLicenseRef
);
```
If you can't describe your licensing terms with standardized SPDX identifiers, put the terms in a file in the package and point users there:

```javascript
```javascript
assert.deepEqual(
valid('SEE LICENSE IN LICENSE.txt'),
{
  validForNewPackages: true,
  validForOldPackages: true,
  inFile: 'LICENSE.txt'
}
);
```javascript

assert.deepEqual(
valid('SEE LICENSE IN license.md'),
{
  validForNewPackages: true,
  validForOldPackages: true,
  inFile: 'license.md'
}
);
```javascript

If there aren't any licensing terms, use `UNLICENSED`:

```javascript
```javascript
var unlicensed = {
  validForNewPackages: true,
  validForOldPackages: true,
  unlicensed: true
};
assert.deepEqual(valid('UNLICENSED'), unlicensed);
assert.deepEqual(valid('UNLICENCED'), unlicensed);
```javascript

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Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
link a "work that uses the Library" with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
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of these things:

a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
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d) If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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1.237 fsnotify v1.4.7

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1.238 libfile-basedir-perl 0.08-1

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File-BaseDir

============

This module can be used to find directories and files as specified by the Freedekstop.org Base Directory Specification.

INSTALLATION
To install this module type the following:

    perl Build.PL
    ./Build
    ./Build test
    ./Build install

DEPENDENCIES

This module requires these other modules which can be obtained from the CPAN <http://cpan.org> if they are not allready installed on your system:

Module::Build
Carp
Exporter
File::Spec

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---
abstract: 'Use the Freedesktop.org base directory specification'
author:
  - 'Jaap Karssenberg <pardus@cpan.org>'
build_requires:
  File::Temp: '0'
  File::Which: '0'
  Module::Build: '0.24'
  Test::More: '0'
configure_requires:
  Module::Build: '0.42'
dynamic_config: 1
generated_by: 'Module::Build version 0.4208, CPAN::Meta::Converter version 2.150005'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: '1.4'
name: File-BaseDir
provides:
File::BaseDir:
    file: lib/File/BaseDir.pm
    version: '0.08'
File::IconTheme:
    file: lib/File/IconTheme.pm
    version: '0.08'
File::UserDirs:
    file: lib/File/UserDirs.pm
    version: '0.08'
requires:
    Carp: '0'
    Exporter: '0'
    File::Spec: '0'
    IPC::System::Simple: '0'
resources:
    license: http://dev.perl.org/licenses/
    version: '0.08'
x_serialization_backend: 'CPAN::Meta::YAML version 0.016'

Found in path(s):
* /opt/cola/permits/1144894910_1616101382.85/0/libfile-basedir-perl-0-08-orig-1-tar-gz/File-BaseDir-0.08/META.yml
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=head1 NAME

File::BaseDir - Use the Freedesktop.org base directory specification

=head1 SYNOPSIS

use File::BaseDir qw/xdg_data_files/;
for ( xdg_data_files('mime/globs') ) {
    # do something
}

=head1 DESCRIPTION

This module can be used to find directories and files as specified by the Freedesktop.org Base Directory Specification. This specifications gives a mechanism to locate directories for configuration, application data and cache data. It is suggested that desktop applications for e.g. the Gnome, KDE or Xfce platforms follow this layout. However, the same layout can just as well be used for non-GUI applications.

This module forked from L<File::MimeInfo>.

This module follows version 0.6 of BaseDir specification.
=head1 EXPORT

None by default, but all methods can be exported on demand. Also the groups ":lookup" and ":vars" are defined. The ":vars" group contains all routines with a "xdg_" prefix; the ":lookup" group contains the routines to locate files and directories.

=head1 METHODS

=over 4

=item C<new()> Simple constructor to allow Object Oriented use of this module.

=back

=head2 Lookup

The following methods are used to lookup files and folders in one of the search paths.

=over 4

=item C<data_home(@PATH)> Takes a list of file path elements and returns a new path by appending them to the data home directory. The new path does not need to exist. Use this when writing user specific application data.

Example:

# data_home is: /home/USER/.local/share
$path = $bd->data_home('Foo', 'Bar', 'Baz');
# returns: /home/USER/.local/share/Foo/Bar/Baz

=item C<data_dirs(@PATH)> Looks for directories specified by C<@PATH> in the data home and other data directories. Returns (possibly empty) list of readable directories. In scalar context only the first directory found is returned. Use this to lookup application data.

=item C<data_files(@PATH)> Looks for files specified by C<@PATH> in the data home and other data directories. Only returns files that are readable. In scalar context only
the first file found is returned. Use this to lookup application data.

=item C<config_home(@PATH)>

Takes a list of path elements and appends them to the config home directory returning a new path. The new path does not need to exist. Use this when writing user specific configuration.

=item C<config_dirs(@PATH)>

Looks for directories specified by C<@PATH> in the config home and other config directories. Returns (possibly empty) list of readable directories. In scalar context only the first directory found is returned. Use this to lookup configuration.

=item C<config_files(@PATH)>

Looks for files specified by C<@PATH> in the config home and other config directories. Returns a (possibly empty) list of files that are readable. In scalar context only the first file found is returned. Use this to lookup configuration.

=item C<cache_home(@PATH)>

Takes a list of path elements and appends them to the cache home directory returning a new path. The new path does not need to exist.

=back

=head2 Variables

The following methods only returns the value of one of the XDG variables.

=over 4

=item C<xdg_data_home>

Returns either C<$ENV[XDG_DATA_HOME]> or it's default value. Default is F<$HOME/local/share>.

=item C<xdg_data_dirs>

Returns either C<$ENV[XDG_DATA_DIRS]> or it's default value as list. Default is F</usr/local/share>, F</usr/share>.

=item C<xdg_config_home>

Returns either C<$ENV[XDG_CONFIG_HOME]> or it's default value.
Default is \$HOME/.config.

=item C<xdg_config_dirs>

Returns either \$ENV{XDG_CONFIG_DIRS} or it's default value as list.
Default is \$/etc/xdg.

=item C<xdg_cache_home>

Returns either \$ENV{XDG_CACHE_HOME} or it's default value.
Default is \$HOME/.cache.

=head1 NON-UNIX PLATFORMS

The use of L<File::Spec> ensures that all paths are returned in the appropriate
form for the current platform. On Windows this module will try to set C<$HOME>
to a sensible value if it is not defined yet. On other platforms one can use
e.g. L<File::HomeDir> to set $HOME before loading File::BaseDir.

Please note that the specification is targeting Unix platforms only and
will only have limited relevance on other platforms. Any platform dependent
behavior in this module should be considered an extension of the spec.

=head1 BACKWARDS COMPATIBILITY

The methods C<xdg_data_files()> and C<xdg_config_files()> are exported for
backwards compatibility with version 0.02. They are identical to C<data_files()>
and C<config_files()> respectively but without the C<wantarray> behavior.

=head1 BUGS

Please mail the author if you encounter any bugs.

=head1 AUTHOR

Jaap Karssenberg \| Pardus [Larus] E<lt>pardus@cpan.orgE<gt>

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Currently being maintained by Kim Ryan

=head1 SEE ALSO

L<http://www.freedesktop.org/wiki/Specifications/basedir-spec>
1.239 libidn 2.2.0 2

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1.241 go-toml v1.2.0

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included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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1.252 wayland 1.18.0-1

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1.253 libhttp-negotiate-perl 6.01-1

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=head1 NAME

HTTP::Negotiate - choose a variant to serve
SYNOPSIS

use HTTP::Negotiate qw(choose);

# ID QS Content-Type Encoding Char-Set Lang Size
$variants =
[[\'var1\', 1.000, \'text/html\', undef, \'iso-8859-1\', \'en\', 3000],
 ['var2', 0.950, \'text/plain\', \'gzip\', \'us-ascii\', \'no\', 400],
 ['var3', 0.3, \'image/gif\', undef, undef, undef, 43555], ];

@preferred = choose($variants, $request_headers);
$the_one   = choose($variants);

DESCRIPTION

This module provides a complete implementation of the HTTP content
negotiation algorithm specified in F<draft-ietf-http-v11-spec-00.ps>
chapter 12. Content negotiation allows for the selection of a
preferred content representation based upon attributes of the
negotiable variants and the value of the various Accept* header fields
in the request.

The variants are ordered by preference by calling the function
choose().

The first parameter is reference to an array of the variants to
choose among.
Each element in this array is an array with the values [Sid, $qs,
$content_type, $content_encoding, $charset, $content_language,
$content_length] whose meanings are described
below. The $content_encoding and $content_language can be either a
single scalar value or an array reference if there are several values.

The second optional parameter is either a HTTP::Headers or a HTTP::Request
object which is searched for "Accept*" headers. If this
parameter is missing, then the accept specification is initialized
from the CGI environment variables HTTP_ACCEPT, HTTP_ACCEPT_CHARSET,
HTTP_ACCEPT_ENCODING and HTTP_ACCEPT_LANGUAGE.

In an array context, choose() returns a list of [variant
identifier, calculated quality, size] tuples. The values are sorted by
quality, highest quality first. If the calculated quality is the same
for two variants, then they are sorted by size (smallest first). I<E.g.>:

([\'var1\', 1, 2000], [\'var2\', 0.3, 512], [\'var3\', 0.3, 1024]);

Note that also zero quality variants are included in the return list
even if these should never be served to the client.

In a scalar context, it returns the identifier of the variant with the highest score or C<undef> if none have non-zero quality.

If the $HTTP::Negotiate::DEBUG variable is set to TRUE, then a lot of noise is generated on STDOUT during evaluation of choose().

=head1 VARIANTS

A variant is described by a list of the following values. If the attribute does not make sense or is unknown for a variant, then use C<undef> instead.

=over 3

=item identifier

This is a string that you use as the name for the variant. This identifier for the preferred variants returned by choose().

=item qs

This is a number between 0.000 and 1.000 that describes the "source quality". This is what F<draft-ietf-http-v11-spec-00.ps> says about this value:

Source quality is measured by the content provider as representing the amount of degradation from the original source. For example, a picture in JPEG form would have a lower qs when translated to the XBM format, and much lower qs when translated to an ASCII-art representation. Note, however, that this is a function of the source - an original piece of ASCII-art may degrade in quality if it is captured in JPEG form. The qs values should be assigned to each variant by the content provider; if no qs value has been assigned, the default is generally "qs=1".

=item content-type

This is the media type of the variant. The media type does not include a charset attribute, but might contain other parameters. Examples are:

  text/html
  text/html;version=2.0
  text/plain
  image/gif
  image/jpg
This is one or more content encodings that has been applied to the variant. The content encoding is generally used as a modifier to the content media type. The most common content encodings are:

- gzip
- compress

This is the character set used when the variant contains text. The charset value should generally be C<undef> or one of these:

- us-ascii
- iso-8859-1 ...
- iso-8859-9
- iso-2022-jp
- iso-2022-jp-2
- iso-2022-kr
- unicode-1-1
- unicode-1-1-utf-7
- unicode-1-1-utf-8

This describes one or more languages that are used in the variant. Language is described like this in F<draft-ietf-http-v11-spec-00.ps>: A language is in this context a natural language spoken, written, or otherwise conveyed by human beings for communication of information to other human beings. Computer languages are explicitly excluded.

The language tags are defined by RFC 3066. Examples are:

- no Norwegian
- en International English
- en-US US English
- en-cockney

This is the number of bytes used to represent the content.
The following Accept* headers can be used for describing content preferences in a request (This description is an edited extract from F<draft-ietf-http-v11-spec-00.ps>):

=over 3

=item Accept

This header can be used to indicate a list of media ranges which are acceptable as a response to the request. The "*" character is used to group media types into ranges, with "*/" indicating all media types and "type/*" indicating all subtypes of that type.

The parameter q is used to indicate the quality factor, which represents the user's preference for that range of media types. The parameter mbx gives the maximum acceptable size of the response content. The default values are: q=1 and mbx=Infinity. If no Accept header is present, then the client accepts all media types with q=1.

For example:

Accept: audio/*;q=0.2;mbx=200000, audio/basic

would mean: "I prefer audio/basic (of any size), but send me any audio type if it is the best available after an 80% mark-down in quality and its size is less than 200000 bytes"

=item Accept-Charset

Used to indicate what character sets are acceptable for the response. The "us-ascii" character set is assumed to be acceptable for all user agents. If no Accept-Charset field is given, the default is that any charset is acceptable. Example:

Accept-Charset: iso-8859-1, unicode-1-1

=item Accept-Encoding

Restricts the Content-Encoding values which are acceptable in the response. If no Accept-Encoding field is present, the server may assume that the client will accept any content encoding. An empty Accept-Encoding means that no content encoding is acceptable. Example:

Accept-Encoding: compress, gzip
This field is similar to Accept, but restricts the set of natural languages that are preferred in a response. Each language may be given an associated quality value which represents an estimate of the user's comprehension of that language. For example:

Accept-Language: no, en-gb;q=0.8, de;q=0.55

would mean: "I prefer Norwegian, but will accept British English (with 80% comprehension) or German (with 55% comprehension)."
DESCRIPTION
This module provides a complete implementation of the HTTP content negotiation algorithm specified in draft-ietf-http-v11-spec-00.ps chapter 12. Content negotiation allows for the selection of a preferred content representation based upon attributes of the negotiable variants and the value of the various Accept* header fields in the request.

The variants are ordered by preference by calling the function choose() 

The first parameter is reference to an array of the variants to choose among. Each element in this array is an array with the values [$id, $qs, $content_type, $content_encoding, $charset, $content_language, $content_length] whose meanings are described below. The $content_encoding and $content_language can be either a single scalar value or an array reference if there are several values.

The second optional parameter is either a HTTP::Headers or a HTTP::Request object which is searched for "Accept*" headers. If this parameter is missing, then the accept specification is initialized from the CGI environment variables HTTP_ACCEPT, HTTP_ACCEPT_CHARSET, HTTP_ACCEPT_ENCODING and HTTP_ACCEPT_LANGUAGE.

In an array context, choose() returns a list of [variant identifier, calculated quality, size] tuples. The values are sorted by quality, highest quality first. If the calculated quality is the same for two variants, then they are sorted by size (smallest first). *E.g.*:

(["var1", 1, 2000], ["var2", 0.3, 512], ["var3", 0.3, 1024]);

Note that also zero quality variants are included in the return list even if these should never be served to the client.

In a scalar context, it returns the identifier of the variant with the highest score or `undef' if none have non-zero quality.

If the $HTTP::Negotiate::DEBUG variable is set to TRUE, then a lot of noise is generated on STDOUT during evaluation of choose().

VARIANTS
A variant is described by a list of the following values. If the
attribute does not make sense or is unknown for a variant, then use
`undef' instead.

identifier
This is a string that you use as the name for the variant. This
identifier for the preferred variants returned by choose().

qs This is a number between 0.000 and 1.000 that describes the "source
quality". This is what draft-ietf-http-v11-spec-00.ps says about this
value:

Source quality is measured by the content provider as representing
the amount of degradation from the original source. For example, a
picture in JPEG form would have a lower qs when translated to the XBM
format, and much lower qs when translated to an ASCII-art
representation. Note, however, that this is a function of the source
- an original piece of ASCII-art may degrade in quality if it is
captured in JPEG form. The qs values should be assigned to each
variant by the content provider; if no qs value has been assigned,
the default is generally "qs=1".

count-type
This is the media type of the variant. The media type does not
include a charset attribute, but might contain other parameters.
Examples are:

    text/html
    text/html;version=2.0
    text/plain
    image/gif
    image/jpg

count-encoding
This is one or more content encodings that has been applied to the
variant. The content encoding is generally used as a modifier to the
content media type. The most common content encodings are:

    gzip
    compress

count-charset
This is the character set used when the variant contains text. The
charset value should generally be `undef' or one of these:

    us-ascii
    iso-8859-1 ... iso-8859-9
    iso-2022-jp
    iso-2022-jp-2
content-language
This describes one or more languages that are used in the variant. Language is described like this in draft-ietf-http-v11-spec-00.ps: A language is in this context a natural language spoken, written, or otherwise conveyed by human beings for communication of information to other human beings. Computer languages are explicitly excluded.

The language tags are defined by RFC 3066. Examples are:

no Norwegian
en International English
en-US US English
en-cockney

content-length
This is the number of bytes used to represent the content.

ACCEPT HEADERS
The following Accept* headers can be used for describing content preferences in a request (This description is an edited extract from draft-ietf-http-v11-spec-00.ps):

Accept
This header can be used to indicate a list of media ranges which are acceptable as a response to the request. The "*" character is used to group media types into ranges, with "*/*" indicating all media types and "type/*" indicating all subtypes of that type.

The parameter q is used to indicate the quality factor, which represents the user's preference for that range of media types. The parameter mbx gives the maximum acceptable size of the response content. The default values are: q=1 and mbx=infinity. If no Accept header is present, then the client accepts all media types with q=1.

For example:

Accept: audio/*, q=0.2, mbx=200000, audio/basic

would mean: "I prefer audio/basic (of any size), but send me any audio type if it is the best available after an 80% mark-down in quality and its size is less than 200000 bytes"

Accept-Charset
Used to indicate what character sets are acceptable for the response. The "us-ascii" character set is assumed to be acceptable for all user agents. If no Accept-Charset field is given, the default is that any charset is acceptable. Example:

```
Accept-Charset: iso-8859-1, unicode-1-1
```

Accept-Encoding
Restricts the Content-Encoding values which are acceptable in the response. If no Accept-Encoding field is present, the server may assume that the client will accept any content encoding. An empty Accept-Encoding means that no content encoding is acceptable. Example:

```
Accept-Encoding: compress, gzip
```

Accept-Language
This field is similar to Accept, but restricts the set of natural languages that are preferred in a response. Each language may be given an associated quality value which represents an estimate of the user's comprehension of that language. For example:

```
Accept-Language: no, en-gb;q=0.8, de;q=0.55
```

would mean: "I prefer Norwegian, but will accept British English (with 80% comprehension) or German (with 55% comprehension).

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That's all there is to it!
Here is a list of the main contributors to lm-sensors version 3.

* Frodo Looijaard
  Original author of libsensors, sensors-detect, sensors and isadump.
* Merlin Hughes
  Original author of sensord.
* Bob Schlaermann
  Dynamic chip feature detection (a.k.a. generic chip support) in 
  libsensors and sensors.
* Mark M. Hoffman
  Many improvements to the libsensors configuration file scanner.
* Jean Delvare
  New libsensors API, and migration of sensors and sensord thereto.
  Many optimizations in libsensors and sensors.
  Configuration file converter.
  Rewrite of sensors-detect.
  Support for multiple configuration files in libsensors.

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK:       // %bb.0:
; CHECK-NEXT:    ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT:    mov v0.d[1], v0.d[0]
; CHECK-NEXT:    ret
%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}

; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right register classes.

define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
%L34 = load i8, i8* %0
%Cmp56 = icmp sgt i8 undef, %L34
br label %CF246

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; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
; CHECK-LABEL: test_sitofp_fixed:
     ; First, extend each i32 to i64
     ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
     ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
     ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
     ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
     ; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
     ; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
     ; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
     ; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0

     ; Next, convert each to double.

; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]

; CHECK: ret
%flt = sitofp <16 x i32> %in to <16 x double>
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>  
ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}

; NOTE: Assertions have been autogenerated by utils/update_llvm_test_checks.py
; RUN: llc -vs -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking FileCheck %s
; D31946
; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)
define fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK:     # %bb.0:
; CHECK-NEXT: pushq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: subq $32, %rsp
; CHECK-NEXT: .cfi_offset %rbx, -16
; CHECK-NEXT: movaps %xmm1, {{{[-0-9]+}(%[sb]p)}} # 16-byte Spill
define fp128 @TestFabs(fp128 %a) {
    ; CHECK-LABEL: TestFabs:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    andps {{.*}}(%rip), %xmm0
    ; CHECK-NEXT:    retq
    %res = call fp128 @llvm.fabs.f128(fp128 %a)
    ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
    ; CHECK-LABEL: TestCopysign:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    andps {{.*}}(%rip), %xmm0
    ; CHECK-NEXT:    orps %xmm1, %xmm0
    ; CHECK-NEXT:    retq
    %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
    ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
    ; CHECK-LABEL: TestFneg:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    pushq %rax
    ; CHECK-NEXT:    .cfi_def_cfa_offset 16
    ; CHECK-NEXT:    movaps %xmm0, %xmm1

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = {"min-legal-vector-width" = "512"}
; RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG:  %[[C1:const[0-9]??]] = bitcast i32 805874720 to i32
; CHECK-DAG:  %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK-DAG:  %[[C3:const[0-9]??]] = bitcast i32 805873720 to i32
; CHECK-DAG:  %[[C4:const[0-9]??]] = bitcast i32 805873688 to i32
; CHECK:  %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT:  %1 = load volatile i8, i8* %0
; CHECK-NEXT:  %[[M1:const_mat[0-9]??]] = add i32 %[[C2]], 4
; CHECK-NEXT:  %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT:  %3 = load volatile i8, i8* %2
; CHECK-NEXT:  %[[M2:const_mat[0-9]??]] = add i32 %[[C2]], 31
; CHECK-NEXT:  %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT:  %5 = load volatile i8, i8* %4
; CHECK-NEXT:  %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT:  %7 = load volatile i8, i8* %6
; CHECK-NEXT:  %[[M3:const_mat[0-9]??]] = add i32 %[[C1]], 7
; CHECK-NEXT:  %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT:  %9 = load volatile i8, i8* %8
; CHECK-NEXT:  %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT:  store i8 %9, i8* %10
; CHECK-NEXT:  %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT:  store i8 %7, i8* %11
; CHECK-NEXT:  %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT:  store i8 %5, i8* %12
; CHECK-NEXT:  %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT:  store i8 %3, i8* %13
; CHECK-NEXT:  %14 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT:  store i8* %14, i8** @goo

@goo = global i8* undef

define void @foo_i8() {
  entry:

%0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
%1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
%2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
%3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
%4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
store i8 %4, i8* inttoptr(i32 805873688 to i8*)
store i8 %3, i8* inttoptr(i32 805873719 to i8*)
store i8 %2, i8* inttoptr(i32 805873720 to i8*)
store i8 %1, i8* inttoptr(i32 805873727 to i8*)
store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG:  %[[C1:const[0-9]??]] = bitcast i32 805874752 to i32
; CHECK-DAG:  %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK:  %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT:  %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT:  %[[M1:const_mat[0-9]??]] = add i32 %[[C2]], 4
; CHECK-NEXT:  %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT:  %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT:  %[[M2:const_mat[0-9]??]] = add i32 %[[C2]], 32
; CHECK-NEXT:  %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT:  %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT:  %[[M3:const_mat[0-9]??]] = add i32 %[[C2]], 62
; CHECK-NEXT:  %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT:  %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT:  %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT:  %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT:  %[[M4:const_mat[0-9]??]] = add i32 %[[C1]], 22
; CHECK-NEXT:  %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT:  %11 = load volatile i16, i16* %10, align 2

define void @foo_i16() {
  entry:
  %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
  %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
  %2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
  %3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
  %4 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
  ret void
}

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG:  %[[C1:const[0-9]??]] = bitcast i32 805874816 to i32
define void @foo_i32() {
    entry:
    %0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
    %1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
    %2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
    %3 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
    %4 = load volatile i32, i32* inttoptr (i32 805874848 to i32*), align 4
    ret void
}

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; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
   ; CHECK-LABEL: define {{\[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
   ; CHECK-SAME: (<8 x i64>* \[\[ARG:%.*\]\], <8 x i64> \[\[ARG1_VAL:%.*\]\])
   ; CHECK-NEXT: bb:
   ; CHECK-NEXT: store <8 x i64> \[\[ARG1_VAL\]\], <8 x i64>* \[\[ARG\]\]
   ; CHECK-NEXT: ret void
   ;
   bb:
   %tmp = load <8 x i64>, <8 x i64>* %arg1
   store <8 x i64> %tmp, <8 x i64>* %arg
   ret void
}
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
   ; CHECK-LABEL: define {{\[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
   ; CHECK-SAME: (<8 x i64>* \[\[ARG:%.*\]\])
   ; CHECK-NEXT: bb:

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    %tmp = load <8 x i64>, <8 x i64>* %arg
  ; CHECK-NEXT:    %tmp = load <8 x i64>, <8 x i64>* %arg1
  ; CHECK-NEXT:    store <8 x i64> %tmp, <8 x i64>* %arg
  ; CHECK-NEXT:    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    %tmp = load <8 x i64>, <8 x i64>* %arg
  ; CHECK-NEXT:    store <8 x i64> %tmp, <8 x i64>* %arg
  ; CHECK-NEXT:    ret void
}
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> %arg1, <8 x i64>* %arg
  ; CHECK-NEXT: ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  ; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
  ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT: ret void
}

define void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp) {
  ; CHECK-NEXT: %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1) #1 {
  ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> %arg1, <8 x i64>* %arg
  ; CHECK-NEXT: ret void
}

%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT: ret void
  ;
  bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
  ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  ; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
  ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT: ret void
  ;
  bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should not promote

define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ; CHECK-LABEL: define ([^@]+) @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
  ; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT: ret void

  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
  ; CHECK-LABEL: define ([^@]+) @avx512_legal256_prefer256_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP2]] to i8*
  ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP]])
  ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT: ret void

  bb:
  %tmp = alloc %8 x i64>, align 32
  %tmp2 = alloc %8 x i64>, align 32
  %tmp3 = bitcast %8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg,
  <8 x i64>* %arg1) #2 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
  ; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT: ret void
  
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512legal512_prefer256_call_avx512legal256_prefer256(<8 x i64>* %arg) #1 {
  ; CHECK-LABEL: define {{[^@]+}}@avx512legal512_prefer256_call_avx512legal256_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT: call fastcc void @callee_avx512legal512_prefer256_call_avx512legal256_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP]])
  ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT: ret void
  
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512legal512_prefer256_call_avx512legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
define internal fastcc void @callee_avx2legal256_prefer256_call_avx2legal512_prefer256(<8 x i64>* %arg,
  <8 x i64>* %arg1) #3 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx2legal256_prefer256_call_avx2legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg
        store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1) #4 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg1
        store <8 x i64> %tmp, <8 x i64>* %arg
        ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1) #4 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg1
        store <8 x i64> %tmp, <8 x i64>* %arg
        ret void
}
define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
    ; CHECK-LABEL: define ([[^@]+])@avx2_legal512_prefer256_call_avx2_legal256_prefer256
    ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
    ; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
    ; CHECK-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
    ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
    ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* %arg, align 2
    ; CHECK-NEXT: ret void

    bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}

; If the arguments are scalar, its ok to promote.
define i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
    ; CHECK-LABEL: define ([[^@]+])@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32 [[X:%.*]], i32 [[Y:%.*]])
    ; CHECK-NEXT: [[C:%.*]] = add i32 [[X]], [[Y]]
    ; CHECK-NEXT: ret i32 [[C]]
    ;
    %A = load i32, i32* %X
    %B = load i32, i32* %Y
    %C = add i32 %A, %B
    ret i32 %C
}

define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
    ; CHECK-LABEL: define ([[^@]+])@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32* [[B:%.*]])
    ; CHECK-NEXT: [[A:%.*]] = alloca i32
; CHECK-NEXT:     store i32 1, i32* [[A]]
; CHECK-NEXT:     [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:     [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:     [[C:%.*]] = call i32
@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:     ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A

%C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
ret i32 %C
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %X, i32* %Y) #2 {
; CHECK-LABEL: define
{[^@]+}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT:     [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT:     ret i32 [[C]]
;
%A = load i32, i32* %X
%B = load i32, i32* %Y

%C = add i32 %A, %B
ret i32 %C
}

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256
; CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT:     [[A:%.*]] = alloca i32
; CHECK-NEXT:     store i32 1, i32* [[A]]
; CHECK-NEXT:     [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT:     [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT:     [[C:%.*]] = call i32
@scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:     ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A

%C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
ret i32 %C
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5
attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for vpmaddwd+vpaddd
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=cascadelake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=cooperlake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=cannonlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=icelake-client | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=icelake-server | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

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This file primarily contains tests for specific places in X86ISelLowering.cpp that needed to be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

```c
define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: add256:
  ; CHECK:
  // %bb.0:
  ; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
  ; CHECK-NEXT:    vpadd 32(%rsi), %ymm1, %ymm1
  ; CHECK-NEXT:    vpadd (%rsi), %ymm0, %ymm0
  ; CHECK-NEXT:    vmovdqa %ymm0, (%rdx)
  ; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %d = load <16 x i32>, <16 x i32>* %a
  %e = load <16 x i32>, <16 x i32>* %b
  %f = add <16 x i32> %d, %e
  store <16 x i32> %f, <16 x i32>* %c
  ret void
}

define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: add512:
  ; CHECK:
  // %bb.0:
  ; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
  ; CHECK-NEXT:    vpadd %zmm0, %zmm0
  ; CHECK-NEXT:    vmovdqa64 %zmm0, (%rdx)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %d = load <16 x i32>, <16 x i32>* %a
  %e = load <16 x i32>, <16 x i32>* %b
  %f = add <16 x i32> %d, %e
  store <16 x i32> %f, <16 x i32>* %c
  ret void
}

define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: avg_v64i8_256:
  ; CHECK:
  // %bb.0:
  ; CHECK-NEXT:    vmovdqa (%rsi), %ymm0
  ; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm1
  ; CHECK-NEXT:    vpavgb (%rdi), %ymm0, %ymm0
  ; CHECK-NEXT:    vpavgb %ymm0, (%rax)
  ; CHECK-NEXT:    vmovdqu %ymm1, (%rax)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %d = load <64 x i8>, <64 x i8>* %a
  %e = load <64 x i8>, <64 x i8>* %b
  %f = add <64 x i8> %d, %e
  store <64 x i8> %f, <64 x i8>* %c
  ret void
}
```
define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width=512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK: 
; CHECK-NEXT: 
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nsw <64 x i32> %3, <64 x i32> %2
%6 = add nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, %3
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width=256" {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK: 
; CHECK-NEXT: 
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nsw <64 x i32> %3, <64 x i32> %2
%6 = add nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, %3
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}
define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, 32(%rdx)) "min-legal-vector-width="256" {
    ; CHECK-LABEL: pmaddwd_32_256:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
    ; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq

    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width="512" {
    ; CHECK-LABEL: pmaddwd_32_512:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpmaddwd (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq

    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}
define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: psubus_64i8_max_256:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %x = load <64 x i8>, <64 x i8>* %xptr
    %y = load <64 x i8>, <64 x i8>* %yptr
    %cmp = icmp ult <64 x i8> %x, %y
    %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
    %res = sub <64 x i8> %max, %y
    store <64 x i8> %res, <64 x i8>* %zptr
    ret void
}

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: psubus_64i8_max_512:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %x = load <64 x i8>, <64 x i8>* %xptr
    %y = load <64 x i8>, <64 x i8>* %yptr
    %cmp = icmp ult <64 x i8> %x, %y
    %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
    %res = sub <64 x i8> %max, %y
    store <64 x i8> %res, <64 x i8>* %zptr
    ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: _Z9test_charPcS_i_256:
    ; CHECK:    # %bb.0: # %entry
    ; CHECK-NEXT: movl %edx, %eax
    ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT: xorl %ecx, %ecx
    ; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: LBB8_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm4
; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT: vpmaddwd %ymm2, %ymm3, %ymm2
; CHECK-NEXT: vpmaddwd (%rsi,%rcx), %ymm3
; CHECK-NEXT: vpaddd %ymm4, %ymm3, %ymm3
; CHECK-NEXT: vpaddd %ymm1, %ymm3, %ymm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB8_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm1
; CHECK-NEXT: vpaddd %ymm0, %ymm2, %ymm0
; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpaddd %ymm0, %xmm0, %xmm0
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 unde
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> <i32 2, i32 3, i32 undef, i32 unde
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> <i32 1, i32 undef, i32 undef, i32 unde
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK: # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT:  .LBB9_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpadd %zmm1, %zmm2, %zmm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB9_1
}
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde}
undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef

%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 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%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi i64 [ 0, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx4 = add <16 x i32> %bin.rdx, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define i32 @sad_16i8_512() "min-legal-vector-width"="512" { ; CHECK-LABEL: sad_16i8_512:
  ; CHECK:   # %bb.0: # %entry
  ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
  ; CHECK-NEXT: .p2align 4, 0x90
  ; CHECK-NEXT: .LBB11_1: # %vector.body
  ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef,
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbto16f32_256:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vpmovw2m %ymm0, %k0
    ; CHECK-NEXT: kshiftrw $8, %k0, %k1
    ; CHECK-NEXT: vpmovm2d %k1, %ymm0
    ; CHECK-NEXT: vcvtdq2ps %ymm0, %ymm0
    ; CHECK-NEXT: vpmovm2d %k0, %ymm1
    ; CHECK-NEXT: vcvtdq2ps %ymm1, %ymm1
    ; CHECK-NEXT: vmovaps %ymm1, (%rdi)
    ; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f32_512:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vpmovw2m %zmm0, %k0
    ; CHECK-NEXT: kshiftrw $8, %k0, %k1
    ; CHECK-NEXT: vpmovm2d %k1, %zmm0
    ; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT: vpmovm2d %k0, %zmm1
    ; CHECK-NEXT: vcvtdq2ps %zmm1, %zmm1
    ; CHECK-NEXT: vmovaps %zmm1, (%rdi)
    ; CHECK-NEXT: vmovaps %zmm0, 32(%rdi)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {

 define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {  
    ; CHECK-LABEL: sbto16f64_512:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vpmovw2m %ymm0, %k0  
    ; CHECK-NEXT:  vpmovm2d %k0, %ymm0  
    ; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm1  
    ; CHECK-NEXT:  vextracti128 s1, %ymm0, %xmm0  
    ; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm0  
    ; CHECK-NEXT:  vpmovm2d %k0, %ymm2  
    ; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm3  
    ; CHECK-NEXT:  vextracti128 s1, %ymm2, %xmm2  
    ; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm2  
    ; CHECK-NEXT:  vmovaps %ymm2, 32(%rdi)  
    ; CHECK-NEXT:  vmovaps %ymm3, (%rdi)  
    ; CHECK-NEXT:  vmovaps %ymm0, 96(%rdi)  
    ; CHECK-NEXT:  vmovaps %ymm1, 64(%rdi)  
    ; CHECK-NEXT:  vzeroupper  
    ; CHECK-NEXT:  retq  
    %mask = icmp slt <16 x i16> %a, zeroinitializer  
    %1 = sitofp <16 x i1> %mask to <16 x double>  
    store <16 x double> %1, <16 x double>* %res  
    ret void  
}  

 define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: sbto16f32_256:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vpmovw2m %ymm0, %k0  
    ; CHECK-NEXT:  vpmovm2d %k0, %zmm0  
    ; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm1  
    ; CHECK-NEXT:  vextracti64x4 $1, %zmm0, %ymm0  
    ; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm0  
    ; CHECK-NEXT:  vmovaps %zmm0, 64(%rdi)  
    ; CHECK-NEXT:  vmovaps %zmm1, (%rdi)  
    ; CHECK-NEXT:  vzeroupper  
    ; CHECK-NEXT:  retq  
    %mask = icmp slt <16 x i16> %a, zeroinitializer  
    %1 = sitofp <16 x i1> %mask to <16 x double>  
    store <16 x double> %1, <16 x double>* %res  
    ret void  
}  

 define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: ubto16f32_256:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vpmovw2m %ymm0, %k0  
    ; CHECK-NEXT:  kshiftrw $8, %k0, %k1  
    ; CHECK-NEXT:  vpmovm2d %k1, %ymm0  

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}
define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
  # %bb.0:
  ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
  ; CHECK-NEXT:  vpmovm2d %k0, %zmm0
  ; CHECK-NEXT:  vpsrd $31, %zmm0, %zmm0
  ; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm1
  ; CHECK-NEXT:  vextracti64x4 $1, %zmm0, %ymm0
  ; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm0
  ; CHECK-NEXT:  vmovaps %zmm0, 64(%rdi)
  ; CHECK-NEXT:  vmovaps %zmm1, (%rdi)
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %mask = icmp slt <16 x i16> %a, zeroinitializer
  %1 = uitofp <16 x i1> %mask to <16 x double>
  store <16 x double> %1, <16 x double>* %res
  ret void
}

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
  # %bb.0:
  ; CHECK-NEXT:  vcvttps2dq (%rdi), %ymm1
  ; CHECK-NEXT:  vpsld $31, %ymm1, %ymm1
  ; CHECK-NEXT:  vpmovd2m %ymm1, %k0
  ; CHECK-NEXT:  vcvttps2dq 32(%rdi), %ymm1
  ; CHECK-NEXT:  vpsld $31, %ymm1, %ymm1
  ; CHECK-NEXT:  vpmovd2m %ymm1, %k1
  ; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
  ; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
  ; CHECK-NEXT:  retq
  %a = load <16 x float>, <16 x float>* %ptr
  %mask = fptoui <16 x float> %a to <16 x i1>
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
  ret <16 x i16> %select
}
define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
  ; CHECK-LABEL: test_16f32toub_512:  
  ; CHECK:      # %bb.0:  
  ; CHECK-NEXT:  
  vcvtps2dq (%rdi), %zmm1  
  vpslld $31, %zmm1, %zmm1  
  vmovd2m %zmm1, %k1  
  vpmovd2m %zmm0, %ymm0 [%k1] [z]  
  ; CHECK-NEXT: retq  
  %a = load <16 x float>, <16 x float>* %ptr  
  %mask = fptoui <16 x float> %a to <16 x i1>  
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
  ret <16 x i16> %select  
}

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {  
  ; CHECK-LABEL: test_16f32tosb_256:  
  ; CHECK:      # %bb.0:  
  ; CHECK-NEXT:  
  vcvtps2dq (%rdi), %ymm1  
  vpmovd2m %ymm1, %k0  
  vcvttps2dq 32(%rdi), %ymm1  
  vpmovd2m %ymm1, %k1  
  kunpckbw %k0, %k1, %k1  
  vmovdqu16 %ymm0, %ymm0 {%k1} [z]  
  ; CHECK-NEXT: retq  
  %a = load <16 x float>, <16 x float>* %ptr  
  %mask = fptosi <16 x float> %a to <16 x i1>  
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
  ret <16 x i16> %select  
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
  ; CHECK-LABEL: test_16f32tosb_512:  
  ; CHECK:      # %bb.0:  
  ; CHECK-NEXT:  
  vcvtps2dq (%rdi), %zmm1  
  vmovd2m %zmm1, %k1  
  vmovdqu16 %ymm0, %ymm0 {%k1} [z]  
  ; CHECK-NEXT: retq  
  %a = load <16 x float>, <16 x float>* %ptr  
  %mask = fptosi <16 x float> %a to <16 x i1>  
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
  ret <16 x i16> %select  
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {  
  ; CHECK-AVX512-LABEL: mul256:  

; CHECK-AVX512: # %bb.0:
; CHECK-AVX512-NEXT: vmovdq4 (%rdi), %ymm0
; CHECK-AVX512-NEXT: vmovdq4 32(%rdi), %ymm1
; CHECK-AVX512-NEXT: vmovdq4 (%rsi), %ymm2
; CHECK-AVX512-NEXT: vmovdq4 32(%rsi), %ymm3
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm4 = ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm5 = ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm4 = ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm5 = ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpmullw %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm4, %ymm4
; CHECK-AVX512-NEXT: vpand %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm4 = ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm5 = ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-AVX512-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpmullw %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm4, %ymm4
; CHECK-AVX512-NEXT: vpand %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm4 = ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} ymm5 = ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpmullw %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpand %ymm5, %ymm4, %ymm4
; CHECK-AVX512-NEXT: vpand %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vzeroupper
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: mul256:
; CHECK-VBMI: # %bb.0:
; CHECK-VBMI-NEXT: vmovdq4 (%rdi), %ymm0
; CHECK-VBMI-NEXT: vmovdq4 32(%rdi), %ymm1
; CHECK-VBMI-NEXT: vmovdq4 (%rsi), %ymm2
; CHECK-VBMI-NEXT: vmovdq4 32(%rsi), %ymm3
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm4 = ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} ymm5 = ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
zmm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,39,39,39,39,39,39,48,48,49,49,49,50,50,50,50,51,51,51,51,52,52,52,52,52,53,53,54,54,55,55]
; CHECK-AVX512-NEXT:  vpunpcklbw {[.*#]} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,39,39,39,39,39,39,39,39,39,39,48,48,49,49,49,49,50,50,50,50,51,51,51,51,52,52,52,52,52,53,53,54,54,55,55]
; CHECK-AVX512-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vpandq %zmm3, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-AVX512-NEXT:  vzeroupper
; CHECK-AVX512-NEXT:  retq
:
; CHECK-VBMI-LABEL:  mul512:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT:  vpunpckhbw {[.*#]} zmm2 =
zmm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
; CHECK-VBMI-NEXT:  vpunpckhbw {[.*#]} zmm3 =
zmm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
; CHECK-VBMI-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:  vmovdqa64 {{.*#}} zmm1 =
[0,2,4,6,8,10,12,14,64,66,68,70,72,74,76,78,80,82,84,86,88,90,92,94,32,34,36,38,40,42,44,46,96,98,100,102,104,106,108,110,48,50,52,54,56,58,60,62,112,114,116,118,120,122,124,126]
; CHECK-VBMI-NEXT:  vpermi2b %zmm2, %zmm0, %zmm1

; CHECK-VBMI-NEXT:  vmovdqa64 %zmm1, (%rdx)
; CHECK-VBMI-NEXT:  vzeroupper
; CHECK-VBMI-NEXT:  retq

%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

}; CHECK-LABEL:  mload_v4i32:

; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {  
; CHECK-LABEL:  mload_v4i32:

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define <16 x i32> @trunc_v16i64_v16i32(<16 x i64> * %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i32:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT: vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT: vpmovqd %ymm0, %xmm0
; CHECK-NEXT: vpmovqd %ymm1, %xmm1
; CHECK-NEXT: vpmovqd %ymm2, %xmm2
; CHECK-NEXT: vpmovqd %ymm3, %xmm3
; CHECK-NEXT: vinserti128 $1, %xmm1, %ymm0, %ymm0
; CHECK-NEXT: vpmovq $1, %ymm1
; CHECK-NEXT: vpmovq %ymm2, %xmm2
; CHECK-NEXT: vpmovq %ymm3, %xmm3
; CHECK-NEXT: vinserti128 $1, %ymm1, %ymm0, %ymm0
; CHECK-NEXT: retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i32>
ret <16 x i32> %b
}

define <16 x i8> @trunc_v16i64_v16i8(<16 x i64> * %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i8:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT: vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT: vpmovqd %ymm0, %xmm0
; CHECK-NEXT: vpmovqd %ymm1, %xmm1
; CHECK-NEXT: vpmovqd %ymm2, %xmm2
; CHECK-NEXT: vpmovqd %ymm3, %xmm3
; CHECK-NEXT: vpmovq $1, %ymm3
; CHECK-NEXT: vpmovq %ymm0, %xmm0
; CHECK-NEXT: vpmovq %ymm1, %xmm1
; CHECK-NEXT: vpmovq %ymm2, %xmm2
; CHECK-NEXT: vpunpckldq {{.*#+}} %xmm2 = %xmm2[0],%xmm3[0],%xmm2[1],%xmm3[1]
; CHECK-NEXT: vpunpckldq {{.*#+}} %xmm0 = %xmm0[0],%xmm1[0],%xmm0[1],%xmm1[1]
; CHECK-NEXT: vpunpckldq {{.*#+}} %xmm0 = %xmm0[0],%xmm2[0]
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i8>

```assembly
ret <16 x i8> %b
}
define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
 ; CHECK-LABEL: trunc_v16i32_v16i8:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
 ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
 ; CHECK-NEXT: vpmovdb %ymm1, %xmm1
 ; CHECK-NEXT: vpmovdb %ymm0, %xmm0
 ; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %a = load <16 x i32>, <16 x i32>* %x
 %b = trunc <16 x i32> %a to <16 x i8>
ret <16 x i8> %b
}
define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
 ; CHECK-LABEL: trunc_v8i64_v8i8:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
 ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
 ; CHECK-NEXT: vpmovq %ymm1, %xmm1
 ; CHECK-NEXT: vpmovq %ymm0, %xmm0
 ; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %a = load <8 x i64>, <8 x i64>* %x
 %b = trunc <8 x i64> %a to <8 x i8>
ret <8 x i8> %b
}
define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
 ; CHECK-LABEL: trunc_v8i64_v8i16:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
 ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
 ; CHECK-NEXT: vpmovqw %ymm1, %xmm1
 ; CHECK-NEXT: vpmovqw %ymm0, %xmm0
 ; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %a = load <8 x i64>, <8 x i64>* %x
 %b = trunc <8 x i64> %a to <8 x i16>
ret <8 x i16> %b
}
define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
; CHECK: # %bb.0:
; CHECK-NEXT: vpsrlq $48, 32(%rdi), %ymm1
; CHECK-NEXT: vpsrlq $48, (%rdi), %ymm2
; CHECK-NEXT: vmovdqa {{.*#+}} ymm0 = [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30]
; CHECK-NEXT: vpermi2w %ymm1, %ymm2, %ymm0
; CHECK-NEXT: retq
%a = load <8 x i64>, <8 x i64>* %x
%b = lshr <8 x i64> %a, <i64 48>
%c = trunc <8 x i64> %b to <8 x i32>
ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm1
; CHECK-NEXT: vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT: vpermi2w %rdi, %ymm1, %ymm0
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %x
%b = lshr <16 x i32> %a, <i32 16>
%c = trunc <16 x i32> %b to <16 x i16>
ret <16 x i16> %c
}

define <32 x i8> @trunc_v32i16_v32i8_zeroes(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-AVX512: # %bb.0:
; CHECK-AVX512-NEXT: vpsrlw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT: vpsrlw $8, (%rdi), %ymm1
; CHECK-AVX512-NEXT: vpackuswb %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT: vpermi2b 32(%rdi), %ymm1, %ymm0
; CHECK-AVX512-NEXT: retq
%a = load <32 x i16>, <32 x i16>* %x
%b = lshr <32 x i16> %a, <i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c

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define <8 x i32> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i32_sign:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vpvsraq $48, 32(%rdi), %ymm0
    ; CHECK-NEXT:  vpvsraq $48, (%rdi), %ymm1
    ; CHECK-NEXT:  vpmovqd %ymm1, %xmm1
    ; CHECK-NEXT:  vpmovq %ymm0, %xmm0
    ; CHECK-NEXT:  vinsert $128 $1, %xmm0, %ymm1, %ymm0
    ; CHECK-NEXT:  retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = ashr <8 x i64> %a, 48
    %c = trunc <8 x i64> %b to <8 x i32>
    ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_sign(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_sign:
    ; CHECK-AVX512:       # %bb.0:
    ; CHECK-AVX512-NEXT:  vpvsraw $8, 32(%rdi), %ymm0
    ; CHECK-AVX512-NEXT:  vpvsraw $8, (%rdi), %ymm1
    ; CHECK-AVX512-NEXT:  vpacksswb %ymm0, %ymm1, %ymm0
    ; CHECK-AVX512-NEXT:  vpermq {{.*#+}} %ymm0 = %ymm0[0,2,1,3]
    ; CHECK-AVX512-NEXT:  retq
    ; CHECK-VBMI:       # %bb.0:
    ; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm1
    ; CHECK-VBMI-NEXT:  vpmovdqa {{.*#+}} %ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
    ; CHECK-VBMI-NEXT:  vperm %{.*#+} %ymm0 = %ymm0[0,2,1,3]
    ; CHECK-VBMI-NEXT:  retq
    %a = load <16 x i32>, <16 x i32>* %x
    %b = ashr <16 x i32> %a, 16
    %c = trunc <16 x i32> %b to <16 x i16>
    ret <16 x i16> %c
}

define <32 x i8> @trunc_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_sign:
    ; CHECK-AVX512:       # %bb.0:
    ; CHECK-AVX512-NEXT:  vpvsraw $8, 32(%rdi), %ymm0
    ; CHECK-AVX512-NEXT:  vpvsraw $8, (%rdi), %ymm1
    ; CHECK-AVX512-NEXT:  vpacksswb %ymm0, %ymm1, %ymm0
    ; CHECK-AVX512-NEXT:  vpermq {{.*#+}} %ymm0 = %ymm0[0,2,1,3]
    ; CHECK-AVX512-NEXT:  retq
    ; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_sign:
    ; CHECK-VBMI:       # %bb.0:
    ; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm1
    ; CHECK-VBMI-NEXT:  vpmovdqa {{.*#+}} %ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
    ; CHECK-VBMI-NEXT:  vperm %{.*#+} %ymm0 = %ymm0[0,2,1,3]
    ; CHECK-VBMI-NEXT:  retq
%a = load <32 x i16>, <32 x i16>* %x
%b = ashr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
}

define void @zext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: zext_v16i8_v16i64:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm1 = xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT:  vpshufd {{.*#+}} xmm2 = xmm1[2,3,0,1]
; CHECK-NEXT:  vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT:  vpshufd {{.*#+}} xmm3 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzwxq {{.*#+}} ymm3 = xmm1[2,3,0,1]
; CHECK-NEXT:  vextracti128 $1, %ymm1, %xmm2
; CHECK-NEXT:  vpmovzwxq {{.*#+}} ymm2 = xmm0[0],zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT:  vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:  vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT:  vmovdqa %ymm2, 96(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovzxbw %xmm0, (%rdi)
; CHECK-NEXT:  vpmovzxbw %xmm0, %ymm0
; CHECK-NEXT:  vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpmovzwxq %xmm1, %ymm0
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT:  vpshufd {{.*#+}} xmm3 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzwxq %ymm3, %xmm0
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT:  vpmovzwxq %ymm2, %xmm0
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}
define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {  
; CHECK-LABEL: vselect_split_v8i16_setcc:  
; CHECK: # %bb.0:  
; CHECK-NEXT: vmovdqa (%rsi), %ymm2  
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3  
; CHECK-NEXT: vpcmpeq %xmm1, %xmm0, %k1  
; CHECK-NEXT: kshiftrb $4, %k1, %k2  
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 [%k2]  
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 [%k1]  
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)  
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)  
; CHECK-NEXT: vzeroupper  
; CHECK-NEXT: retq  
  
%a = sext <16 x i8> %x to <16 x i64>  
store <16 x i64> %a, <16 x i64>* %y  
ret void  
}

define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {  
; CHECK-LABEL: vselect_split_v8i32_setcc:  
; CHECK: # %bb.0:  
; CHECK-NEXT: vmovdqa (%rsi), %ymm2  
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3  
; CHECK-NEXT: vpcmpeq %xmm1, %ymm0, %k1  
; CHECK-NEXT: kshiftrb $4, %k1, %k2  
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 [%k2]  
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 [%k1]  
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)  
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)  
; CHECK-NEXT: vzeroupper  
; CHECK-NEXT: retq  
  
%a = load <8 x i64>, <8 x i64>* %p  
%y = load <8 x i64>, <8 x i64>* %q  
%a = icmp eq <8 x i16> %s, %t  
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y  
store <8 x i64> %b, <8 x i64>* %r  
ret void  
}

%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i32> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
   ; CHECK-LABEL: vselect_split_v16i8_setcc:
   ; CHECK: # %bb.0:
   ; CHECK-NEXT: vmovdq (%rsi), %ymm2
   ; CHECK-NEXT: vmovdq 32(%rsi), %ymm3
   ; CHECK-NEXT: vpcmpeqb %xmm1, %xmm0, %k1
   ; CHECK-NEXT: kshiftrw $8, %k1, %k2
   ; CHECK-NEXT: vmovdq32 32(%rdi), %ymm3 [%k2]
   ; CHECK-NEXT: vmovdq32 (%rdi), %ymm2 [%k1]
   ; CHECK-NEXT: vmovdq %ymm2, (%rdx)
   ; CHECK-NEXT: vmovdq %ymm3, 32(%rdx)
   ; CHECK-NEXT: vzeroupper
   ; CHECK-NEXT: retq
   %x = load <16 x i32>, <16 x i32>* %p
   %y = load <16 x i32>, <16 x i32>* %q
   %a = icmp eq <16 x i8> %s, %t
   %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
   store <16 x i32> %b, <16 x i32>* %r
ret void
}

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
   ; CHECK-LABEL: vselect_split_v16i16_setcc:
   ; CHECK: # %bb.0:
   ; CHECK-NEXT: vmovdq (%rsi), %ymm2
   ; CHECK-NEXT: vmovdq 32(%rsi), %ymm3
   ; CHECK-NEXT: vpcmpeqw %ymm1, %ymm0, %k1
   ; CHECK-NEXT: kshiftrw $8, %k1, %k2
   ; CHECK-NEXT: vmovdq32 32(%rdi), %ymm3 [%k2]
   ; CHECK-NEXT: vmovdq32 (%rdi), %ymm2 [%k1]
   ; CHECK-NEXT: vmovdq %ymm2, (%rdx)
   ; CHECK-NEXT: vmovdq %ymm3, 32(%rdx)
   ; CHECK-NEXT: vzeroupper
   ; CHECK-NEXT: retq
   %x = load <16 x i32>, <16 x i32>* %p
   %y = load <16 x i32>, <16 x i32>* %q
   %a = icmp eq <16 x i16> %s, %t
   %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
   store <16 x i32> %b, <16 x i32>* %r
define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: trunc_packus_v16i32_v16i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vpacksusd 32(%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-NEXT:  vpmovuswb %ymm0, %xmm0
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <16 x i32>, <16 x i32>* %p
    %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %d = icmp sgt <16 x i32> %c, zeroinitializer
    %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
    %f = trunc <16 x i32> %e to <16 x i8>
    ret <16 x i8> %f
}

define void @trunc_packus_v16i32_v16i8_store(<16 x i32>* %p, <16 x i8>* %q) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vpacksusd 32(%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-NEXT:  vpmovuswb %ymm0, (%rsi)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <16 x i32>, <16 x i32>* %p
    %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %d = icmp sgt <16 x i32> %c, zeroinitializer
    %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
    %f = trunc <16 x i32> %e to <16 x i8>
    store <16 x i8> %f, <16 x i8>* %q
    ret void
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: v64i1_argument_return:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vpacksusd 64(%rdi), %ymm0, %ymmm0
    ; CHECK-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-NEXT:  vpmovuswb %ymmm0, %xmm0
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <64 x i1>, <64 x i1>* %p
    %b = icmp slt <64 x i1> %a, <i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255>
    %c = select <64 x i1> %b, <64 x i1> %a, <64 x i1> <i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255>
    %d = icmp sgt <64 x i1> %c, zeroinitializer
    %e = select <64 x i1> %d, <64 x i1> %c, <64 x i1> zeroinitializer
    %f = trunc <64 x i1> %e to <64 x i8>
    store <64 x i8> %f, <64 x i8>* %q
    ret void
}
define void @v64i1_shuffle(<64 x i8>* %x, <64 x i8>* %y) "min-legal-vector-width"="256" {
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm1
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm0
    ; CHECK-NEXT: vptestnmb %ymm1, %ymm1, %k0
    ; CHECK-NEXT: kshiftrd $1, %k0, %k1
    ; CHECK-NEXT: movq $-3, %rax
    ; CHECK-NEXT: kmovq %rax, %k2
    ; CHECK-NEXT: kandq %k2, %k1, %k1
    ; CHECK-NEXT: kshiftlq $63, %k0, %k2
    ; CHECK-NEXT: kshiftrq $62, %k2, %k2
    ; CHECK-NEXT: korq %k2, %k1, %k1
    ; CHECK-NEXT: movq $-5, %rax
    ; CHECK-NEXT: kmovq %rax, %k2
    ; CHECK-NEXT: kandq %k2, %k1, %k1
    ; CHECK-NEXT: kshiftlq $63, %k0, %k2
    ; CHECK-NEXT: kshiftrq $61, %k2, %k2
    ; CHECK-NEXT: korq %k2, %k1, %k1
    ; CHECK-NEXT: movq $-9, %rax
    ; CHECK-NEXT: kmovq %rax, %k2
    ; CHECK-NEXT: kandq %k2, %k1, %k1
    ; CHECK-NEXT: kshiftlq $63, %k0, %k2
    ; CHECK-NEXT: kshiftrq $60, %k2, %k2
    ; CHECK-NEXT: korq %k2, %k1, %k1
    ; CHECK-NEXT: movq $-17, %rax
    ; CHECK-NEXT: kmovq %rax, %k2
    ; CHECK-NEXT: kandq %k2, %k1, %k1
    ; CHECK-NEXT: kshiftlq $5, %k0, %k2
    ; CHECK-NEXT: kshiftrq $59, %k2, %k2
    ; CHECK-NEXT: korq %k2, %k1, %k1
    ; CHECK-NEXT: movq $-33, %rax
    ; CHECK-NEXT: kmovq %rax, %k2
    ; CHECK-NEXT: kandq %k2, %k1, %k1
    ; CHECK-NEXT: kshiftlq $4, %k0, %k2
    ; CHECK-NEXT: kshiftrq $58, %k2, %k2
    ; CHECK-NEXT: korq %k2, %k1, %k1
    ; CHECK-NEXT: movq $-65, %rax
    ; CHECK-NEXT: kmovq %rax, %k2
    ; CHECK-NEXT: kandq %k2, %k1, %k1
}
; CHECK-NEXT:    kshiftrd $7, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $57, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-129, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $6, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $56, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-257, %rax # imm = 0xFEFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $9, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $55, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-513, %rax # imm = 0xFDFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $8, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $54, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $11, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $53, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $10, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $52, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-4097, %rax # imm = 0xEFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $13, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $51, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-8193, %rax # imm = 0xDEFF
; CHECK-NEXT:    kmovq %rax, %k2
entry:
  %a = load <64 x i8>, <64 x i8>* %x
  %b = icmp eq <64 x i8> %a, zeroinitializer
  %shuf = shufflevector <64 x i1> %b, <64 x i1> undef, <64 x i32> %i32 1, %i32 2, %i32 3, %i32 4, %i32 5, %i32 6, %i32 7, %i32 8, %i32 9, %i32 10, %i32 11, %i32 12, %i32 13, %i32 14, %i32 15, %i32 16, %i32 17, %i32 18, %i32 19, %i32 20, %i32 21, %i32 22, %i32 23, %i32 24, %i32 25, %i32 26, %i32 27, %i32 28, %i32 29, %i32 30, %i32 31, %i32 32, %i32 33, %i32 34, %i32 35, %i32 36, %i32 37, %i32 38, %i32 39, %i32 40, %i32 41, %i32 42, %i32 43, %i32 44, %i32 45, %i32 46, %i32 47, %i32 48, %i32 49, %i32 50, %i32 51, %i32 52, %i32 53, %i32 54, %i32 55, %i32 56, %i32 57, %i32 58, %i32 59, %i32 60, %i32 61, %i32 62, %i32 63, %i32 64>
  call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, i32 1, <64 x i1> %shuf)
  ret void
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>*, i32, <64 x i1>)
store i64 %3, i64* @mem64_dst, align 8
%4 = load i32, i32* %1, align 4
ret i32 %4
}

define void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
; CHECK:
; # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = sext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_zext:
; CHECK:
; # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
%ext = zext <8 x i1> %cmp to <8 x i64>
store <8 x i64> %ext, <8 x i64>* %zptr
ret void
}

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {


; CHECK-LABEL: var_rotate_v16i8:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
; CHECK-NEXT: vpsubb %xmm1, %xmm2, %xmm2
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm1 =
; xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zero,xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xmm1[15],zero
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm0 =
; xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
; CHECK-NEXT: vpsllvw %ymm1, %ymm0, %ymm1
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm2 =
; xmm2[0],zero,xmm2[1],zero,xmm2[2],zero,xmm2[3],zero,xmm2[4],zero,xmm2[5],zero,xmm2[6],zero,xmm2[7],zero,xmm2[8],zero,xmm2[9],zero,xmm2[10],zero,xmm2[11],zero,xmm2[12],zero,xmm2[13],zero,xmm2[14],zero,xmm2[15],zero
; CHECK-NEXT: vpsrlvw %ymm2, %ymm0, %ymm0
; CHECK-NEXT: vpor %ymm0, %ymm1, %ymm0
; CHECK-NEXT: vpmovvwb %ymm0, %xmm0
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
%shl = shl <16 x i8> %a, %b
%lshr = lshr <16 x i8> %a, %b8
%or = or <16 x i8> %shl, %lshr
ret <16 x i8> %or

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: var_rotate_v32i8:
; CHECK: # %bb.0:
; CHECK-NEXT: vpsllw $4, %ymm0, %ymm2
; CHECK-NEXT: vpsrlw $4, %ymm0, %ymm3
; CHECK-NEXT: vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT: vpsllw $5, %ymm1, %ymm1
; CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
; CHECK-NEXT: vpsllw $2, %ymm0, %ymm2
; CHECK-NEXT: vpsrlw $6, %ymm0, %ymm3
; CHECK-NEXT: vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT: vpaddd %ymm1, %ymm1, %ymm1
; CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
; CHECK-NEXT: vpsrlw $7, %ymm0, %ymm2
; CHECK-NEXT: vpaddd %ymm0, %ymm0, %ymm3
; CHECK-NEXT: vpternlogq $248, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT: vpaddd %ymm1, %ymm1, %ymm1
; CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
; CHECK-NEXT: retq
%b8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
%shl = shl <32 x i8> %a, %b
%lshr = lshr <32 x i8> %a, %b8
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or
}

define <32 x i8> @splatvar_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: splatvar_rotate_v32i8:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vpbroadcast %xmm1, %xmm1
    ; CHECK-NEXT: vpand {.*}(%rip), %xmm1, %xmm1
    ; CHECK-NEXT: vmovzxbq {.*#} xmm2 =
    ; CHECK-NEXT: vpsll &%xmm1, %ymm0, %ymm3
    ; CHECK-NEXT: vpmovda {.*#} xmm4 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
    ; CHECK-NEXT: vpsubb %ymm1, %ymm4, %ymm1
    ; CHECK-NEXT: vpsllw %xmm2, %ymm4, %ymm2
    ; CHECK-NEXT: vpbroadcast %ymm2, %ymm2
    ; CHECK-NEXT: vpmovzxbq {.*#} xmm1 =
    ; CHECK-NEXT: vpsrl &%ymm1, %ymm0, %ymm5
    ; CHECK-NEXT: vpand %ymm2, %ymm3, %ymm2
    ; CHECK-NEXT: vpsrlw %ymm1, %ymm4, %ymm0
    ; CHECK-NEXT: vpsrlw $8, %ymm0, %ymm0
    ; CHECK-NEXT: vpbroadcast %ymm0, %ymm0
    ; CHECK-NEXT: vpternlogq $236, %ymm5, %ymm2, %ymm0
    ; CHECK-NEXT: retq
    ; CHECK-NEXT: ret
    %splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
    %splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %splat
    %shl = shl <32 x i8> %a, %splat
    %lshr = lshr <32 x i8> %a, %splat8
    %or = or <32 x i8> %shl, %lshr
    ret <32 x i8> %or
}

define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: constant_rotate_v32i8:
    ; CHECK-AVX512: # %bb.0:
    ; CHECK-AVX512-NEXT: vpsllw $4, %ymm0, %ymm1
    ; CHECK-AVX512-NEXT: vpand {.*}(%rip), %ymm1, %ymm1
    ; CHECK-AVX512-NEXT: vbroadcasti128 {.*#} ymm2 =
    ; CHECK-AVX512-NEXT: vpbroadcast %ymm0, %ymm0
    ; CHECK-AVX512-NEXT: vpternlogq $236, %ymm5, %ymm2, %ymm0
    ; CHECK-AVX512-NEXT: retq
    %splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
    %splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %splat
    %shl = shl <32 x i8> %a, %splat
    %lshr = lshr <32 x i8> %a, %splat8
    %or = or <32 x i8> %shl, %lshr
    ret <32 x i8> %or
}
define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
  CHECK-LABEL: splatconstant_rotate_v32i8:
  CHECK:
  # %bb.0:
  CHECK-NEXT: vpsllw $4, %ymm0, %ymm1
  CHECK-NEXT: vpsrlw $4, %ymm0, %ymm0
  CHECK-NEXT: vpternlogq $216, [[*]](%rip), %ymm1, %ymm0
  CHECK-NEXT: retq
  %shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31>
  %lshr = lshr <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31>
  %or = or <32 x i8> %shl, %lshr
  ret <32 x i8> %or
}

define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
  CHECK-LABEL: splatconstant_rotate_mask_v32i8:
  CHECK:
  # %bb.0:
  CHECK-NEXT: vpsllw $4, %ymm0, %ymm1
  CHECK-NEXT: vpsrlw $4, %ymm0, %ymm0
  CHECK-NEXT: vpternlogq $216, [[*]](%rip), %ymm1, %ymm0
  CHECK-NEXT: vpand [[*]](%rip), %ymm0, %ymm0
  CHECK-NEXT: retq
  %shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31>
  %lshr = lshr <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31>
  %rmask = and <32 x i8> %lshr, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31>
  ret <32 x i8> %or
}

%lmask = and <32 x i8> %shl, <i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33>, %or = or <32 x i8> %lmask, %rmask
ret <32 x i8> %or

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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp

LINK_LIBS
 lldbCore
 lldbHost
 lldbSymbol
LINK COMPONENTS
 Support
)

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; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
    br i1 undef, label %while.end, label %while.body.lr.ph

  while.body.lr.ph: ; preds = %entry
    br label %while.body

  while.body: ; preds = %exit.2, %while.body.lr.ph
    %lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
    switch i32 undef, label %exit [{
      i32 1, label %sw.bb.i
      i32 2, label %sw.bb3.i
    ]

  sw.bb.i: ; preds = %while.body
    unreachable
sw.bb3.i: ; preds = %while.body unreachable

exit: ; preds = %while.body

switch i32 undef, label %exit.2 [
  i32 1, label %sw.bb.i17
  i32 2, label %sw.bb3.i20
]

sw.bb.i17: ; preds = %exit
  %0 = bitcast %struct.0* %lsr.iv to i32*
  %1 = load i32, i32* %0, align 4
  unreachable

sw.bb3.i20: ; preds = %exit
  %2 = bitcast %struct.0* %lsr.iv to i8**
  %3 = load i8*, i8** %2, align 4
  unreachable

exit.2: ; preds = %exit
  %cegp22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
  br label %while.body

while.end: ; preds = %entry
  ret void
]

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" } ; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes ; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=4 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS________OPM,IS__TUNIT_OPM ; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=3 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_OPM,NOT_CGSCC_NPM,NOT_TUNIT_OPM,IS__TUNIT____,IS________NP M,IS__TUNIT_NPM ; RUN: opt -attributor-cgsc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_NPM,IS__CGSCC____,IS________OPM,IS__CGSCC_OPM ; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgsc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS________NP M,IS__CGSCC_NPM ; Test that we only promote arguments when the caller/callee have compatible
target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{[{^[@]+}]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:.%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS_TUNIT_NPM-LABEL: define
{[{^[@]+}]}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:.%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT: [[TMP:.%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
;
; IS_TUNIT_OPM-LABEL: define {^[[@]+]}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS_TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS_TUNIT_OPM-NEXT: bb:
; IS_TUNIT_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS_TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS_TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
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```c
; IS__TUNIT_OPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT:   ret void
;
; IS__TUNIT_NPM-LABEL: define ([[@+]]) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT:   bb:
; IS__TUNIT_NPM-NEXT:   [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT:   [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT:   ret void
;
; IS__CGSCC_OPM-LABEL: define ([[@+]]) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT:   bb:
; IS__CGSCC_OPM-NEXT:   [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:   call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])
; IS__TUNIT_NPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT:   ret void
;
; IS__CGSCC_NPM-LABEL: define ([[@+]]) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture writeonly 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT:   bb:
; IS__CGSCC_NPM-NEXT:   [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:   call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:   ret void
;
; IS__CGSCC_NPM-LABEL: define ([[@+]]) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT:   bb:
; IS__CGSCC_NPM-NEXT:   [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:   call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
```
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ; NOT_TUNIT_NPM-LABEL: define
  {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
  ; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
  ; NOT_TUNIT_NPM-NEXT: bb:
    [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
    store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
    ret void
  ;
  ; IS__TUNIT_NPM-LABEL: define
  {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
  ; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
  ; IS__TUNIT_NPM-NEXT: bb:
    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
    store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
    [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
    store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
    ret void
  ;
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
  ;
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; IS__TUNIT_OPM-LABEL: define ([[^@]+] )@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define ([[^@]+] )@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define ([[^@]+] )@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define ([[^@]+] )@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1) #1 {
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}

define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  ;
  ; IS__TUNIT_OPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
  ; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
  ; IS__TUNIT_OPM-NEXT: bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonnull readonly align 64
dereferenceable(64) [[TMP]])
  ; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
  ; IS__TUNIT_OPM-NEXT: [[ARG:%.*]] = alloca <8 x i64>, align 32
  ; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
  ; IS__TUNIT_OPM-NEXT: bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nonfree
nonnull writeonly align 64 dereferenceable(64) [[TMP0]], <8 x i64>* noalias nocapture nonfree
nonnull writeonly align 64 dereferenceable(64) [[TMP0]])
  ; IS__TUNIT_OPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
  ; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nonfree
nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
  ; IS__TUNIT_OPM-NEXT: bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 2
dereferenceable(64) [[ARG]], align 2)
    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nonfree
nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
  ; IS__TUNIT_OPM-NEXT: bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
  }
}

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; IS__CGSCC_OPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:   call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:   ret void
;
; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:.#*]])
; IS__CGSCC_NPM-NEXT:   bb:
; IS__CGSCC_NPM-NEXT:   [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:   call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonfree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:   ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-LABEL: define
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:.#*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:.#*]])
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg1) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg1 = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %arg1, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %arg, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg1) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg1 = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %arg1, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %arg, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg1) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg1 = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %arg1, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %arg, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg1) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg1 = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %arg1, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 { 
  @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512:
    %arg = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %arg, <8 x i64>* %arg
    ret void
}
; IS__TUNIT_NPM-NEXT:   [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT:   call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])
; IS__TUNIT_NPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT:   ret void

; IS__CGSCC_OPM-LABEL: define ({{[^@]+}}) @avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* noalias nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT:   bb:
; IS__CGSCC_OPM-NEXT:     [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:     [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:     [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:     call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:   call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:     [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__CGSCC_OPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:   ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256

; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define
{{[^@]+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256

; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {

; IS__TUNIT_OPM-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256

; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; _IS__CGSCC_NPM-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_call_avx512_legal128_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {
;
; _NOT_TUNIT_NPM-LABEL: define
  {{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
      <8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [ARG:%.*] , <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; _NOT_TUNIT_NPM-NEXT: bb:
; _NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; _NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; _NOT_TUNIT_NPM-NEXT:  ret void
;
; _IS__UNIT_NPM-LABEL: define
  {{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
      <8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [ARG:%.*] , <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; _IS__UNIT_NPM-NEXT: bb:
; _IS__UNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; _IS__UNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; _IS__UNIT_NPM-NEXT:  ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
;
; _IS__TUNIT_OPM-LABEL: define
  {{[^@]+}} @avx512_legal512_prefer256_call_avx512_legal256_prefer256
      <8 x i64>* nocapture nofree nonnull writeonly [ARG:%.*])
; _IS__TUNIT_OPM-NEXT: bb:
; _IS__TUNIT_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:    call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:    ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8
x i64>* readonly %arg1) #3 {
...

bb:

; NOT_TUNIT_NPM-LABEL: define
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; NOT_TUNIT_NPM-NEXT:    store <8 x i64> [[ARG1_PRIV]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:    ret void

; IS__TUNIT_NPM-LABEL: define [[^@]+] @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:    [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:    store <8 x i64> [[ARG1_PRIV]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:    ret void

bb:
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@callee_avx2_legal512_prefer256_call_avx2_legal128_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

; IS__CGSCC_OPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:   ret void

; IS__CGSCC_NPM-LABEL: define ( *[^@]+ ) @avx2_legal256_prefer256_call_avx2_legal128_prefer256
; IS__CGSCC_NPM-SAME: ( <8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT:   bb:
; IS__CGSCC_NPM-NEXT:     [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:     [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:     [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP2]] to i8*
; IS__CGSCC_NPM-NEXT:     call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:     call fastcc void @callee_avx2_legal256_prefer256(<8 x i64>* [%tmp2, <8 x i64>* %arg], align 2
; IS__CGSCC_NPM-NEXT:     %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
; IS__CGSCC_NPM-NEXT:     store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
; IS__CGSCC_NPM-NEXT:     ret void

; bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @ callee_avx2_legal512_prefer256_call_avx2_legal128_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal128_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {

; NOT_TUNIT_NPM-LABEL: define
{[^@]+} @callee_avx2_legal512_prefer256_call_avx2_legal128_prefer256
; NOT_TUNIT_NPM-SAME: ( <8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT:   bb:
; NOT_TUNIT_NPM-NEXT:     [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:     store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:     ret void

}
define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}
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; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonnull readonly align 64 dereferenceable(64) [[TMP]])

%tmp4 = load <8 x i64>, <8 x i64>* [[TMP4]], <8 x i64>* [[ARG]], align 2
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonnull readonly align 64 dereferenceable(64) [[TMP]])

%tmp4 = load <8 x i64>, <8 x i64>* [[TMP4]], <8 x i64>* [[ARG]], align 2
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; RUN: llc -O3 -mtriple=powerpc-unknown-linux-gnu -mcpu=e500 -mattr=spe < %s | FileCheck %s

; PowerPC SPE is a rare in-tree target that has the FP_TO_SINT node marked as Legal.

; Verify that fptosi(42.1) isn't simplified when the rounding mode is unknown.
; Verify that no gross errors happen.
; CHECK-LABEL: @f20
; COMMON: cfdctsiz
define i32 @f20(double %a) strictfp {
  entry:
  %result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1, metadata !"fpexcept.strict")
  strictfp
  ret i32 %result
}

@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)

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==============================================================================
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; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,AVX512VL256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,AVX512VL512
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,SKX512

define void @zext256() "min-legal-vector-width"="256" {
  ; VEC256-LABEL: 'zext256'
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
define void @zext512() "min-legal-vector-width"="512" { 
  ; AVX-LABEL: 'zext512'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

  %A = zext <8 x i16> undef to <8 x i64>
  %B = zext <8 x i32> undef to <8 x i64>
  %C = zext <16 x i8> undef to <16 x i32>
  %D = zext <16 x i16> undef to <16 x i32>
  %E = zext <32 x i8> undef to <32 x i16>
  ret void
}
<8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL512-LABEL: 'zext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; SKX512-LABEL: 'zext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
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; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; %A = zext <8 x i16> undef to <8 x i64>
; %B = zext <8 x i32> undef to <8 x i64>
; %C = zext <16 x i8> undef to <16 x i32>
; %D = zext <16 x i16> undef to <16 x i32>
; %E = zext <32 x i8> undef to <32 x i16>
ret void
;

define void @sext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
define void @sext512() "min-legal-vector-width"="512" {
    ; AVX-LABEL: 'sext512'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ret void
}

define void @sext512() "min-legal-vector-width"="512" {
    ; AVX512VL256-LABEL: 'sext512'
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ret void
}

define void @sext512() "min-legal-vector-width"="512" {
    ; AVX512VL512-LABEL: 'sext512'
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ret void
}
<16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @trunc256() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc256'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: \%C = trunc <8 x i64> undef to <8 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: \%D = trunc <16 x i32> undef to <16 x i16>
AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: \%E = trunc <16 x i32> undef to <16 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: \%F = trunc <32 x i16> undef to <32 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

AVX512VL256-LABEL: 'trunc256'
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: \%A = trunc <8 x i64> undef to <8 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: \%B = trunc <8 x i64> undef to <8 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: \%C = trunc <8 x i64> undef to <8 x i8>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: \%D = trunc <16 x i32> undef to <16 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: \%E = trunc <16 x i32> undef to <16 x i8>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: \%F = trunc <32 x i16> undef to <32 x i8>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

AVX512VL512-LABEL: 'trunc256'
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: \%A = trunc <8 x i64> undef to <8 x i32>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: \%B = trunc <8 x i64> undef to <8 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: \%C = trunc <8 x i64> undef to <8 x i8>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: \%D = trunc <16 x i32> undef to <16 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: \%E = trunc <16 x i32> undef to <16 x i8>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: \%F = trunc <32 x i16> undef to <32 x i8>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

SKX256-LABEL: 'trunc256'
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: \%A = trunc <8 x i64> undef to <8 x i32>
SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: \%B = trunc <8 x i64> undef to <8 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: \%C = trunc <8 x i64> undef to <8 x i8>
SKX256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: \%D = trunc <16 x i32> undef to <16 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: \%E = trunc <16 x i32> undef to <16 x i8>
define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
    %A = trunc <8 x i64> undef to <8 x i32>
    %B = trunc <8 x i64> undef to <8 x i16>
    %C = trunc <8 x i64> undef to <8 x i8>
    %D = trunc <16 x i32> undef to <16 x i16>
    %E = trunc <16 x i32> undef to <16 x i8>
    %F = trunc <32 x i16> undef to <32 x i8>
    ret void
}

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 25 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 50 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'zext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i64 = zext <16 x i1> undef to <16 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; %V2i64 = zext <2 x i1> undef to <2 x i64>
%V4i64 = zext <4 x i1> undef to <4 x i64>
%V8i64 = zext <8 x i1> undef to <8 x i64>

%V2i32 = zext <2 x i1> undef to <2 x i32>
%V4i32 = zext <4 x i1> undef to <4 x i32>
%V8i32 = zext <8 x i1> undef to <8 x i32>
%V16i32 = zext <16 x i1> undef to <16 x i32>

%V2i16 = zext <2 x i1> undef to <2 x i16>
%V4i16 = zext <4 x i1> undef to <4 x i16>
%V8i16 = zext <8 x i1> undef to <8 x i16>
%V16i16 = zext <16 x i1> undef to <16 x i16>
%V32i16 = zext <32 x i1> undef to <32 x i16>

%V2i8 = zext <2 x i1> undef to <2 x i8>
%V4i8 = zext <4 x i1> undef to <4 x i8>
%V8i8 = zext <8 x i1> undef to <8 x i8>
%V16i8 = zext <16 x i1> undef to <16 x i8>
%V32i8 = zext <32 x i1> undef to <32 x i8>
%V64i8 = zext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @sext256_vXi1() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'sext256_vXi1'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
  ; AVX512VL256-LABEL: 'sext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 42 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: 'sext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i64 = sext <16 x i1> undef to <16 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i64 = sext <32 x i1> undef to <32 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i64 = sext <64 x i1> undef to <64 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V128i64 = sext <128 x i1> undef to <128 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V256i64 = sext <256 x i1> undef to <256 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V512i64 = sext <512 x i1> undef to <512 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V1024i64 = sext <1024 x i1> undef to <1024 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i128 = sext <8 x i128>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %I64 = sext i1 undef to i64

SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>

SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>

SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>

SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32

SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
<16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>

%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>

%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'trunc_vXi1'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
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; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

%V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>
%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>
%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to <16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>
%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>
ret i32 undef
}
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1.258 libauthen-sasl-perl 2.1600-1
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1.260 libxml 2.9.10+dfsg-5

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<td>lib/interception/mach_override</td>
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libfreedreno_layout = static_library(
    'freedreno_layout',
    [
        'fd5_layout.c',
        'fd6_layout.c',
        'freedreno_layout.c',
    ],
    include_directories : [inc_freedreno, inc_include, inc_src, inc_mapi, inc_mesa, inc_gallium, inc_gallium_aux],
    c_args : [no_override_init_args],
    gnu_symbol_visibility : 'hidden',
    dependencies : idep_nir_headers,
    build_by_default : false,
)

foreach major : ['5', '6']
    test(
        'fd' + major + '_layout',
        executable(
            'fd' + major + '_layout',
            [
                'fd_layout_test.c',
                'fd' + major + '_layout_test.c',
                freedreno_xml_header_files,
            ],
            link_with: libfreedreno_layout,
            dependencies : idep_mesautil,
            include_directories: [inc_include,
                                inc_src,
                                inc_mapi,
                                inc_mesa,
                                inc_gallium,
                                inc_gallium_aux,
                                inc_freedreno],
            ),
            suite : ['freedreno'],
    )
endforeach
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1.278 bind 9.16.1-0ubuntu2.7

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  * Solaris 10 + Sun Studio fixes.
  Alek Storm <alek.storm@gmail.com>
  * Slicing support for repeated scalar fields for the Python API.
  Oleg Smolsky <oleg.smolsky@gmail.com>
  * MS Visual Studio error format option.
  * Detect unordered_map in stl_hash.m4.
  Brian Olson <brianolson@google.com>
  * gzip/zlib I/O support.
  Michael Poole <mdpoole@troilus.org>
  * Fixed warnings about generated constructors not explicitly initializing
    all fields (only present with certain compiler settings).
  * Added generation of field number constants.
  Wink Saville <wink@google.com>
  * Fixed initialization ordering problem in logging code.
  Will Pierce <willp@nuclei.com>
  * Small patch improving performance of in Python serialization.
  Alexandre Vassalotti <alexandre@peadrop.com>
  * Emacs mode for Protocol Buffers (editors/protobuf-mode.el).
  Scott Stafford <scott.stafford@gmail.com>
  * Added Swap(), SwapElements(), and RemoveLast() to Reflection interface.
  Alexander Melnikov <alm@sibmail.ru>
  * HPUX support.
  Oliver Jowett <oliver.jowett@gmail.com>
  * Detect whether zlib is new enough in configure script.
  * Fixes for Solaris 10 32/64-bit confusion.
  Evan Jones <evanj@mit.edu>
  * Optimize Java serialization code when writing a small message to a stream.
  * Optimize Java serialization of strings so that UTF-8 encoding happens only
    once per string per serialization call.
  * Clean up some Java warnings.
  * Fix bug with permanent callbacks that delete themselves when run.
  Michael Kucharski <m.kucharski@gmail.com>
  * Added CodedInputStream.getTotalBytesRead().
  Kacper Kowalik <xarthsius.kk@gmail.com>
  * Fixed m4/acx_pthread.m4 problem for some Linux distributions.
  William Orr <will@worrbase.com>
  * Fixed detection of sched_yield on Solaris.
  * Added atomicops for Solaris
  Andrew Paprocki <andrew@ishiboo.com>
* Fixed minor IBM xIC compiler build issues
* Added atomicops for AIX (POWER)

1.286 libxcb 1.14-2

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1.287 xorg-server 1.20.9-2ubuntu1.2~20.04.2

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=head1 NAME

File::Listing - parse directory listing

=head1 SYNOPSIS

use File::Listing qw(parse_dir);
$ENV{LANG} = "C";  # dates in non-English locales not supported
for (parse_dir(`ls -l`)) {
    ($name, $type, $size, $mtime, $mode) = @$_;
    next if $type ne 'f';  # plain file
    #...
}

# directory listing can also be read from a file
open(LISTING, "zcat ls-lR.gz|");
$dir = parse_dir(
    LISTING, '+0000');

=head1 DESCRIPTION

This module exports a single function called parse_dir(), which can be
used to parse directory listings.

The first parameter to parse_dir() is the directory listing to parse.
It can be a scalar, a reference to an array of directory lines or a
glob representing a filehandle to read the directory listing from.

The second parameter is the time zone to use when parsing time stamps
in the listing. If this value is undefined, then the local time zone is
assumed.

The third parameter is the type of listing to assume. Currently
supported formats are 'unix', 'apache' and 'dosftp'. The default
value is 'unix'. Ideally, the listing type should be determined
automatically.

The fourth parameter specifies how unparsable lines should be treated.
Values can be 'ignore', 'warn' or a code reference. Warn means that
the perl warn() function will be called. If a code reference is
passed, then this routine will be called and the return value from it
will be incorporated in the listing. The default is 'ignore'.

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Only the first parameter is mandatory.

The return value from parse_dir() is a list of directory entries. In a scalar context the return value is a reference to the list. The directory entries are represented by an array consisting of [$filename, $filetype, $filesize, $filetime, $filemode]. The $filetype value is one of the letters 'f', 'd', 'l' or '?'. The $filetime value is the seconds since Jan 1, 1970. The $filemode is a bitmask like the mode returned by stat().

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Based on lsparse.pl (from Lee McLoughlin's ftp mirror package) and Net::FTP's parse_dir (Graham Barr).

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Found in path(s):
* /opt/cola/permits/1138401293_1614281814.28/0/libfile-listing-perl-6-04-orig-1-tar-gz/File-Listing-6.04/lib/File/Listing.pm
No license file was found, but licenses were detected in source scan.

license: perl

Found in path(s):
* /opt/cola/permits/1138401293_1614281814.28/0/libfile-listing-perl-6-04-orig-1-tar-gz/File-Listing-6.04/META.yml
No license file was found, but licenses were detected in source scan.

NAME
File::Listing - parse directory listing

SYNOPSIS
use File::Listing qw(parse_dir);
ENV{LANG} = "C"; # dates in non-English locales not supported
for (parse_dir(\'ls -l\')) {
    (Sname, Sfiletype, Ssize, Smtime, Smode) = @$_;
    next if $filetype ne 'f'; # plain file
    #...
}

# directory listing can also be read from a file
open(LISTING, "zcat ls-IR.gz");
$dir = parse_dir("LISTING, +0000");
DESCRIPTION
This module exports a single function called parse_dir(), which can be used to parse directory listings.

The first parameter to parse_dir() is the directory listing to parse. It can be a scalar, a reference to an array of directory lines or a glob representing a filehandle to read the directory listing from.

The second parameter is the time zone to use when parsing time stamps in the listing. If this value is undefined, then the local time zone is assumed.

The third parameter is the type of listing to assume. Currently supported formats are 'unix', 'apache' and 'dosftp'. The default value is 'unix'. Ideally, the listing type should be determined automatically.

The fourth parameter specifies how unparseable lines should be treated. Values can be 'ignore', 'warn' or a code reference. Warn means that the perl warn() function will be called. If a code reference is passed, then this routine will be called and the return value from it will be incorporated in the listing. The default is 'ignore'.

Only the first parameter is mandatory.

The return value from parse_dir() is a list of directory entries. In a scalar context the return value is a reference to the list. The directory entries are represented by an array consisting of [ $filename, $filetype, $filesize, $filetime, $filemode ]. The $filetype value is one of the letters 'f', 'd', 'l' or '?'. The $filetime value is the seconds since Jan 1, 1970. The $filemode is a bitmask like the mode returned by stat().

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Found in path(s):
*/opt/cola/permits/1138401293_1614281814.28/0/libfile-listing-perl-6-04-orig-1-tar-gz/File-Listing-6.04/README
1.296 humanity-icon-theme 0.6.15

1.296.1 Available under license:

Sebastian Porta <sebastianporta@gmail.com>
Oliver Scholtz <schollit泽@ yahoo.de>
Daniel Planas <daniplanas.a@gmail.com>
Dennis Fisher, for his amazing work with emblems to make them look beautiful.

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LGPL version 3 can be found in `/usr/share/common-licenses/LGPL-3'.
# Bender: "Well, I don't have anything else
# planned for today. Let's get drunk!"

## NAME
WWW::RobotRules - database of robots.txt-derived permissions

## SYNOPSIS
use WWW::RobotRules;
my $rules = WWW::RobotRules->new('MOMspider/1.0');

use LWP::Simple qw(get);

{
    my $url = "http://some.place/robots.txt";
    my $robots_txt = get $url;
    $rules->parse($url, $robots_txt) if defined $robots_txt;
}

{
    my $url = "http://some.other.place/robots.txt";
    my $robots_txt = get $url;
    $rules->parse($url, $robots_txt) if defined $robots_txt;
}

# Now we can check if a URL is valid for those servers
# whose "robots.txt" files we've gotten and parsed:
if($rules->allowed($url)) {
    $c = get $url;
    ...
}

## DESCRIPTION
This module parses F</robots.txt> files as specified in "A Standard for Robot Exclusion", at <http://www.robotstxt.org/wc/norobots.html>
Webmasters can use the F</robots.txt> file to forbid conforming robots from accessing parts of their web site.

The parsed files are kept in a WWW::RobotRules object, and this object provides methods to check if access to a given URL is prohibited. The same WWW::RobotRules object can be used for one or more parsed F</robots.txt> files on any number of hosts.
The following methods are provided:

=item $rules = WWW::RobotRules->new($robot_name)

This is the constructor for WWW::RobotRules objects. The first argument given to new() is the name of the robot.

=item $rules->parse($robot_txt_url, $content, $fresh_until)

The parse() method takes as arguments the URL that was used to retrieve the F<robots.txt> file, and the contents of the file.

=item $rules->allowed($uri)

Returns TRUE if this robot is allowed to retrieve this URL.

=item $rules->agent([$name])

Get/set the agent name. NOTE: Changing the agent name will clear the robots.txt rules and expire times out of the cache.

=head1 ROBOTS.TXT

The format and semantics of the "/robots.txt" file are as follows (this is an edited abstract of <http://www.robotstxt.org/wc/norobots.html>):

The file consists of one or more records separated by one or more blank lines. Each record contains lines of the form

<field-name>: <value>

The field name is case insensitive. Text after the '#' character on a line is ignored during parsing. This is used for comments. The following <field-names> can be used:

=User-Agent

The value of this field is the name of the robot the record is describing access policy for. If more than one I<User-Agent> field is present the record describes an identical access policy for more than
one robot. At least one field needs to be present per record. If the value is '*', the record describes the default access policy for any robot that has not not matched any of the other records.

The I<User-Agent> fields must occur before the I<Disallow> fields. If a record contains a I<User-Agent> field after a I<Disallow> field, that constitutes a malformed record. This parser will assume that a blank line should have been placed before that I<User-Agent> field, and will break the record into two. All the fields before the I<User-Agent> field will constitute a record, and the I<User-Agent> field will be the first field in a new record.

=item Disallow

The value of this field specifies a partial URL that is not to be visited. This can be a full path, or a partial path; any URL that starts with this value will not be retrieved.

=back

Unrecognized records are ignored.

=head1 ROBOTS.TXT EXAMPLES

The following example "robots.txt" file specifies that no robots should visit any URL starting with "cyberworld/map/" or "tmp/":

    User-agent: *
    Disallow: /cyberworld/map/  # This is an infinite virtual URL space
    Disallow: /tmp/  # these will soon disappear

This example "robots.txt" file specifies that no robots should visit any URL starting with "cyberworld/map/", except the robot called "cybermapper":

    User-agent: *
    Disallow: /cyberworld/map/  # This is an infinite virtual URL space

    # Cybermapper knows where to go.
    User-agent: cybermapper
    Disallow:

This example indicates that no robots should visit this site further:

    # go away
    User-agent: *
    Disallow: /
This is an example of a malformed robots.txt file.

```
# robots.txt for ancientcastle.example.com
# I've locked myself away.
User-agent: *
Disallow: /
# The castle is your home now, so you can go anywhere you like.
User-agent: Belle
Disallow: /west-wing/ # except the west wing!
# It's good to be the Prince...
User-agent: Beast
Disallow:
```

This file is missing the required blank lines between records.
However, the intention is clear.

=head1 SEE ALSO

L<LWP::RobotUA>, L<WWW::RobotRules::AnyDBM_File>

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  * /opt/cola/permits/1141297419_1614968609.26/0/libwww-robotrules-perl-6-02-orig-1-tar-gz/WWW-RobotRules-6.02/lib/WWW/RobotRules.pm
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Found in path(s):
  * /opt/cola/permits/1141297419_1614968609.26/0/libwww-robotrules-perl-6-02-orig-1-tar-gz/WWW-RobotRules-6.02/META.yml
No license file was found, but licenses were detected in source scan.

NAME

WWW::RobotRules - database of robots.txt-derived permissions

SYNOPSIS

```
use WWW::RobotRules;
my $rules = WWW::RobotRules->new('MOMspider/1.0');
```

```
use LWP::Simple qw(get);
```
my $url = "http://some.place/robots.txt";
my $robots_txt = get $url;
$rules->parse($url, $robots_txt) if defined $robots_txt;

my $url = "http://some.other.place/robots.txt";
my $robots_txt = get $url;
$rules->parse($url, $robots_txt) if defined $robots_txt;

# Now we can check if a URL is valid for those servers
# whose "robots.txt" files we've gotten and parsed:
if($rules->allowed($url)) {
    $c = get $url;
    ...
}

DESCRIPTION
This module parses /robots.txt files as specified in "A Standard for
Webmasters can use the /robots.txt file to forbid conforming robots from
accessing parts of their web site.

The parsed files are kept in a WWW::RobotRules object, and this object
provides methods to check if access to a given URL is prohibited. The
same WWW::RobotRules object can be used for one or more parsed
/robots.txt files on any number of hosts.

The following methods are provided:

$rules = WWW::RobotRules->new($robot_name)
    This is the constructor for WWW::RobotRules objects. The first
    argument given to new() is the name of the robot.

$rules->parse($robot_txt_url, $content, $fresh_until)
    The parse() method takes as arguments the URL that was used to
    retrieve the /robots.txt file, and the contents of the file.

$rules->allowed($uri)
    Returns TRUE if this robot is allowed to retrieve this URL.

$rules->agent([$name])
    Get/set the agent name. NOTE: Changing the agent name will clear the
    robots.txt rules and expire times out of the cache.
ROBOTS.TXT

The format and semantics of the "/robots.txt" file are as follows (this is an edited abstract of <http://www.robotstxt.org/wc/norobots.html>):

The file consists of one or more records separated by one or more blank lines. Each record contains lines of the form

```
<field-name>: <value>
```

The field name is case insensitive. Text after the '#' character on a line is ignored during parsing. This is used for comments. The following <field-names> can be used:

User-Agent
The value of this field is the name of the robot the record is describing access policy for. If more than one *User-Agent* field is present the record describes an identical access policy for more than one robot. At least one field needs to be present per record. If the value is '*', the record describes the default access policy for any robot that has not not matched any of the other records.

The *User-Agent* fields must occur before the *Disallow* fields. If a record contains a *User-Agent* field after a *Disallow* field, that constitutes a malformed record. This parser will assume that a blank line should have been placed before that *User-Agent* field, and will break the record into two. All the fields before the *User-Agent* field will constitute a record, and the *User-Agent* field will be the first field in a new record.

Disallow
The value of this field specifies a partial URL that is not to be visited. This can be a full path, or a partial path; any URL that starts with this value will not be retrieved.

Unrecognized records are ignored.

ROBOTS.TXT EXAMPLES

The following example "/robots.txt" file specifies that no robots should visit any URL starting with "/cyberworld/map/" or "/tmp/":

```
User-agent: *
Disallow: /cyberworld/map/ # This is an infinite virtual URL space
Disallow: /tmp/ # these will soon disappear
```

This example "/robots.txt" file specifies that no robots should visit any URL starting with "/cyberworld/map/", except the robot called "cybermapper":

```
User-agent: *
Disallow: /cyberworld/map/ # This is an infinite virtual URL space

# Cybermapper knows where to go.
User-agent: cybermapper
Disallow:

This example indicates that no robots should visit this site further:

# go away
User-agent: *
Disallow: /

This is an example of a malformed robots.txt file.

# robots.txt for ancientcastle.example.com
# I've locked myself away.
User-agent: *
Disallow: /
# The castle is your home now, so you can go anywhere you like.
User-agent: Belle
Disallow: /west-wing/ # except the west wing!
# It's good to be the Prince...
User-agent: Beast
Disallow:

This file is missing the required blank lines between records. However, the intention is clear.

SEE ALSO
LWP::RobotUA, WWW::RobotRules::AnyDBM_File

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Found in path(s):
* /opt/cola/permits/1141297419_1614968609.26/0/libwww-robotrules-perl-6-02-orig-1-tar-gz/WWW-RobotRules-6.02/README

1.298 afero v1.1.2
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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form
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medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
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linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
link a "work that uses the Library" with the Library to produce a
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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies
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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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1.302 libfont-afm-perl 1.20-2

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* /opt/cola/permits/1144895069_1616098494.04/0/libfont-afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/META.yml

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=head1 NAME

Font::AFM - Interface to Adobe Font Metrics files

=head1 SYNOPSIS

use Font::AFM;
$fh = new Font::AFM "Helvetica";
$copyright = $fh->Notice;
$w = $fh->Wx->{"aring"};
$w = $fh->stringwidth("Gisle", 10);
$fh->dump; # for debugging
=head1 DESCRIPTION

This module implements the Font::AFM class. Objects of this class are initialised from an AFM (Adobe Font Metrics) file and allow you to obtain information about the font and the metrics of the various glyphs in the font.

All measurements in AFM files are given in terms of units equal to 1/1000 of the scale factor of the font being used. To compute actual sizes in a document, these amounts should be multiplied by (scale factor of font)/1000.

The following methods are available:

=over 3

=item $afm = Font::AFM->new($fontname)

Object constructor. Takes the name of the font as argument. Croaks if the font can not be found.

=item $afm->latin1_wx_table()

Returns a 256-element array, where each element contains the width of the corresponding character in the iso-8859-1 character set.

=item $afm->stringwidth($string, [$fontsize])

Returns the width of the argument string. The string is assumed to be encoded in the iso-8859-1 character set. A second argument can be used to scale the width according to the font size.

=item $afm->FontName

The name of the font as presented to the PostScript language C<findfont> operator, for instance "Times-Roman".

=item $afm->FullName

Unique, human-readable name for an individual font, for instance "Times Roman".

=item $afm->FamilyName

Human-readable name for a group of fonts that are stylistic variants of a single design. All fonts that are members of such a group should have exactly the same C<FamilyName>. Example of a family name is "Times".
=item $afm->Weight

Human-readable name for the weight, or "boldness", attribute of a font. Examples are C<Roman>, C<Bold>, C<Light>.

=item $afm->ItalicAngle

Angle in degrees counterclockwise from the vertical of the dominant vertical strokes of the font.

=item $afm->IsFixedPitch

If C<true>, the font is a fixed-pitch (monospaced) font.

=item $afm->FontBBox

A string of four numbers giving the lower-left x, lower-left y, upper-right x, and upper-right y of the font bounding box. The font bounding box is the smallest rectangle enclosing the shape that would result if all the characters of the font were placed with their origins coincident, and then painted.

=item $afm->UnderlinePosition

Recommended distance from the baseline for positioning underline strokes. This number is the y coordinate of the center of the stroke.

=item $afm->UnderlineThickness

Recommended stroke width for underlining.

=item $afm->Version

Version number of the font.

=item $afm->Notice

Trademark or copyright notice, if applicable.

=item $afm->Comment

Comments found in the AFM file.

=item $afm->EncodingScheme

The name of the standard encoding scheme for the font. Most Adobe fonts use the C<AdobeStandardEncoding>. Special fonts might state
=item $afm->CapHeight

Usually the y-value of the top of the capital H.

=item $afm->XHeight

Typically the y-value of the top of the lowercase x.

=item $afm->Ascender

Typically the y-value of the top of the lowercase d.

=item $afm->Descender

Typically the y-value of the bottom of the lowercase p.

=item $afm->Wx

Returns a hash table that maps from glyph names to the width of that glyph.

=item $afm->BBox

Returns a hash table that maps from glyph names to bounding box information. The bounding box consist of four numbers: llx, lly, urx, ury.

=item $afm->dump

Dumps the content of the Font::AFM object to STDOUT. Might sometimes be useful for debugging.

=back

The AFM specification can be found at:


=head1 ENVIRONMENT

=over 10

=item METRICS

Contains the path to search for AFM-files. Format is as for the PATH environment variable. The default path built into this library is:
Kerning data and composite character data are not yet parsed.
Ligature data is not parsed.

Found in path(s):
* /opt/cola/permits/1144895069_1616098494.04/0/libfont-afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/AFM.pm
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* /opt/cola/permits/1144895069_1616098494.04/0/libfont-afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/TimesBoldItalic.pm
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* /opt/cola/permits/1144895069_1616098494.04/0/libfont-afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/HelveticaBoldOblique.pm
NAME
Font::AFM - Interface to Adobe Font Metrics files

SYNOPSIS
use Font::AFM;
$h = new Font::AFM "Helvetica";
$copyright = $h->Notice;
$w = $h->Wx->{"aring"};
$w = $h->stringwidth("Gisle", 10);
$h->dump; # for debugging

DESCRIPTION
This module implements the Font::AFM class. Objects of this
class are initialised from an AFM-file and allows you to obtain
information about the font and the metrics of the various glyphs
in the font.

All measurements in AFM files are given in terms of units equal
to 1/1000 of the scale factor of the font being used. To compute
actual sizes in a document, these amounts should be multiplied
by (scale factor of font)/1000.

The following methods are available:

$afm = Font::AFM->new($fontname)
   Object constructor. Takes the name of the font as argument.
   It will croak if the font can not be found.

$afm->latin1_wx_table()
   Returns a 256 element array, where each element contains the
   width of the corresponding character in the iso-8859-1
   character set.

$afm->stringwidth($string, [$fontsize])
   Returns the width of the string passed as argument. The
   string is assumed to be encoded in the iso-8859-1 character
   set. A second argument can be used to scale the width
   according to the font size.

$afm->FontName
   The name of the font as presented to the PostScript language
   `findfont' operator, for instance "Times-Roman".

$afm->FullName
   Unique, human-readable name for an individual font, for
   instance "Times Roman".
$afm->FamilyName
   Human-readable name for a group of fonts that are stylistic
   variants of a single design. All fonts that are member of
   such a group should have exactly the same 'FamilyName'.
   Example of a family name is "Times".

$afm->Weight
   Human-readable name for the weight, or "boldness", attribute
   of a font. Examples are 'Roman', 'Bold', 'Light'.

$afm->ItalicAngle
   Angle in degrees counterclockwise from the vertical of the
   dominant vertical strokes of the font.

$afm->IsFixedPitch
   If the value is 'true', it indicated that the font is a
   fixed-pitch (monospaced) font.

$afm->FontBBox
   A string of four numbers giving the lower-left x, lower-left
   y, upper-right x, and upper-right y of the font bounding box.
   The font bounding box is the smallest rectangle enclosing the
   shape that would result if all the characters of the font
   were placed with their origins coincident, and then painted.

$afm->UnderlinePosition
   Recommended distance from the baseline for positioning
   underline strokes. This number is the y coordinate of the
   center of the stroke.

$afm->UnderlineThickness
   Recommended stroke width for underlining.

$afm->Version
   Version number of the font.

$afm->Notice
   Trademark or copyright notice, if applicable.

$afm->Comment
   Comments found in the AFM file.

$afm->EncodingScheme
   The name of the standard encoding scheme for the font. Most
   Adobe fonts use the 'AdobeStandardEncoding'. Special fonts
   might state 'FontSpecific'.

$afm->CapHeight
Usually the y-value of the top of the capital H.

$afm->XHeight
Typically the y-value of the top of the lowercase x.

$afm->Ascender
Typically the y-value of the top of the lowercase d.

$afm->Descender
Typically the y-value of the bottom of the lowercase p.

$afm->Wx
Returns a hash table that maps from glyph names to the width of that glyph.

$afm->BBox
Returns a hash table that maps from glyph names to bounding box information. The bounding box consist of 4 numbers: llx, lly, urx, ury.

$afm->dump
Dumps the content of the Font::AFM object to STDOUT. Might sometimes be useful for debugging.

The AFM specification can be found at:

ENVIRONMENT
METRICS Contains the path to search for AFM-files. Format is as for the PATH environment variable. The default path built into this library is:

/usr/lib/afm:/usr/local/lib/afm:/usr/openwin/lib/fonts/afm/:.

BUGS
Kerning data and composite character data is not yet parsed.
Ligature data is not parsed.

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* /opt/cola/permits/1144895069_1616098494.04/0/libfont.afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/HelveticaOblique.pm

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* /opt/cola/permits/1144895069_1616098494.04/0/libfont.afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/CourierOblique.pm
* /opt/cola/permits/1144895069_1616098494.04/0/libfont.afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/CourierBoldOblique.pm
* /opt/cola/permits/1144895069_1616098494.04/0/libfont.afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/CourierBold.pm
* /opt/cola/permits/1144895069_1616098494.04/0/libfont.afm-perl-1-20-orig-1-tar-gz/Font-AFM-1.20/lib/Font/Metrics/Courier.pm

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1.303 libassuan 2.5.3-7ubuntu2

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XML-XPathEngine

This module is used to add XPath support to XML modules.

INSTALLATION

To install this module, run the following commands:

  perl Makefile.PL
  make
  make test
  make install

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abstract: 'a re-usable XPath engine for DOM-like trees'
author:
  - 'Michel Rodriguez <mirod@cpan.org>'
built requires:
  ExtUtils::MakeMaker: 0
configure requires:
  ExtUtils::MakeMaker: 0
dynamic config: 1
generated by: 'ExtUtils::MakeMaker version 6.66, CPAN::Meta::Converter version 2.120921'
license: unknown
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: 1.4
name: XML::XPathEngine
no_index:
directory:
  - t
  - inc
requires:
  Test::More: 0
  version: 0.14

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=head1 NAME

XML::XPathEngine - a re-usable XPath engine for DOM-like trees

=head1 DESCRIPTION

This module provides an XPath engine, that can be re-used by other module/classes that implement trees.

In order to use the XPath engine, nodes in the user module need to mimick DOM nodes. The degree of similitude between the user tree and a DOM dictates how much of the XPath features can be used. A module implementing all of the DOM should be able to use this module very easily (you might need to add the cmp method on nodes in order to get ordered result sets).

This code is a more or less direct copy of the L<XML::XPath> module by Matt Sergeant. I only removed the XML processing part to remove the dependency on XML::Parser, applied a couple of patches, renamed a whole lot of methods
to make Pod::Coverage happy, and changed the docs.

The article eXtending XML XPath, http://www.xmltwig.com/article/extending_xml_xpath/ should give authors who want to use this module enough background to do so.

Otherwise, my email is below :--)

B<WARNING>: while the underlying code is rather solid, this module mostly lacks docs. As they say, "patches welcome" ...

=head1 SYNOPSIS

use XML::XPathEngine;

my $tree= my_tree->new( ...);
my $xp = XML::XPathEngine->new();

my @nodeset = $xp->find('/root/kid/grandkid[1]', $tree); # find all first grankids

package XML::MyTree;

# needs to provide DOM methods

=head1 DETAILS

=head1 API

XML::XPathEngine will provide the following methods:

=head2 new

=head2 findnodes ($path, $context)

Returns a list of nodes found by $path, optionally in context $context. In scalar context returns an XML::XPathEngine::NodeSet object.

=head2 findnodes_as_string ($path, $context)

Returns the nodes found as a single string. The result is not guaranteed to be valid XML though (it could for example be just text if the query returns attribute values).

=head2 findnodes_as_strings ($path, $context)

Returns the nodes found as a list of strings, one per node found.

=head2 findvalue ($path, $context)
Returns the result as a string (the concatenation of the values of the result nodes).

=head2 findvalues($path, $context)

Returns the values of the result nodes as a list of strings.

=head2 exists ($path, $context)

Returns true if the given path exists.

=head2 matches($node, $path, $context)

Returns true if the node matches the path.

=head2 find ($path, $context)

The find function takes an XPath expression (a string) and returns either a XML::XPathEngine::NodeSet object containing the nodes it found (or empty if no nodes matched the path), or one of XML::XPathEngine::Literal (a string), XML::XPathEngine::Number, or XML::XPathEngine::Boolean. It should always return something - and you can use ->isa() to find out what it returned. If you need to check how many nodes it found you should check $nodeset->size. See L<XML::XPathEngine::NodeSet>.

=head2 getNodeText ($path)

Returns the text string for a particular node. Returns a string, or undef if the node doesn't exist.

=head2 set_namespace ($prefix, $uri)

Sets the namespace prefix mapping to the uri.

Normally in XML::XPathEngine the prefixes in XPath node tests take their context from the current node. This means that foo:bar will always match an element <foo:bar> regardless of the namespace that the prefix foo is mapped to (which might even change within the document, resulting in unexpected results). In order to make prefixes in XPath node tests actually map to a real URI, you need to enable that via a call to the set_namespace method of your XML::XPathEngine object.

=head2 clear_namespaces ()

Clears all previously set namespace mappings.

=head2 get_namespace ($prefix, $node)
Returns the uri associated to the prefix for the node (mostly for internal usage)

=head2 set_strict_namespaces ($strict)

By default, for historical as well as convenience reasons, XML::XPathEngine
has a slightly non-standard way of dealing with the default namespace.

If you search for C</tag> it will return elements C<tag>. As far as I understand it,
if the document has a default namespace, this should not return anything.
You would have to first do a C<set_namespace>, and then search using the namespace.

Passing a true value to C<set_strict_namespaces> will activate this behaviour, passing a
false value will return it to its default behaviour.

=head2 set_var ($var, $val)

Sets an XPath variable (that can be used in queries as C<$var>)

=head2 get_var ($var)

Returns the value of the XPath variable (mostly for internal usage)

=head2 $XML::XPathEngine::Namespaces

Set this to 0 if you don't want namespace processing to occur. This
will make everything a little (tiny) bit faster, but you'll suffer for it,
probably.

=head1 Node Object Model

Nodes need to provide the same API as nodes in XML::XPath (at least the access
API, not the tree manipulation one).

=head1 Example

Please see the test files in t/ for examples on how to use XPath.

=head1 XPath extension

The module supports the XPath recommendation to the same extent as XML::XPath
(that is, rather completely).

It includes a perl-specific extension: direct support for regular expressions.

You can use the usual (in Perl!) C<=> and C<!~> operators. Regular expressions
are / delimited (no other delimiter is accepted, \ inside regexp must be
backslashed), the C<imsx> modifiers can be used.

$xp->findnodes('//@att[.=~ /^v.$/]'); # returns the list of attributes att
  # whose value matches ^v.$

=head1 SEE ALSO

L<XML::XPath>

L<HTML::TreeBuilder::XPath>, L<XML::Twig::XPath> for exemples of using this module

L<Tree::XPathEngine> for a similar module for non-XML trees.

L<http://www.xmltwig.com/article/extending_xml_xpath/> for background
information. The last section of the article summarizes how to reuse XML::XPath.
As XML::XPathEngine offers the same API it should help you

=head1 AUTHOR

Michel Rodriguez, C<< <mirod@cpan.org> >>
Most code comes directly from XML::XPath, by Matt Sergeant.

=head1 BUGS

Please report any bugs or feature requests to
C<bug-tree-xpathengine@rt.cpan.org>, or through the web interface at
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=cut

1; # End of XML::XPathEngine

Found in path(s):
* /opt/cola/permits/1144894937_1616113371.2/0/libxml-xpathengine-perl-0-14-orig-1-tar-gz/XML-XPathEngine-
0.14/lib/XML/XPathEngine.pm
No license file was found, but licenses were detected in source scan.

{ system $^X . qx{ -p -i -e's{^((.*)distribution_type: module(.*))}{$1
$2license: perl$3}m' Makefile}; } 

Found in path(s):
* /opt/cola/permits/1144894937_1616113371.2/0/libxml-xpathengine-perl-0-14-orig-1-tar-gz/XML-XPathEngine-0.14/Makefile.PL

1.309 x-crypto v0.0.0-20191227163750-53104e6ec876

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1.310 libssh 0.9.3-2ubuntu2.1

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1.311 mpfr 4.0.2 1

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1.313 at-spi2-core 2.36.0-2

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That's all there is to it!
1.314 xfonts-utils 7.7+6

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1.315 libffi 3.3 4

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colperl 0.56-7
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X11::Protocol, version 0.56

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module is free software; you can redistribute and/or modify it under
the same terms as Perl itself. (As an exception, the file Keysyms.pm,
which is derived from a file in the standard X11 distribution, has
another, less restrictive copying policy, as do some of the extension
modules in the directory Protocol/Ext: see those files for details).

Module list information:123456789 123456789 123456789 123456789 12345
X11::ProtocolbdpORaw interface to X Window System servers
X11::KeysymsbdpX11 key symbols (translation of keysymdef.h)
X11::AuthbdpORead and handle X11 `.Xauthority' files
New in this version: 0.56
- Fix infinite loop regression in robust_req in 0.54 caused by handle_input change (found by Scott Smedley)

X11::Protocol and the related modules in this distribution are a rough equivalent of Xlib (libX11.a, with a bit of Xau and Xext mixed in) used for drawing windows on and otherwise manipulating X11 window servers.

OTHER THINGS YOU NEED

* perl 5.002  (not tested recently; recent versions have only been tested with 5.6.1 and 5.8. *)

* an X11 server  (any version; it also doesn't have to be on the same computer, but you must be able to connect to it over a socket. For Win32 users, a free X server is apparently available from http://sources.redhat.com/win32-x11/ though the author hasn't tried it)

* documentation about the X protocol (or at least Xlib, though its interface is subtly different). O'Reilly has a good book, and the full X distribution includes the definitive specification. As of this writing, the latest version can be obtained from the X.org CVS repository at:


For Debian users, this is in the xspecs package.

Speaking of Debian, this module is also available as the Debian package libr11-protocol-perl”; thanks to tony mancill for maintaing it.

************************** NOTE **************************

* This module's interface is admittedly a little cumbersome, but its low level approach is intentional -- a higher-level interface, ideally more object-oriented, easier to use, and plug-compatible with an XS Xlib interface, would be a significant different project. (A project that this author made an abortive start at, but is unlikely to finish any time soon.)

* The interface has become stable, though mainly through the passage of time rather than through extensive debugging. For better or worse, it probably isn't going to change much in the future.
BUILDING

Like any other module:
% perl Makefile.PL
% make
% make test
% make install

DOCUMENTATION

See the PODs at the end of each .pm file (converted into man pages by the makefile) for detailed information.

EXAMPLES

test.pl is a good example of a complete program; it's also a translation of an Xlib based program (in this case, 'basicwin' from O'Reilly & Associates's _Xlib Programming Manual_).

eg/anim.pl demonstrates buffered animation (needs IO::Select).

eg/full_test.pl uses just about every request in the protocol. You might not want to run it, since it can do weird things to your display, but it can show prototype calls if the documentation is unclear.

eg/long-run.pl is a program that runs for a long time and allocates many windows, as a stress-test of the resource-allocation code.

eg/random-win.pl demonstrates how to handle requests that might cause errors, by picking random resource identifiers and trying to paint over them with randomly-colored rectangles if they're windows.

eg/render-clock.pl uses the Render extension to draw an analog clock. It's a bit spiffier that "xclock -render", in the author's opinion, but falls short of fdclock.

eg/render-test.pl is like eg/full_test.pl, but just for the Render extension.

eg/teletype.pl shows how it's possible to connect to more than one server at once (needs IO::Select).

eg/widgets* are four examples that all do the same thing, with different tools. They demonstrate all the techniques needed to implement two simple custom widgets, a scroll bar and a progress meter, with the different options available for X11 programming from
Perl:
- widgets1.pl uses X11::Protocol directly.
- widgets2.pl uses X11::Xlib from Ken Fox's X11-Motif distribution. Unfortunately, even the latest alpha version of X11-Motif I could find is missing interfaces for all the needed Xlib structures, so the code has to play games with pack() to get some calls to work. This may mean that the example may not work correctly on platforms whose Xlib structures are laid out differently that the author's Linux x86 box.
- widgets3.pl is an example of how programming X11::Protocol would be nicer with an object-oriented interface. It's a blatant ploy to get someone else interested in picking up the half-finished library it depends on, which is not included in this distribution (email me if you're interested).
- widgets.c is a C Xlib version of the same functionality, for comparison.

eg/wintree.pl is a small utility that acts like a cross between xlsclients, xwininfo, and pstree, showing the hierarchy of windows and their parents. Takes a window ID, or by default starts at the root. "-g" includes geometries, "-v" uses VT100 line drawing characters. Beware that it hardcodes some assumptions about how certain X servers encode resource IDs.

SEE ALSO

This is by no means the only way to put windows up on an X server from Perl.

* The most popular and portable windowing interface for Perl is Perl/Tk, available as the Tk module from CPAN.

* The Motif widget set, as well as some support for other standard X libraries, can be accessed using the X11::Motif modules, available from CPAN. An alpha version is also available directly from the author's homepage at http://www.vulpes.com/X11-Motif-1.2a8.tar.gz .

* Several other X widgets sets have Perl interfaces, including GTK (the Gtk module), Qt (the PerlQt module), wxWindows (the Wx module) and Sx (a simplified Athena wrapper, available with the similarly named module).

* For testing or automating the operation of other programs, the Xlib-based X11::GUITest module has more specialized support, including for the XTEST extension, than X11::Protocol.

Send questions, bug reports, and feature requests to me, Stephen
McCaman <SMCCAM@cpan.org>.

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  * /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/README

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  * /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/Protocol/Ext/XFree86.Misc.pm
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  * /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/Protocol/Connection/UNIXSocket.pm
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* /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/Protocol/Connection/Socket.pm
* /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/Auth.pm
* /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/Protocol/Constants.pm
No license file was found, but licenses were detected in source scan.

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# program is free software; you can redistribute and/or modify it
# under the same terms as Perl itself.

Found in path(s):
* /opt/cola/permits/1125633967_1611366616.19/0/libx11-protocol-perl-0.56-orig-tar-gz/X11-Protocol-0.56/Protocol/Connection/FileHandle.pm

1.317 libfile-mimeinfo-perl 0.29-1
1.317.1 Available under license :
No license file was found, but licenses were detected in source scan.

# File-MimeInfo
This module can be used to determine the mime type of a file; it's a replacement for [File::MMagic](https://metacpan.org/pod/File::MMagic) trying to implement the freedesktop specification for using the shared mime-info database. The package comes with a script called `mimetype` that can be used as a `file(1)` work-alike.

## INSTALLATION

To install this module type the following:

```
perl Makefile.PL
make
make test
make install
```

## DEPENDENCIES

This module expects the freedesktop mime database to be installed, some linux distributions include it, otherwise it can obtained from:

http://freedesktop.org/Software/shared-mime-info

This module requires these other modules which can be obtained from the [CPAN](https://metacpan.org) if they are not already installed on your system:

* Carp
* Exporter
* Fcntl
* Pod::Usage
* File::Basename
* File::BaseDir
* File::DesktopEntry

## COPYRIGHT AND LICENCE

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Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/README.md
No license file was found, but licenses were detected in source scan.
File::MimeInfo - Determine file type from the file name

=head1 SYNOPSIS

use File::MimeInfo;
my $mime_type = mimetype($file);
my $mime_type2 = mimetype('test.png');

=head1 DESCRIPTION

This module can be used to determine the mime type of a file. It tries to implement the freedesktop specification for a shared MIME database.

For this module shared-mime-info-spec 0.13 was used.

This package only uses the globs file. No real magic checking is used. The L<File::MimeInfo::Magic> package is provided for magic typing.

If you want to determine the mimetype of data in a memory buffer you should use L<File::MimeInfo::Magic> in combination with L<IO::Scalar>.

This module loads the various data files when needed. If you want to hash data earlier see the C<rehash> methods below.

=head1 EXPORT

The method C<mimetype> is exported by default. The methods C<inodetype>, C<globs>, C<extensions>, C<describe>, C<mimetype_canon> and C<mimetype_isa> can be exported on demand.

=head1 METHODS

=over 4

=item C<new()>

Simple constructor to allow Object Oriented use of this module. If you want to use this, include the package as C<use File::MimeInfo ();> to avoid importing sub C<mimetype()>.

=item C<mimetype($file)>

Returns a mimetype string for C<$file>, returns undef on failure.

This method bundles C<inodetype> and C<globs>.

If these methods are unsuccessful the file is read and the mimetype defaults
to 'text/plain' or to 'application/octet-stream' when the first ten chars
of the file match ascii control chars (white spaces excluded).
If the file doesn't exist or isn't readable C<undef> is returned.

=item C<inodetype($file)>

Returns a mimetype in the 'inode' namespace or undef when the file is
actually a normal file.

=item C<globs($file)>

Returns a mimetype string for C<$file> based on the filename and filename extensions.
Returns undef on failure. The file doesn't need to exist.

Behaviour in list context (wantarray) is unspecified and will change in future
releases.

=item C<default($file)>

This method decides whether a file is binary or plain text by looking at
the first few bytes in the file. Used to decide between "text/plain" and
"application/octet-stream" if all other methods have failed.

The spec states that we should check for the ascii control chars and let
higher bit chars pass to allow utf8. We try to be more intelligent using
perl utf8 support.

=item C<extensions($mimetype)>

In list context, returns the list of filename extensions that map to the given mimetype.
In scalar context, returns the first extension that is found in the database
for this mimetype.

=item C<describe($mimetype, $lang)>

Returns a description of this mimetype as supplied by the mime info database.
You can specify a language with the optional parameter C<$lang>, this should be
the two letter language code used in the xml files. Also you can set the global
variable C<$File::MimeInfo::LANG> to specify a language.

This method returns undef when no xml file was found (i.e. the mimetype
doesn't exist in the database). It returns an empty string when the xml file doesn't
contain a description in the language you specified.

I<Currently no real xml parsing is done, it trusts the xml files are nicely formatted.>

=item C<mimetype_canon($mimetype)>
Returns the canonical mimetype for a given mimetype. Deprecation mimetypes are typically aliased to their canonical variants. This method only checks aliases, doesn't check whether the mimetype exists.

Use this method as a filter when you take a mimetype as input.

=item C<mimetype_isa($mimetype)>

=item C<mimetype_isa($mimetype, $mimetype)>

When given only one argument this method returns a list with mimetypes that are parent classes for this mimetype.

When given two arguments returns true if the second mimetype is a parent class of the first one.

This method checks the subclasses table and applies a few rules for implicit subclasses.

=item C<rehash()>

Rehash the data files. Glob information is preparsed when this method is called.

If you want to by-pass the XDG basedir system you can specify your database directories by setting C<@File::MimeInfo::DIRS>. But normally it is better to change the XDG basedir environment variables.

=item C<rehash_aliases()>

Rehashes the F<mime/aliases> files.

=item C<rehash_subclasses()>

Rehashes the F<mime/subclasses> files.

=back

=head1 DIAGNOSTICS

This module throws an exception when it can't find any data files, when it can't open a data file it found for reading or when a subroutine doesn't get enough arguments. In the first case you either don't have the freedesktop mime info database installed, or your environment variables point to the wrong places. In the second case you have the database installed, but it is broken (the mime info database should logically be world readable).

=head1 TODO
Make an option for using some caching mechanism to reduce init time.

Make C<describe()> use real xml parsing ?

=head1 LIMITATIONS

Perl versions prior to 5.8.0 do not have the `:utf8` IO Layer, thus for the default method and for reading the xml files utf8 is not supported for these versions.

Since it is not possible to distinguish between encoding types (utf8, latin1, latin2 etc.) in a straightforward manner only utf8 is supported (because the spec recommends this).

This module does not yet check extended attributes for a mimetype. Patches for this are very welcome.

=head1 AUTHOR

Jaap Karssenberg E<lt>pardus@cpan.orgE<gt>
Maintained by Michiel Beijen E<lt>michiel.beijen@gmail.comE<gt>

=head1 COPYRIGHT

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=head1 SEE ALSO

L<File::BaseDir>,
L<File::MimeInfo::Magic>,
L<File::MimeInfo::Applications>,
L<File::MimeInfo::Rox>

=over 4

=item related CPAN modules

L<File::MMagic>

=item freedesktop specifications used

L<http://www.freedesktop.org/wiki/Specifications/shared-mime-info-spec>,
L<http://www.freedesktop.org/wiki/Specifications/basedir-spec>,
L<http://www.freedesktop.org/wiki/Specifications/desktop-entry-spec>

=item freedesktop mime database
mimetype - Determine file type

NAME

mimetype - Determine file type

SYNOPSIS

mimetype [options] [-] files

DESCRIPTION

This script tries to determine the mime type of a file using the Shared MIME-info database. It is intended as a kind of file(1) work-alike, but uses mimetypes instead of descriptions.

If one symlinks the file command to mimetype it will behave a little more compatible, see file(--file-compat).

Commandline options to specify alternative magic files are not implemented the same because of the conflicting data formats. Also the wording of the descriptions will differ.

For naming switches I followed the manpage of file(1) version 4.02 when possible. They seem to differ completely from the spec in the 'utilities' chapter of IEEE Std 1003.1-2001 (POSIX).

OPTIONS

-a, --all

Show output of all rules that match the file.

TODO: this method now just returns one match for each method (globs, magic, etc.).
Do not prepend filenames to output lines (brief mode).

=item B<--database>=I<mimedir>:I<mimedir>:...

Force the program to look in these directories for the shared mime-info
database. The directories specified by the basedir specification
are ignored.

=item B<-d>, B<--describe>

Print file descriptions instead of mime types, this is the
default when using L</--file-compat>.

=item B<-D>, B<--debug>

Print debug information about how the mimetype was determined.

=item B<-f> I<namefile>, B<--namefile>=I<namefile>

Read the names of the files to be examined from the file 'namefile'
(one per line) before the argument list.

=item B<--file-compat>

Make mimetype behave a little more L<file(1)> compatible. This
is turned on automatically when you call mimetype by a link
called 'file'.

A single '-' won't be considered a separator
between options and filenames anymore, but becomes identical to L</--stdin>.
( You can still use '-' as separator, but
that is not backward compatible with the original file command. )
Also the default becomes to print descriptions instead of mimetypes.

=item B<-F> I<string>, B<--separator>=I<string>

Use string as custom separator between the file name and its mimetype
or description, defaults to '.'.

=item B<-h>, B<--help>

=item B<-u>, B<--usage>

Print a help message and exits.

=item B<-i>, B<--mimetype>
Use mime types, opposite to `L</--describe>`,
this is the default when _not_ using `L</--file-compat>`.

`=item B</-L>, B</--dereference>`

Follow symbolic links.

`=item B</-l> I<code>, B</--language>=I<code>`

The language attribute specifies a two letter language code, this makes
descriptions being outputted in the specified language.

`=item B</-M>, B</--magic-only>`

Do not check for extensions, globs or inode type, only look at the content
of the file. This is particularly useful if for some reason you don't trust
the name or the extension a file has.

`=item B</-N>, B</--noalign>`

Do not align output fields.

`=item B</--output-format>`

If you want an alternative output format, you can specify a format string
containing the following escapes:

`%f` for the filename
`%d` description
`%m` mime type

Alignment is not available when using this,
you need to post-process the output to do that.

`=item B</--stdin>`

Determine type of content from STDIN, less powerful then normal file checking
because it only uses magic typing. This will happen also if the STDIN filehandle
is a pipe.

To use this option `L<IO::Scalar>` needs to be installed.

`=item B</-v>, B</--version>`

Print the version of the program and exit.
ENVIRONMENT

Over 4

Item XDG_DATA_HOME

Item XDG_DATA_DIRS

These variables can list base directories to search for data files. The shared mime-info will be expected in the "mime" sub directory of one of these directories. If these are not set, there will be searched for the following directories:

$HOME/.local/share/mime
/usr/local/share/mime
/usr/share/mime

See also the "XDG Base Directory Specification"
L<http://freedesktop.org/Standards/basedir-spec>

back

FILES

The base dir for all data files is determined by two environment variables, see L</ENVIRONMENT>.

Over 4

Item F<BASE/mime/packages/SOURCE.xml>

All other files are compiled from these source files. To re-compile them use B<update-mime-database(1)>.

Item F<BASE/mime/globs>

Compiled information about globs.

Item F<BASE/mime/magic>

Compiled information about magic numbers.

Item F<BASE/mime/MEDIA/SUBTYPE.xml>

Descriptions of a mimetype in multiple languages, used for the L</--describe> switch.

back
=head1 DIAGNOSTICS

If a file has an empty mimetype or an empty description, most probably the file doesn't exist and the given name doesn't match any globs. An empty description can also mean that there is no description available in the language you specified.

The program exits with a non-zero exit value if either the commandline arguments failed, a module it depends on wasn't found or the shared mime-info database wasn't accessible. See L<File::MimeInfo> for more details.

=head1 TODO

The 'all' switch doesn't really show all matches, but only one per mime-typing method. This needs to be implemented in the modules first.

=head1 BUGS

If you find bugs, please file them in our Github issue tracker at L<https://github.com/mbeijen/File-MimeInfo/issues>.

B<mimetype> doesn't provide a switch for looking inside compressed files because it seems to me that this can only be done by un-compressing the file, something that defeats the purpose. On the other hand the option should exist for strict compatibility with file(1). Possibly a subclass should be made for this one day.

=head1 AUTHOR

Jaap Karssenberg E<lt>pardus@cpan.orgE<gt>
Maintained by Michiel Beijen E<lt>michiel.beijen@gmail.comE<gt>

=head1 COPYRIGHT

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=head1 SEE ALSO

L<file(1)>,
L<update-mime-database(1)>,
L<File::MimeInfo(3)>,
L<http://freedesktop.org/Software/shared-mime-info>

Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/mimetype

No license file was found, but licenses were detected in source scan.

=head1 NAME

mimeopen - Open files by mimetype

=head1 SYNOPSIS

mimeopen [options] [-] files

=head1 DESCRIPTION

This script tries to determine the mimetype of a file and open it with the default desktop application. If no default application is configured the user is prompted with an "open with" menu in the terminal.

To use this script you need the freedesktop mime-info database and the freedesktop desktop-file-utils package. See L<File::MimeInfo::Applications(3)> for more details.

=head1 OPTIONS

=over 4

=item B<-a>, B<--ask>

Do not execute the default application but ask which application to run. This does not change the default application.

=item B<-d>, B<--ask-default>

Let the user choose a new default program for given files.

=item B<-n>, B<--no-ask>

Don't ask the user which program to use. Choose the default program or the first program known to handle the file mimetype. This does not set the default application.

=item B<-M>, B<--magic-only>

Do not check for extensions, globs or inode type, only look at the content of the file. This is particularly useful if for some reason you don't trust
the name or the extension a file has.

=item B <--database>=I<mimedir>:I<mimedir>:...=

Force the program to look in these directories for the shared mime-info database. The directories specified by the basedir specification are ignored.

=item B <-D>, B <--debug>

Print debug information about how the mimetype was determined.

=item B <-h>, B <--help>

=item B <-u>, B <--usage>

Print a help message and exits.

=item B <-v>, B <--version>

Print the version of the program and exit.

=back

=head1 DEPRECATED OPTIONS

=over 4

=item B <-L>, B <--dereference>

Follow symbolic links. Deprecated because this is the logical default for this command. Ignored silently.

=back

=head1 BUGS

If you find bugs, please file them in our Github issue tracker at L<https://github.com/mbeijen/File-MimeInfo/issues>.

See File::MimeInfo::Applications(3) and File::DesktopEntry(3) for some limitations.

=head1 AUTHOR

Jaap Karssenberg E<lt>pardus@cpan.orgE<gt>
Maintained by Michiel Beijen E<lt>michiel.beijen@gmail.comE<gt>
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SEE ALSO

*mimetype(1)*, *
<update-mime-database(1)>, *
<update-desktop-database(1)>, *
<File::MimeInfo(3)>, *
<File::MimeInfo::Applications(3)>

Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/mimeopen
No license file was found, but licenses were detected in source scan.

NAME

File::MimeInfo::Magic - Determine file type with magic

SYNOPSIS

use File::MimeInfo::Magic;
my $mime_type = mimetype($file);

DESCRIPTION

This module inherits from File::MimeInfo, it is transparent to its functions but adds support for the freedesktop magic file.

Magic data is hashed when you need it for the first time. If you want to force hashing earlier use the rehash() function.

EXPORT

The method mimetype is exported by default. The methods magic, inodetype, globs and describe can be exported on demand.

METHODS
See also L<File::MimeInfo> for methods that are inherited.

=item C<mimetype($file)>

Returns a mime-type string for C<$file>, returns undef on failure.

This method bundles C<inodetype()>, C<globs()> and C<magic()>.

Magic rules with an priority of 80 and higher are checked before C<globs()> is called, all other magic rules afterwards.

If this doesn't work the file is read and the mime-type defaults to 'text/plain' or to 'application/octet-stream' when the first ten chars of the file match ascii control chars (white spaces excluded).
If the file doesn't exist or isn't readable C<undef> is returned.

If C<$file> is an object reference only C<magic> and the default method are used. See below for details.

=item C<magic($file)>

Returns a mime-type string for C<$file> based on the magic rules, returns undef on failure.

C<$file> can be an object reference, in that case it is supposed to have a C<seek()> and a C<read()> method. This allows you for example to determine the mimetype of data in memory by using L<IO::Scalar>.

Be aware that when using a filehandle or an C<IO::> object you need to set the C<utf8> binmode yourself if appropriate.

=item C<rehash()>  

Rehash the data files. Glob and magic information is preparsed when this method is called.

If you want to by-pass the XDG basedir system you can specify your database directories by setting C<@File::MimeInfo::DIRS>. But normally it is better to change the XDG basedir environment variables.
These routines are imported from L<File::MimeInfo>.

=head1 SEE ALSO

L<File::MimeInfo>

=head1 LIMITATIONS

Only word sizes of 1, 2 or 4 are supported. Any other word size is ignored and will cause a warning.

=head1 AUTHOR

Jaap Karssenberg E<lt>pardus@cpan.orgE<gt>
Maintained by Michiel Beijen E<lt>michiel.beijen@gmail.comE<gt>

=head1 COPYRIGHT

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Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/lib/File/MimeInfo/Magic.pm
No license file was found, but licenses were detected in source scan.

=head1 NAME

File::MimeInfo::Cookbook - various code snippets

=head1 DESCRIPTION

Some code snippets for non-basic uses of the L<File::MimeInfo> module:

=over

=item B<Matching an extension>

A file does not have to actually exist in order to get a mimetype for it. This means that the following will work:
my $extension = '*.txt';
my $mimetype = mimetype($extension);

=item B<Mimetyping an scalar>

If you want to find the mimetype of a scalar value you need magic
mimetyping; after all a scalar doesn't have a filename or inode.
What you need to do is to use IO::Scalar:

use File::MimeInfo::Magic;
use IO::Scalar;

my $io_scalar = new IO::Scalar($data);
my $mimetype = mimetype($io_scalar);

In fact most other C<IO::> will work as long as they support the C<seek()> and C<read()> methods. Of course if you want really obscure things to happen you can always write your own IO object and feed it in there.

Be aware that when using a filehandle like this you need to set the C<utf8> binmode yourself if appropriate.

=item B<Mimetyping a filehandle>

Regrettably for non-seekable filehandles like STDIN simply using an C<IO::> object will not work. You will need to buffer enough of the data for a proper mimetyping. For example you could mimetype data from STDIN like this:

use File::MimeInfo::Magic;
use IO::Scalar;

my $data;
read(STDIN, $data, $File::MimeInfo::Magic::max_buffer);
my $io_scalar = new IO::Scalar($data);
my $mimetype = mimetype($io_scalar);

Be aware that when using a filehandle like this you need to set the C<utf8> binmode yourself if appropriate.

=item B<Creating a new filename>

Say you have a temporary file that you want to save with a more proper filename.

use File::MimeInfo::Magic qw#mimetype extensions#;
use File::Copy;
my $tmpfile = '/tmp/foo';
my $mimetype = mimetype($tmpfile);
my $extension = extensions($mimetype);
my $newfile = 'untitled1';
$newfile .= '.'.$extension if length $extension;
move($tmpfile, $newfile);

=item B<Force the use of a certain database directory>

Normally you just need to add the dir where your mime database lives
to either the XDG_DATA_HOME or XDG_DATA_DIRS environment variables
for it to be found. But in some rare cases you may want to by-pass
this system all together. Try one of the following:

@File::MimeInfo::DIRS = ('/home/me/share/mime');
eval 'use File::MimeInfo';
die if $@;

or:

use File::MimeInfo;
@File::MimeInfo::DIRS = ('/home/me/share/mime');
File::MimeInfo->rehash();

This can also be used for switching between databases at run time
while leaving other XDG configuration stuff alone.

=back

=head1 AUTHOR

Jaap Karssenberg E<lt>pardus@cpan.orgE<gt>
Maintained by Michiel Beijen E<lt>michiel.beijen@gmail.comE<gt>

=head1 COPYRIGHT

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modify it under the same terms as Perl itself.

=head1 SEE ALSO

L<File::MimeInfo>

=cut

Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-
File::MimeInfo::Applications - Find programs to open a file by mimetype

=head1 SYNOPSIS

use File::MimeInfo::Magic;
use File::MimeInfo::Applications;

my $file = '/foo/bar';
my $mimetype = mimetype($file)
    || die "Could not find mimetype for $file\n";

my ($default, @other) = mime_applications($mimetype);

if (defined $default) {
    $default->system($file)
} else {
    # prompt user with choice from @others
    # ...
}

=head1 DESCRIPTION

This module tries to find applications that can open files
with a certain mimetype. This is done in the way suggested by
the freedesktop Desktop Entry specification. This module is
intended to be compatible with file managers and other applications that
implement this specification.

This module depends on L<File::DesktopEntry> being installed.

To use this module effectively you need to have the desktop-file-utils
package from freedesktop and run update-desktop-database after installing
new .desktop files.

At the moment of writing this module is compatible with the way Nautilus (Gnome)
and with Thunar (XFCE) handle applications for mimetypes. I understand KDE
is still working on implementing the freedesktop mime specifications but will
follow. At the very least all perl applications using this module are using
the same defaults.

=head1 EXPORT
All methods are exported by default.

=head1 METHODS

=over 4

=item C<mime_applications(MIMETYPE)>

Returns an array of L<File::DesktopEntry> objects. The first
is the default application for this mimetype, the rest are
applications that say they can handle this mimetype.

If the first result is undefined there is no default application
and it is good practice to ask the user which application he wants
to use.

=item C<mime_applications_all(MIMETYPE)>

Like C<mime_applications()> but also takes into account applications that
can open mimetypes from which MIMETYPE inherits. Parent mimetypes tell
something about the data format, all code inherits from text/plain for example.

=item C<mime_applications_set_default(MIMETYPE, APPLICATION)>

Save a default application for this mimetype. This action will
affect other applications using the same mechanism to find a default
application.

APPLICATION can either be a File::DesktopEntry object or
the basename of a .desktop file.

=item C<mime_applications_set_custom(MIMETYPE, COMMAND)>

Save a custom shell command as default application.
Generates a DesktopEntry file on the fly and calls
C<mime_applications_set_custom>. Returns the DesktopEntry object.

No checks are done at all on COMMAND.
It should however contain at least one word.

=back

=head1 NOTES

This module looks for associations files in the order specified in version 1.0
of the MIME applications specification. It will also attempt a last-resort
fallback to the legacy file
F<$XDG_DATA_HOME/applications/defaults.list>. In all cases, it will only write
to the recommended per-user defaults file located at
F<$XDG_CONFIG_HOME/mimeapps.list>.

=head1 AUTHOR

Jaap Karssenberg E<lt>pardus@cpan.orgE<gt>
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=head1 COPYRIGHT

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=head1 SEE ALSO

L<File::DesktopEntry>,
L<File::MimeInfo>,
L<File::MimeInfo::Magic>,
L<File::BaseDir>

L<http://freedesktop.org/wiki/Specifications/mime-apps-spec/>

=cut

Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/lib/File/MimeInfo/Applications.pm
No license file was found, but licenses were detected in source scan.

=head1 NAME

File::MimeInfo::Rox - Open files by mimetype "Rox style"

=head1 SYNOPSIS

use File::MimeInfo::Magic;
use File::MimeInfo::Rox qw/:magic/;

# open some file with the appropriate program
mime_system($somefile);

# more verbose version
my $mt = mimetype($somefile)
    || die "Could not find mimetype for $somefile\n";
mime_system($somefile, $mt)
    || die "No program to open $somefile available\n"

=head1 DESCRIPTION

This module tries to mimic the behaviour of the rox file
browser L<http://rox.sf.net> when "opening" data files.
It determines the mime type and searches in rox's C<Choices>
directories for a program to handle that mimetype.

See the rox documentation for an extensive discussion of this
mechanism.

=head1 EXPORT

The methods C<mime_exec> and C<mime_system> are exported,
if you use the export tag C:<magic> you get the same methods
but L<File::MimeInfo::Magic> will be used for mimetype lookup.

=head1 ENVIRONMENT

The environment variable C<CHOICESPATH> is used when searching
for rox's config dirs. It defaults to
C<$ENV{HOME}/Choices:/usr/local/share/Choices:/usr/share/Choices>

=head1 METHODS

=over 4

=item C<mime_system($file)>

=item C<mime_system($file, $mimetype, @_)>

Try to open C<$file> with the appropriate program for files of
it's mimetype. You can use C<$mimetype> to force the mimetype.
Also if you already know the mimetype it saves a lot of time
to just tell it.

If either the mimetype couldn't be determined or
no appropriate program could be found C<undef> is returned.
If the actual L<system> fails an exception is raised.

All remaining arguments are passed on to the handler.

=item C<mime_exec($file)>

=item C<mime_exec($file, $mimetype, @_)>
Like C\texttt{mime\_system()} but uses L\texttt{exec} instead of L\texttt{system}, so it B\texttt{never returns} if successful.

=item C\texttt{suggest\_script\_name($mimetype)}

Returns the list C\texttt{($dir, $file)} for the suggested place to write new script files (or symlinks) for mimetype C\texttt{$mimetype}.

The suggested dir doesn't need to exist.

=back

=head1 AUTHOR

Jaap Karssenberg \texttt{pardus@cpan.org}
Maintained by Michiel Beijen \texttt{michiel.beijen@gmail.com}

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=head1 SEE ALSO

L\texttt{File::MimeInfo},
L\texttt{File::MimeInfo::Magic},
L\texttt{http://rox.sourceforge.net}

cut

Found in path(s):
* /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/lib/File/MimeInfo/Rox.pm
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---

abstract: 'Determine file types'
author:
- 'Jaap Karssenberg <pardus@cpan.org>'
build_requires:
  Carp: '0'
 Exporter: '0'
  Fcntl: '0'
  File::BaseDir: '0.03'
  File::DesktopEntry: '0.04'
  Pod::Usage: '0'
  Test::More: '0.88'
configure_requires:
  ExtUtils::MakeMaker: '6.30'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.18, CPAN::Meta::Converter version 2.150010'
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: '1.4'
name: File-MimeInfo
no_index:
directory:
  - t
  - inc
requires:
  perl: '5.006001'
resources:
  bugtracker: https://github.com/mbeijen/File-MimeInfo/issues
  repository: https://github.com/mbeijen/File-MimeInfo
version: '0.29'
x_contributors:
  - 'Bernhard Rosenkr€=§nzer <bero@lindev.ch>'
  - 'Christian Ludwig <chrissicool@gmail.com>'
  - 'David Steinbrunner <dsteinbrunner@pobox.com>'
  - 'Jitka Plesnikova <jplesnik@redhat.com>'
  - 'Jonathan Dowland <j mdl@debian.org>'
  - 'Michiel Beijen <michiel.beijen@gmail.com>'
  - 'Nitish Bezzala <nbezzala@yahoo.com>'
  - 'Patrick Burroughs (Celti) <celti@celti.name>'
  - 'Rudolf Leermakers <rudolf@hatsuseno.org>'
  - 'Sean Smith <ssmith@ncsgraphics.com>'
  - 'Stefan Seifert <nine@detonation.org>'
  - 'brian m. carlson <sandals@crustytoothpaste.net>'
  - 'gregor herrmann <gregoa@debian.org>'
x_serialization_backend: 'CPAN::Meta::YAML version 0.018'

Found in path(s):
  * /opt/cola/permits/1144894964_1616102004.47/0/libfile-mimeinfo-perl-0-29-orig-1-tar-gz/File-MimeInfo-0.29/META.yml

1.318 iptables 1.8.4-3ubuntu2

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1.319 xft 2.3.3-0ubuntu1

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1.324 icu 67

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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
```
%.S(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%:o: $(srcdir)/%.c

%.S(DYNAMIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<
%:o: $(srcdir)/%.cpp

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s/(.*).o\[ :\]*/\1.o $@ : /g" > $@; \n| -s $@ ]|| rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "s/(.*).o\[ :\]*/\1.o $@ : /g" > $@; \n| -s $@ ]|| rm -f $@'

## Versioned libraries rules
%.S$(SO).$(SO_TARGET_VERSION_MAJOR): %.S$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%.S$(SO): %.S$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s {*F}.$(SO).$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup

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1.328 curl 7.68.0-1ubuntu2.5

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===============

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### 1.329 iconv 2.31

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Version 3, 29 June 2007

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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification").

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1.
above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do
this, you must alter all the notices that refer to this License, so
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This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or
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under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
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If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
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5. A program that contains no derivative of any portion of the
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However, linking a "work that uses the Library" with the Library
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contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or
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a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy
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d) Verify that the user has already received a copy of these
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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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of using an interface provided by the Library.

A "Combined Work" is a work produced by combining or linking an
Application with the Library. The particular version of the Library
with which the Combined Work was made is also called the "Linked
Version".

The "Minimal Corresponding Source" for a Combined Work means the
Corresponding Source for the Combined Work, excluding any source code
for portions of the Combined Work that, considered in isolation, are
based on the Application, and not on the Linked Version.

The "Corresponding Application Code" for a Combined Work means the
object code and/or source code for the Application, including any data
and utility programs needed for reproducing the Combined Work from the
Application, but excluding the System Libraries of the Combined Work.

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If you modify a copy of the Library, and, in your modifications, a facility refers to a function or data to be supplied by an Application that uses the facility (other than as an argument passed when the facility is invoked), then you may convey a copy of the modified version:

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The object code form of an Application may incorporate material from a header file that is part of the Library. You may convey such object code under terms of your choice, provided that, if the incorporated material is not limited to numerical parameters, data structure layouts and accessors, or small macros, inline functions and templates (ten or fewer lines in length), you do both of the following:

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b) Give prominent notice with the combined library that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.
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1.331 glibc 2.31-0ubuntu9.2

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.
In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
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functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or
link a "work that uses the Library" with the Library to produce a
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a) Accompany the work with the complete corresponding
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uses the Library", as object code and/or source code, so that the
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Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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d) If a facility in the modified Library refers to a function or a
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in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
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In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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