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```
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 */
```

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1.4 base-files 3.0.14 :r74.2

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necessary. Here is a sample; alter the names:

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`Gnomovision' (which makes passes at compilers) written by James Hacker.

    <signature of Ty Coon>, 1 April 1989 
    Ty Coon, President of Vice 

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Name: base-passwd 
Maintainer: Colin Watson <cjwatson@debian.org>

Files: update-passwd.c, man/* 
Copyright: Copyright 1999-2002 Wichert Akkerman <wichert@deephackmode.org> 
Copyright 2002, 2003, 2004 Colin Watson <cjwatson@debian.org>
License: GPL-2
Files: passwd.master, group.master
License: PD
X-Notes: Originally written by Ian Murdock <imurdock@debian.org> and Bruce Perens <bruce@pixar.com>.

License: GPL-2
Files: doc/*
Copyright: Copyright 2001, 2002 Joey Hess
Copyright 2007 David Mandelberg
License: GPL-2

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1.6 bash 4.2 :r8
1.6.1 Available under license :
From friedman@cli.com Thu May 25 12:19:06 1995
Flags: 10
Return-Path: friedman@cli.com
Received: from po.cwru.edu (root@po.CWRU.Edu [129.22.4.2]) by odin.INS.CWRU.Edu with ESMTP (8.6.10+cwru/CWRU-2.1-ins)
id MAA08685; Thu, 25 May 1995 12:19:05 -0400 (from friedman@cli.com for <chet@odin.INS.CWRU.Edu>)
Received: from cli.com (cli.com [192.31.85.1]) by po.cwru.edu with SMTP (8.6.10+cwru/CWRU-2.3)
id MAA11299; Thu, 25 May 1995 12:19:00 -0400 (from friedman@cli.com for <chet@po.cwru.edu>)
Received: from tepui.cli.com by cli.com (4.1/SMI-4.1)
id AA27213; Thu, 25 May 95 11:18:25 CDT
Received: by tepui.cli.com (4.1) id AA16031; Thu, 25 May 95 11:18:23 CDT
Message-Id: <9505251618.AA16031@tepui.cli.com>
From: friedman@gnu.ai.mit.edu (Noah Friedman)
To: chet@po.cwru.edu
Subject: Bash scripts
Reply-To: friedman@gnu.ai.mit.edu
In-Reply-To: <chet@odin.ins.cwru.edu> Thu, 25 May 1995 11:19:59 -0400
References: <9505251519.AA06424.SM@odin.INS.CWRU.Edu>
Date: Thu, 25 May 95 11:18:21 CST

>Hi. I snagged some of your bash functions from your home directory on
>the FSF machines (naughty, I know), and I was wondering if you'd let
>me distribute them with bash-2.0. Thanks.

Sure. I think there's a later copy in
~ftp/friedman/shell-inits/init-4.89.tar.gz. There are also some elisp and
es frobs in that file.
It should serve as a pretty good example of how to get carried away. :-)

From mikel@ora.com Tue Aug 1 12:13:20 1995
Flags: 10
Return-Path: mikel@ora.com
Received: from ruby.ora.com (ruby.ora.com [198.112.208.25]) by odin.INS.CWRU.Edu with ESMTP (8.6.12+cwru/CWRU-2.1-ins)
id MAA01565; Tue, 1 Aug 1995 12:13:18 -0400 (from mikel@ora.com for <chet@odin.INS.CWRU.Edu>)
Received: (from fax@localhost) by ruby.ora.com (8.6.12/8.6.11) with UUCP id MAA23251; Tue, 1 Aug 1995 12:07:51 -0400
Received: by los.ora.com (4.1/Spike-2.1)
id AA00672; Tue, 1 Aug 95 08:57:32 EDT
From: mikel@ora.com (Michael Loukides)
Message-Id: <9508011257.AA00672@los.ora.com>
Subject: Re: Ksh debugger from Rosenblatt's book [for bash]
To: Chet Ramey <chet@odin.INS.CWRU.Edu>
Cc: cmarie@ora.com, cam@iinet.com.au, brosenblatt@tm.com
In-Reply-To: Chet Ramey <chet@odin.INS.CWRU.Edu>, Mon, 31 Jul 1995 16:22:48 -0400

I've modified a (modified) version of Bill Rosenblatt's ksh debugger
to work with bash-2.0. Does ORA have any problem with me distributing
it with bash-2.0?

That's great!

Go ahead and circulate it; in fact, we should probably grab it and
stick it in our ftp archive, and put a reference to it in the book.
(Too late to actually discuss the thing, at least for this edition).

-------

From spcecdt@armory.com Wed May 10 10:21:11 1995
Flags: 10
Return-Path: spcecdt@armory.com
Received: from po.cwru.edu (root@po.CWRU.Edu [129.22.4.2]) by odin.INS.CWRU.Edu with ESMTP (8.6.10+cwru/CWRU-2.1-ins)
id KAA22876; Wed, 10 May 1995 10:21:10 -0400 (from spcecdt@armory.com for <chet@odin.INS.CWRU.Edu>)
Received: from deepthought.armory.com (mmdf@deepthought.armory.com [192.122.209.42]) by po.cwru.edu with SMTP (8.6.10+cwru/CWRU-2.3)
id BAA16354; Wed, 10 May 1995 01:33:22 -0400 (from spcecdt@armory.com for <chet@po.cwru.edu>)
From: John DuBois <spcecdt@armory.com>
Date: Tue, 9 May 1995 22:33:12 -0700
In-Reply-To: Chet Ramey <chet@odin.ins.cwru.edu>
"ksh scripts" (May 9, 1:36pm)
X-Www: http://www.armory.com/~spcecdt/
X-Mailer: Mail User's Shell (7.2.5 10/14/92)
To: chet@po.cwru.edu
Subject: Re: ksh scripts
Message-ID: <9505092233.aa13001@deepthought.armory.com>
Sure. The canonical versions are available on ftp.armory.com; you might want to pick up the latest versions before modifying them.

John

On May 9, 1:36pm, Chet Ramey wrote:

Hi. I'm the maintainer of bash (the GNU `Bourne Again shell') for the FSF.

I picked up a tar file of ksh scripts you wrote from an anon FTP site a while back. I'd like your permission to include modified versions of some of them in the next major bash distribution (with proper credit given, of course). Is it OK if I do that?

Chet Ramey

``The lyf so short, the craft so long to lerne.`` - Chaucer

Chet Ramey, Case Western Reserve University

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Version 3, 29 June 2007

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(This file is under construction.)

If you've contributed to gas and your name isn't listed here, it is not meant as a slight. I just don't know about it. Email me, nickc@redhat.com and I'll correct the situation.

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Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliiCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of
Buffalo University and Torbjorn Granlund of the Swedish Institute of
Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS
back end (tc-mips.c, tc-mips.h), and contributed Rose format support
that hasn't been merged in yet. Ralph Campbell worked with the MIPS
code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors
(tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format
(obj-ieee), was written by Steve Chamberlain of Cygnus Solutions.
Steve also modified the COFF back end (obj-coffbfd) to use BFD for
some low-level operations, for use with the Hitachi, 29k and Zilog
targets.
John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.
Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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We call this license the Lesser General Public License because it does to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.
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Julian Seward, Cambridge, UK.
jseward@bzip.org
bzip2/libbzip2 version 1.0.4 of 20 December 2006

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1.21 gcc 4.7.2 :0.0

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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GCC RUNTIME LIBRARY EXCEPTION

Version 3.1, 31 March 2009

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1.22 gdbm 1.1 :r3

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1.23 google-protobuf/C++ 3

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1.25 hibernate-entitymanager 4.0.1.Final

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1.26 init-ifupdown 1.0

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1.28 ipaddress 1.0.17

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1.30 Java-WebSocket 1.3.0

1.30.1 Available under license:

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1.32 json 20140107
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That's all there is to it!
/* nicstar.c v0.22  Jawaid Bazyar (bazyar@hypermall.com)
 * nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
 *
 * Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
 * http://www.hypermall.com/
 * 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
 * interrupts us (except possibly for removal/insertion of the cable?)
 * 10/4/97 - began heavy inline documentation of the code. Corrected typos
 * and spelling mistakes.
 * 10/5/97 - added code to handle PHY interrupts, disable PHY on
 * loss of link, and correctly re-enable PHY when link is
 * re-established. (put back CFG_PHYIE)
 *
 * Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
 *
 * R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
 *
 * Linux driver for the IDT77201 NICStAR PCI ATM controller.
 * PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
 * see init_nicstar() for PHY initialization to change this. This driver
 * expects the Linux ATM stack to support scatter-gather lists
 * (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
 *
 * Implementing minimal-copy of received data:
 * IDT always receives data into a small buffer, then large buffers
 * as needed. This means that data must always be copied to create
 * the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
 * Fix is simple: make large buffers large enough to hold entire
 * SDU, and leave <small_buffer_data> bytes empty at the start. Then
 * copy small buffer contents to head of large buffer.
 * Trick is to avoid fragmenting Linux, due to need for a lot of large
 * buffers. This is done by 2 things:
 * 1) skb->destructor / skb->atm.recycle_buffer
 * combined, allow nicstar_free_rx_skb to be called to
* recycle large data buffers
* 2) skb_clone of received buffers
* See nicstar_free_rx_skb and linearize_buffer for implementation
* details.
*
*
*
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* Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.
*
* M. Welsh, 6 July 1996
*
*
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Although the Lesser General Public License is Less protective of the users` freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.
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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.
If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer’s own use and reverse engineering for debugging such modifications.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user’s computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.
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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.
c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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1.47 maven-compiler-plugin 2.3.2

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1.48 maven-release-plugin 2.4.1

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a) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.

b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord
with subsection 6b.

d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

e) Convey the object code using peer-to-peer transmission, provided you inform other peers where the object code and Corresponding Source of the work are being offered to the general public at no charge under subsection 6d.

A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

A "User Product" is either (1) a "consumer product", which means any tangible personal property which is normally used for personal, family, or household purposes, or (2) anything designed or sold for incorporation into a dwelling. In determining whether a product is a consumer product, doubtful cases shall be resolved in favor of coverage. For a particular product received by a particular user, "normally used" refers to a typical or common use of that class of product, regardless of the status of the particular user or of the way in which the particular user actually uses, or expects or is expected to use, the product. A product is a consumer product regardless of whether the product has substantial commercial, industrial or non-consumer uses, unless such uses represent the only significant mode of use of the product.

"Installation Information" for a User Product means any methods, procedures, authorization keys, or other information required to install and execute modified versions of a covered work in that User Product from a modified version of its Corresponding Source. The information must suffice to ensure that the continued functioning of the modified object code is in no case prevented or interfered with solely because modification has been made.

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The requirement to provide Installation Information does not include a requirement to continue to provide support service, warranty, or updates for a work that has been modified or installed by the recipient, or for the User Product in which it has been modified or installed. Access to a network may be denied when the modification itself materially and adversely affects the operation of the network or violates the rules and protocols for communication across the network.

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*
**************************************************************************/
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*
__FBSDID("$FreeBSD: src/sys/dev/hifn/hifn7751.c,v 1.40 2007/03/21 03:42:49 sam Exp $");
/*

* An OCF module that uses Intels IXP CryptACC API to do the crypto.
* This driver requires the IXP400 Access Library that is available
* from Intel in order to operate (or compile).
* 
* Written by David McCullough <david_mccullough@mcafee.com>
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*/

#include <linux/version.h>
#elif LINUX_VERSION_CODE < KERNEL_VERSION(2,6,38) && !defined(AUTOCONF_INCLUDED)
#include <linux/config.h>
#endif
#include <linux/module.h>
#include <linux/init.h>
#include <linux/list.h>
#include <linux/slab.h>
#include <linux/sched.h>
#include <linux/wait.h>
#include <linux/crypto.h>
#include <linux/interrupt.h>
#include <asm/scatterlist.h>
#include <IxTypes.h>
#include <IxOsBuffMgt.h>
#include <IxNpeDl.h>
#include <IxCryptoAcc.h>
#include <IxQMgr.h>
#include <IxOsServices.h>
#include <IxOsCacheMMU.h>

#include <cryptodev.h>
#include <uio.h>

#ifndef IX_MBUF_PRIV
#define IX_MBUF_PRIV(x) ((x)->priv)
#endif

struct ixp_data;

struct ixp_q {
    struct list_head ixp_q_list;
    struct ixp_data* ixp_q_data;
    struct cryptop* ixp_q_crp;
    struct cryptodesc* ixp_q_ccred;
    struct cryptodesc* ixp_q_acrd;
    IX_MBUF ixp_q_mbuf;
    UINT8* ixp_hash_dest; /* Location for hash in client buffer */
    UINT8* ixp_hash_src; /* Location of hash in internal buffer */
    unsigned char ixp_q_iv_data[IX_CRYPTO_ACC_MAX_CIPHER_IV_LENGTH];
    unsigned char* ixp_q_iv;
};

struct ixp_data {
    int ixp_registered; /* is the context registered */
    int ixp_crd_flags; /* detect direction changes */

    int ixp_cipher_alg;
    int ixp_auth_alg;

    UINT32 ixp_ctx_id;
    UINT32 ixp_hash_key_id; /* used when hashing */
    IxCryptoAccCtx ixp_ctxt;
    IX_MBUF ixp_pri_mbuf;
    IX_MBUF ixp_sec_mbuf;

    struct work_struct ixp_pending_work;
    struct work_struct ixp_registration_work;
    struct list_head ixp_q; /* unprocessed requests */
#ifdef __ixp46X

#define MAX_IOP_SIZE /* words */
#define MAX_OOP_SIZE

#define MAX_PARAMS

struct ixp_pkq {
    struct list_head pkq_list;
    struct cryptkop *pkq_krp;

    IXCryptoAccPkeEauInOperands pkq_op;
    IXCryptoAccPkeEauOpResult pkq_result;

    UINT32 pkq_ibuf0[MAX_IOP_SIZE];
    UINT32 pkq_ibuf1[MAX_IOP_SIZE];
    UINT32 pkq_ibuf2[MAX_IOP_SIZE];
    UINT32 pkq_obuf[MAX_OOP_SIZE];
};

static LIST_HEAD(ixp_pkq); /* current PK wait list */
static struct ixp_pkq *ixp_pk_cur;
static spinlock_t ixp_pkq_lock;

#endif /* __ixp46X */

static int ixp_blocked = 0;
static int32_t ixp_id = -1;
static struct ixp_data **ixp_sessions = NULL;
static u_int32_t ixp_sesnum = 0;

static int ixp_process(device_t, struct cryptop *, int);
static int ixp_newsession(device_t, u_int32_t *, struct cryptoini *);
static int ixp_freesession(device_t, u_int64_t);

#ifdef __ixp46X
static int ixp_kprocess(device_t, struct cryptkop *krp, int hint);
#endif

#if LINUX_VERSION_CODE < KERNEL_VERSION(2,6,20)
static kmem_cache_t *qcache;
#else
static struct kmem_cache *qcache;
#endif

#define debug ixp_debug
static int ixp_debug = 0;
module_param(ixp_debug, int, 0644);
MODULE_PARM_DESC(ixp_debug, "Enable debug");

static int ixp_init_crypto = 1;
module_param(ixp_init_crypto, int, 0444); /* RO after load/boot */
MODULE_PARM_DESC(ixp_init_crypto, "Call ixCryptoAccInit (default is 1)");

static void ixp_process_pending(void *arg);
static void ixp_registration(void *arg);
#if LINUX_VERSION_CODE >= KERNEL_VERSION(2,6,20)
static void ixp_process_pending_wq(struct work_struct *work);
static void ixp_registration_wq(struct work_struct *work);
#endif

/*
 * dummy device structure
 */

static struct {
    softc_device_declsc_dev;
} ixpdev;

static device_method_t ixp_methods = {
    /* crypto device methods */
    DEVMETHOD(cryptodev_newsession,ixp_newsession),
    DEVMETHOD(cryptodev_freesession,ixp_freesession),
    DEVMETHOD(cryptodev_process,ixp_process),
    #ifdef __ixp46X
    DEVMETHOD(cryptodev_kprocess,ixp_kprocess),
    #endif
};

/*
 * Generate a new software session.
 */

static int
ixp_newsession(device_t dev, u_int32_t *sid, struct cryptoini *cri)
{
    struct ixp_data *ixp;
    u_int32_t i;
    #define AUTH_LEN(cri, def) \
    (cri->cri_mlen ? cri->cri_mlen : (def))
    (cri->cri_mlen ? cri->cri_mlen : (def))
    dprintk("%s():alg %d\n", __FUNCTION__,cri->cri_alg);
    if (sid == NULL || cri == NULL) {
        dprintk("%s,%d - EINVAL\n", __FILE__, __LINE__);
        return EINVAL;
    }

    dprintk("%s,%d - EINVAL\n", __FILE__, __LINE__);
    return EINVAL;
}
if (ixp_sessions) {
    for (i = 1; i < ixp_sesnum; i++)
        if (ixp_sessions[i] == NULL)
            break;
    else
        i = 1; /* NB: to silence compiler warning */
}

if (ixp_sessions == NULL || i == ixp_sesnum) {
    struct ixp_data **ixpd;
}

if (ixp_sessions == NULL) {
    i = 1; /* We leave ixp_sessions[0] empty */
    ixp_sesnum = CRYPTO_SW_SESSIONS;
} else
    ixp_sesnum *= 2;

ixpd = kmalloc(ixp_sesnum * sizeof(struct ixp_data *), SLAB_ATOMIC);
if (ixpd == NULL) {
    /* Reset session number */
    if (ixp_sesnum == CRYPTO_SW_SESSIONS)
        ixp_sesnum = 0;
    else
        ixp_sesnum /= 2;
    dprintk("%s,%d: ENOBUFS\n", __FILE__, __LINE__);
    return ENOBUFS;
}
memset(ixpd, 0, ixp_sesnum * sizeof(struct ixp_data *));

/* Copy existing sessions */
if (ixp_sessions) {
    memcpy(ixpd, ixp_sessions,
           (ixp_sesnum / 2) * sizeof(struct ixp_data *));
    kfree(ixp_sessions);
}

ixp_sessions = ixpd;

ixp_sessions[i] = (struct ixp_data *) kmalloc(sizeof(struct ixp_data),
      SLAB_ATOMIC);
if (ixp_sessions[i] == NULL) {
    ixp_freesession(NULL, i);
    dprintk("%s,%d: EINVAL\n", __FILE__, __LINE__);
    return ENOBUFS;
}
*sid = i;

ixp = ixp_sessions[i];
memset(ixp, 0, sizeof(*ixp));

ixp->ixp_cipher_alg = -1;
ixp->ixp_auth_alg = -1;
ixp->ixp_ctx_id = -1;
INIT_LIST_HEAD(&ixp->ixp_q);

ixp->ixp_ctx.useDifferentSrcAndDestMbufs = 0;

while (cri) {
    switch (cri->cri_alg) {
    case CRYPTO_DES_CBC:
        ixp->ixp_cipher_alg = cri->cri_alg;
        ixp->ixp_ctx.cipherCtx.cipherAlgo = IX_CRYPTO_ACC_CIPHER_DES;
        ixp->ixp_ctx.cipherCtx.cipherMode = IX_CRYPTO_ACC_MODE_CBC;
        ixp->ixp_ctx.cipherCtx.cipherKeyLen = (cri->cri_klen + 7) / 8;
        ixp->ixp_ctx.cipherCtx.cipherBlockLen = IX_CRYPTO_ACC_DES_BLOCK_64;
        ixp->ixp_ctx.cipherCtx.cipherInitialVectorLen =
        IX_CRYPTO_ACC_DES_IV_64;
        memcpy(ixp->ixp_ctx.cipherCtx.key.cipherKey, 
        cri->cri_key, (cri->cri_klen + 7) / 8);
        break;

    case CRYPTO_3DES_CBC:
        ixp->ixp_cipher_alg = cri->cri_alg;
        ixp->ixp_ctx.cipherCtx.cipherAlgo = IX_CRYPTO_ACC_CIPHER_3DES;
        ixp->ixp_ctx.cipherCtx.cipherMode = IX_CRYPTO_ACC_MODE_CBC;
        ixp->ixp_ctx.cipherCtx.cipherKeyLen = (cri->cri_klen + 7) / 8;
        ixp->ixp_ctx.cipherCtx.cipherBlockLen = IX_CRYPTO_ACC_DES_BLOCK_64;
        ixp->ixp_ctx.cipherCtx.cipherInitialVectorLen =
        IX_CRYPTO_ACC_DES_IV_64;
        memcpy(ixp->ixp_ctx.cipherCtx.key.cipherKey, 
        cri->cri_key, (cri->cri_klen + 7) / 8);
        break;

    case CRYPTO_RIJNDAEL128_CBC:
        ixp->ixp_cipher_alg = cri->cri_alg;
        ixp->ixp_ctx.cipherCtx.cipherAlgo = IX_CRYPTO_ACC_CIPHER_AES;
        ixp->ixp_ctx.cipherCtx.cipherMode = IX_CRYPTO_ACC_MODE_CBC;
        ixp->ixp_ctx.cipherCtx.cipherKeyLen = (cri->cri_klen + 7) / 8;
        ixp->ixp_ctx.cipherCtx.cipherBlockLen = 16;
        ixp->ixp_ctx.cipherCtx.cipherInitialVectorLen = 16;
        memcpy(ixp->ixp_ctx.cipherCtx.key.cipherKey, 
        cri->cri_key, (cri->cri_klen + 7) / 8);
        break;
case CRYPTO_MD5:
case CRYPTO_MD5_HMAC:
  ixp->ixp_auth_alg = cri->cri_alg;
  ixp->ixp_ctx.authCtx.authAlgo = IX_CRYPTO_ACC_AUTH_MD5;
  ixp->ixp_ctx.authCtx.authDigestLen = AUTH_LEN(cri, MD5_HASH_LEN);
  ixp->ixp_ctx.authCtx.authLen = 0;
  /* Only MD5_HMAC needs a key */
  if (cri->cri_alg == CRYPTO_MD5_HMAC) {
    ixp->ixp_ctx.authCtx.authKeyLen = (cri->cri_klen + 7) / 8;
    if (ixp->ixp_ctx.authCtx.authKeyLen >
        sizeof(ixp->ixp_ctx.authCtx.key.authKey)) {
      printk("ixp4xx: Invalid key length for MD5_HMAC - %d bits\n",
             cri->cri_klen);
      ixp_freesession(NULL, i);
      return EINVAL;
    }
    memcpy(ixp->ixp_ctx.authCtx.key.authKey,
           cri->cri_key, (cri->cri_klen + 7) / 8);
  }
  break;

case CRYPTO_SHA1:
case CRYPTO_SHA1_HMAC:
  ixp->ixp_auth_alg = cri->cri_alg;
  ixp->ixp_ctx.authCtx.authAlgo = IX_CRYPTO_ACC_AUTH_SHA1;
  ixp->ixp_ctx.authCtx.authDigestLen = AUTH_LEN(cri, SHA1_HASH_LEN);
  ixp->ixp_ctx.authCtx.aadLen = 0;
  /* Only SHA1_HMAC needs a key */
  if (cri->cri_alg == CRYPTO_SHA1_HMAC) {
    ixp->ixp_ctx.authCtx.authKeyLen = (cri->cri_klen + 7) / 8;
    if (ixp->ixp_ctx.authCtx.authKeyLen >
        sizeof(ixp->ixp_ctx.authCtx.key.authKey)) {
      printk("ixp4xx: Invalid key length for SHA1_HMAC - %d bits\n",
             cri->cri_klen);
      ixp_freesession(NULL, i);
      return EINVAL;
    }
    memcpy(ixp->ixp_ctx.authCtx.key.authKey,
           cri->cri_key, (cri->cri_klen + 7) / 8);
  }
  break;

default:
  printk("ixp: unknown algo 0x%0x\n", cri->cri_alg);
  ixp_freesession(NULL, i);
return EINVAL;
}
cri = cri->cri_next;
}

#if LINUX_VERSION_CODE >= KERNEL_VERSION(2,6,20)
INIT_WORK(&ixp->ixp_pending_work, ixp_process_pending_wq);
INIT_WORK(&ixp->ixp_registration_work, ixp_registration_wq);
#else
INIT_WORK(&ixp->ixp_pending_work, ixp_process_pending, ixp);
INIT_WORK(&ixp->ixp_registration_work, ixp_registration, ixp);
#endif
return 0;
}

/*
 * Free a session.
 */
static int
ixp_freesession(device_t dev, u_int64_t tid)
{
    u_int32_t sid = CRYPTO_SESID2LID(tid);

dprintfk("%s\n", __FUNCTION__);
    if (sid > ixp_sesnum || ixp_sessions == NULL || ixp_sessions[sid] == NULL) {
        dprintfk("%s,%d: EINVAL\n", __FILE__, __LINE__);
        return EINVAL;
    }

    /* Silently accept and return */
    if (sid == 0)
        return 0;

    if (ixp_sessions[sid]) {
        if (ixp_sessions[sid]->ixp_ctx_id != -1) {
            ixCryptoAccCtxUnregister(ixp_sessions[sid]->ixp_ctx_id);
            ixp_sessions[sid]->ixp_ctx_id = -1;
        }
        kfree(ixp_sessions[sid]);
    }
    ixp_sessions[sid] = NULL;
    if (ixp_blocked)
        crypto_unblock(ixp_id, CRYPTO_SYMQ);
}
return 0;
}

/*
 * callback for when hash processing is complete
 */

static void
ixp_hash_perform_cb(
    UINT32 hash_key_id,
    IX_MBUF *bufp,
    IxCryptoAccStatus status)
{
    struct ixp_q *q;

dprintf("%s(%u, %p, 0x%x)n", __FUNCTION__, hash_key_id, bufp, status);

    if (bufp == NULL) {
        printk("ixp: NULL buf in %s\n", __FUNCTION__);
        return;
    }

    q = IX_MBUF_PRIV(bufp);
    if (q == NULL) {
        printk("ixp: NULL priv in %s\n", __FUNCTION__);
        return;
    }

    if (status == IX_CRYPTO_ACC_STATUS_SUCCESS) {
        /* On success, need to copy hash back into original client buffer */
        memcpy(q->ixp_hash_dest, q->ixp_hash_src,
            (q->ixp_q_data->ixp_auth_alg == CRYPTO_SHA1) ?
            SHA1_HASH_LEN : MD5_HASH_LEN);
    } else {
        printk("ixp: hash perform failed status=\%d\n", status);
        q->ixp_q_crp->crp_etype = EINVAL;
    }

    /* Free internal buffer used for hashing */
    kfree(IX_MBUF_MDATA(&q->ixp_q_mbuf));

    crypto_done(q->ixp_q_crp);
    kmem_cache_free(qcache, q);
}

/*
* setup a request and perform it
*/
static void
ixp_q_process(struct ixp_q *q)
{
  IxCryptoAccStatus status;
  struct ixp_data *ixp = q->ixp_q_data;
  int auth_off = 0;
  int auth_len = 0;
  int crypt_off = 0;
  int crypt_len = 0;
  int icv_off = 0;
  char *crypt_func;

dprintk("%s(%p)\n", __FUNCTION__, q);

  if (q->ixp_q_ccrd) {
    if (q->ixp_q_ccrd->crd_flags & CRD_F_ENCRYPT) {
      if (q->ixp_q_ccrd->crd_flags & CRD_F_IV_EXPLICIT) {
        q->ixp_q_iv = q->ixp_q_ccrd->crd_iv;
      } else {
        q->ixp_q_iv = q->ixp_q_iv_data;
        read_random(q->ixp_q_iv, ixp->ixp_ctx.cipherCtx.cipherInitialVectorLen);
      }
      if ((q->ixp_q_ccrd->crd_flags & CRD_F_IV_PRESENT) == 0)
        crypto_copyback(q->ixp_q_cr->crp_flags, q->ixp_q_cr->crp_buf,
                      q->ixp_q_ccrd->crd_inject,
                      ixp->ixp_ctx.cipherCtx.cipherInitialVectorLen,
                      (caddr_t) q->ixp_q_iv);
    } else {
      if (q->ixp_q_ccrd->crd_flags & CRD_F_IV_EXPLICIT)
        q->ixp_q_iv = q->ixp_q_ccrd->crd_iv;
      else {
        q->ixp_q_iv = q->ixp_q_iv_data;
        crypto_copydata(q->ixp_q_cr->crp_flags, q->ixp_q_cr->crp_buf,
                        q->ixp_q_ccrd->crd_inject,
                        ixp->ixp_ctx.cipherCtx.cipherInitialVectorLen,
                        (caddr_t) q->ixp_q_iv);
      }
    }
  }

  if (q->ixp_q_acrd) {
    auth_off = q->ixp_q_acrd->crd_skip;
    auth_len = q->ixp_q_acrd->crd_len;
    icv_off = q->ixp_q_acrd->crd_inject;
  }

  crypt_off = q->ixp_q_ccrd->crd_skip;
crypt_len = q->ixp_q_ccrd->crd_len;
} else { /* if (q->ixp_q_acrd) */
  auth_off = q->ixp_q_acrd->crd_skip;
  auth_len = q->ixp_q_acrd->crd_len;
  icv_off  = q->ixp_q_acrd->crd_inject;
}

if (q->ixp_q_crp->crp_flags & CRYPTO_F_SKBUF) {
  struct sk_buff *skb = (struct sk_buff *) q->ixp_q_crp->crp_buf;
  if (skb_shinfo(skb)->nr_frags) {
    /*
     * DAVIDM fix this limitation one day by using
     * a buffer pool and chaining, it is not currently
     * needed for current user/kernel space acceleration
     */
    printk("ixp: Cannot handle fragmented skb's yet !\n");
    q->ixp_q_crp->crp_etype = ENOENT;
    goto done;
  }
  IX_MBUF_MLEN(&q->ixp_q_mbuf) =
  IX_MBUF_PKT_LEN(&q->ixp_q_mbuf) = skb->len;
  IX_MBUF_MDATA(&q->ixp_q_mbuf) = skb->data;
} else if (q->ixp_q_crp->crp_flags & CRYPTO_F_IOV) {
  struct uio *uiop = (struct uio *) q->ixp_q_crp->crp_buf;
  if (uiop->uio_iovcnt != 1) {
    /*
     * DAVIDM fix this limitation one day by using
     * a buffer pool and chaining, it is not currently
     * needed for current user/kernel space acceleration
     */
    printk("ixp: Cannot handle more than 1 iovec yet !\n");
    q->ixp_q_crp->crp_etype = ENOENT;
    goto done;
  }
  IX_MBUF_MLEN(&q->ixp_q_mbuf) =
  IX_MBUF_PKT_LEN(&q->ixp_q_mbuf) = uiop->uio_iov[0].iov_len;
  IX_MBUF_MDATA(&q->ixp_q_mbuf) = uiop->uio_iov[0].iov_base;
} else /* contig buffer */ {
  IX_MBUF_MLEN(&q->ixp_q_mbuf)  =
  IX_MBUF_PKT_LEN(&q->ixp_q_mbuf) = q->ixp_q_crp->crp_ilen;
  IX_MBUF_MDATA(&q->ixp_q_mbuf) = q->ixp_q_crp->crp_buf;
}

IX_MBUF_PRIV(&q->ixp_q_mbuf) = q;

if (ixp->ixp_auth_alg == CRYPTO_SHA1 || ixp->ixp_auth_alg == CRYPTO_MD5) {
  /*
   * For SHA1 and MD5 hash, need to create an internal buffer that is big
* enough to hold the original data + the appropriate padding for the
* hash algorithm.
*/

UINT8 *tbuf = NULL;

IX_MBUF_MLEN(&q->ixp_q_mbuf) = IX_MBUF_PKT_LEN(&q->ixp_q_mbuf) =
((IX_MBUF_MLEN(&q->ixp_q_mbuf) * 8) + 72 + 511) / 8;
tbuf = kmalloc(IX_MBUF_MLEN(&q->ixp_q_mbuf), SLAB_ATOMIC);

if (IX_MBUF_MDATA(&q->ixp_q_mbuf) == NULL) {
    printk("ixp: kmalloc(%u, SLAB_ATOMIC) failed\n",
           IX_MBUF_MLEN(&q->ixp_q_mbuf));
    q->ixp_q_crp->crp_etype = ENOMEM;
    goto done;
}
memcpy(tbuf, &(IX_MBUF_MDATA(&q->ixp_q_mbuf))[auth_off], auth_len);

/* Set location in client buffer to copy hash into */
q->ixp_hash_dest = &(IX_MBUF_MDATA(&q->ixp_q_mbuf))[auth_off + auth_len];

IX_MBUF_MDATA(&q->ixp_q_mbuf) = tbuf;

/* Set location in internal buffer for where hash starts */
q->ixp_hash_src = &(IX_MBUF_MDATA(&q->ixp_q_mbuf))[auth_len];

crypt_func = "ixCryptoAccHashPerform";
status = ixCryptoAccHashPerform(ixp->ixp_ctx.authCtx.authAlgo,
    &q->ixp_q_mbuf, ixp_hash_perform_cb, 0, auth_len, auth_len,
    &ixp->ixp_hash_key_id);
} else {
crypt_func = "ixCryptoAccAuthCryptPerform";
status = ixCryptoAccAuthCryptPerform(ixp->ixp_ctx_id, &q->ixp_q_mbuf,
    NULL, auth_off, auth_len, crypt_off, crypt_len, icv_off,
    q->ixp_q_iv);
}

if (IX_CRYPTO_ACC_STATUS_SUCCESS == status)
    return;

if (IX_CRYPTO_ACC_STATUS_QUEUE_FULL == status) {
    q->ixp_q_crp->crp_etype = ENOMEM;
    goto done;
}

printk("ixp: %s failed %u\n", crypt_func, status);
q->ixp_q_crp->crp_etype = EINVAL;
done:
crypto_done(q->ixp_q_crp);
kmem_cache_free(qcache, q);
}

/*
 * because we cannot process the Q from the Register callback
 * we do it here on a task Q.
 */

static void
ixp_process_pending(void *arg)
{
    struct ixp_data *ixp = arg;
    struct ixp_q *q = NULL;

dprintf("%s(%p)\n", __FUNCTION__, arg);

    if (!ixp)
        return;

    while (!list_empty(&ixp->ixp_q)) {
        q = list_entry(ixp->ixp_q.next, struct ixp_q, ixp_q_list);
        list_del(&q->ixp_q_list);
        ixp_q_process(q);
    }
}

#if LINUX_VERSION_CODE >= KERNEL_VERSION(2,6,20)
static void
ixp_process_pending_wq(struct work_struct *work)
{
    struct ixp_data *ixp = container_of(work, struct ixp_data, ixp_pending_work);
    ixp_process_pending(ixp);
}
#endif

/*
 * callback for when context registration is complete
 */

static void
ixp_register_cb(UINT32 ctx_id, IX_MBUF *bufp, IxCryptoAccStatus status)
{
    int i;
    struct ixp_data *ixp;
struct ixp_q *q;

dprintf("%s(%d, %p, %d)\n", __FUNCTION__, ctx_id, bufp, status);

/*
 * free any buffer passed in to this routine
 */
if (bufp) {
    IX_MBUF_MLEN(bufp) = IX_MBUF_PKT_LEN(bufp) = 0;
    kfree(IX_MBUF_MDATA(bufp));
    IX_MBUF_MDATA(bufp) = NULL;
}

for (i = 0; i < ixp_sesnum; i++) {
    ixp = ixp_sessions[i];
    if (ixp && ixp->ixp_ctx_id == ctx_id)
        break;
}
if (i >= ixp_sesnum) {
    printk("ixp: invalid context id %d\n", ctx_id);
    return;
}

if (IX_CRYPTO_ACC_STATUS_WAIT == status) {
    /* this is normal to free the first of two buffers */
    dprintf("ixp: register not finished yet.\n");
    return;
}

if (IX_CRYPTO_ACC_STATUS_SUCCESS != status) {
    printk("ixp: register failed 0x%\n", status);
    while (!list_empty(&ixp->ixp_q)) {
        q = list_entry(ixp->ixp_q.next, struct ixp_q, ixp_q_list);
        list_del(&q->ixp_q_list);
        q->ixp_q_crp->crp_etype = EINVAL;
        crypto_done(q->ixp_q_crp);
        kmem_cache_free(qcache, q);
    }
    return;
}

/*
 * we are now registered, we cannot start processing the Q here
 * or we get strange errors with AES (DES/3DES seem to be ok).
 */
ixp->ixp_registered = 1;
schedule_work(&ixp->ixp_pending_work);
/* 
* callback for when data processing is complete 
*/

static void
ixp_perform_cb(
UINT32 ctx_id,
IX_MBUF *sbufp,
IX_MBUF *dbufp,
IxCryptoAccStatus status)
{
    struct ixp_q *q;

dprintf("%s(%d, %p, %p, 0x%x)\n", __FUNCTION__, ctx_id, sbufp,
            dbufp, status);

    if (sbufp == NULL) {
        printk("ixp: NULL sbuf in ixp_perform_cb\n");
        return;
    }

    q = IX_MBUF_PRIV(sbufp);
    if (q == NULL) {
        printk("ixp: NULL priv in ixp_perform_cb\n");
        return;
    }

    if (status != IX_CRYPTO_ACC_STATUS_SUCCESS) {
        printk("ixp: perform failed status=%d\n", status);
        q->ixp_q_crp->crp_etype = EINVAL;
    }

    crypto_done(q->ixp_q_crp);
    kmem_cache_free(qcache, q);
}

/*
* registration is not callable at IRQ time, so we defer
* to a task queue, this routines completes the registration for us
* when the task queue runs
*
* Unfortunately this means we cannot tell OCF that the driver is blocked,
* we do that on the next request.
*/
static void
ixp_registration(void *arg)
{
    struct ixp_data *ixp = arg;
    struct ixp_q *q = NULL;
    IX_MBUF *pri = NULL, *sec = NULL;
    int status = IX_CRYPTO_ACC_STATUS_SUCCESS;

    if (!ixp) {
        printk("ixp: ixp_registration with no arg\n");
        return;
    }

    if (ixp->ixp_ctx_id != -1) {
        ixCryptoAccCtxUnregister(ixp->ixp_ctx_id);
        ixp->ixp_ctx_id = -1;
    }  

    if (list_empty(&ixp->ixp_q)) {
        printk("ixp: ixp_registration with no Q\n");
        return;
    }

    /*
     * setup the primary and secondary buffers
     */
    q = list_entry(ixp->ixp_q.next, struct ixp_q, ixp_q_list);
    if (q->ixp_q_acrd) {
        pri = &ixp->ixp_pri_mbuf;
        sec = &ixp->ixp_sec_mbuf;
        IX_MBUF_MLEN(pri) = IX_MBUF_PKT_LEN(pri) = 128;
        IX_MBUF_MDATA(pri) = (unsigned char *) kmalloc(128, SLAB_ATOMIC);
        IX_MBUF_MLEN(sec) = IX_MBUF_PKT_LEN(sec) = 128;
        IX_MBUF_MDATA(sec) = (unsigned char *) kmalloc(128, SLAB_atomic);
    }  

    /* Only need to register if a crypt op or HMAC op */
    if (!(ixp->ixp_auth_alg == CRYPTO_SHA1 ||
          ixp->ixp_auth_alg == CRYPTO_MD5)) {
        status = ixCryptoAccCtxRegister(
            &ixp->ixp_ctx,
            pri, sec,
            ixp_register_cb,
            ixp_perform_cb,
            &ixp->ixp_ctx_id);
    } else {
        /* Otherwise we start processing pending q */
    }
schedule_work(&ixp->ixp_pending_work);
}

if (IX_CRYPTO_ACC_STATUS_SUCCESS == status) return;

if (IX_CRYPTO_ACC_STATUS_EXCEED_MAX_TUNNELS == status) {
printk("ixp: ixCryptoAccCtxRegister failed (out of tunnels)\n");
ixp_blocked = 1;
/* perhaps we should return EGAIN on queued ops ? */
return;
}

printk("ixp: ixCryptoAccCtxRegister failed %d\n", status);
ixp->ixp_ctx_id = -1;

/*
 * everything waiting is toasted
 */
while (!list_empty(&ixp->ixp_q)) {
q = list_entry(ixp->ixp_q.next, struct ixp_q, ixp_q_list);
list_del(&q->ixp_q_list);
q->ixp_q_crp->crp_etype = ENOENT;
crypto_done(q->ixp_q_crp);
kmem_cache_free(qcache, q);
}
}

#if LINUX_VERSION_CODE >= KERNEL_VERSION(2,6,20)
static void
ixp_registration_wq(struct work_struct *work)
{
struct ixp_data *ixp = container_of(work, struct ixp_data,
ixp_registration_work);
ixp_registration(ixp);
}
#endif

/*
 * Process a request.
 */
static int
ixp_process(device_t dev, struct cryptop *crp, int hint)
{
struct ixp_data *ixp = container_of(work, struct ixp_data,
ixp_registration_work);
ixp_registration(ixp);
}

/*
dprintf("%s\n", __FUNCTION__);

/* Sanity check */
if (crp == NULL) {
    dprintf("%s,%d: EINVAL\n", __FILE__, __LINE__);
    return EINVAL;
}

crp->crp_etype = 0;

if (ixp_blocked)
    return ERESTART;

if (crp->crp_desc == NULL || crp->crp_buf == NULL) {
    dprintf("%s,%d: EINVAL\n", __FILE__, __LINE__);
    crp->crp_etype = EINVAL;
    goto done;
}

/*
 * find the session we are using
 */

lid = crp->crp_sid & 0xffffffff;
if (lid >= ixp_sesnum || lid == 0 || ixp_sessions == NULL || ixp_sessions[lid] == NULL) {
    crp->crp_etype = ENOENT;
    dprintf("%s,%d: ENOENT\n", __FILE__, __LINE__);
    goto done;
}

ixp = ixp_sessions[lid];

/*
 * setup a new request ready for queuing
 */

q = kmem_cache_alloc(qcache, SLAB_ATOMIC);
if (q == NULL) {
    dprintf("%s,%d: ENOMEM\n", __FILE__, __LINE__);
    crp->crp_etype = ENOMEM;
    goto done;
}

/*
 * save some cycles by only zeroing the important bits
 */
memset(&q->ixp_q_mbuf, 0, sizeof(q->ixp_q_mbuf));
q->ixp_q_ccrd = NULL;
q->ixp_q_acrd = NULL;
/*
 * point the cipher and auth descriptors appropriately
 * check that we have something to do
 */
if (crp->crp_desc->crd_alg == ixp->ixp_cipher_alg)
q->ixp_q_ccrd = crp->crp_desc;
else if (crp->crp_desc->crd_alg == ixp->ixp_auth_alg)
q->ixp_q_acrd = crp->crp_desc;
else {
  crp->crp_etype = ENOENT;
dprintk("%s,%d: bad desc match: ENOENT\n", __FILE__, __LINE__);
goto done;
}
if (crp->crp_desc->crd_next) {
  if (crp->crp_desc->crd_next->crd_alg == ixp->ixp_cipher_alg)
    q->ixp_q_ccrd = crp->crp_desc->crd_next;
  else if (crp->crp_desc->crd_next->crd_alg == ixp->ixp_auth_alg)
    q->ixp_q_acrd = crp->crp_desc->crd_next;
  else {
    crp->crp_etype = ENOENT;
dprintk("%s,%d: bad desc match: ENOENT\n", __FILE__, __LINE__);
goto done;
  }
}

/*
 * If there is a direction change for this context then we mark it as
 * unregistered and re-register is for the new direction. This is not
 * a very expensive operation and currently only tends to happen when
 * user-space application are doing benchmarks
 *
 * DM - we should be checking for pending requests before unregistering.
 */
if (q->ixp_q_ccrd && ixp->ixp_registered &&
ixp->ixp_crd_flags != (q->ixp_q_ccrd->crd_flags & CRD_F_ENCRYPT)) {
dprintk("%s - detected direction change on session\n", __FUNCTION__);
ixp->ixp_registered = 0;
}

/*
 * if we are registered, call straight into the perform code
 */
if (ixp->ixp_registered) {
  ixp_q_process(q);
  return 0;
}
if (q->ixp_q_ccrd) {
    ixp->ixp_crd_flags = (q->ixp_q_ccrd->crd_flags & CRD_F_ENCRYPT);
    if (q->ixp_q_ccrd->crd_flags & CRD_F_ENCRYPT) {
        ixp->ixp_ctx.operation = q->ixp_q_acrd ?
            IX_CRYPTO_ACC_OP_ENCRYPT_AUTH : IX_CRYPTO_ACC_OP_ENCRYPT;
    } else {
        ixp->ixp_ctx.operation = q->ixp_q_acrd ?
            IX_CRYPTO_ACC_OP_AUTH_DECRYPT : IX_CRYPTO_ACC_OP_DECRYPT;
    }
} else {
    /* q->ixp_q_acrd must be set if we are here */
    ixp->ixp_ctx.operation = IX_CRYPTO_ACC_OP_AUTH_CALC;
}

status = list_empty(&ixp->ixp_q);
list_add_tail(&q->ixp_q_list, &ixp->ixp_q);
if (status)
    schedule_work(&ixp->ixp_registration_work);
return 0;

done:
if (q)
    kmem_cache_free(qcache, q);
crypto_done(crp);
return 0;

#endif __ixp46X

static int
ixp_copy_ibuf(struct crparam *p, IxCryptoAccPkeEauOperand *op, UINT32 *buf)
{
unsigned char *src = (unsigned char *) p->crp_p;
unsigned char *dst;
int len, bits = p->crp_nbits;

dprintf("%s()\n", __FUNCTION__);

if (bits > MAX_IOP_SIZE * sizeof(UINT32) * 8) {
    dprintf("%s - ibuf too big (%d > %d)\n", __FUNCTION__,
            bits, MAX_IOP_SIZE * sizeof(UINT32) * 8);
    return -1;
}

len = (bits + 31) / 32; /* the number UINT32's needed */

dst = (unsigned char *) &buf[len];
dst--;

while (bits > 0) {
    *dst-- = *src++;
    bits -= 8;
}

#if 0 /* no need to zero remaining bits as it is done during request alloc */
while (dst > (unsigned char *) buf)
    *dst-- = '0';
#endif

op->pData = buf;
op->dataLen = len;
return 0;
}

/*
 * copy out the result, be as forgiving as we can about small output buffers
 */

static int
ixp_copy_obuf(struct crparam *p, IxCryptoAccPkeEauOpResult *op, UINT32 *buf)
{
    unsigned char *dst = (unsigned char *) op->pData;
    unsigned char *src = (unsigned char *) p->crp_p;
    int len, z, bits = p->crp_nbits;

dprintf("%s()\n", __FUNCTION__);

    len = op->dataLen * sizeof(UINT32);

    /* skip leading zeroes to be small buffer friendly */
z = 0;
while (z < len && src[z] == '\0')
z++;

src += len;
src--;
len -= z;

while (len > 0 && bits > 0) {
*dst++ = *src--;
len--;
bits -= 8;
}

while (bits > 0) {
*dst++ = '\0';
bits -= 8;
}

if (len > 0) {
dprintf("%s - obuf is %d (z=%d, ob=%d) bytes too small\n",
__FUNCTION__, len, z, p->crp_nbits / 8);
return -1;
}

return 0;

/*
 * the parameter offsets for exp_mod
 */
#define IXP_PARAM_BASE 0
#define IXP_PARAM_EXP 1
#define IXP_PARAM_MOD 2
#define IXP_PARAM_RES 3

/*
 * key processing complete callback, is also used to start processing
 * by passing a NULL for pResult
 */
static void
ixp_kperform_cb(
IxCryptoAccPkeEauOperation operation,
IxCryptoAccPkeEauOpResult *pResult,
BOOL carryOrBorrow,
IxCryptoAccStatus status)
{
    struct ixp_pkq *q, *tmp;
    unsigned long flags;

dprintk("%s(0x%x, %p, %ld, 0x%x)\n", __FUNCTION__, operation, pResult,
carryOrBorrow, status);

    /* handle a completed request */
    if (pResult) {
        if (ixp_pk_cur && &ixp_pk_cur->pkq_result == pResult) {
            q = ixp_pk_cur;
            if (status != IX_CRYPTO_ACC_STATUS_SUCCESS) {
                dprintk("%s() - op failed 0x%x\n", __FUNCTION__, status);
                q->pkq_krp->krp_status = ERANGE; /* could do better */
            } else {
                /* copy out the result */
                if (ixp_copy_obuf(&q->pkq_krp->krp_param[IXP_PARAM_RES],
                                 &q->pkq_result, q->pkq_obuf))
                    q->pkq_krp->krp_status = ERANGE;
                crypto_kdone(q->pkq_krp);
                kfree(q);
                ixp_pk_cur = NULL;
            } else
                printk("%s - callback with invalid result pointer\n", __FUNCTION__);
        }
    }

    spin_lock_irqsave(&ixp_pkq_lock, flags);
    if (ixp_pk_cur || list_empty(&ixp_pkq)) {
        spin_unlock_irqrestore(&ixp_pkq_lock, flags);
        return;
    }

    list_for_each_entry_safe(q, tmp, &ixp_pkq, pkq_list) {
        list_del(&q->pkq_list);
        ixp_pk_cur = q;
        spin_unlock_irqrestore(&ixp_pkq_lock, flags);

        status = ixCryptoAccPkeEauPerform(
            IX_CRYPTO_ACC_OP_EAU_MOD_EXP,
            &q->pkq_op,
            ixp_kperform_cb,
            &q->pkq_result);

        if (status == IX_CRYPTO_ACC_STATUS_SUCCESS) {
            /* other code */
        }
    }

    spin_lock_irqsave(&ixp_pkq_lock, flags);
    if (ixp_pk_cur || list_empty(&ixp_pkq)) {
        spin_unlock_irqrestore(&ixp_pkq_lock, flags);
    }

    /* other code */
dprint("%s() - ixCryptoAccPkeEauPerform SUCCESS\n", __FUNCTION__); return; /* callback will return here for callback */ } else if (status == IX_CRYPTO_ACC_STATUS_RETRY) { printk("%s() - ixCryptoAccPkeEauPerform RETRY\n", __FUNCTION__); } else { printk("%s() - ixCryptoAccPkeEauPerform failed %d\n", __FUNCTION__, status); } q->pkq_krp->krp_status = ERANGE; /* could do better */ crypto_kdone(q->pkq_krp); kfree(q); spin_lock_irqsave(&ixp_pkq_lock, flags); } spin_unlock_irqrestore(&ixp_pkq_lock, flags); }

static int ixp_kprocess(device_t dev, struct cryptkop *krp, int hint)
{
struct ixp_pkq *q;
int rc = 0;
unsigned long flags;

dprint("%s l1=%d l2=%d l3=%d l4=%d\n", __FUNCTION__,
    krp->krp_param[IXP_PARAM_BASE].crp_nbits,
    krp->krp_param[IXP_PARAM_EXP].crp_nbits,
    krp->krp_param[IXP_PARAM_MOD].crp_nbits,
    krp->krp_param[IXP_PARAM_RES].crp_nbits);

if (krp->krp_op != CRK_MOD_EXP) {
    krp->krp_status = EOPNOTSUPP;
    goto err;
}

q = (struct ixp_pkq *) kmalloc(sizeof(*q), GFP_KERNEL);
if (q == NULL) {
    krp->krp_status = ENOMEM;
    goto err;
}

/*
 * The PKE engine does not appear to zero the output buffer
 * appropriately, so we need to do it all here.
 */
memset(q, 0, sizeof(*q));
q->pkq_krp = krp;
INIT_LIST_HEAD(&q->pkq_list);

if (ixp_copy_ibuf(&krp->krp_param[IXP_PARAM_BASE], &q->pkq_op.modExpOpr.M, q->pkq_ibuf0))
rc = 1;
if (!(rc && ixp_copy_ibuf(&krp->krp_param[IXP_PARAM_EXP], &q->pkq_op.modExpOpr.e, q->pkq_ibuf1))
rc = 2;
if (!(rc && ixp_copy_ibuf(&krp->krp_param[IXP_PARAM_MOD], &q->pkq_op.modExpOpr.N, q->pkq_ibuf2))
rc = 3;

if (rc) {
    kfree(q);
    krp->krp_status = ERANGE;
    goto err;
}

q->pkq_result.pData = q->pkq_obuf;
q->pkq_result.dataLen = (krp->krp_param[IXP_PARAM_RES].crp_nbits + 31) / 32;

spin_lock_irqsave(&ixp_pkq_lock, flags);
list_add_tail(&q->pkq_list, &ixp_pkq);
spin_unlock_irqrestore(&ixp_pkq_lock, flags);

if (!ixp_pk_cur)
ixp_kperform_cb(0, NULL, 0, 0);
return (0);

err:
crypto_kdone(krp);
return (0);
}

#endif CONFIG_OCF_RANDOMHARVEST
/*
 * We run the random number generator output through SHA so that it
 * is FIPS compliant.
 */

static volatile int sha_done = 0;
static unsigned char sha_digest[20];

static void
ixp_hash_cb(UINT8 *digest, IXCryptoAccStatus status)  
{  
dprintf("%s(%p, %d)\n", __FUNCTION__, digest, status);  
if (sha_digest != digest)  
printf("digest error\n");  
if (IX_CRYPTO_ACC_STATUS_SUCCESS == status)  
sha_done = 1;  
else  
sha_done = -status;  
}  
static int  
ixp_read_random(void *arg, u_int32_t *buf, int maxwords)  
{  
IXCryptoAccStatus status;  
int i, n, rc;  

dprintf("%s(%p, %d)\n", __FUNCTION__, buf, maxwords);  
memset(buf, 0, maxwords * sizeof(*buf));  
status = ixCryptoAccPkePseudoRandomNumberGet(maxwords, buf);  
if (status != IX_CRYPTO_ACC_STATUS_SUCCESS) {  
dprintf("%s: ixCryptoAccPkePseudoRandomNumberGet failed %d\n", __FUNCTION__, status);  
return 0;  
}  
/*  
* run the random data through SHA to make it look more random  
*/  
n = sizeof(sha_digest); /* process digest bytes at a time */  
rc = 0;  
for (i = 0; i < maxwords; i += n / sizeof(*buf)) {  
if ((maxwords - i) * sizeof(*buf) < n)  
n = (maxwords - i) * sizeof(*buf);  
sha_done = 0;  
status = ixCryptoAccPkeHashPerform(IX_CRYPTO_ACC_AUTH_SHA1, (UINT8 *)&buf[i], n, ixp_hash_cb, sha_digest);  
if (status != IX_CRYPTO_ACC_STATUS_SUCCESS) {  
dprintf("ixCryptoAccPkeHashPerform failed %d\n", status);  
return -EIO;  
}  
while (!sha_done)  
schedule();  
if (sha_done < 0) {  
dprintf("ixCryptoAccPkeHashPerform failed CB %d\n", -sha_done);  
return 0;  
}  
}
memcpy(&buf[i], sha_digest, n);
rc += n / sizeof(*buf);
}

return rc;
}
#endif /* CONFIG_OCF_RANDOMHARVEST */
#endif /* __ixp46X */

/*
 * our driver startup and shutdown routines
 */

static int
ixp_init(void)
{
    dprintk("%s(%p)\n", __FUNCTION__, ixp_init);

    if (ixp_init_crypto && ixCryptoAccInit() != IX_CRYPTO_ACC_STATUS_SUCCESS)
        printk("ixCryptoAccInit failed, assuming already initialised\n");

    qcache = kmem_cache_create("ixp4xx_q", sizeof(struct ixp_q), 0,
        SLAB_HWCACHE_ALIGN, NULL
#if LINUX_VERSION_CODE < KERNEL_VERSION(2,6,23)
    , NULL
#endif
    #if LINUX_VERSION_CODE < KERNEL_VERSION(2,6,23)
    , NULL
    #endif
    #if LINUX_VERSION_CODE < KERNEL_VERSION(2,6,23)
    , NULL
    #endif
    #if LINUX_VERSION_CODE < KERNEL_VERSION(2,6,23)
    , NULL
    #endif
    if (!qcache) {
        printk("failed to create Qcache\n");
        return -ENOENT;
    }

    memset(&ixpdev, 0, sizeof(ixpdev));
    softc_device_init(&ixpdev, "ixp4xx", 0, ixp_methods);

    ixp_id = crypto_get_driverid(softc_get_device(&ixpdev),
        CRYPTOCAP_F_HARDWARE);
    if (ixp_id < 0)
        panic("IXP/OCF crypto device cannot initialize!");

    #define REGISTER(alg) \
        crypto_register(ixp_id,alg,0,0)
    REGISTER(CRYPTO_DES_CBC);
REGISTER(CRYPTO_3DES_CBC);
REGISTER(CRYPTO_RIJNDAEL128_CBC);
#ifdef CONFIG_OCF_IXP4XX_SHA1_MD5
REGISTER(CRYPTO_MD5);
REGISTER(CRYPTO_SHA1);
#endif
REGISTER(CRYPTO_MD5_HMAC);
REGISTER(CRYPTO_SHA1_HMAC);
#undef REGISTER

#ifdef __ixp46X
spin_lock_init(&ixp_pkq_lock);
/*
 * we do not enable the go fast options here as they can potentially
 * allow timing based attacks
 *
 * http://www.openssl.org/news/secadv_20030219.txt
 */
ixCryptoAccPkeEauExpConfig(0, 0);
crypto_kregister(ixp_id, CRK_MOD_EXP, 0);
#ifdef CONFIG_OCF_RANDOMHARVEST
crypto_rregister(ixp_id, ixp_read_random, NULL);
#endif
#endif

return 0;
}

static void
ixp_exit(void)
{
dprintk("%s()
", __FUNCTION__);  
crypto_unregister_all(ixp_id);
ixp_id = -1;
kmem_cache_destroy(qcache);
qcache = NULL;
}

module_init(ixp_init);
module_exit(ixp_exit);

MODULE_LICENSE("Dual BSD/GPL");
MODULE_AUTHOR("David McCullough <dmccullough@cyberguard.com>");
MODULE_DESCRIPTION("ixp (OCF module for IXP4xx crypto)");

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 * 
 * Driver for the PA Semi PWRficient DMA Crypto Engine
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 */
/*$KAME: md5.c,v 1.5 2000/11/08 06:13:08 itojun Exp $*/
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*
*/

/*
* Register definitions for 5601 BlueSteel Networks Ubiquitous Broadband
* Security "uBSec" chip. Definitions from revision 2.8 of the product
* datasheet.
*/

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Version 2, June 1991

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*/

/*
Chat -- a program for automatic session establishment (i.e. dial
the phone and log in).

* Standard termination codes:
* 0 - successful completion of the script
* 1 - invalid argument, expect string too large, etc.
* 2 - error on an I/O operation or fatal error condition.
* 3 - timeout waiting for a simple string.
* 4 - the first string declared as "ABORT"
* 5 - the second string declared as "ABORT"
* 6 - ... and so on for successive ABORT strings.
*
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 * passprompt.c - pppd plugin to invoke an external PAP password prompter
 *
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/*
 Unix SMB/CIFS implementation.

trivial database library

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* * Copyright 2000 Mitchell Blank Jr.
* * Based in part on work from Jens Axboe and Paul Mackerras.
* * Updated to ppp-2.4.1 by Bernhard Kaindl
* * * Updated to ppp-2.4.2 by David Woodhouse 2004.
* * - disconnect method added
* * - remove_options() abuse removed.
* *
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1.66 pugixml 1.4
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pugixml 1.4 - an XML processing library

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src/ - header and source files

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1.67 python-async 0.6.1 :r0

1.67.1 Available under license:

Metadata-Version: 1.0
Name: async
Version: 0.6.1
Summary: Async Framework
Home-page: http://gitorious.org/git-python/async
Author: Sebastian Thiel
Author-email: byronimo@gmail.com
License: BSD License
Description: Async is a framework to process interdependent tasks in a pool of workers
Platform: UNKNOWN
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1.68 python-ipaddress 1.0 :r0

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1.70 python-pip 1.5.6 :r0
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1.71 python-pycurl 7.19.0 :r3
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1.77 simple-jsonrpc 0.3.4

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1.80 v86d 0.1.10 :r1
1.80.1 Available under license:
   v86d -- A x86 Emulation Daemon

1. Purpose
---
v86d provides a backend for kernel drivers that need to execute x86 BIOS code. The code is executed in a controlled environment and the results are passed back to the kernel via the netlink interface.

2. Supported architectures
---
This version of v86d supports the following architectures:
x86, x86-64 (amd64).

3. Configuration
---
3.1. The C library
---
v86d can be compiled against two different C libraries: glibc and klibc, glibc being the default.

Compiling against glibc will result in a dynamically linked executable that is not suitable for inclusion in initrds.

Compiling against klibc will result in a relatively small, statically linked binary, which can be included in an initrd.

To compile v86d against klibc, run ./configure --with-klibc after making sure that a recent (1.4 or newer) version of klibc is installed on your system.

3.2. The emulator backend
---
On x86, the code executed by v86d can be run either in a fully
software-emulated environment (x86emu) or a virtualized environment (vm86) supported by the CPU. On other arches or 64-bit systems, the code is always run by x86emu.

To choose the x86emu backend on a x86 system, run `./configure --with-x86emu`.

4. Installation & Usage

To configure, build and install v86d with the default settings, run:

```
# ./configure --default
# make
# make install
```

If the kernel that you're currently running doesn't provide uvesafb, you can use the KDIR variable to point make to a different kernel tree (e.g. `make KDIR=/usr/src/linux`).

v86d isn't meant to be used in an interactive way. It should be started and called by the kernel. If you want to see it in action, build and load the uvesafb kernel module.

If you want to include v86d into an initramfs image, misc/initramfs provides a minimal config file parsable by gen_init_cpio.

5. Licensing

v86d is licensed under GPL v2.

--
Michal Januszewski <spock@gentoo.org>

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/\ Skeleton implementation for Bison's Yacc-like parsers in C

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1.82 xmltodict 0.8.3

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2013-11-06

1.83 zlib 1.2.7 :r1

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