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Text Part Number: 78EE117C99-11886992

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## 1.7 gcc 4.3.2

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```
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## 1.10 i2c-tools 3.0.2

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## 1.14 kernel 2.6.28

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Ty Coon, President of Vice

That's all there is to it!

/\* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)

\* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)

\*

\* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.

\* <http://www.hypermall.com/>

\* 10/1/97 - commented out CFG\_PHYIE bit - we don't care when the PHY

\* interrupts us (except possibly for removal/insertion of the cable?)

\* 10/4/97 - began heavy inline documentation of the code. Corrected typos

\* and spelling mistakes.

\* 10/5/97 - added code to handle PHY interrupts, disable PHY on

\* loss of link, and correctly re-enable PHY when link is

\* re-established. (put back CFG\_PHYIE)

\*

\* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.

\*

\* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997

\*

\* Linux driver for the IDT77201 NICStAR PCI ATM controller.

\* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;

\* see init\_nicstar() for PHY initialization to change this. This driver

\* expects the Linux ATM stack to support scatter-gather lists

\* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.

\*

\* Implementing minimal-copy of received data:

\* IDT always receives data into a small buffer, then large buffers

\* as needed. This means that data must always be copied to create

\* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)

\* Fix is simple: make large buffers large enough to hold entire

\* SDU, and leave <small\_buffer\_data> bytes empty at the start. Then

\* copy small buffer contents to head of large buffer.

\* Trick is to avoid fragmenting Linux, due to need for a lot of large

\* buffers. This is done by 2 things:

\* 1) skb->destructor / skb->atm.recycle\_buffer

\* combined, allow nicstar\_free\_rx\_skb to be called to

\* recycle large data buffers

\* 2) skb\_clone of received buffers  
\* See nicstar\_free\_rx\_skb and linearize\_buffer for implementation  
\* details.  
\*  
\*  
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\*  
\* M. Welsh, 6 July 1996  
\*  
\*  
\*/

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3. [3]Viraj Bais <vbais@mailman1.intel.com> and [4]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
4. [5]Michael Barone <michael,barone@lmco.com> GPSVME fixes
5. [6]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca>IPv6 support
6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
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14. [15]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port
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16. [17]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
17. [18]John Hay <jhay@icomtek.csr.co.za> IPv6 support and testing
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21. [22]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
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  28. [31]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
  29. [32]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
  30. [33]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
  31. [34]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
  32. [35]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG
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  41. [45]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
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  43. [47]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
  44. [48]Michael Shields <shields@tembel.org> USNO clock driver
  45. [49]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
  46. [50]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
  47. [51]Kenneth Stone <ken@sdd.hp.com> HP-UX port
  48. [52]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
  49. [53]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock driver
  50. [54]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
  51. [55]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD
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### THE BASIC LIBRARY FUNCTIONS

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Written by: Philip Hazel  
Email local part: ph10  
Email domain: cam.ac.uk

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## 1.28 tcpdump 3.9.8

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```
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   version 1.2.3, July 18th, 2005
```

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