Open Source Used In Cloud Services Platform Series System Software 2.7.0

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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1.2 rsync 3.1.2-6.el7_6.1
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1.5 audit 2.8.5 4.el7

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From strings/apr_fnmatch.c, include/apr_fnmatch.h, misc/unix/getopt.c, file_io/unix/mktemp.c, strings/apr_strings.c:

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From network_io/unix/inet_ntop.c, network_io/unix/inet_pton.c:

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From dso/aix/dso.c:

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From strings/apr_strnatcmp.c, include/apr_strings.h:

strnatcmp.c -- Perform 'natural order' comparisons of strings in C.
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From strings/apr_snprintf.c:

*
* cvt - IEEE floating point formatting routines.
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* *

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1.8 slang 2.2.4 11.el7

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1.9 diffutils 3.3 5.el7
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From RPM File Metadata:GPLv3+
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#define GDIFF_MAIN
#include "diff.h"
#include "assert.h"
#include "paths.h"
#include <c-stack.h>
#include <dirname.h>
#include <error.h>
#include <exclude.h>
#include <exitfail.h>
#include <filenamecat.h>
#include <file-type.h>
#include <fnmatch.h>
#include <getopt.h>
#include <hard-locale.h>
#include <preargs.h>
#include <proarg.h>
#include <sh-quote.h>
#include <stat-time.h>
#include <timespec.h>
#include <version-etc.h>
#include <xalloc.h>
#include <xreadlink.h>
#include <binary-io.h>

/* The official name of this program (e.g., no 'g' prefix). */
#define PROGRAM_NAME "diff"

#define AUTHORS\
proper_name ("Paul Eggert"),\
proper_name ("Mike Haertel"),\
proper_name ("David Hayes"),\
proper_name ("Richard Stallman"),\
proper_name ("Len Tower")

#ifndef GUTTER_WIDTH_MINIMUM
#define GUTTER_WIDTH_MINIMUM 3
#endif

struct regexp_list
{
  char *regexps; /* chars representing disjunction of the regexps */
  size_t len; /* chars used in 'regexps' */
  size_t size; /* size malloc'ed for 'regexps'; 0 if not malloc'ed */
  bool multiple_regexps; /* Does 'regexps' represent a disjunction? */
struct re_pattern_buffer *buf;
};

static int compare_files (struct comparison const *, char const *, char const *);
static void add_regexp (struct regexp_list *, char const *);
static void summarize_regexp_list (struct regexp_list *);
static void specify_style (enum output_style);
static void specify_value (char const **, char const *, char const *);
static void try_help (char const *, char const *) __attribute__((noreturn));
static void check_stdout (void);
static void usage (void);

/* If comparing directories, compare their common subdirectories
   recursively. */
static bool recursive;

/* In context diffs, show previous lines that match these regexps. */
static struct regexp_list function_regexp_list;

/* Ignore changes affecting only lines that match these regexps. */
static struct regexp_list ignore_regexp_list;

#if O_BINARY
/* Use binary I/O when reading and writing data (--binary).
   On POSIX hosts, this has no effect. */
static bool binary;
#else
enum { binary = true };
#endif

/* If one file is missing, treat it as present but empty (-N). */
static bool new_file;

/* If the first file is missing, treat it as present but empty
   (--unidirectional-new-file). */
static bool unidirectional_new_file;

/* Report files compared that are the same (-s).
   Normally nothing is output when that happens. */
static bool report_identical_files;

static char const shortopts[] =
"0123456789abBcCdDeEfFhHiIlLnNpPqrsStTuU:vW:wX:yZ";

/* Values for long options that do not have single-letter equivalents. */
enum {
    BINARY_OPTION = CHAR_MAX + 1,
FROM_FILE_OPTION,
HELP_OPTION,
HORIZON_LINES_OPTION,
IGNORE_FILE_NAME_CASE_OPTION,
INHIBIT_HUNK_MERGE_OPTION,
LEFT_COLUMN_OPTION,
LINE_FORMAT_OPTION,
NO_DEREFERENCE_OPTION,
NO_IGNORE_FILE_NAME_CASE_OPTION,
NORMAL_OPTION,
SDIFF_MERGE_ASSIST_OPTION,
STRIP_TRAILING_CR_OPTION,
SUPPRESS_BLANKEMPTY_OPTION,
SUPPRESS_COMMON_LINES_OPTION,
TABSIZE_OPTION,
TO_FILE_OPTION,

/* These options must be in sequence. */
UNCHANGED_LINE_FORMAT_OPTION,
OLD_LINE_FORMAT_OPTION,
NEW_LINE_FORMAT_OPTION,

/* These options must be in sequence. */
UNCHANGED_GROUP_FORMAT_OPTION,
OLD_GROUP_FORMAT_OPTION,
NEW_GROUP_FORMAT_OPTION,
CHANGED_GROUP_FORMAT_OPTION
};

static char const group_format_option[sizeof "--unchanged-group-format"] =
{
"--unchanged-group-format",
"--old-group-format",
"--new-group-format",
"--changed-group-format"
};

static char const line_format_option[sizeof "--unchanged-line-format"] =
{
"--unchanged-line-format",
"--old-line-format",
"--new-line-format"
};

static struct option const longopts[] =
{
{ "binary", 0, 0, BINARY_OPTION},
{ "brief", 0, 0, 'q' },
{"to-file", 1, 0, TO_FILE_OPTION},
{"unchanged-group-format", 1, 0, UNCHANGED_GROUP_FORMAT_OPTION},
{"unchanged-line-format", 1, 0, UNCHANGED_LINE_FORMAT_OPTION},
{"unidirectional-new-file", 0, 0, 'P'},
{"unified", 2, 0, 'U'},
{"version", 0, 0, 'v'},
{"width", 1, 0, 'W'},
{0, 0, 0, 0}
};
/* Return a string containing the command options with which diff was invoked.
Spaces appear between what were separate ARGV-elements.
There is a space at the beginning but none at the end.
If there were no options, the result is an empty string.
Arguments: OPTIONVEC, a vector containing separate ARGV-elements, and COUNT,
the length of that vector. */
static char *
option_list (char **optionvec, int count)
{
int i;
size_t size = 1;
char *result;
char *p;
for (i = 0; i < count; i++)
size += 1 + shell_quote_length (optionvec[i]);
p = result = xmalloc (size);
for (i = 0; i < count; i++)
{
*p++ = ' ';
p = shell_quote_copy (p, optionvec[i]);
}
*p = '\0';
return result;
}

/* Return an option value suitable for add_exclude. */
static int
exclude_options (void)
{
return EXCLUDE_WILDCARDS | (ignore_file_name_case ? FNM_CASEFOLD : 0);

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int main (int argc, char **argv)
{
  int exit_status = EXIT_SUCCESS;
  int c;
  int i;
  int prev = -1;
  int ocontext = -1;
  bool explicit_context = false;
  size_t width = 0;
  bool show_c_function = false;
  char const *from_file = NULL;
  char const *to_file = NULL;
  uintmax_t numval;
  char *numend;

  /* Do our initializations. */
  exit_failure = EXIT_TROUBLE;
  initialize_main (&argc, &argv);
  set_program_name (argv[0]);
  setlocale (LC_ALL, "");
  bindtextdomain (PACKAGE, LOCALEDIR);
  textdomain (PACKAGE);
  c_stack_action (0);
  function_regexp_list.buf = &function_regexp;
  ignore_regexp_list.buf = &ignore_regexp;
  re_set_syntax (RE_SYNTAX_GREP | RE_NO_POSIX_BACKTRACKING);
  excluded = new_exclude ();

  /* Decode the options. */

  while ((c = getopt_long (argc, argv, shortopts, longopts, NULL)) != -1)
  {
    switch (c)
    {
    case 0:
      break;
    case '0':
    case '1':
    case '2':
    case '3':
    case '4':
    case '5':
    case '6':
    case '7':
    ...
case '8':
    case '9':
        if (! ISDIGIT (prev))
            ocontext = c - '0';
        else if (LIN_MAX / 10 < ocontext
            || ((ocontext = 10 * ocontext + c - '0') < 0))
            ocontext = LIN_MAX;
        break;

    case 'a':
        text = true;
        break;

    case 'b':
        if (ignore_white_space < IGNORE_SPACE_CHANGE)
            ignore_white_space = IGNORE_SPACE_CHANGE;
        break;

    case 'Z':
        if (ignore_white_space < IGNORE_SPACE_CHANGE)
            ignore_white_space |= IGNORE_TRAILING_SPACE;
        break;

    case 'B':
        ignore_blank_lines = true;
        break;

    case 'C':
    case 'U':
    {
        if (optarg)
            {
                numval = strtoumax (optarg, &numend, 10);
                if (*numend)
                    try_help ("invalid context length \"%s\"", optarg);
                if (LIN_MAX < numval)
                    numval = LIN_MAX;
            }
        else
            numval = 3;
    
    specify_style (c == 'U' ? OUTPUT_UNIFIED : OUTPUT_CONTEXT);
    if (context < numval)
        context = numval;
    explicit_context = true;
    }
    break;
case 'c':
    specify_style (OUTPUT_CONTEXT);
    if (context < 3)
        context = 3;
    break;

case 'd':
    minimal = true;
    break;

case 'D':
    specify_style (OUTPUT_IFDEF);
    {
        static char const C_ifdef_group_formats[] =
            "%%=%c#ifndef %s
%%<#endif /* ! %s */
%c#ifdef %s
%%>#endif /* %s */
%c#ifndef %s
%%<#else /* %s */
%%>#endif /* %s */
";
        char *b = xmalloc (sizeof C_ifdef_group_formats
            + 7 * strlen (optarg) - 14 /* 7*"%s" */
            - 8 /* 5"%\%" + 3"%c" */);
        sprintf (b, C_ifdef_group_formats,
            optarg, optarg, optarg, optarg);
        for (i = 0; i < sizeof group_format / sizeof group_format[0]; i++)
            specify_value (&group_format[i], b, "-D");
        b += strlen (b) + 1;
    }
    break;

case 'e':
    specify_style (OUTPUT_ED);
    break;

case 'E':
    if (ignore_white_space < IGNORE_SPACE_CHANGE)
        ignore_white_space |= IGNORE_TAB_EXPANSION;
    break;

case 'f':
    specify_style (OUTPUT_FORWARD_ED);
    break;

case 'F':
    add_regexp (&function_regexp_list, optarg);
    break;
case 'h':
    /* Split the files into chunks for faster processing.
       Usually does not change the result.

       This currently has no effect. */
    break;

case 'H':
    speed_large_files = true;
    break;

case 'i':
    ignore_case = true;
    break;

case 'I':
    add_regexp (&ignore_regexp_list, optarg);
    break;

case 't':
    if (!pr_program[0])
        try_help ("pagination not supported on this host", NULL);
    paginate = true;
    #ifdef SIGCHLD
    /* Pagination requires forking and waiting, and
       System V fork+wait does not work if SIGCHLD is ignored. */
    signal (SIGCHLD, SIG_DFL);
    #endif
    break;

case 'L':
    if (!file_label[0])
        file_label[0] = optarg;
    else if (!file_label[1])
        file_label[1] = optarg;
    else
        fatal ("too many file label options");
    break;

case 'n':
    specify_style (OUTPUT_RCS);
    break;

case 'N':
    new_file = true;
    break;
case 'p':
    show_c_function = true;
    add_regexp (&function_regexp_list, "^[:alpha:]$\]");
    break;

case 'P':
    unidirectional_new_file = true;
    break;

case 'q':
    brief = true;
    break;

case 'r':
    recursive = true;
    break;

case 's':
    report_identical_files = true;
    break;

case 'S':
    specify_value (&starting_file, optarg, "-S");
    break;

case 't':
    expand_tabs = true;
    break;

case 'T':
    initial_tab = true;
    break;

case 'u':
    specify_style (OUTPUT_UNIFIED);
    if (context < 3)
        context = 3;
    break;

case 'v':
    version_etc (stdout, PROGRAM_NAME, PACKAGE_NAME, Version, AUTHORS, (char *) NULL);
    check_stdout ();
    return EXIT_SUCCESS;

case 'w':
    ignore_white_space = IGNORE_ALL_SPACE;
    break;
case 'x':
    add_exclude (excluded, optarg, exclude_options ());
    break;

case 'X':
    if (add_exclude_file (add_exclude, excluded, optarg,
        exclude_options (), 'u'))
        pfatal_with_name (optarg);
    break;

case 'y':
    specify_style (OUTPUT_SDIFF);
    break;

case 'W':
    numval = strtoumax (optarg, &numend, 10);
    if (! (0 < numval && numval <= SIZE_MAX) || *numend)
        try_help ("invalid width '%s", optarg);
    if (width != numval)
    {
        if (width)
            fatal ("conflicting width options");
        width = numval;
    }
    break;

case BINARY_OPTION:
#if O_BINARY
    binary = true;
    if (! isatty (STDOUT_FILENO))
        set_binary_mode (STDOUT_FILENO, O_BINARY);
#endif
    break;

case FROM_FILE_OPTION:
    specify_value (&from_file, optarg, "--from-file");
    break;

case HELP_OPTION:
    usage ()
    check_stdout ();
    return EXIT_SUCCESS;

case HORIZON_LINES_OPTION:
    numval = strtoumax (optarg, &numend, 10);
    if (*numend)
        try_help ("invalid horizon length '%s", optarg);
horizon_lines = MAX (horizon_lines, MIN (numval, LIN_MAX));
break;

case IGNORE_FILE_NAME_CASE_OPTION:
    ignore_file_name_case = true;
    break;

case INHIBIT_HUNK_MERGE_OPTION:
    /* This option is obsolete, but accept it for backward
       compatibility. */
    break;

case LEFT_COLUMN_OPTION:
    left_column = true;
    break;

case LINE_FORMAT_OPTION:
    specify_style (OUTPUT_IFDEF);
    for (i = 0; i < sizeof line_format / sizeof line_format[0]; i++)
        specify_value (&line_format[i], optarg, "--line-format");
    break;

case NO_DEREFERENCE_OPTION:
    no_dereference_symlinks = true;
    break;

case NO_IGNORE_FILE_NAME_CASE_OPTION:
    ignore_file_name_case = false;
    break;

case NORMAL_OPTION:
    specify_style (OUTPUT_NORMAL);
    break;

case SDIFF_MERGE_ASSIST_OPTION:
    specify_style (OUTPUT_SDIFF);
    sdiff_merge_assist = true;
    break;

case STRIP_TRAILING_CR_OPTION:
    strip_trailing_cr = true;
    break;

case SUPPRESS_BLANK_EMPTY_OPTION:
    suppress_blank_empty = true;
    break;

case SUPPRESS_COMMON_LINES_OPTION:
suppress_common_lines = true;
break;

case TABSIZE_OPTION:
    numval = strtoumax (optarg, &numend, 10);
    if (! (0 < numval && numval <= SIZE_MAX) || *numend)
        try_help ("invalid tabsize \"%s\"", optarg);
    if (tabsize != numval)
        fatal ("conflicting tabsize options");
    tabsize = numval;
    break;

case TO_FILE_OPTION:
    specify_value (&to_file, optarg, "--to-file");
    break;

case UNCHANGED_LINE_FORMAT_OPTION:
    case OLD_LINE_FORMAT_OPTION:
    case NEW_LINE_FORMAT_OPTION:
        specify_style (OUTPUT_IFDEF);
        c -= UNCHANGED_LINE_FORMAT_OPTION;
        specify_value (&line_format[c], optarg, line_format_option[c]);
        break;

case UNCHANGED_GROUP_FORMAT_OPTION:
    case OLD_GROUP_FORMAT_OPTION:
    case NEW_GROUP_FORMAT_OPTION:
    case CHANGED_GROUP_FORMAT_OPTION:
        specify_style (OUTPUT_IFDEF);
        c -= UNCHANGED_GROUP_FORMAT_OPTION;
        specify_value (&group_format[c], optarg, group_format_option[c]);
        break;

default:
    try_help (NULL, NULL);
} } 
    prev = c;
    }

if (output_style == OUTPUT_UNSPECIFIED)
{ 
    if (show_c_function)
    { 
        specify_style (OUTPUT_CONTEXT);
        if (ocontext < 0)
context = 3;

else

specify_style (OUTPUT_NORMAL);

if (output_style != OUTPUT_CONTEXT || hard.locale (LC_TIME))
{
    
    #if (defined STAT_TIMESPEC || defined STAT_TIMESPEC_NS \\
    || defined HAVE_STRUCT_STAT_ST_SPARE1)
    time_format = "%Y-%m-%d %H:%M:%S,%N %z;";
    
    #else
    time_format = "%Y-%m-%d %H:%M:%S %z;";
    
    #endif

    
    else

    {
        /* See POSIX 1003.1-2001 for this format. */
        
        time_format = "%a %b %e %T %Y";
    }

    
    
    
    if (0 <= ocontext
        
        && (output_style == OUTPUT_CONTEXT
            
            || output_style == OUTPUT_UNIFIED)
        
        && (context < ocontext
            
            || (ocontext < context && ! explicit_context))
        context = ocontext;

        
        if (! tabsize)
            tabsize = 8;
        
        if (! width)
            width = 130;

        
    
    
    /* Maximize first the half line width, and then the gutter width,
    
    according to the following constraints:

    1. Two half lines plus a gutter must fit in a line.
    2. If the half line width is nonzero:
        a. The gutter width is at least GUTTER_WIDTH_MINIMUM.
        b. If tabs are not expanded to spaces,
        
        a half line plus a gutter is an integral number of tabs,
        
        so that tabs in the right column line up. */

    
    intmax_t t = expand_tabs ? 1 : tabsize;
    intmax_t w = width;
    intmax_t off = (w + t + GUTTER_WIDTH_MINIMUM) / (2 * t) * t;
    sdiff_half_width = MAX (0, MIN (off - GUTTER_WIDTH_MINIMUM, w - off)),

    

sdiff_column2_offset = sdiff_half_width ? off : w;
}

/* Make the horizon at least as large as the context, so that
shift_boundaries has more freedom to shift the first and last hunks. */
if (horizon_lines < context)
    horizon_lines = context;

summarize_regexp_list (&function_regexp_list);
summarize_regexp_list (&ignore_regexp_list);

if (output_style == OUTPUT_IFDEF)
{
    for (i = 0; i < sizeof line_format / sizeof line_format[0]; i++)
        if (!line_format[i])
            line_format[i] = "\n";
    if (!group_format[OLD])
        group_format[OLD]
            = group_format[CHANGED] ? group_format[CHANGED] : "<";
    if (!group_format[NEW])
        group_format[NEW]
            = group_format[CHANGED] ? group_format[CHANGED] : ">";
    if (!group_format[UNCHANGED])
        group_format[UNCHANGED] = "=";
    if (!group_format[CHANGED])
        group_format[CHANGED] = concat (group_format[OLD],
            group_format[NEW], "");
}

no_diff_means_no_output =
    (output_style == OUTPUT_IFDEF ?
    (!*group_format[UNCHANGED]
    || (STREQ (group_format[UNCHANGED], "=")
    && !*line_format[UNCHANGED])))
    : (output_style != OUTPUT_SDIFF) | suppress_common_lines);

files_can_be_treated_as_binary =
    (brief & binary
    & ~ (ignore_blank_lines | ignore_case | strip_trailing_cr
    | (ignore_regexp_list.regexps || ignore_white_space)))

switch_string = option_list (argv + 1, optind - 1);

if (from_file)
{
    if (to_file)
        fatal ("--from-file and --to-file both specified");
    else
for (; optind < argc; optind++)
{
    int status = compare_files (NULL, from_file, argv[optind]);
    if (exit_status < status)
        exit_status = status;
}
}
else
{
    if (to_file)
    for (; optind < argc; optind++)
    {
        int status = compare_files (NULL, argv[optind], to_file);
        if (exit_status < status)
            exit_status = status;
    }
    else
    {
        if (argc - optind != 2)
        {
            if (argc - optind < 2)
                try_help ("missing operand after " ARGوب "", argv[argc - 1]);
            else
                try_help ("extra operand " ARGوب "", argv[optind + 2]);
        }
        exit_status = compare_files (NULL, argv[optind], argv[optind + 1]);
    }
}

/* Print any messages that were saved up for last. */
print_message_queue ();

cHECK_STDOUT ();
exit (exit_status);
return exit_status;
}

/* Append to REGLIST the regexp PATTERN. */

static void
add_regexp (struct regexp_list *reglist, char const *pattern)
{
    size_t patlen = strlen (pattern);
    char const *m = re_compile_pattern (pattern, patlen, reglist->buf);
    if (m != 0)
        error (0, 0, "%s: %s", pattern, m);

else
{
    char *regexps = reglist->regexps;
    size_t len = reglist->len;
    bool multiple_regexps = reglist->multiple_regexps = regexps != 0;
    size_t newlen = reglist->len = len + 2 * multiple_regexps + patlen;
    size_t size = reglist->size;

    if (size <= newlen)
    {
        if (!size)
            size = 1;

        do size *= 2;
        while (size <= newlen);

        reglist->size = size;
        reglist->regexps = regexps = xrealloc (regexps, size);
    }
    if (multiple_regexps)
    {
        regexps[len++] = '\';
        regexps[len++] = '|';
    }
    memcpy (regexps + len, pattern, patlen + 1);
}
}

/* Ensure that REGLIST represents the disjunction of its regexps.
   This is done here, rather than earlier, to avoid O(N^2) behavior. */

static void
summarize_regexp_list (struct regexp_list *reglist)
{
    if (reglist->regexps)
    {
        /* At least one regexp was specified. Allocate a fastmap for it. */
        reglist->buf->fastmap = xmalloc (1 << CHAR_BIT);
        if (reglist->multiple_regexps)
        {
            /* Compile the disjunction of the regexps.
               (If just one regexp was specified, it is already compiled.) */
            char const *m = re_compile_pattern (reglist->regexps, reglist->len,
                reglist->buf);
            if (m)
                error (EXIT_TROUBLE, 0, "%s: %s", reglist->regexps, m);
        }
    }
}
}
static void
try_help (char const *reason_msgid, char const *operand)
{
if (reason_msgid)
error (0, 0, _(reason_msgid), operand);
error (EXIT_TROUBLE, 0, _("Try '%s --help' for more information."),
program_name);
abort ();
}
static void
check_stdout (void)
{
if (ferror (stdout))
fatal ("write failed");
else if (fclose (stdout) != 0)
pfatal_with_name (_("standard output"));
}
static char const * const option_help_msgid[] = {
N_(" --normal
output a normal diff (the default)"),
N_("-q, --brief
report only when files differ"),
N_("-s, --report-identical-files report when two files are the same"),
N_("-c, -C NUM, --context[=NUM] output NUM (default 3) lines of copied context"),
N_("-u, -U NUM, --unified[=NUM] output NUM (default 3) lines of unified context"),
N_("-e, --ed
output an ed script"),
N_("-n, --rcs
output an RCS format diff"),
N_("-y, --side-by-side
output in two columns"),
N_("-W, --width=NUM
output at most NUM (default 130) print columns"),
N_(" --left-column
output only the left column of common lines"),
N_(" --suppress-common-lines do not output common lines"),
"",
N_("-p, --show-c-function
show which C function each change is in"),
N_("-F, --show-function-line=RE show the most recent line matching RE"),
N_(" --label LABEL
use LABEL instead of file name\n"
"
(can be repeated)"),
"",
N_("-t, --expand-tabs
expand tabs to spaces in output"),
N_("-T, --initial-tab
make tabs line up by prepending a tab"),
N_(" --tabsize=NUM
tab stops every NUM (default 8) print columns"),
N_(" --suppress-blank-empty suppress space or tab before empty output lines"),
N_("-l, --paginate
pass output through 'pr' to paginate it"),
"",
N_("-r, --recursive
recursively compare any subdirectories found"),
N_(" --no-dereference
don't follow symbolic links"),
N_("-N, --new-file
treat absent files as empty"),

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N_("--unidirectional-new-file treat absent first files as empty"),
N_("--ignore-file-name-case ignore case when comparing file names"),
N_("--no-ignore-file-name-case consider case when comparing file names"),
N_("-x, --exclude=FILE exclude files that match PAT"),
N_("-X, --exclude-from=FILE exclude files that match any pattern in FILE"),
N_("--no-ignore-file-name-case consider case when comparing file names"),
N_("-S, --starting-file=FILE start with FILE when comparing directories"),
N_("--from-file=FILE1 compare FILE1 to all operands\n" "FILE1 can be a directory"),
N_("--to-file=FILE2 compare all operands to FILE2\n" "FILE2 can be a directory"),
N_("--ignore-case ignore case differences in file contents"),
N_("-E, --ignore-tab-expansion ignore changes due to tab expansion"),
N_("-Z, --ignore-trailing-space ignore white space at line end"),
N_("-b, --ignore-space-change ignore changes in the amount of white space"),
N_("-w, --ignore-all-space ignore all white space"),
N_("-B, --ignore-blank-lines ignore changes where lines are all blank"),
N_("-I, --ignore-matching-lines=RE ignore changes where all lines match RE"),
N_("-a, --text treat all files as text"),
N_("--strip-trailing-cr strip trailing carriage return on input"),
N_("--binary read and write data in binary mode"),
#if O_BINARY
N_("--ifdef=NAME output merged file with '#ifdef NAME' diffs"),
N_("--GTYPE-group-format=GFMT format GTYPE input groups with GFMT"),
N_("--line-format=LFMT format all input lines with LFMT"),
N_("--LTYPE-line-format=LFMT format LTYPE input lines with LFMT"),
N_(" These format options provide fine-grained control over the output\n" "of diff, generalizing -D/--ifdef."),
N_(" LTYPE is 'old', 'new', or 'unchanged'. GTYPE is LTYPE or 'changed'."),
N_(" GFMT (only) may contain:\n" %< lines from FILE1\n" %> lines from FILE2\n" %= lines common to FILE1 and FILE2\n" %-%[WIDTH][PREC][dox] LETTER printf-style spec for LETTER\n" LETTERs are as follows for new group, lower case for old group:\n" F first line number\n" L last line number\n" N number of lines = L-F+1\n" E F-1\n" M L+1\n" %(%A=B?T:E) if A equals B then T else E"),
N_(" LFMT (only) may contain:\n" %L contents of line\n" %l contents of line, excluding any trailing newline\n" %-%[WIDTH][PREC][dox] printf-style spec for input line number"),
Both GFMT and LFMT may contain:

% % \n
%c'C'  the single character C
%c'O00'  the character with octal code OOO\n
C    the character C (other characters represent themselves),

", "

-d, --minimal            try hard to find a smaller set of changes",
", "

--horizon-lines=NUM  keep NUM lines of the common prefix and suffix",
", "

--speed-large-files  assume large files and many scattered small changes",
", "

--help               display this help and exit",
", "

-v, --version            output version information and exit",
", "

FILES are 'FILE1 FILE2' or 'DIR1 DIR2' or 'DIR FILE...' or 'FILE... DIR'.",
", "

If --from-file or --to-file is given, there are no restrictions on FILE(s).",
", "

If a FILE is '-', read standard input.
",
", "

Exit status is 0 if inputs are the same, 1 if different, 2 if trouble.
",
", "

); static void usage (void) {
char const * const *p;

printf (_("Usage: %s [OPTION]... FILES\n"), program_name);
printf (_("\nCompare FILES line by line.\n"), stdout);

for (p = option_help_msgid;  *p;  p++)
{
    if (!**p)
putchar ('\n');
else
{
    char const *msg = _(*p);
    char const *nl;
while ((nl = strchr (msg, '\n')))
    {
        int msglen = nl + 1 - msg;
printf (_("%s\n"), msg);
msg = nl + 1;
    }

printf (_("%s\n" + 2 * (*msg != ' ' && *msg != '-'), msg);

    
Mandatory arguments to long options are mandatory for short options too.\n")}, stdout);

    
for (p = option_help_msgid;  *p;  p++)
{
    if (!**p)
putchar ('\n');
else
{
    char const *msg = _(*p);
    char const *nl;
while ((nl = strchr (msg, '\n')))
    {
        int msglen = nl + 1 - msg;
printf (_("%s\n"), msg);
msg = nl + 1;
    }

printf (_("%s\n" + 2 * (*msg != ' ' && *msg != '-'), msg);

    );

}}}
}
}
emit_bug_reporting_address ();
}
/* Set VAR to VALUE, reporting an OPTION error if this is a
conflict. */
static void
specify_value (char const **var, char const *value, char const *option)
{
if (*var && ! STREQ (*var, value))
{
error (0, 0, _("conflicting %s option value '%s'"), option, value);
try_help (NULL, NULL);
}
*var = value;
}
/* Set the output style to STYLE, diagnosing conflicts. */
static void
specify_style (enum output_style style)
{
if (output_style != style)
{
if (output_style != OUTPUT_UNSPECIFIED)
try_help ("conflicting output style options", NULL);
output_style = style;
}
}
/* Set the last-modified time of *ST to be the current time. */
static void
set_mtime_to_now (struct stat *st)
{
#ifdef STAT_TIMESPEC
gettime (&STAT_TIMESPEC (st, st_mtim));
#else
struct timespec t;
gettime (&t);
st->st_mtime = t.tv_sec;
# if defined STAT_TIMESPEC_NS
STAT_TIMESPEC_NS (st, st_mtim) = t.tv_nsec;
# elif defined HAVE_STRUCT_STAT_ST_SPARE1
st->st_spare1 = t.tv_nsec / 1000;
# endif
#endif
}

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/* Compare two files (or dirs) with parent comparison PARENT and names NAME0 and NAME1. (If PARENT is null, then the first name is just NAME0, etc.) This is self-contained; it opens the files and closes them.

Value is EXIT_SUCCESS if files are the same, EXIT_FAILURE if different, EXIT_TROUBLE if there is a problem opening them. */

static int compare_files (struct comparison const *parent,
    char const *name0,
    char const *name1)
{
    struct comparison cmp;
#define DIR_P(f) (S_ISDIR (cmp.file[f].stat.st_mode) != 0)
    register int f;
    int status = EXIT_SUCCESS;
    bool same_files;
    char *free0;
    char *free1;

    /* If this is directory comparison, perhaps we have a file that exists only in one of the directories. If so, just print a message to that effect. */

    if (! ((name0 && name1) || (unidirectional_new_file && name1) || new_file))
    {
        char const *name = name0 ? name0 : name1;
        char const *dir = parent->file[!name0].name;

        /* See POSIX 1003.1-2001 for this format. */
        message ("Only in %s: %s\n", dir, name);

        /* Return EXIT_FAILURE so that diff_dirs will return EXIT_FAILURE ("some files differ"). */
        return EXIT_FAILURE;
    }

    memset (cmp.file, 0, sizeof cmp.file);
    cmp.parent = parent;

    /* cmp.file[f].desc markers */
#define NONEXISTENT (-1) /* nonexistent file */
#define UNOPENED (-2) /* unopened file (e.g. directory) */
#define ERRNO_ENCODE(errno) (-3 - (errno)) /* encoded errno value */
# define ERRNO_DECODE(desc) (-3 - (desc)) /* inverse of ERRNO_ENCODE */

cmp.file[0].desc = name0 ? UNOPENED : NONEXISTENT;
cmp.file[1].desc = name1 ? UNOPENED : NONEXISTENT;

/* Now record the full name of each file, including nonexistent ones. */

if (!name0)
    name0 = name1;
if (!name1)
    name1 = name0;

if (!parent)
{
    free0 = NULL;
    free1 = NULL;
    cmp.file[0].name = name0;
    cmp.file[1].name = name1;
}
else
{
    cmp.file[0].name = free0
    = file_name_concat (parent->file[0].name, name0, NULL);
    cmp.file[1].name = free1
    = file_name_concat (parent->file[1].name, name1, NULL);
}

/* Stat the files. */

for (f = 0; f < 2; f++)
{
    if (cmp.file[f].desc != NONEXISTENT)
    {
        if (f && file_name_cmp (cmp.file[f].name, cmp.file[0].name) == 0)
        {
            cmp.file[f].desc = cmp.file[0].desc;
            cmp.file[f].stat = cmp.file[0].stat;
        }
        else if (STREQ (cmp.file[f].name, "-"))
        {
            cmp.file[f].desc = STDIN_FILENO;
            if (binary && ! isatty (STDIN_FILENO))
                set_binary_mode (STDIN_FILENO, O_BINARY);
            if (fstat (STDIN_FILENO, &cmp.file[f].stat) != 0)
                cmp.file[f].desc = ERRNO_ENCODE (errno);
            else
        }
}
if (S_ISREG (cmp.file[f].stat.st_mode))
{
    off_t pos = lseek (STDIN_FILENO, 0, SEEK_CUR);
    if (pos < 0)
        cmp.file[f].desc = ERRNO_ENCODE (errno);
    else
        cmp.file[f].stat.st_size =
            MAX (0, cmp.file[f].stat.st_size - pos);
}

/* POSIX 1003.1-2001 requires current time for stdin. */
set_mtime_to_now (&cmp.file[f].stat);
}
else if ((no_dereference_symlinks
    ? lstat (cmp.file[f].name, &cmp.file[f].stat)
    : stat (cmp.file[f].name, &cmp.file[f].stat))
    != 0)
    cmp.file[f].desc = ERRNO_ENCODE (errno);
}

/* Mark files as nonexistent as needed for -N and -P, if they are
inaccessible empty regular files (the kind of files that 'patch'
creates to indicate nonexistent backups), or if they are
top-level files that do not exist but their counterparts do
exist. */
for (f = 0; f < 2; f++)
    if (((new_file || (f == 0 && unidirectional_new_file))
        && (cmp.file[f].desc == UNOPENED
            ? (S_ISREG (cmp.file[f].stat.st_mode)
                && ! (cmp.file[f].stat.st_mode & (S_IRWXU | S_IRWXG | S_IRWXO))
                && cmp.file[f].stat.st_size == 0)
            : ((cmp.file[f].desc == ERRNO_ENCODE (ENOENT)
                    || cmp.file[f].desc == ERRNO_ENCODE (EBADF))
                && ! parent
                && (cmp.file[1 - f].desc == UNOPENED
                    || cmp.file[1 - f].desc == STDIN_FILENO))))
        cmp.file[f].desc = NONEXISTENT;

for (f = 0; f < 2; f++)
    if (cmp.file[f].desc == NONEXISTENT)
        { memset (&cmp.file[f].stat, 0, sizeof cmp.file[f].stat);
            cmp.file[f].stat.st_mode = cmp.file[1 - f].stat.st_mode;
        }
for (f = 0; f < 2; f++)
{
    int e = ERRNO_DECODE (cmp.file[f].desc);
    if (0 <= e)
    {
        errno = e;
        perror_with_name (cmp.file[f].name);
        status = EXIT_TROUBLE;
    }
}

if (status == EXIT_SUCCESS && ! parent && DIR_P (0) != DIR_P (1))
{
    /* If one is a directory, and it was specified in the command line,
    use the file in that dir with the other file's basename. */
    int fnm_arg = DIR_P (0);
    int dir_arg = 1 - fnm_arg;
    char const *fnm = cmp.file[fnm_arg].name;
    char const *dir = cmp.file[dir_arg].name;
    char const *filename = cmp.file[dir_arg].name = free0
    = find_dir_file_pathname (dir, last_component (fnm));

    if (STREQ (fnm, "."))
        fatal ("cannot compare "." to a directory");

    if ((no_dereference_symlinks
        ? lstat (filename, &cmp.file[dir_arg].stat)
        : stat (filename, &cmp.file[dir_arg].stat))
        != 0)
    {
        perror_with_name (filename);
        status = EXIT_TROUBLE;
    }
}

if (status != EXIT_SUCCESS)
{
    /* One of the files should exist but does not. */
}
else if (cmp.file[0].desc == NONEXISTENT
    && cmp.file[1].desc == NONEXISTENT)
{
    /* Neither file "exists", so there's nothing to compare. */
}
else if ((same_files
    = (cmp.file[0].desc != NONEXISTENT
        && cmp.file[1].desc != NONEXISTENT)
&& 0 < same_file (&cmp.file[0].stat, &cmp.file[1].stat)
&& same_file_attributes (&cmp.file[0].stat,
&cmp.file[1].stat))
&& no_diff_means_no_output)
{
/* The two named files are actually the same physical file. We know they are identical without actually reading them. */
}
else if (DIR_P (0) & DIR_P (1))
{
    if (output_style == OUTPUT_IFDEF)
        fatal ("-D option not supported with directories");
/* If both are directories, compare the files in them. */
    if (parent && !recursive)
    {
        /* But don't compare dir contents one level down unless -r was specified. See POSIX 1003.1-2001 for this format. */
        message ("Common subdirectories: %s and %s\n", cmp.file[0].name, cmp.file[1].name);
    }
    else
        status = diff_dirs (&cmp, compare_files);
}
else if ((DIR_P (0) | DIR_P (1))
|| (parent
    && !((S_ISREG (cmp.file[0].stat.st_mode)
        || S_ISLNK (cmp.file[0].stat.st_mode))
    && (S_ISREG (cmp.file[1].stat.st_mode)
        && S_ISLNK (cmp.file[1].stat.st_mode))))
{
    if (cmp.file[0].desc == NONEXISTENT || cmp.file[1].desc == NONEXISTENT)
    {
/* We have a subdirectory that exists only in one directory. */
        if ((DIR_P (0) | DIR_P (1))
            && recursive
            && (unidirectional_new_file
                && cmp.file[0].desc == NONEXISTENT))
            status = diff_dirs (&cmp, compare_files);
else
        {
            char const *dir;

/* PARENT must be non-NULL here. */
assert (parent);

dir = parent->file[cmp.file[0].desc == NONEXISTENT].name;

/* See POSIX 1003.1-2001 for this format. */
message("Only in %s: %s\n", dir, name0);

status = EXIT_FAILURE;
}

else
{
/* We have two files that are not to be compared. */

/* See POSIX 1003.1-2001 for this format. */
message5("File %s is a %s while file %s is a %s\n", 
    file_label[0] ? file_label[0] : cmp.file[0].name,
    file_type(&cmp.file[0].stat),
    file_type(&cmp.file[1].stat));

/* This is a difference. */
status = EXIT_FAILURE;
}

else if (S_ISLNK(cmp.file[0].stat.st_mode) 
|| S_ISLNK(cmp.file[1].stat.st_mode))
{
/* We get here only if we use lstat(), not stat(). */
assert(no_dereference_symlinks);

if (S_ISLNK(cmp.file[0].stat.st_mode) 
&& S_ISLNK(cmp.file[1].stat.st_mode))
{
/* Compare the values of the symbolic links. */
char *link_value[2] = { NULL, NULL };

for (f = 0; f < 2; f++)
{
    link_value[f] = xreadlink(cmp.file[f].name);
    if (link_value[f] == NULL)
    {
        perror_with_name(cmp.file[f].name);
        status = EXIT_TROUBLE;
        break;
    }
}

if (status == EXIT_SUCCESS)
{
if ( ! STREQ (link_value[0], link_value[1]))
{
    message ("Symbolic links %s and %s differ\n",
            cmp.file[0].name, cmp.file[1].name);
    /* This is a difference. */
    status = EXIT_FAILURE;
}
for (f = 0; f < 2; f++)
    free (link_value[f]);
else
/* We have two files that are not to be compared, because
   one of them is a symbolic link and the other one is not. */
message5 ("File %s is a %s while file %s is a %s\n",
            file_label[0] ? file_label[0] : cmp.file[0].name,
            file_type (&cmp.file[0].stat),
            file_type (&cmp.file[1].stat));
    /* This is a difference. */
    status = EXIT_FAILURE;
}
else if (files_can_be_treated_as_binary
        && S_ISREG (cmp.file[0].stat.st_mode)
        && S_ISREG (cmp.file[1].stat.st_mode)
        && cmp.file[0].stat.st_size != cmp.file[1].stat.st_size)
{
    message ("Files %s and %s differ\n",
            file_label[0] ? file_label[0] : cmp.file[0].name,
    status = EXIT_FAILURE;
}
else
/* Both exist and neither is a directory. */
/* Open the files and record their descriptors. */

int oflags = O_RDONLY | (binary ? O_BINARY : 0);

    if (cmp.file[0].desc == UNOPENED)
        if ((cmp.file[0].desc = open (cmp.file[0].name, oflags, 0)) < 0)
            perror_with_name (cmp.file[0].name);
status = EXIT_TROUBLE;

if (cmp.file[1].desc == UNOPENED)
{
    if (same_files)
        cmp.file[1].desc = cmp.file[0].desc;
    else if ((cmp.file[1].desc = open (cmp.file[1].name, oflags, 0)) < 0)
    {
        perror_with_name (cmp.file[1].name);
        status = EXIT_TROUBLE;
    }
}
/* Compare the files, if no error was found. */

if (status == EXIT_SUCCESS)
    status = diff_2_files (&cmp);

/* Close the file descriptors. */

if (0 <= cmp.file[0].desc && close (cmp.file[0].desc) != 0)
{
    perror_with_name (cmp.file[0].name);
    status = EXIT_TROUBLE;
}
    if (0 <= cmp.file[1].desc && cmp.file[0].desc != cmp.file[1].desc &&
        close (cmp.file[1].desc) != 0)
    {
        perror_with_name (cmp.file[1].name);
        status = EXIT_TROUBLE;
    }

/* Now the comparison has been done, if no error prevented it,
    and STATUS is the value this function will return. */

if (status == EXIT_SUCCESS)
{
    if (report_identical_files && !DIR_P (0))
        message ("Files %s and %s are identical",
                file_label[0] ? file_label[0] : cmp.file[0].name,
    else
    {
        /* Flush stdout so that the user sees differences immediately.
        This can hurt performance, unfortunately. */
        if (fflush (stdout) != 0)
pfatal_with_name ("standard output");
}

free (free0);
free (free1);

return status;
}

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1.10 gdk-pixbuf 2.36.12-3.el7

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1.14 postfix 2.10.1-7.el7

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1.18 elfutils 0.176 2.el7

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1.23 util-linux 2.23.2-61.el7

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Index: debug.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/debug.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u u -r1.1 -r1.3
--- debug.c 7 Sep 2007 10:01:21 -0000 1.1
+++ debug.c 15 Jan 2008 13:49:25 -0000 1.3
@@ -52,7 +52,11 @@
void X86EMU_trace_regs (void)
{
 if (DEBUG_TRACE()) {

static void print_decoded_instruction (void)

Index: ops2.c

===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops2.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- ops2.c	7 Sep 2007 10:01:21 -0000	1.1
+++ ops2.c	20 Mar 2008 15:48:34 -0000	1.3
@@ -149,8 +149,69 @@
target += (s16) M.x86.R_IP;
DECODE_PRINTF2("%04x
", target);
TRACE_AND_STEP();
- if (cond)
+ if (cond) {
  M.x86.R_IP = (u16)target;
  JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " LONG COND ");
+ }
+ DECODE_CLEAR_SEGOVR();
+ END_OF_INSTR();
+}
+
+/***************************************************************************/
+REMARKS:
+Handles opcode 0x0f,0xC8-0xCF
+s32 x86emu_bswap(s32 reg)
+{ 
+ // perform the byte swap 
+ s32 temp = reg;
+ reg = (temp & 0xFF000000) >> 24;

reg |= (temp & 0xFF0000) >> 8;
reg |= (temp & 0xFF00) << 8;
reg |= (temp & 0xFF) << 24;
return reg;
}

void x86emuOp2_bswap(u8 op2)
{
    /* byte swap 32 bit register */
    START_OF_INSTR();
    DECODE_PRINTF("BSWAP\t");
    switch (op2) {
    case 0xc8:
        DECODE_PRINTF("EAX\n");
        M.x86.R_EAX = x86emu_bswap(M.x86.R_EAX);
        break;
    case 0xc9:
        DECODE_PRINTF("ECX\n");
        M.x86.R_ECX = x86emu_bswap(M.x86.R_ECX);
        break;
    case 0xca:
        DECODE_PRINTF("EDX\n");
        M.x86.R_EDX = x86emu_bswap(M.x86.R_EDX);
        break;
    case 0xcb:
        DECODE_PRINTF("EBX\n");
        M.x86.R_EBX = x86emu_bswap(M.x86.R_EBX);
        break;
    case 0xcc:
        DECODE_PRINTF("ESP\n");
        M.x86.R_ESP = x86emu_bswap(M.x86.R_ESP);
        break;
    case 0xcd:
        DECODE_PRINTF("EBP\n");
        M.x86.R_EBP = x86emu_bswap(M.x86.R_EBP);
        break;
    case 0xce:
        DECODE_PRINTF("ESI\n");
        M.x86.R_ESI = x86emu_bswap(M.x86.R_ESI);
        break;
    case 0xcf:
        DECODE_PRINTF("EDI\n");
        M.x86.R EDI = x86emu_bswap(M.x86.R EDI);
        break;
    }
@@ -1702,14 +1763,14 @@ /*  0xc5 */ x86emuOp2_illegal_op,
/*  0xc6 */ x86emuOp2_illegal_op,
/*  0xc7 */ x86emuOp2_illegal_op,
-/*  0xc8 */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xc9 */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xca */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xcb */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xcc */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xcd */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xce */ x86emuOp2_illegal_op, /* TODO: bswap */
-/*  0xcf */ x86emuOp2_illegal_op, /* TODO: bswap */
+/*  0xc8 */ x86emuOp2_bswap,
+/*  0xc9 */ x86emuOp2_bswap,
+/*  0xca */ x86emuOp2_bswap,
+/*  0xcb */ x86emuOp2_bswap,
+/*  0xcc */ x86emuOp2_bswap,
+/*  0xcd */ x86emuOp2_bswap,
+/*  0xce */ x86emuOp2_bswap,
+/*  0xcf */ x86emuOp2_bswap,
/*  0xd0 */ x86emuOp2_illegal_op,
/*  0xd1 */ x86emuOp2_illegal_op,
Index: ops.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops.c,v
retrieving revision 1.1
diff -u -u -r1.1 ops.c
--- ops.c 7 Sep 2007 10:01:21 -0000 1.1
+++ ops.c 20 Mar 2008 16:52:00 -0000
@@ -1061,7 +1061,11 @@
 imm = (s8)fetch_byte_imm();
 DECODE_PRINTF2("PUSH\t%d\n", imm);
 TRACE_AND_STEP();
- push_word(imm);
+ if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+ push_long(imm);
+ } else {
+ push_word(imm);
+ }
 DECODE_CLEAR_SEGOVR();
 END_OF_INSTR();
}
@@ -1256,8 +1260,10 @@
 target = (u16)(M.x86.R_IP + (s16)offset);
 DECODE_PRINTF2("%x\n", target);
 TRACE_AND_STEP();

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- if (cond)
  + if (cond) {
      M.x86.R_IP = target;
      JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " NEAR COND ");
    +}
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -2516,9 +2522,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
-      /* move them until CX is ZERO. */
-      count = M.x86.R_CX;
+      /* move them until (E)CX is ZERO. */
+      count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
      M.x86.R_CX = 0;
+      if (M.x86.mode & SYSMODE_32BIT_REP)
+        M.x86.R_ECX = 0;
      M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
  while (count--) {
@@ -2526,6 +2534,8 @@
    store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, val);
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
+    if (M.x86.intr & INTR_HALTED)
+      break;
    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -2559,9 +2569,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
-      /* move them until CX is ZERO. */
-      count = M.x86.R_CX;
+      /* move them until (E)CX is ZERO. */
+      count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
      M.x86.R_CX = 0;
+      if (M.x86.mode & SYSMODE_32BIT_REP)
+        M.x86.R_ECX = 0;
      M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
  while (count--) {
@@ -2574,6 +2586,8 @@
    M.x86.R_SI += inc;
M.x86.R_DI += inc;
+ if (M.x86.intr & INTR_HALTED)
+ break;
}
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2598,16 +2612,21 @@
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
  /* REPE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    val1 = fetch_data_byte(M.x86.R_SI);
    val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
    cmp_byte(val1, val2);
- M.x86.R_CX -= 1;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+ else
+                M.x86.R_CX -= 1;
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
    if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && (ACCESS_FLAG(F_ZF) == 0) ) break;
    if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
+ if (M.x86.intr & INTR_HALTED)
+ break;
  }
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
@@ -2644,8 +2663,8 @@
TRACE_AND_STEP();
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
  /* REPE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    if ( (M.x86.mode & SYSMODE_PREFIX_REPE_DATA) {
        val1 = fetch_data_long(M.x86.R_SI);
        val2 = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
@@ -2655,11 +2674,16 @@
        cmp_word((u16)val1, (u16)val2);
    }
- M.x86.R_CX -= 1;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
M.x86.R_ECX -= 1;
else
M.x86.R_CX -= 1;
M.x86.R_SI += inc;
M.x86.R_DI += inc;
if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && ACCESS_FLAG(F_ZF) == 0 ) break;
if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;

if (M.x86.intr & INTR_HALTED)
break;

M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
}
else {
TRACE_AND_STEP();
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
/* dont care whether REPE or REPNE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AL);
- M.x86.R_CX -= 1;
+ M.x86.R_ECX -= 1;
+ else
+ M.x86.R_CX -= 1;
M.x86.R_DI += inc;
+ if (M.x86.intr & INTR_HALTED)
+ break;
}
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
@@ -2741,11 +2765,16 @@
TRACE_AND_STEP();
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
/* dont care whether REPE or REPNE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
M.x86.R_CX = 0;
+if (M.x86.mode & SYSMODE_32BIT_REP)
+ M.x86.R_ECX = 0;
+ M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} while (count--) {
@@ -2783,9 +2812,11 @@
count = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
/* dont care whether REPE or REPNE */
- /* move them until CX is ZERO. */
- count = M.x86.R_CX;
+ /* move them until (E)CX is ZERO. */
+ count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
M.x86.R_CX = 0;
+if (M.x86.mode & SYSMODE_32BIT_REP)
+ M.x86.R_ECX = 0;
+ M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} while (count--) {
@@ -2795,6 +2826,8 @@
store_data_word_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AX);
M.x86.R_DI += inc;
  if (M.x86.intr & INTR_HALTED)
    break;
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
@end -2817,11 +2850,16 @-
  inc = 1;
  if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    /* move them until CX is ZERO. */
    while (M.x86.R_CX != 0) {
      /* move them until (E)CX is ZERO. */
      while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        M.x86.R_AL = fetch_data_byte(M.x86.R_SI);
        if (M.x86.mode & SYSMODE_32BIT_REP)
          M.x86.R_ECX -= 1;
        else
          M.x86.R_CX -= 1;
        M.x86.R_SI += inc;
        if (M.x86.intr & INTR_HALTED)
          break;
      }
      M.x86.R_CX = 0;
    }
  else {
@end -2859,9 +2897,11 @-
  count = 1;
  if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    /* move them until CX is ZERO. */
    count = M.x86.R_CX;
    /* move them until (E)CX is ZERO. */
    count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
    M.x86.R_CX = 0;
    if (M.x86.mode & SYSMODE_32BIT_REP)
      M.x86.R_ECX = 0;
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
  }
  while (count--) {
@end -2871,6 +2911,8 @-
    M.x86.R_AD = fetch_data_word(M.x86.R_SI);
  }
  M.x86.R_SI += inc;
  if (M.x86.intr & INTR_HALTED)
    break;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2894,26 +2936,36 @@
inc = 1;
if (M.x86.mode & SYSMODE_PREFIX_REPE) {
    /* REPE */
    - /* move them until CX is ZERO. */
    - while (M.x86.R_CX != 0) {
        + /* move them until (E)CX is ZERO. */
        + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_CX : M.x86.R_CX) != 0) {
            val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
            cmp_byte(M.x86.R_AL, val2);
            - M.x86.R_CX -= 1;
            + if (M.x86.mode & SYSMODE_32BIT_REP)
                - M.x86.R_CX -= 1;
                + if (M.x86.mode & SYSMODE_32BIT_REP)
                + M.x86.R_ECX -= 1;
                else
                M.x86.R_DX -= 1;
                M.x86.R_DI += inc;
                if (ACCESS_FLAG(F_ZF) == 0)
                    break;
                + if (M.x86.intr & INTR_HALTED)
                    break;
            }
            M.x86.mode &= ~SYSMODE_PREFIX_REPE;
        } else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
            /* REPNE */
            - /* move them until CX is ZERO. */
            - while (M.x86.R_CX != 0) {
                + /* move them until (E)CX is ZERO. */
                + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
                    val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
                    cmp_byte(M.x86.R_AL, val2);
                    - M.x86.R_CX -= 1;
                    + if (M.x86.mode & SYSMODE_32BIT_REP)
                        - M.x86.R_CX -= 1;
                        + if (M.x86.mode & SYSMODE_32BIT_REP)
                        + M.x86.R_ECX -= 1;
                        else
                        M.x86.R_DX -= 1;
                        if (ACCESS_FLAG(F_ZF))
                            break; /* zero flag set means equal */
                        + if (M.x86.intr & INTR_HALTED)
                            break;
                    }
                    M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
                } else {
                    @@ -2951,8 +3003,8 @@
                    TRACE_AND_STEP();
                    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
/* REPE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_long(M.x86.R_EAX, val);
    }
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_word(M.x86.R_AX, (u16)val);
    }
    M.x86.R_CX -= 1;
+            if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+            else
+                M.x86.R_CX -= 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF) == 0)
        break;
+            if (M.x86.intr & INTR_HALTED)
+                break;
    }
} else if (M.x86.mode & SYSMODE_PREFIX_REPE) {
/* REPNE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_long(M.x86.R_EAX, val);
    }
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_word_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_word(M.x86.R_AX, (u16)val);
    }
    M.x86.R_CX -= 1;
+            if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+            else
+                M.x86.R_CX -= 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF))
        break;          /* zero flag set means equal */
+            if (M.x86.intr & INTR_HALTED)
+                break;
    }
    M.x86.mode &= ~SYSMODE_PREFIX_REPE;
} else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
/* REPNE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_long(M.x86.R_EAX, val);
    }
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_word_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_word(M.x86.R_AX, (u16)val);
    }
    M.x86.R_CX -= 1;
+            if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+            else
+                M.x86.R_CX -= 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF))
        break;          /* zero flag set means equal */
+            if (M.x86.intr & INTR_HALTED)
+                break;
    }
}
M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
} else {
@@ -3238,9 +3300,9 @@
    DECODE_PRINTF("RETn");
    imm = fetch_word_imm();
    DECODE_PRINTF2("%x\n", imm);
-RETURN_TRACE("RET",M.x86.saved_cs,M.x86.saved_ip);
    TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
+RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
    M.x86.R_SP += imm;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -3254,9 +3316,9 @@
{
    START_OF_INSTR();
    DECODE_PRINTF("RETn");
-RETURN_TRACE("RET",M.x86.saved_cs,M.x86.saved_ip);
    TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
+RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -3471,10 +3533,10 @@
DECODE_PRINTF("RETF\n");
    imm = fetch_word_imm();
    DECODE_PRINTF2("%x\n", imm);
-RETURN_TRACE("RETF",M.x86.saved_cs,M.x86.saved_ip);
    TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
    M.x86.R_CS = pop_word();
+RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
    M.x86.R_SP += imm;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -3488,10 +3550,10 @@
{
    START_OF_INSTR();
    DECODE_PRINTF("RETFn");
-RETURN_TRACE("RETF",M.x86.saved_cs,M.x86.saved_ip);
    TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
    M.x86.R_CS = pop_word();
+RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -4020,8 +4082,11 @@
ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
    - M.x86.R_CX -= 1;
    - if (M.x86.R_CX != 0 && !ACCESS_FLAG(F_ZF)) /* CX != 0 and !ZF */
+    if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        M.x86.R_ECX -= 1;
+    else
+        M.x86.R_CX -= 1;
+    if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
+         !ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and !ZF */
        M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4041,8 +4106,11 @@
ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
    - M.x86.R_CX -= 1;
    - if (M.x86.R_CX != 0 && ACCESS_FLAG(F_ZF)) /* CX != 0 and ZF */
+    if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        M.x86.R_ECX -= 1;
+    else
+        M.x86.R_CX -= 1;
+    if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
+         ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and ZF */
        M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4062,8 +4130,11 @@
ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
    - M.x86.R_CX -= 1;
    - if (M.x86.R_CX != 0)
+    if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        M.x86.R_ECX -= 1;
+    else
+        M.x86.R_CX -= 1;
+    if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0) /* (E)CX != 0 */
        M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4085,8 +4156,10 @@
target = (u16)(M.x86.R_IP + offset);
    DECODE_PRINTF2("%x\n", target);
    TRACE_AND_STEP();
if (M.x86.R_CX == 0) {
    M.x86.R_IP = target;
    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " CXZ ");
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}

ip = (s16)fetch_word_imm();
ip += (s16)M.x86.R_IP;
DECODE_PRINTF2("%04x\n", ip);
JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, ip, " NEAR ");
TRACE_AND_STEP();
M.x86.R_IP = (u16)ip;
DECODE_CLEAR_SEGOVR();

cs = fetch_word_imm();
DECODE_PRINTF2("%04x: ", cs);
DECODE_PRINTF2("%04x\n", cs);
JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, cs, ip, " FAR ");
TRACE_AND_STEP();
M.x86.R_IP = ip;
M.x86.R_CS = cs;

offset = (s8)fetch_byte_imm();
target = (u16)(M.x86.R_IP + offset);
DECODE_PRINTF2("%x\n", target);
JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, target, " BYTE ");
TRACE_AND_STEP();
M.x86.R_IP = target;
DECODE_CLEAR_SEGOVR();

DECODE_PRINTF("REPNE\n");
TRACE_AND_STEP();
M.x86.mode |= SYSMODE_PREFIX_REPNE;
if (M.x86.mode & SYSMODE_PREFIX_ADDR)
    M.x86.mode |= SYSMODE_32BIT_REP;
DECODE_CLEAR_SEGOVR();

DECODE_PRINTF("REPE\n");
TRACE_AND_STEP();
M.x86.mode |= SYSMODE_PREFIX_REPE;
if (M.x86.mode & SYSMODE_PREFIX_ADDR)
    M.x86.mode |= SYSMODE_32BIT_REPE;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
}
@@ -5013,12 +5093,14 @@
  break;
  case 4: /* jmp word ptr */
    destval = fetch_data_word(destoffset);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, destval, " WORD ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
  case 5: /* jmp far ptr */
    destval = fetch_data_word(destoffset);
    destval2 = fetch_data_word(destoffset + 2);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, destval2, destval, " FAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    M.x86.R_CS = destval2;
Index: prim_ops.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/prim_ops.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- prim_ops.c 7 Sep 2007 10:01:21 -0000 1.1
+++ prim_ops.c 16 Jan 2008 14:18:15 -0000 1.3
@@ -1921,7 +1921,7 @@
 void imul_long_direct(u32 *res_lo, u32* res_hi,u32 d, u32 s)
 {
 #ifdef __HAS_LONG_LONG__
-    s64 res = (s64)d * (s64)s;
+    s64 res = (s64)(s32)d * (s64)(s32)s;
 *
-*res_lo = (u32)res;
-*res_hi = (u32)(res >> 32);
@@ -2013,7 +2013,7 @@
 void mul_long(u32 s)
 {
 #ifdef __HAS_LONG_LONG__
-    u64 res = (u32)M.x86.R_EAX * (u32)s;
+    u64 res = (u64)M.x86.R_EAX * s;
 *
  M.x86.R_EAX = (u32)res;
  M.x86.R_EDX = (u32)(res >> 32);
@@ -2312,16 +2312,15 @@
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* in until CX is ZERO. */
+    /* in until CX is ZERO. */
while (count--) {
    single_in(size);
    M.x86.R_DI += inc;
}
M.x86.R_CX = 0;
if (M.x86.mode & SYSMODE_PREFIX_DATA) {
    M.x86.R_ECX = 0;
}
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    if (M.x86.mode & SYSMODE_32BIT_REP) {
        u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
                      M.x86.R_ECX : M.x86.R_CX);
        while (count--) {
            single_out(size);
            M.x86.R_SI += inc;
        }
        M.x86.R_CX = 0;
    }
}
-#else
-#include <sys/io.h>
-#endif

#ifdef IN_MODULE
#include "xf86_ansic.h"
@@ -220,7 +215,7 @@
{
  DB(if (DEBUG_IO_TRACE())
  printk("inb %#04x \n", addr);
-  return inb(addr);
+  return 0;
}

/****************************************************************************
@@ -235,7 +230,7 @@
{
  DB(if (DEBUG_IO_TRACE())
  printk("inw %#04x \n", addr);
-  return inw(addr);
+  return 0;
}

/****************************************************************************
@@ -250,7 +245,7 @@
{
  DB(if (DEBUG_IO_TRACE())
  printk("inl %#04x \n", addr);
-  return inl(addr);
+  return 0;
}

/****************************************************************************
@@ -264,7 +259,6 @@
{
  DB(if (DEBUG_IO_TRACE())
  printk("outb %02x -> %#04x \n", val, addr);
-  outb(val, addr);
  return;
}

@@ -279,7 +273,6 @@
{
  DB(if (DEBUG_IO_TRACE())
  printk("outw %04x -> %#04x \n", val, addr);
-  outw(val, addr);
  return;
}
DB(if (DEBUG_IO_TRACE())
    printk("outl %#08x -> %#04x \n", val, addr);
    outl(val, addr);
    return;
}

void X86EMU_setMemBase(void *base, size_t size)
{
    M.mem_base = (int) base;
    M.mem_base = (unsigned long) base;
    M.mem_size = size;
}

Index: include/x86emu/debug.h
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/debug.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/debug.h	7 Sep 2007 10:01:21 -0000	1.1
+++ include/x86emu/debug.h	20 Mar 2008 15:25:27 -0000	1.4
@@ -40,8 +40,6 @@
    #define DEBUG_SYSINT()        	(M.x86.debug & DEBUG_SYSINT_F)
    #define DEBUG_TRACECALL()     	(M.x86.debug & DEBUG_TRACECALL_F)
    #define DEBUG_TRACECALLREGS() 	(M.x86.debug & DEBUG_TRACECALL_REGS_F)
+  #define DEBUG_TRACEJMP()       (M.x86.debug & DEBUG_TRACEJMP_F)
+  #define DEBUG_TRACEJMPREGS()   (M.x86.debug & DEBUG_TRACEJMP_REGS_F)
    #define DEBUG_SYS()           	0
    #define DEBUG_MEM_TRACE()     	0
    #define DEBUG_IO_TRACE()      	0
@@ -96,6 +96,8 @@
    #define DEBUG_SYSINT()       0
    #define DEBUG_TRACECALL()    0
    #define DEBUG_TRACECALLREGS() 0
    +#define DEBUG_TRACEJMP()    0
    +#define DEBUG_TRACEJMPREGS() 0
    */-------- Macros and type definitions ---------------*/

    /* checks to be enabled for "runtime" */
    @ @ -78,6 +76,8 @@
    # define DEBUG_SYSINT() (M.x86.debug & DEBUG_SYSINT_F)
    # define DEBUG_TRACECALL() (M.x86.debug & DEBUG_TRACECALL_F)
    # define DEBUG_TRACECALLREGS() (M.x86.debug & DEBUG_TRACECALL_REGS_F)
    +# define DEBUG_TRACEJMP() (M.x86.debug & DEBUG_TRACEJMP_F)
    +# define DEBUG_TRACEJMPREGS() (M.x86.debug & DEBUG_TRACEJMP_REGS_F)
    # define DEBUG_SYS() (M.x86.debug & DEBUG_SYS_F)
    # define DEBUG_MEM_TRACE() (M.x86.debug & DEBUG_MEM_TRACE_F)
    # define DEBUG_IO_TRACE() (M.x86.debug & DEBUG_IO_TRACE_F)
    @ @ -96,6 +96,8 @@
    # define DEBUG_SYSINT() 0
    # define DEBUG_TRACECALL() 0
    # define DEBUG_TRACECALLREGS() 0
    +# define DEBUG_TRACEJMP() 0

+## define DEBUG_TRACEJMPREGS() 0
+## define DEBUG_SYS() 0
+## define DEBUG_MEM_TRACE() 0
+## define DEBUG_IO_TRACE() 0
+ @@ -169,14 +171,20 @@
+    x86emu_dump_regs();                           \ 
+    if (DEBUG_TRACECALL())                        \ 
+        printk("%04x:%04x: CALL %s%04x:%04x\n", u , v, s, w, x);
+    ## define RETURN_TRACE(u,v,w,x,s)              \ 
+    if (DEBUG_TRACECALLREGS())
+        x86emu_dump_regs();                        \ 
+        if (DEBUG_TRACECALL())                     \ 
+            printk("%04x:%04x: %s
",u,v,n);        
+        printk("%04x:%04x: RET %s %04x:%04x\n",u,v,s,w,x);
+    ## define JMP_TRACE(u,v,w,x,s)                 
+    + if (DEBUG_TRACEJMPREGS())
+        x86emu_dump_regs();                      \ 
+        if (DEBUG_TRACEJMP())                    
+            printk("%04x:%04x: JMP %s%04x:%04x\n", u , v, s, w, x);
+    #else
+    # define CALL_TRACE(u,v,w,x,s)
+    -define RETURN_TRACE(n,u,v)
+    +# define RETURN_TRACE(u,v,w,x,s)
+    #+# define JMP_TRACE(u,v,w,x,s)
+    #endif
+
+##ifdef DEBUG
+Index: include/x86emu/regs.h
+===================================================================
+RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/regs.h,v
+retrieving revision 1.1
+retrieving revision 1.4
+diff -u -u -r1.1 -r1.4
+--- include/x86emu/regs.h 7 Sep 2007 10:01:21 -0000 1.1
+++ include/x86emu/regs.h 15 Jan 2008 13:46:40 -0000 1.4
@@ -231,6 +231,9 @@
+    #define SYSMODE_PREFFIX_REPNE    0x00000100
+    #define SYSMODE_PREFFIX_DATA     0x00000200
+    #define SYSMODE_PREFFIX_ADDR     0x00000400
+    #+# phueper: for REP(E|NE) Instructions, we need to decide wether it should be using
+    #+#the 32bit ECX register as or the 16bit CX register as count register
+    +#define SYSMODE_32BIT_REP       0x00000800
+    #define SYSMODE_INTR_PENDING    0x10000000
+    #define SYSMODE_EXTRN_INTR      0x20000000
+    #define SYSMODE_HALTED          0x40000000
+    @ @ -231.6 +231.9 @ @
+    #define SYSMODE_SEGOVR_GS      | 

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SYSMODE_SEGOVR_SS  |\nSYSMODE_PREFIX_DATA |\n- SYSMODE_PREFIX_ADDR)\n+ SYSMODE_PREFIX_ADDR  |\n+ SYSMODE_32BIT_REP)

#define INTR_SYNCH     0x1
#define INTR_ASYNCH    0x2
@@ -274,9 +278,9 @@
*/
    u32              mode;
    volatile int     intr; /* mask of pending interrupts */
-int              debug;
+    volatile int    debug;
#endif DEBUG
@@ -189,6 +181,8 @@
#define DEBUG_TRACEJMP_REGS_F  0x020000
#define DEBUG_TRACEJMP_REGS_F  0x040000
#define DEBUG_SYS_F          (DEBUG_SVC_F|DEBUG_FS_F|DEBUG_PROC_F)
void X86EMU_trace_regs(void);
@@ -200,5 +194,4 @@
#ifdef __cplusplus
}                       /* End of "C" linkage for C++ */
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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{ TARGET_FREEBSD_NR___acl_aclcheck_fd, "__acl_aclcheck_fd", "%s(%d, %d, %#x)", NULL, NULL },
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{ TARGET_FREEBSD_NR___acl_aclcheck_link, "__acl_aclcheck_link", "%s("%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_fd, "__acl_delete_fd", "%s(%d, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_file, "__acl_delete_file", "%s("%s", %d)", NULL, NULL },
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{ TARGET_FREEBSD_NR___acl_get_link, "__acl_get_link", "%s("%s", %d, %#x)", NULL, NULL },
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{ TARGET_FREEBSD_NR___acl_set_file, "__acl_set_file", "%s("%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_set_link, "__acl_set_link", "%s("%s", %d, %#x)", NULL, NULL },
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{ TARGET_FREEBSD_NR_chmod, "chmod", "%s("%s\",%#o")", NULL, NULL },
{ TARGET_FREEBSD_NR_chown, "chown", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chroot, "chroot", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_getres, "clock_getres", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_gettime, "clock_gettime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_close, "close", "%s(%d)\n", NULL, NULL },
{ TARGET_FREEBSD_NR_connect, "connect", "%s(%d,%#x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_dup, "dup", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_dup2, "dup2", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_eaccess, "eaccess", "%s("%s\",%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_execve, "execve", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_exit, "exit", "%s(%d)\n", NULL, NULL },
{ TARGET_FREEBSD_NR_extattrctl, "extattrctl", "%s("%s\", %d, "%s\", %d, "%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_delete_fd, "extattr_delete_fd", "%s(%d,%d,\"\")", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_delete_file, "extattr_delete_file", "%s("%s\", %d, %d, "%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_delete_link, "extattr_delete_link", "%s("%s\", %d, %d, "%s\")", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_get_fd, "extattr_get_fd", "%s(%d,%d,\"\",%#x,%d)\", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_get_file, "extattr_get_file", "%s("%s\", %d, %d, %d, %d, %d)\", NULL, NULL },
{ TARGET_FREEBSD_NR_extattr_get_file, "extattr_get_file", "%s("%s\", %d, %d, %d, %d)\", NULL, NULL },
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   History
   -------

   Before (and including) argparse 1.1, the argparse package was licensed under
   Apache License v2.0.

   After argparse 1.1, all project files from the argparse project were deleted
   due to license compatibility issues between Apache License 2.0 and GNU GPL v2.

   The project repository then had a clean start with some files taken from
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David Gibson <david@gibson.dropbear.id.au>
(principal original author of dtc and libfdt)
2 November 2007

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1.26 libjpeg 1.2.90 8.el7

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DOCUMENTATION ROADMAP
=====================
This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:
install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran, rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.

Programmer and internal documentation:
libjpeg.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.
filelist.doc Road map of IJG files.
coderules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW
========

This package contains C software to implement JPEG image compression and decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images. JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.
In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included "jpegtran", a utility for lossless transcoding between different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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The Unix configuration script "configure" was produced with GNU Autoconf. It is copyright by the Free Software Foundation but is freely distributable. The same holds for its supporting scripts (config.guess, config.sub, ltconfig, ltmain.sh). Another support script, install-sh, is copyright by M.I.T. but is also freely distributable.

It appears that the arithmetic coding option of the JPEG spec is covered by patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot legally be used without obtaining one or more licenses. For this reason, support for arithmetic coding has been removed from the free JPEG software. (Since arithmetic coding provides only a marginal gain over the unpatented Huffman mode, it is unlikely that very many implementations will support it.) So far as we are aware, there are no patent restrictions on the remaining code.

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The IJG distribution formerly included code to read and write GIF files. To avoid entanglement with the Unisys LZW patent, GIF reading support has been removed altogether, and the GIF writer has been simplified to produce “uncompressed GIFs”. This technique does not use the LZW algorithm; the resulting GIF files are larger than usual, but are readable by all standard GIF decoders.

We are required to state that
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REFERENCES
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We highly recommend reading one or more of these references before trying to understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is Wallace, Gregory K. "The JPEG Still Picture Compression Standard", Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44. (Adjacent articles in that issue discuss MPEG motion picture compression, applications of JPEG, and related topics.) If you don't have the CACM issue handy, a PostScript file containing a revised version of Wallace's article is available at ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz. The file (actually a preprint for an article that appeared in IEEE Trans. Consumer Electronics) omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...


The JPEG standard itself is not available electronically; you must order a
paper copy through ISO or ITU. (Unless you feel a need to own a certified official copy, we recommend buying the Pennebaker and Mitchell book instead; it's much cheaper and includes a great deal of useful explanatory material.) In the USA, copies of the standard may be ordered from ANSI Sales at (212) 642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI doesn't take credit card orders, but Global does.) It's not cheap: as of 1992, ANSI was charging $95 for Part 1 and $47 for Part 2, plus 7% shipping/handling. The standard is divided into two parts, Part 1 being the actual specification, while Part 2 covers compliance testing methods. Part 1 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 1: Requirements and guidelines" and has document numbers ISO/IEC IS 10918-1, ITU-T T.81. Part 2 is titled "Digital Compression and Coding of Continuous-tone Still Images, Part 2: Compliance testing" and has document numbers ISO/IEC IS 10918-2, ITU-T T.83.

Some extensions to the original JPEG standard are defined in JPEG Part 3, a newer ISO standard numbered ISO/IEC IS 10918-3 and ITU-T T.84. IJG currently does not support any Part 3 extensions.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department
C-Cube Microsystems, Inc.
1778 McCarthy Blvd.
Milpitas, CA 95035
phone (408) 944-6300, fax (408) 944-6314

A PostScript version of this document is available by FTP at ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz. There is also a plain text version at ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from ftp.sgi.com or from ftp://ftp.uu.net/graphics/jpeg/. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from ftp://ftp.sgi.com/graphics/tiff/.

ARCHIVE LOCATIONS

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The "official" archive site for this software is ftp.uu.net (Internet address 192.48.96.9). The most recent released version can always be found there in directory graphics/jpeg. This particular version will be archived as ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.

Numerous Internet sites maintain copies of the UUNET files. However, only ftp.uu.net is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the ftp.uu.net release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups comp.graphics.misc, news.answers, and other groups. It is available on the World Wide Web at http://www.faqs.org/faqs/jpeg-faq/ and other news.answers archive sites, including the official news.answers archive at rtfm.mit.edu: ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/. If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body

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RELATED SOFTWARE
==============

Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBMPLUS software, which provides many useful operations on PPM-format image files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest version is distributed by the NetPBM group, and is available from numerous sites, notably ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/. Unfortunately PBMPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from ftp://havefun.stanford.edu/pub/jpeg/. This program
is designed for research and experimentation rather than production use; it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS
===============
Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read. (For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO
=====

The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving
quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.

Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.27 libxfont 2.0.3 1.el7

1.27.1 Available under license:

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/* lib/font/fontfile/gunzip.c
   written by Mark Eichin <eichin@kitten.gen.ma.us> September 1996.
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1.36 giflib 4.1.6 9.el7

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1.37 libconfig 1.4.9 5.el7

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1.52 open-ldap 2.4.44 21.el7_6

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a purpose that is entirely well-defined independent of the
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Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
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Most GNU software, including some libraries, is covered by the ordinary
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The reason we have a separate public license for some libraries is that
they blur the distinction we usually make between modifying or adding to a
program and simply using it. Linking a program with a library, without
changing the library, is in some sense simply using the library, and is
analogous to running a utility program or application program. However, in
a textual and legal sense, the linked executable is a combined work, a
derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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Appendix: How to Apply These Terms to Your New Libraries

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1.58 systemtap 0.16.0-3.el7
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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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1.68 man-db 2.6.3-11.el7

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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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Although the Lesser General Public License is less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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.. parsed-literal::

   lib/gssapi/generic/gssapi_err_generic.et
   lib/gssapi/mechglue/g_accept_sec_context.c
   lib/gssapi/mechglue/g_acquire_cred.c
   lib/gssapi/mechglue/g_canon_name.c
   lib/gssapi/mechglue/g_compare_name.c
   lib/gssapi/mechglue/g_context_time.c
   lib/gssapi/mechglue/g_delete_sec_context.c
   lib/gssapi/mechglue/g_dsp_name.c
   lib/gssapi/mechglue/g_dsp_status.c
   lib/gssapi/mechglue/g_dup_name.c
   lib/gssapi/mechglue/g_exp_sec_context.c
   lib/gssapi/mechglue/g_export_name.c
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```plaintext
include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/prop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c
kprop/kproplog.c
```

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class="n">g_rel_buffer</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_cred</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_name</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_oid_set</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_buffer</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_cred</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_name</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_oid_set</span><span class="o">.</span><span class="n">c</span>  
<span class="n">lib</span><span class="o">/</span><span class="n">gssapi</span><span class="o">/</span><span class="n">mechglue</span><span class="o">/</span><span class="n">g_rel_buffer</span><span class="o">.</span><span class="n">c" /&gt; 

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lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
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lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c
and the initial implementation of incremental propagation, including the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c
kprop/kproplog.c

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/* -*.- mode: c; c-basic-offset: 4; indent-tabs-mode: nil -*.- */
/*

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*/
.. _mitK5license:

MIT Kerberos License information
================================

.. toctree::
   :hidden:
   :hidden:

   copyright.rst

.. include::  notice.rst

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# This program is intended to be used by "make check-copyright". It
# checks for violations of the coding standards related to copyright
# and license statements in source code comments.

import os
import sys
import re

def warn(fname, ln, msg):
    print '%s: %d: %s' % (fname, ln + 1, msg)

def indicates_license(line):
    return 'Copyright' in line or 'COPYRIGHT' in line or 'License' in line

# Check a comment for boilerplate violations. Return true if the comment
# is a license statement.
def check_comment(comment, fname, ln, code_seen, nonlicense_seen):
    text_seen = False
    is_license = False
    for line in comment:
        if not is_license and indicates_license(line):
            is_license = True
        if text_seen:
            warn(fname, ln, 'License begins after first line of comment')
        elif code_seen:
            warn(fname, ln, 'License after code')
        elif nonlicense_seen:
            warn(fname, ln, 'License after non-license comments')
        break
    # DB2 licenses start with '/*-:' and we don't want to change them.
    if line != '' and line != '-*:'
        text_seen = True
    return is_license

def check_file(lines, fname):
    # Skip emacs mode line if present.
    ln = 0
    if '-*- mode: c;' in lines[ln]:
        ln += 1

# Check filename comment if present.
m = re.match(r'/\* (.*)? */', lines[ln])
if m:
    if m.group(1) != fname:
        warn(fname, ln, 'Wrong filename in comment')
    ln += 1

# Scan for license statements.
in_comment = False
code_seen = False
nonlicense_seen = False
for line in lines[ln:]:
    # Strip out whitespace and comments contained within a line.
    if not in_comment:
        line = re.sub(r'/\*.*?\*/', '', line)
        line = line.strip()
    if not in_comment and '/*' in line:
        (line, sep, comment_part) = line.partition('/*')
        comment = [comment_part.strip()]
        comment_starts_at = ln
        in_comment = True
        elif in_comment and '*/' not in line:
            comment.append(line.lstrip('*').lstrip())
        elif in_comment:
            (comment_part, sep, line) = line.partition('*/')
            comment.append(comment_part.strip())
    is_license = check_comment(comment, fname, comment_starts_at, code_seen, nonlicense_seen)
    nonlicense_seen = nonlicense_seen or not is_license
    in_comment = False
    if line.strip() != '':
        code_seen = True
        ln += 1

for fname in sys.argv[1:]:
    if fname.startswith('./'):
        fname = fname[2:]
    f = open(fname)
    lines = f.readlines()
    f.close()
    check_file(lines, fname)
1.72 iputils 20160308 10.el7

1.72.1 Available under license:

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1.73 linux-kernel 3.10.87

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/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)
* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
*
* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
* http://www.hypermall.com/
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
* interrupts us (except possibly for removal/insertion of the cable?)
* 10/4/97 - began heavy inline documentation of the code. Corrected typos
* and spelling mistakes.
* 10/5/97 - added code to handle PHY interrupts, disable PHY on
* loss of link, and correctly re-enable PHY when link is
* re-established. (put back CFG_PHYIE)
*
* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
*
* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
*
* Linux driver for the IDT77201 NICStAR PCI ATM controller.
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
* see init_nicstar() for PHY initialization to change this. This driver
* expects the Linux ATM stack to support scatter-gather lists
* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
*
* Implementing minimal-copy of received data:
* IDT always receives data into a small buffer, then large buffers
* as needed. This means that data must always be copied to create
* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
* Fix is simple: make large buffers large enough to hold entire
* SDU, and leave <small_buffer_data> bytes empty at the start. Then
* copy small buffer contents to head of large buffer.
* Trick is to avoid fragmenting Linux, due to need for a lot of large
* buffers. This is done by 2 things:
* 1) skb->destructor / skb->atm.recycle_buffer
*       combined, allow nicstar_free_rx_skb to be called to
*       recycle large data buffers
* 2) skb_clone of received buffers
* See nicstar_free_rx_skb and linearize_buffer for implementation
* details.
*
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*
* M. Welsh, 6 July 1996
*
*
*/
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Once this change is made in a given copy, it is irreversible for
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subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the
Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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1.77 jansson 2.10 1.el7
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[This is the first released version of the library GPL. It is
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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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"Source code" for a work means the preferred form of the work for
making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.
If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software
patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1
above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

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c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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this, you must alter all the notices that refer to this License, so
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Once this change is made in a given copy, it is irreversible for
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This option is useful when you wish to copy part of the code of
the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.
For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

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Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

TERMS AND CONDITIONS

0. Definitions.

"This License" refers to version 3 of the GNU General Public License.

"Copyright" also means copyright-like laws that apply to other kinds of works, such as semiconductor masks.

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The "System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

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such as by intimate data communication or control flow between those subprograms and other parts of the work.

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d) If the work has interactive user interfaces, each must display Appropriate Legal Notices; however, if the Program has interactive interfaces that do not display Appropriate Legal Notices, your work need not make them do so.

A compilation of a covered work with other separate and independent works, which are not by their nature extensions of the covered work, and which are not combined with it such as to form a larger program, in or on a volume of a storage or distribution medium, is called an "aggregate" if the compilation and its resulting copyright are not
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a) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.

b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

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- Mark Andrews &lt;mark_andrews@isc.org&gt; Leitch atomic clock controller
- Bernd Altmeier &lt;altmeier@atlssoft.de&gt; hopf Elektronik serial line and PCI-bus devices
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- Michael Barone &lt;michael.barone@lmco.com&gt; GPSVME fixes
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- Greg Brackley &lt;greg.brackley@bigfoot.com&gt; Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
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- Torsten Duwe &lt;duwe@immd4.informatik.uni-erlangen.de&gt; Linux port
- Dennis Ferguson &lt;dennis@mrbill.canet.ca&gt; foundation code for NTP Version 2 as specified in RFC-1119
IPv6 support and testing

General maintenance, Windows port interpolation rewrite

NeoClock4X clock driver

GOES clock driver

DEC Alpha port

A/UX port

massive prototyping overhaul

ntpsweep

Oncore driver (Original author)

>14 reference clocks, STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling

RS/6000 AIX modifications, HPUX modifications

RS/6000 AIX port

4.4BSD port, ppsclock, Magnavox GPS clock driver

George Lindholm

SunOS 5.1 port

MD5-based authentication

adaptation of foundation code for Version 3 as specified in RFC-1305

Network I/O, Windows Port, Code Maintenance


Wolfgang Moeller

VMS port

ntprtrace utility

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3. [3] Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
5. [6] Michael Barone <michael.barone@lmco.com> GPSVME fixes
6. [7] Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8] Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
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45. [49] Wilfredo Sanchez <wsanchez@apple.com> added support for NetInfo
46. [50] Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
47. [51] Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
48. [52] Ray Schnitzler <schnitz@unipress.com> Unixware1 port
49. [53] Michael Shields <shields@tembel.org> USNO clock driver
50. [54] Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
51. [55] Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
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former contains code derived from the library, whereas the latter must
be combined with the library in order to run.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)
Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
link a "work that uses the Library" with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
engineering for debugging such modifications.

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directing the user to the copy of this License. Also, you must do one
of these things:

a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above
specifyd materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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``#---------------------BS_STVARS_040_01----------------------#''
SELECT COUNT(@@GLOBAL.license);
COUNT(@@GLOBAL.license)
1
1 Expected
``#---------------------BS_STVARS_040_02----------------------#''
SET @@GLOBAL.license=1;
ERROR HY000: Variable 'license' is a read only variable
Expected error 'Read only variable'
SELECT COUNT(@@GLOBAL.license);
COUNT(@@GLOBAL.license)
1
1 Expected

'#---------------------BS_STVARS_040_03----------------------#'
SELECT @@GLOBAL.license = VARIABLE_VALUE
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
@@GLOBAL.license = VARIABLE_VALUE
1
1 Expected
SELECT COUNT(@@GLOBAL.license);
COUNT(@@GLOBAL.license)
1
1 Expected
SELECT COUNT(VARIABLE_VALUE)
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
COUNT(VARIABLE_VALUE)
1
1 Expected

'#---------------------BS_STVARS_040_04----------------------#'
SELECT @@license = @@GLOBAL.license;
@@license = @@GLOBAL.license
1
1 Expected

'#---------------------BS_STVARS_040_05----------------------#'
SELECT COUNT(@@license);
COUNT(@@license)
1
1 Expected
SELECT COUNT(@@local.license);
ERROR HY000: Variable 'license' is a GLOBAL variable
Expected error 'Variable is a GLOBAL variable'
SELECT COUNT(@@SESSION.license);
ERROR HY000: Variable 'license' is a GLOBAL variable
Expected error 'Variable is a GLOBAL variable'
SELECT COUNT(@@GLOBAL.license);
COUNT(@@GLOBAL.license)
1
1 Expected
SELECT license = @@SESSION.license;
ERROR 42S22: Unknown column 'license' in 'field list'
Expected error 'Readonly variable'
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mysql-test\license_basic.test

# Variable Name: license
# Scope: Global
# Access Type: Static
# Data Type: string
# Creation Date: 2008-02-07
# Author : Sharique Abdullah
# Description: Test Cases of Dynamic System Variable license that checks the behavior of this variable in the following ways
# * Value Check
# * Scope Check

--echo '#---------------------BS_STVARS_040_01----------------------#'
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_02----------------------#'
# Check if Value can set

--error ER_INCORRECT_GLOBAL_LOCAL_VAR
SET @@GLOBAL.license=1;
--echo Expected error 'Read only variable'

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_03----------------------#'

# Check if the value in GLOBAL Table matches value in variable #

SELECT @@GLOBAL.license = VARIABLE_VALUE
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
--echo 1 Expected

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

SELECT COUNT(VARIABLE_VALUE)
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_04----------------------#'

# Check if accessing variable with and without GLOBAL point to same variable #

SELECT @@license = @@GLOBAL.license;
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_05----------------------#'

# Check if license can be accessed with and without @@ sign

SELECT COUNT(@@license);
--echo 1 Expected

--Error ER_INCORRECT_GLOBAL_LOCAL_VAR
SELECT COUNT(@@local.license);
--echo Expected error 'Variable is a GLOBAL variable'

--Error ER_INCORRECT_GLOBAL_LOCAL_VAR
SELECT COUNT(@@SESSION.license);
--echo Expected error 'Variable is a GLOBAL variable'

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--Error ER_BAD_FIELD_ERROR
SELECT license = @@SESSION.license;
--echo Expected error 'Readonly variable'

== MariaDB ==

The Debian package of MySQL was first debianzed on 1997-04-12 by Christian Schwarz <schwarz@debian.org> and ist maintained since 1999-04-20 by Christian Hammers <ch@debian.org>.

The MariaDB packages were initally made by http://ourdelta.org/, and are now managed by the MariaDB development team, maria-developers@lists.launchpad.net

MariaDB can be downloaded from http://downloads.askmonty.org/mariadb/

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The manual had to be removed as it is not free in the sense of the Debian Free Software Guidelines (DFSG).

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Mesa 3-D graphics library
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1.105 libarchive 3.1.2 12.el7

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1.106 effective-tld-names 2.62.2-2.el7

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1.107 keyutils 1.5.8 3.el7

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of
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linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

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++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the pre-processing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a coff port (including considerable amounts of reverse engineering), a sparc opcode file rewrite, decstation, rs6000, and hp300hpux host ports, updated "know" assertions and made them work, much other reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.
Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).
H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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1.114 xmlrpc-c 1.32.5 1905.svn2451.el7
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1.115 gnu-tls 3.3.29-9.el7_6

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that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least
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This license, the Lesser General Public License, applies to some specially designated software packages--typically libraries--of the Free Software Foundation and other authors who decide to use it. You can use it too, but we suggest you first think carefully about whether this license or the ordinary General Public License is the better strategy to use in any particular case, based on the explanations below.

When we speak of free software, we are referring to freedom of use, not price. Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish); that you receive source code or can get it if you want it; that you can change the software and use pieces of it in new free programs; and that you are informed that you can do these things.

To protect your rights, we need to make restrictions that forbid distributors to deny you these rights or to ask you to surrender these rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser...
General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and
distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public
License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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**1.128 libgsm 1.0.13 11.el7**

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Berlin, 28.11.1994
Jutta Degener
Carsten Bormann

oOo

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[This is the first released version of the library GPL. It is
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Preamble

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Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The
former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.
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4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

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work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
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d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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- Bogusz, Jakub
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```

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1.134 tdb 1.3.16-1.el7

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1.135 libaio 0.4.9-127.el7

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(This file is under construction.)

If you've contributed to gas and your name isn't listed here, it is
not meant as a slight. I just don't know about it. Email me,
nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into
the documentation, and info on specific files will go into an AUTHORS
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++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.
The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliiCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compile support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of
Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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root function must still compute square roots.)
@end enumerate
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@end enumerate

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**1.136 systemd 219 67.el7**

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lib/gssapi/mechglue/g_canon_name.c
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kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
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1.141 glibc 2.17 292.el7

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commit ef7344f09c5ce00eb519ed14598b2a8e39c68387
Author: Joseph Myers <joseph@codesourcery.com>
Date: Sun Dec 22 14:49:48 2013 +0000

Flatten sysdeps/unix/bsd/bsd4.4 into sysdeps/unix/bsd.

As discussed in
commit ef7344f09c5ce00eb519ed14598b2a8e39c68387
Author: Joseph Myers <joseph@codesourcery.com>
Date: Sun Dec 22 14:49:48 2013 +0000

Flatten sysdeps/unix/bsd/bsd4.4 into sysdeps/unix/bsd.

As discussed in
<https://sourceware.org/ml/libc-alpha/2012-04/msg00840.html> and
<https://sourceware.org/ml/libc-alpha/2012-04/msg00989.html>, it seems appropriate to flatten sysdeps/unix/bsd/bsd4.4 into sysdeps/unix/bsd.

The bulk of the patch is just moving files. The only other changes are: update paths in sysdeps/mach/hurd/Implies and sysdeps/unix/sysv/linux/wait3.c; merge the two syscalls.list files, with the removal of syscalls that were in sysdeps/unix/bsd/syscalls.list but overridden in the bsd4.4 directory by .c files there.

Tested x86_64. The installed shared libraries are identical before and after the patch except for libc.so where the move of wait3.c (included by sysdeps/unix/sysv/linux/wait3.c) affects debug info, but the disassembly is unchanged.

- sysdeps/mach/hurd/Implies: Change unix/bsd/bsd4.4 to unix/bsd.
- sysdeps/unix/bsd/syscalls.list (chflags): Add entry from sysdeps/unix/bsd/bsd4.4/syscalls.list.
  (fchflags): Likewise.
  (revoke): Likewise.
  (setlogin): Likewise.
  (sigaltstack): Likewise.
(wait4): Likewise.
(sigblock): Remove.
(sigsetmask): Likewise.
(wait3): Likewise.
(waitpid): Likewise.
* sysdeps/unix/bsd/bsd4.4/syscalls.list: Remove file.
* sysdeps/unix/bsd/bsd4.4/Makefile: Move to ...
* sysdeps/unix/bsd/Makefile: ... here.
* sysdeps/unix/bsd/bsd4.4/Versions: Move to ...
* sysdeps/unix/bsd/Versions: ... here.
* sysdeps/unix/bsd/bsd4.4/bits/sockaddr.h: Move to ...
* sysdeps/unix/bsd/bits/sockaddr.h: ... here.
* sysdeps/unix/bsd/bsd4.4/cmsg_nxthdr.c: Move to ...
* sysdeps/unix/bsd/cmsg_nxthdr.c: ... here.
* sysdeps/unix/bsd/bsd4.4/sigblock.c: Move to ...
* sysdeps/unix/bsd/sigblock.c: ... here.
* sysdeps/unix/bsd/bsd4.4/sigsetmask.c: Move to ...
* sysdeps/unix/bsd/sigsetmask.c: ... here.
* sysdeps/unix/bsd/bsd4.4/sigvec.c: Move to ...
* sysdeps/unix/bsd/sigvec.c: ... here.
* sysdeps/unix/bsd/bsd4.4/tcdrain.c: Move to ...
* sysdeps/unix/bsd/tcdrain.c: ... here.
* sysdeps/unix/bsd/bsd4.4/tcgetattr.c: Move to ...
* sysdeps/unix/bsd/tcgetattr.c: ... here.
* sysdeps/unix/bsd/bsd4.4/tcsetattr.c: Move to ...
* sysdeps/unix/bsd/tcsetattr.c: ... here.
* sysdeps/unix/bsd/bsd4.4/wait.c: Move to ...
* sysdeps/unix/bsd/wait.c: ... here.
* sysdeps/unix/bsd/bsd4.4/wait3.c: Move to ...
* sysdeps/unix/bsd/wait3.c: ... here.
* sysdeps/unix/bsd/bsd4.4/waitpid.c: Move to ...
* sysdeps/unix/bsd/waitpid.c: ... here.

diff --git a/sysdeps/mach/hurd/Implies b/sysdeps/mach/hurd/Implies
index b6063463ce34f3b7..d2d5234c1fdf1522 100644
--- a/sysdeps/mach/hurd/Implies
+++ b/sysdeps/mach/hurd/Implies
@@ -2,4 +2,4 @@
# Hurd-based GNU systems.
gnu
# The Hurd provides a rough superset of the functionality of 4.4 BSD.
-unix/bsd/bsd4.4
+unix/bsd
diff --git a/sysdeps/unix/bsd/bsd4.4/Makefile b/sysdeps/unix/bsd/Makefile
similarity index 100%
rename from sysdeps/unix/bsd/bsd4.4/Makefile
rename to sysdeps/unix/bsd/Makefile

diff --git a/sysdeps/unix/bsd4.4/versions b/sysdeps/unix/bsd4.4/versions
    similarity index 100%
rename from sysdeps/unix/bsd4.4/versions
rename to sysdeps/unix/bsd/versions

diff --git a/sysdeps/unix/bsd4.4/bits/sockaddr.h b/sysdeps/unix/bsd/bits/sockaddr.h
    similarity index 100%
rename from sysdeps/unix/bsd4.4/bits/sockaddr.h
rename to sysdeps/unix/bsd/bits/sockaddr.h

diff --git a/sysdeps/unix/bsd4.4/syscalls.list b/sysdeps/unix/bsd4.4/syscalls.list
    deleted file mode 100644
index a4d354685424ac61..0000000000000000
    --- a/sysdeps/unix/bsd4.4/syscalls.list
    +++ /dev/null
    @ @ -1,8 +0,0 @@
    # File nameCallerSyscall name# argsStrong nameWeak names
    -
    -chflags-chflags2chflags
    -fchflags-fchflags2fchflags
    -revoke-revoke1revoke
    -setlogin-setlogin2setlogin
    -sigaltstack-sigaltstack2__sigaltstacksigaltstack
    -wait4-wait44__wait4wait4

diff --git a/sysdeps/unix/bsd4.4/cmsng_nthdr.c b/sysdeps/unix/bsd/cmsng_nthdr.c
    similarity index 100%
rename from sysdeps/unix/bsd4.4/cmsng_nthdr.c
rename to sysdeps/unix/bsd/cmsng_nthdr.c

diff --git a/sysdeps/unix/bsd4.4/sigblock.c b/sysdeps/unix/bsd/sigblock.c
    similarity index 100%
rename from sysdeps/unix/bsd4.4/sigblock.c
rename to sysdeps/unix/bsd/sigblock.c

diff --git a/sysdeps/unix/bsd4.4/sigsetmask.c b/sysdeps/unix/bsd/sigsetmask.c
    similarity index 100%
rename from sysdeps/unix/bsd4.4/sigsetmask.c
rename to sysdeps/unix/bsd/sigsetmask.c

diff --git a/sysdeps/unix/bsd4.4/sigvec.c b/sysdeps/unix/bsd/sigvec.c
    similarity index 100%
rename from sysdeps/unix/bsd4.4/sigvec.c
rename to sysdeps/unix/bsd/sigvec.c

diff --git a/sysdeps/unix/bsd/syscalls.list b/sysdeps/unix/bsd/syscalls.list
index e84819dc1f19d148..9f48a144d475f342
    --- a/sysdeps/unix/bsd/syscalls.list
    +++ b/sysdeps/unix/bsd/syscalls.list
    @ @ -1,14 +1,16 @@
    # File nameCallerSyscall name# argsStrong nameWeak names
+chflags-chflags2chflags
+fchflags-fchflags2fchflags
flock-flock2__flockflock
getdents-getdirenties4__getdirentiesgetdirenties
getdtsz-gettablesize0__gettablesizegettablesize
getpagesize-getpagesize0__getpagesizegetpagesize
killpg-killpg2killpg
-sigblock-sigblock1__sigblocksigblock
+revoke-revoke1revoke
+setlogin-setlogin2setlogin
+sigaltstack-sigaltstack2__sigaltstacksigaltstack
sigpause-sigpause1__sigpausesigpause
-sigsetmask-sigsetmask1__sigsetmasksigsetmask
sigstack-sigstack2sigstack
sigvec-sigvec3__sigvecsigvec
-wait3-wait33__wait3wait3
-waitpid-waitpid3__waitpidwaitpid
+wait4-wait44__wait4wait4
diff --git a/sysdeps/unix/bsd4.4/tcdrain.c b/sysdeps/unix/bsd/tcdrain.c
similarity index 100%
rename from sysdeps/unix/bsd4.4/tcdrain.c
rename to sysdeps/unix/bsd/tcdrain.c
diff --git a/sysdeps/unix/bsd4.4/tcgetattr.c b/sysdeps/unix/bsd/tcgetattr.c
similarity index 100%
rename from sysdeps/unix/bsd4.4/tcgetattr.c
rename to sysdeps/unix/bsd/tcgetattr.c
diff --git a/sysdeps/unix/bsd4.4/tcsetattr.c b/sysdeps/unix/bsd/tcsetattr.c
similarity index 100%
rename from sysdeps/unix/bsd4.4/tcsetattr.c
rename to sysdeps/unix/bsd/tcsetattr.c
diff --git a/sysdeps/unix/bsd4.4/wait.c b/sysdeps/unix/bsd/wait.c
similarity index 100%
rename from sysdeps/unix/bsd4.4/wait.c
rename to sysdeps/unix/bsd/wait.c
diff --git a/sysdeps/unix/bsd4.4/wait3.c b/sysdeps/unix/bsd/wait3.c
similarity index 100%
rename from sysdeps/unix/bsd4.4/wait3.c
rename to sysdeps/unix/bsd/wait3.c
diff --git a/sysdeps/unix/bsd4.4/waitpid.c b/sysdeps/unix/bsd/waitpid.c
similarity index 100%
rename from sysdeps/unix/bsd4.4/waitpid.c
rename to sysdeps/unix/bsd/waitpid.c
diff --git a/sysdeps/unix/sysv/linux/wait3.c b/sysdeps/unix/sysv/linux/wait3.c
index 0b3bdee771782b7b..2ff027f0e1d83eb2 100644
--- a/sysdeps/unix/sysv/linux/wait3.c
+++ b/sysdeps/unix/sysv/linux/wait3.c
@@ -1 +1 @@
-#include <sysdeps/unix/bsd4.4/wait3.c>
+#include <sysdeps/unix/bsd/wait3.c>
1.142 libnl 1.1.4 3.el7

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1.143 libnfsidmap 0.25 19.el7

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This package was debianized by J. Bruce Fields <bfields@citi.umich.edu> on Fri, 6 Aug 2004 16:10:43 -0400.

The latest version can always be found at http://www.citi.umich.edu/projects/nfsv4/linux/

Authors: J. Bruce Fields, Andy Adamson, Marius Eriksen

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1.144 libogg 1.10.4-2.el7

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1.145 pcre 2.56.1 5.el7
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1.147 Isol 4.87 6.el7

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1.148 radvd 2.17 3.el7

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1.149 coreutils 8.22 24.el7
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   */

/* Extracted from chown.c/chgrp.c and librarified by Jim Meyering. */

#include <config.h>
#include <stdio.h>
#include <sys/types.h>
#include <pwd.h>
#include <grp.h>
#include "system.h"
#include "chown-core.h"
#include "error.h"
#include "ignore-value.h"
#include "quote.h"
#include "root-dev-ino.h"
#include "xfts.h"

#define FTSENT_IS_DIRECTORY(E)\
  ((E)->fts_info == FTS_D\
   || (E)->fts_info == FTS_DC\
   || (E)->fts_info == FTS_DP\
   || (E)->fts_info == FTS_DNR)

enum RCH_status
{
  /* we called fchown and close, and both succeeded */
  RC_ok = 2,

  /* required_uid and/or required_gid are specified, but don't match */
  RC_excluded,

  /* SAME_INODE check failed */
  RC_inode_changed,

  /* open/fchown isn't needed, isn't safe, or doesn't work due to
   * permissions problems; fall back on chown */
  RC_do_ordinary_chown,

  /* open, fstat, fchown, or close failed */
  RC_error
};

extern void
do_not_care (struct Chown_option *chopt)
{
  chopt->verbosity = V_off;
  chopt->root_dev_ino = NULL;
  chopt->affect_symlink_referent = true;
  chopt->recurse = false;
  chopt->force_silent = false;
  chopt->user_name = NULL;
  chopt->group_name = NULL;
}

extern void
do_not_care (struct Chown_option *chopt _GL_UNUSED)
/* Deliberately do not free chopt->user_name or ->group_name.  
   They're not always allocated.  */

/* Convert the numeric group-id, GID, to a string stored in xmalloc'd memory,  
   and return it.  If there's no corresponding group name, use the decimal  
   representation of the ID.  */

extern char *
gid_to_name (gid_t gid)
{
  char buf[INT_BUFSIZE_BOUND (intmax_t)];
  struct group *grp = getgrgid (gid);
  return xstrdup (grp ? grp->gr_name
                    : TYPE_SIGNED (gid_t) ? imaxtostr (gid, buf)
                    : umaxtostr (gid, buf));
}

/* Convert the numeric user-id, UID, to a string stored in xmalloc'd memory,  
   and return it.  If there's no corresponding user name, use the decimal  
   representation of the ID.  */

extern char *
uid_to_name (uid_t uid)
{
  char buf[INT_BUFSIZE_BOUND (intmax_t)];
  struct passwd *pwd = getpwuid (uid);
  return xstrdup (pwd ? pwd->pw_name
                    : TYPE_SIGNED (uid_t) ? imaxtostr (uid, buf)
                    : umaxtostr (uid, buf));
}

/* Allocate a string representing USER and GROUP.  */

static char *
user_group_str (char const *user, char const *group)
{
  char *spec = NULL;

  if (user)
    {
      if (group)
        {
          spec = xmalloc (strlen (user) + 1 + strlen (group) + 1);
          strcpy (strcpy (strcpy (spec, user), ":"), group);
        }
      else
} else if (group) {
    spec = xstrdup (group);
}

return spec;
}

/* Tell the user how/if the user and group of FILE have been changed.  
   If USER is NULL, give the group-oriented messages.  
   CHANGED describes what (if anything) has happened. */

static void describe_change (const char *file, enum Change_status changed, 
    char const *old_user, char const *old_group, 
    char const *user, char const *group)
{
    const char *fmt;
    char *old_spec;
    char *spec;

    if (changed == CH_NOT_APPLIED)
    {
        printf (_("neither symbolic link %s nor referent has been changed\n"), quote (file));
        return;
    }

    spec = user_group_str (user, group);
    old_spec = user_group_str (user ? old_user : NULL, group ? old_group : NULL);

    switch (changed)
    {
    case CH_SUCCEEDED:
        fmt = (user ? _("changed ownership of %s from %s to %s\n") 
          : group ? _("changed group of %s from %s to %s\n") 
          : _("no change to ownership of %s\n");
        break;
    case CH_FAILED:
        if (old_spec)
        {
            fmt = (user ? _("failed to change ownership of %s from %s to %s\n") 
              : group ? _("failed to change group of %s from %s to %s\n") 
              : _("failed to change ownership of %s\n");
            break;
        }
    }
else
{
    fmt = (user ? _("failed to change ownership of %s to %s\n")
           : group ? _("failed to change group of %s to %s\n")
           : _("failed to change ownership of %s\n");
    free (old_spec);
    old_spec = spec;
    spec = NULL;
}
break;

case CH_NO_CHANGE_REQUESTED:
    fmt = (user ? _("ownership of %s retained as %s\n")
             : group ? _("group of %s retained as %s\n")
             : _("ownership of %s retained\n");
    break;
default:
    abort ();
}

printf (fmt, quote (file), old_spec, spec);

free (old_spec);
free (spec);
}

/* Change the owner and/or group of the FILE to UID and/or GID (safely)
only if REQUIRED_UID and REQUIRED_GID match the owner and group IDs
of FILE. ORIG_ST must be the result of 'stat'ing FILE.

The 'safely' part above means that we can't simply use chown(2),
since FILE might be replaced with some other file between the time
of the preceding stat/lstat and this chown call. So here we open
FILE and do everything else via the resulting file descriptor.
We first call fstat and verify that the dev/inode match those from
the preceding stat call, and only then, if appropriate (given the
required_uid and required_gid constraints) do we call fchown.

Return RC_do_ordinary_chown if we can't open FILE, or if FILE is a
special file that might have undesirable side effects when opening.
In this case the caller can use the less-safe ordinary chown.

Return one of the RCH_status values. */

static enum RCH_status
restricted_chown (int cwd_fd, char const *file,
                 struct stat const *orig_st,
                 uid_t uid, gid_t gid,
uid_t required_uid, gid_t required_gid)
{
    enum RCH_status status = RC_ok;
    struct stat st;
    int open_flags = O_NONBLOCK | O_NOCTTY;
    int fd;

    if (required_uid == (uid_t) -1 && required_gid == (gid_t) -1)
        return RC_do_ordinary_chown;

    if (!S_ISREG (orig_st->st_mode))
    {
        if (S_ISDIR (orig_st->st_mode))
            open_flags |= O_DIRECTORY;
        else
            return RC_do_ordinary_chown;
    }

    fd = openat (cwd_fd, file, O_RDONLY | open_flags);
    if (! (0 <= fd
        || (errno == EACCES && S_ISREG (orig_st->st_mode)
            && 0 <= (fd = openat (cwd_fd, file, O_WRONLY | open_flags))))))
        return (errno == EACCES ? RC_do_ordinary_chown : RC_error);

    if (fstat (fd, &st) != 0)
        status = RC_error;
    else if (!SAME_INODE (*orig_st, st))
        status = RC_inode_changed;
    else if (!((required_uid == (uid_t) -1 || required_uid == st.st_uid)
        && (required_gid == (gid_t) -1 || required_gid == st.st_gid))
    {
        if (fchown (fd, uid, gid) == 0)
        {
            status = (close (fd) == 0
                ? RC_ok : RC_error);
            return status;
        }
    }
    else
    {
        status = RC_error;
    }
}

int saved_errno = errno;
close (fd);
errno = saved_errno;
return status;
/* Change the owner and/or group of the file specified by FTS and ENT to UID and/or GID as appropriate.
If REQUIRED_UID is not -1, then skip files with any other user ID.
If REQUIRED_GID is not -1, then skip files with any other group ID.
CHOPT specifies additional options.
Return true if successful. */
static bool
change_file_owner (FTS *fts, FTSENT *ent,
    uid_t uid, gid_t gid,
    uid_t required_uid, gid_t required_gid,
    struct Chown_option const *chopt)
{
    char const *file_full_name = ent->fts_path;
    char const *file = ent->fts_accpath;
    struct stat const *file_stats;
    struct stat stat_buf;
    bool ok = true;
    bool do_chown;
    bool symlink_changed = true;

    switch (ent->fts_info)
    {
        case FTS_D:
            if (chopt->recurse)
            {
                if (ROOT_DEV_INO_CHECK (chopt->root_dev_ino, ent->fts_statp))
                {
                    /* This happens e.g., with "chown -R --preserve-root 0 /"
                     and with "chown -RH --preserve-root 0 symlink-to-root". */
                    ROOT_DEV_INO_WARN (file_full_name);
                    /* Tell fts not to traverse into this hierarchy. */
                    fts_set (fts, ent, FTS_SKIP);
                    /* Ensure that we do not process "/" on the second visit. */
                    ignore_value (fts_read (fts));
                    return false;
                }
                return true;
            }
            break;

        case FTS_DP:
            if (! chopt->recurse)
            {
                return true;
            }
            break;

        case FTS_NS:
            /* For a top-level file or directory, this FTS_NS (stat failed)
indicator is determined at the time of the initial fts_open call.
With programs like chmod, chown, and chgrp, that modify
permissions, it is possible that the file in question is
accessible when control reaches this point. So, if this is
the first time we've seen the FTS_NS for this file, tell
fts_read to stat it "again". */
if (ent->fts_level == 0 && ent->fts_number == 0)
{
ent->fts_number = 1;
fts_set (fts, ent, FTS_AGAIN);
return true;
}
if (! chopt->force_silent)
error (0, ent->fts_errno, _("cannot access %s"),
quote (file_full_name));
ok = false;
break;
case FTS_ERR:
if (! chopt->force_silent)
error (0, ent->fts_errno, "%s", quote (file_full_name));
ok = false;
break;
case FTS_DNR:
if (! chopt->force_silent)
error (0, ent->fts_errno, _("cannot read directory %s"),
quote (file_full_name));
ok = false;
break;
case FTS_DC:/* directory that causes cycles */
if (cycle_warning_required (fts, ent))
{
emit_cycle_warning (file_full_name);
return false;
}
break;
default:
break;
}
if (!ok)
{
do_chown = false;
file_stats = NULL;
}

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else if (required_uid == (uid_t) -1 && required_gid == (gid_t) -1
    && chopt->verbosity == V_off
    && ! chopt->root_dev_ino
    && ! chopt->affect_symlink_referent)
{
    do_chown = true;
    file_stats = ent->fts_statp;
}
else
{
    file_stats = ent->fts_statp;

    /* If this is a symlink and we're dereferencing them,
     * stat it to get info on the referent. */
    if (chopt->affect_symlink_referent && S_ISLNK (file_stats->st_mode))
    {
        if (fstatat (fts->fts_cwd_fd, file, &stat_buf, 0) != 0)
        {
            if (! chopt->force_silent)
                error (0, errno, _("cannot dereference %s"),
                       quote (file_full_name));
            ok = false;
        }
        file_stats = &stat_buf;
    }

do_chown = (ok
    && (required_uid == (uid_t) -1
        || required_uid == file_stats->st_uid)
    && (required_gid == (gid_t) -1
        || required_gid == file_stats->st_gid));
}

/* This happens when chown -LR --preserve-root encounters a symlink-to-/.
 */
if (ok
    && FTSENT_IS_DIRECTORY (ent)
    && ROOT_DEV_INO_CHECK (chopt->root_dev_ino, file_stats))
{
    ROOT_DEV_INO_WARN (file_full_name);
    return false;
}

if (do_chown)
{
    if ( ! chopt->affect_symlink_referent)
    {
        ok = (lchownat (fts->fts_cwd_fd, file, uid, gid) == 0);
    }
/* Ignore any error due to lack of support; POSIX requires 
this behavior for top-level symbolic links with -h, and 
implies that it's required for all symbolic links. */
if (ok && errno == EOPNOTSUPP)
{
    ok = true;
    symlink_changed = false;
}
else
{
    /* If possible, avoid a race condition with --from=O:G and without the 
(-h) --no-dereference option. If fts's stat call determined 
that the uid/gid of FILE matched the --from=O:G-selected 
owner and group IDs, blindly using chown(2) here could lead 
chown(1) or chgrp(1) mistakenly to dereference a *symlink* 
to an arbitrary file that an attacker had moved into the 
place of FILE during the window between the stat and 
chown(2) calls. If FILE is a regular file or a directory 
that can be opened, this race condition can be avoided safely. */

denum RCH_status err
    = restricted_chown (fts->fts_cwd_fd, file, file_stats, uid, gid, 
                        required_uid, required_gid);
switch (err)
{
    case RC_ok:
        break;

    case RC_do_ordinary_chown:
        ok = (chownat (fts->fts_cwd_fd, file, uid, gid) == 0);
        break;

    case RC_error:
        ok = false;
        break;

    case RC_inode_changed:
    case RC_excluded:
        do_chown = false;
        ok = false;
        break;

    default:
        abort ();
}
On some systems (e.g., GNU/Linux 2.4.x), the chown function resets the 'special' permission bits. Do *not* restore those bits; doing so would open a window in which a malicious user, M, could subvert a chown command run by some other user and operating on files in a directory where M has write access. */

if (do_chown && !ok && ! chopt->force_silent)
    error (0, errno, (uid != (uid_t) -1
        ? _("changing ownership of %s")
        : _("changing group of %s")),
        quote (file_full_name));

if (chopt->verbosity != V_off)
{
    bool changed =
        ((do_chown && ok && symlink_changed)
         && ! ((uid == (uid_t) -1 || uid == file_stats->st_uid)
             && (gid == (gid_t) -1 || gid == file_stats->st_gid));

if (changed || chopt->verbosity == V_high)
{
    enum Change_status ch_status =
        (!ok ? CH_FAILED
            : !symlink_changed ? CH_NOT_APPLIED
            : !changed ? CH_NO_CHANGE_REQUESTED
            : CH_SUCCEEDED);
    char *old_usr = file_stats ? uid_to_name (file_stats->st_uid) : NULL;
    char *old_grp = file_stats ? gid_to_name (file_stats->st_gid) : NULL;
    describe_change (file_full_name, ch_status,
        old_usr, old_grp,
        chopt->user_name, chopt->group_name);
    free (old_usr);
    free (old_grp);
}

if ( ! chopt->recurse)
    fts_set (fts, ent, FTS_SKIP);

return ok;
} /* Change the owner and/or group of the specified FILES. BIT_FLAGS specifies how to treat each symlink-to-directory
that is encountered during a recursive traversal.
CHOPT specifies additional options.
If UID is not -1, then change the owner id of each file to UID.
If GID is not -1, then change the group id of each file to GID.
If REQUIRED_UID and/or REQUIRED_GID is not -1, then change only
files with user ID and group ID that match the non(-1) value(s).
Return true if successful. */

extern bool
chown_files (char **files, int bit_flags,
uid_t uid, gid_t gid,
uid_t required_uid, gid_t required_gid,
struct Chown_option const *chopt)
{
    bool ok = true;

    /* Use lstat and stat only if they're needed. */
    int stat_flags = ((required_uid != (uid_t) -1 || required_gid != (gid_t) -1
                      || chopt->affect_symlink_referent
                      || chopt->verbosity != V_off)
                      ? 0 : FTS_NOSTAT);

    FTS *fts = xfts_open (files, bit_flags | stat_flags, NULL);

    while (1)
    {
        FTSENT *ent;

        ent = fts_read (fts);
        if (ent == NULL)
        {
            if (errno != 0)
            {
                /* FIXME: try to give a better message */
                if (! chopt->force_silent)
                    error (0, errno, _("fts_read failed"));
                ok = false;
            }
            break;
        }

        ok &= change_file_owner (fts, ent, uid, gid,
                                 required_uid, required_gid, chopt);
    }

    if (fts_close (fts) != 0)
    {
        error (0, errno, _("fts_close failed"));
ok = false;
}

return ok;
}

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Jean-loup Gailly        Mark Adler
jloup@gzip.org          madler@alumni.caltech.edu

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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Special thanks also go to La'szlo' Ne'meth <nemethl@gyorsposta.hu> who is the author of the
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* the code to support compound words in MySpell
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for CyberLogin for Linux which allows logging onto EON
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/* nicstar.c v0.22  Jawaid Bazyar (bazyar@hypermall.com)
* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
*
* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
* http://www.hypermall.com/
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
* interrupts us (except possibly for removal/insertion of the cable?)
* 10/4/97 - began heavy inline documentation of the code. Corrected typos
* and spelling mistakes.
* 10/5/97 - added code to handle PHY interrupts, disable PHY on
* loss of link, and correctly re-enable PHY when link is
* re-established. (put back CFG_PHYIE)
*
* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
*
* Linux driver for the IDT77201 NICStAR PCI ATM controller.
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
* see init_nicstar() for PHY initialization to change this. This driver
* expects the Linux ATM stack to support scatter-gather lists
* (skb->atm.iovcnt != 0) for Rx skb’s passed to vcc->push.
*
* Implementing minimal-copy of received data:
*   IDT always receives data into a small buffer, then large buffers
*   as needed. This means that data must always be copied to create
*   the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
*   Fix is simple: make large buffers large enough to hold entire
*   SDU, and leave <small_buffer_data> bytes empty at the start. Then
*   copy small buffer contents to head of large buffer.
* Trick is to avoid fragmenting Linux, due to need for a lot of large
* buffers. This is done by 2 things:
*   1) skb->destructor / skb->atm.recycle_buffer
*      combined, allow nicstar_free_rx_skb to be called to
*      recycle large data buffers
*   2) skb_clone of received buffers
* See nicstar_free_rx_skb and linearize_buffer for implementation
* details.
*
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*
* M. Welsh, 6 July 1996
*
*/
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Video4Linux interface and 2.1.x kernel adaptation

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Radio card (ITT sound processor)

bigfoot <bigfoot@net-way.net>
Ragnar Hojland Espinosa <ragnar@macula.net>
ConferenceTV card

+ many more (please mail me if you are missing in this list and would like to be mentioned)

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must
be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy
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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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END OF TERMS AND CONDITIONS

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.
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d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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## Makefile

## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPptions = -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.%$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.%$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<

%.%$(DYNAMIC_O): $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<
%.%$(DYNAMIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "s/$(srcdir)/.o/^[^A\n].o $@ : /g" > $@; \
[ -s $@ ] || rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \"
## Versioned libraries rules

%.%(SO).%(%(SO).%(SO_TARGET_VERSION_MAJOR)): %.%(SO).%(%(SO).%(SO_TARGET_VERSION))
%(RM) $@ && ln -s ${<F} $@
%.%(SO): %.%(SO).%(%(SO_TARGET_VERSION_MAJOR))
%(RM) $@ && ln -s {*F}.%(SO).%(%(SO).%(SO_TARGET_VERSION)) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.
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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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---------------------------------------------------------------------

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is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
root function must still compute square roots.)

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6,
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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)
These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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Mesa 3-D graphics library
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*/

1.172 fdisk 2.23.2-61.el7

1.172.1 Available under license :

size: 8388608, sector size: 512, PT: dos, offset: 446, id=0x8f8378c0
---
#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
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Initialize empty image
f1c9645dbc14efddc7d8a322685f26eb bsd.img
Create new DOS partition table
57e721e38d1266c2d055067c18f2cf9 bsd.img

---layout---------
  __ts_dev__: 10 MB, 10485760 bytes, 20480 sectors
Units = sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disk label type: dos
Disk identifier: 0x00000001

Device Boot Start End Blocks Id System
--------- ------- ------- ------ ----- ------------

Create 1st primary partition
ada64ace122978d00d1d1c0e5ee45d26 bsd.img

---layout---------
  __ts_dev__: 10 MB, 10485760 bytes, 20480 sectors
Units = sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disk label type: dos
Disk identifier: 0x00000001

Device Boot Start End Blocks Id System
--------- ------- ------- ------ ----- ------------
  __ts_dev__1  2048  4095  1024  83  Linux

Create 2st primary partition
1bebf87248e05d6e4e62b749da65d023 bsd.img
Set 2nd partition type
2d8e889f51a88a045f223418dd73fbe bsd.img

---layout---------
  __ts_dev__: 10 MB, 10485760 bytes, 20480 sectors
Units = sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disk label type: dos
Disk identifier: 0x00000001

    Device  Boot  Start  End    Blocks   Id  System
__ts_dev__1     2048  4095  1024  83 Linux
__ts_dev__2     4096 20479  8192 a5 FreeBSD

Create default BSD
2e1cee529cb59c9341afe0f443f196a1 bsd.img

---layout--------

Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help):

BSD disklabel command (m for help):
4 partitions:
#       start       end      size     fstype   [fsize bsize   cpg]
c:     4096     20479     16384     unused        0     0

BSD disklabel command (m for help):
Command (m for help):

b5c121c2091b2ff26b880551feac7112 bsd.img

---layout--------

Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help):

BSD disklabel command (m for help):
4 partitions:
#       start       end      size     fstype   [fsize bsize   cpg]
a:     4096     6144     2049    4.2BSD        0     0     0
c:     4096     20479     16384     unused        0     0
d:      0     16064     16065     unused        0     0
BSD disklabel command (m for help):
Command (m for help):
-------------------
Changes will remain in memory only, until you decide to write them. Be careful before using the write command.

Command (m for help):

BSD disklabel command (m for help):
0 unused 5 4.1BSD 9 4.4LFS d boot
1 swap 6 Eighth Edition a unknown e ADOS
2 Version 6 7 4.2BSD b HPFS f HFS
3 Version 7 8 MS-DOS c ISO-9660 10 AdvFS
4 System V

BSD disklabel command (m for help):
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1.173 dmraid 1.0.0.rc16 28.el7

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++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of
Buffalo University and Torbjorn Granlund of the Swedish Institute of
Computer Science.
Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.
Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.
In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from
such a program is covered only if its contents constitute a work based
on the Library (independent of the use of the Library in a tool for
writing it). Whether that is true depends on what the Library does
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2. You may modify your copy or copies of the Library or any portion
of it, thus forming a work based on the Library, and copy and
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above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices
stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no
charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a
table of data to be supplied by an application program that uses
the facility, other than as an argument passed when the facility
is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
application-supplied function or table used by this function must
be optional: if the application does not supply it, the square
root function must still compute square roots.)

These requirements apply to the modified work as a whole. If
identifiable sections of that work are not derived from the Library,
and can be reasonably considered independent and separate works in
themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on
the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

b) Give prominent notice with the combined library of the fact that part of it is a work based on the Library, and explaining where to find the accompanying uncombined form of the same work.

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[This is the first released version of the library GPL. It is 
numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

The licenses for most software are designed to take away your 
freedom to share and change it. By contrast, the GNU General Public 
Licenses are intended to guarantee your freedom to share and change 
free software--to make sure the software is free for all its users.

This license, the Library General Public License, applies to some 
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your libraries, too.

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To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a
derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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during execution displays copyright notices, you must include the
copyright notice for the Library among them, as well as a reference
directing the user to the copy of this License. Also, you must do one
of these things:

a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

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least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
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1.185 libnotify 0.7.7-1.el7

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    font-style: italic;
    width:300px;
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  var q = document.getElementById("q");
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  q.style.color = "black"
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  var q = document.getElementById("q");
  if( q.value == "" ) {
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<div style="padding:0 1em 0px 0;white-space:nowrap">
<form name=f method="GET" action="http://www.sqlite.org/search">
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1.194 openssl 1.0.2k 19.el7

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1.195 xorg-server 1.20.4-7.el7

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/* zlib.h -- interface of the 'zlib' general purpose compression library
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(in case you are wondering where CyberLogin is, EON changed its login
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The DSP/Bridge project wish to thank all of its contributors, current bridge driver is the result of the work of all of them. If any name is accidentally omitted, let us know by sending a mail to omar.ramirez@ti.com or x095840@ti.com.

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/* nicstar.c v0.22  Jawaid Bazyar (bazyar@hypermall.com)
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 *
 * Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
 * http://www.hypermall.com/
 * 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
 * interrupts us (except possibly for removal/insertion of the cable?)
 * 10/4/97 - began heavy inline documentation of the code. Corrected typos
 * and spelling mistakes.
 * 10/5/97 - added code to handle PHY interrupts, disable PHY on
 * loss of link, and correctly re-enable PHY when link is
 * re-established. (put back CFG_PHYIE)
 *
 * Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
 *
 * R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
 *
 * Linux driver for the IDT77201 NICStAR PCI ATM controller.
 * PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
 * see init_nicstar() for PHY initialization to change this. This driver
 * expects the Linux ATM stack to support scatter-gather lists
 * (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
 *
 * Implementing minimal-copy of received data:
 * IDT always receives data into a small buffer, then large buffers
 * as needed. This means that data must always be copied to create
 * the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
 * Fix is simple: make large buffers large enough to hold entire
 * SDU, and leave <small_buffer_data> bytes empty at the start. Then
 * copy small buffer contents to head of large buffer.
 * Trick is to avoid fragmenting Linux, due to need for a lot of large
 * buffers. This is done by 2 things:
 * 1) skb->destructor / skb->atm.recycle_buffer
 * combined, allow nicstar_free_rx_skb to be called to
 * recycle large data buffers
 * 2) skb_clone of received buffers
 * See nicstar_free_rx_skb and linearize_buffer for implementation
 * details.
 *
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* 
* M. Welsh, 6 July 1996
* 
* */

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QLogic Linux FC-FCoE Driver

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* configure.ac, Makefile.am: The original versions were derived from the
  ones in the XML Catalog Manager project, version 2.2.

  Author: Julio Merino <jmmv@users.sourceforge.net>

* atf-c/ui.c: The format_paragraph and format_text functions were
derived form the ones in the Monotone project, revision
3a0982da308228d796df35f98d787c5c0f2bb56.
Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp: These files were derived from the file_handle, systembuf, pipe and pistream classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* admin/check-style.sh, admin/check-style-common.awk, admin/check-style-cpp.awk, admin/check-style-shell.awk: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

Author: Julio Merino <jmmv84@gmail.com>

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<xsl:stylesheet version="1.0"
xmlns:xsl="http://www.w3.org/1999/XSL/Transform"
xmlns:xi="http://www.w3.org/2001/XInclude"
xmlns:db="http://docbook.org/ns/docbook">

<xsl:template name="isc.copyright.format">
  <xsl:param name="text"/>
  <xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
  <xsl:text>&#10;</xsl:text>
  <xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
  <xsl:if test="translate($rest, '&#9;&#32;', '')">
    <xsl:call-template name="isc.copyright.format">
      <xsl:with-param name="text" select="$rest"/>
    </xsl:call-template>
  </xsl:if>
</xsl:template>

</xsl:stylesheet>
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* configure.ac, Makefile.am: The original versions were derived from the
  ones in the XML Catalog Manager project, version 2.2.

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* atf-c/ui.c: The format_paragraph and format_text functions were
derived from the ones in the Monotone project, revision
  3a0982da308228d796df35f98d787c5c5ff2bb5b6.

  Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp:
  These files were derived from the file_handle, systembuf, pipe and pistream
  classes and tests found in the Boost.Process library.

  Author: Julio Merino <jmmv84@gmail.com>
admin/check-style.sh, admin/check-style-common.awk, admin/check-style-cpp.awk, admin/check-style-shell.awk: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

Author: Julio Merino <jmmv84@gmail.com>

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1.213 e2fsprogs 1.42.9 16.el7

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This is the Debian GNU/Linux prepackaged version of the ss command-line interface parsing library library. It is currently distributed together with the EXT2 file system utilities, which are otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>, from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/
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This is the Debian GNU/Linux prepackaged version of the static EXT2 file system consistency checker (e2fsck.static). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

Sources were obtained from http://sourceforge.net/projects/e2fsprogs

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This package was added to the e2fsprogs debian source package by
Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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This is the Debian GNU/Linux prepackaged version of the EXT2 file system utilities (e2fsck, mke2fs, etc.). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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# This is a Makefile stub which handles the creation of BSD shared libraries.
# In order to use this stub, the following makefile variables must be defined.

# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)

all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:$BSDLIB

$(BSDLIB): $(OBS)
(cd pic; ld -Bshareable -o $(BSDLIB) $(LDFLAGS) $(OBS))
$(MV) pic/$BSDLIB .
$(RM) -f ../$(BSDLIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) `echo $(my_dir) | sed -e 's;lib/;;'"/$(BSDLIB) $(BSDLIB)")

install-shlibs install:: $(BSDLIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSDLIB)"
@$(INSTALL_PROGRAM) $(BSDLIB) \
$(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSDLIB)
@-$(LDCONFIG)

install-strip: install

install-shlibs-strip: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)/$(BSDLIB_INSTALL_DIR)/$(BSDLIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSDLIB)
$(RM) -f ../$(BSDLIB)

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Theodore Ts'o
23-June-2007

-------------------------------------------------------------------------

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concluded that weaker conditions might promote sharing better.

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Theodore Ts'o <tytso@mit.edu> on Fri Dec 14 22:24:35 EST 2007

It is part of the main e2fsprogs distribution, which can be found at:

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Gadi Oxman, August 1995

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<signature of Ty Coon>, 1 April 1989
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Index: tdbsa/tdb.c
===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
/*
 - Unix SMB/CIFS implementation.
+ trivial database library - standalone version

 - trivial database library - private includes
- 
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 ** NOTE! The following LGPL license applies to the tdb
 ** library. This does NOT imply that all of Samba is released
This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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Upstream Author: Theodore Ts'o <tytso@mit.edu>

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This package was put together by Yann Dirson <dirson@debian.org>, from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

From the original distribution:

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*/

1.214 pkg-config 0.27.1 4.el7

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1.235 libwebp 0.3.0-7.el7

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1.236 libteam 1.27 9.el7

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    If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

1.238 mesa 18.3.4 5.el7
1.238.1 Available under license:

Subject: RE: Question about Mesa MLAA license
From: Jorge Jimenez <iryoku@gmail.com>
Date: 01/08/2013 12:50 PM
To: Tom Callaway <tcallawa@redhat.com>
CC: "jorge@iryoku.com" <jorge@iryoku.com>
Yes to both questions.

Thanks,
Jorge

From: Tom Callaway <tcallawa@redhat.com>
Sent: January 8, 2013 6:49 PM
To: Jorge Jimenez <iryoku@gmail.com>
CC: jorge@iryoku.com
Subject: Re: Question about Mesa MLAA license

On 01/08/2013 12:39 PM, Jorge Jimenez wrote:
> Hi Tom,
> 
> What we meant with that is that we made an exception for clause 2.
> Instead of clause 2, in the case of the Mesa project, you have to name
> the technique Jimenez's MLAA in the config options of Mesa. We did that
> just to allow them to solve license issues. This exception should be for
> the Mesa project, and any project using Mesa, like Fedora.
> 
> We want to widespread usage of our MLAA, so we want to avoid any kind of
> license complications. Hope current one is good for Fedora, if not
> please tell, and we'll see what we can do!

Okay, a few more questions:

* If Fedora decides to simply reproduce the quoted statement:
"Uses Jimenez's MLAA. Copyright (C) 2010 by Jorge Jimenez, Belen Masia,
Jose I. Echevarria, Fernando Navarro and Diego Gutierrez."

Specifically, if this is done as part of documentation included with
Mesa, is that sufficient to meet clause 2 even if the Mesa config option
is not set as described in your exception?

* Currently, the Mesa config option for MLAA says: "Morphological
anti-aliasing based on Jimenez's MLAA. 0 to disable, 8 for default
quality". Is this in compliance with your exception?

Thanks again,

~tom

==
Fedora Project

Subject: RE: Question about Mesa MLAA license
From: Jorge Jimenez <iryoku@gmail.com>
Hi Tom,

What we meant with that is that we made an exception for clause 2. Instead of clause 2, in the case of the Mesa project, you have to name the technique Jimenez's MLAA in the config options of Mesa. We did that just to allow them to solve license issues. This exception should be for the Mesa project, and any project using Mesa, like Fedora.

We want to widespread usage of our MLAA, so we want to avoid any kind of license complications. Hope current one is good for Fedora, if not please tell, and we'll see what we can do!

Cheers,
Jorge

From: Tom Callaway <tcallawa@redhat.com>
Sent: January 8, 2013 6:30 PM
To: jorge@iryoku.com
Subject: Question about Mesa MLAA license

Jorge,

Thanks for all of your fantastic graphics work! I have been auditing Fedora (a popular distribution of Linux) for license compliance and I came across your MLAA code in Mesa.

The license says:

* 2. Redistributions in binary form must reproduce the following statement:
   *
   * "Uses Jimenez's MLAA. Copyright (C) 2010 by Jorge Jimenez, Belen Masia, Jose I. Echevarria, Fernando Navarro and Diego Gutierrez."
   *
   * Only for use in the Mesa project, this point 2 is filled by naming the technique Jimenez's MLAA in the Mesa config options.

That wording is unclear. When you say "Only for use in the Mesa project...", it seems like you could either be saying:

- This code may only be used as part of Mesa.

OR

- In Mesa, you can comply with clause 2 by simply selecting "Jimenez's
MLAA” in the Mesa config options.

*****

If the first item is true, then we may have to remove the MLAA code from Fedora's copy of Mesa. However, looking at the license on your SMAA code, I do not believe it to be the case. Please let me know either way!

Thanks in advance,

Tom Callaway
Fedora Legal

==
Fedora Project
/* Legal characters in GLSL are:
 *
 * Identifier characters:
 *
 *   Letters a-z
 *   Letters A-Z
 *   Underscore
 *   Numbers 0-9
 *
 * Punctuation:
 *
 *   Period, plus, dash, slash, asterisk, percent, angled brackets,
 *   square brackets, parentheses, braces, caret, vertical bar,
 *   ampersand, tilde, equals, exclamation point, colon, semicolon,
 *   comma, and question mark
 *
 * Special:
 *
 *   Number sign (as used in preprocessor)
 *
 *   Backslash just before newline as line continuation
 *
 * White space:
 *
 *   Space, horizontal tab, vertical tab, form feed, carriage-return,
 *   and line-feed.
 *
 * [GLSL Language Specification 4.30.6, section 3.1]
 *
 * In this file, we test each of these in turn as follows:
 *
 *   Identifier characters: All pass through unchanged
 *   Punctuation: All pass through unchanged
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## -*-makefile-*-

## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%:o: $(srcdir)/%.c

%$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%:o: $(srcdir)/%.cpp

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$SHELL -ec '$(GEN_DEPS.c) $< \
| sed '"s/\(.*\)\.*\}$/\1.o $@ : /g'" > $@; \n[-s $@ ]|| rm -f $@'

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$SHELL -ec '$(GEN_DEPS.cc) $< \
| sed '"s/\(.*\)\.*\}$/\1.o $@ : /g'" > $@; \n[-s $@ ]|| rm -f $@'

## Versioned libraries rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s {<F}.$(SO).$(SO).$(SO_TARGET_VERSION_MAJOR) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library ‘s’
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

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Contributors (in approximate order of appearance)

[See also the ChangeLog file where individuals are attributed in log entries. Likewise in the FAQ file.]

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 First implementation of read-write locks.
Bug fix in condition variables.

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Bug fixes to dll loading.

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Implementation of async cancelation.

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Bug fixes.

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Bug fixes.

Jef Gearhart

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Mutex enhancements.

Steven Reddies

Bug fix.

Alexander Terekhov

Re-implemented and improved read-write locks;

(with Louis Thomas) re-implemented and improved condition variables;

enhancements to semaphores;

new mutex implementation in 'futex' style;

suggested a robust implementation of pthread_once similar to that implemented by V.Kliathcko;

system clock change handling re CV timeouts;

bug fixes.

Thomas Pfaff

Changes to make C version usable with C++ applications;

re-implemented mutex routines to avoid Win32 mutexes and TryEnterCriticalSection;

procedure to fix Mingw32 thread-safety issues.

Franco Bez

procedure to fix Mingw32 thread-safety issues.

Louis Thomas

( with Alexander Terekhov) re-implemented and improved condition variables.

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Gottlob Frege
re-implemented pthread_once (version 2)
(pthread_once cancellation added by rpj).
Vladimir Kliatchkov
reimplemented pthread_once with the same form
as described by A.Terekhov (later version 2);
implementation of MCS (Mellor-Crummey/Scott) locks.
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# --------------------------------------------------------------

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ICU uses the public domain data and code derived from Time Zone
Database for its time zone support. The ownership of the TZ database
is explained in BCP 175: Procedure for Maintaining the Time Zone
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#
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Glenn Randers-Pehrson
glennrp at users.sourceforge.net
September 29, 2017

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docs/GPLv2.TXT
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src/base/fthash.c
src/base/md5.c
src/base/md5.h
src/bdf/bdf.c
src/bdf/bdf.h
src/bdf/bdfdrivr.c
src/bdf/bdfdrivr.h
src/bdf/bdferror.h
src/bdf/bdflib.c
src/bdf/module.mk
src/bdf/README
src/bdf/rules.mk
src/pcf/module.mk
src/pcf/pcf.c
src/pcf/pcf.h
src/pcf/pcfdrivr.c
src/pcf/pcfdrivr.h
src/pcf/pcferror.h
src/pcf/pcfread.c
src/pcf/pcfread.h
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src/pcf/rules.mk
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```

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1.259 zlib 1.2.5

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   */

1.260 ethtool 4.8 10.el7

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For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

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Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don’t assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

   a) The modified work must itself be a software library.

   b) You must cause the files modified to carry prominent notices
stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in
these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)
Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally
distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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Also add information on how to contact you by electronic and paper mail.

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Yoyodyne, Inc., hereby disclaims all copyright interest in the library 'Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!
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NOTE! This license does *not* cover the so-called "standalone"
applications that use U-Boot services by means of the jump table
provided by U-Boot exactly for this purpose - this is merely
considered normal use of U-Boot, and does *not* fall under the
heading of "derived work" -- see file Licenses/Exceptions for
details.

Also note that the GPL and the other licenses are copyrighted by
the Free Software Foundation and other organizations, but the
instance of code that they refer to (the U-Boot source code) is
copyrighted by me and others who actually wrote it.
-- Wolfgang Denk

Like many other projects, U-Boot has a tradition of including big
blocks of License headers in all files. This not only blows up the
source code with mostly redundant information, but also makes it very
difficult to generate License Clearing Reports. An additional problem
is that even the same licenses are referred to by a number of
slightly varying text blocks (full, abbreviated, different
indentation, line wrapping and/or white space, with obsolete address
information, ...) which makes automatic processing a nightmare.

To make this easier, such license headers in the source files will be
replaced with a single line reference to Unique License Identifiers
as defined by the Linux Foundation's SPDX project [1].

If a "SPDX-License-Identifier:" line references more than one Unique
License Identifier, then this means that the respective file can be
used under the terms of either of these licenses, i. e. with

SPDX-License-Identifier: GPL-2.0+ OR BSD-3-Clause

you can choose between GPL-2.0+ and BSD-3-Clause licensing.

We use the SPDX Unique License Identifiers here; these are available
at [2].

License identifier syntax

1. Placement:

The SPDX license identifier in U-Boot files shall be added at the first
possible line in a file which can contain a comment. For the majority
of files this is the first line, except for scripts which require the
'#!PATH_TO_INTERPRETER' in the first line. For those scripts the SPDX
identifier goes into the second line.
2. Style:

The SPDX license identifier is added in form of a comment. The comment style depends on the file type:

- **C source:** // SPDX-License-Identifier: <SPDX License Expression>
- **C header:** /* SPDX-License-Identifier: <SPDX License Expression> */
- **ASM:** /* SPDX-License-Identifier: <SPDX License Expression> */
- **scripts:** # SPDX-License-Identifier: <SPDX License Expression>
- **.rst:** .. SPDX-License-Identifier: <SPDX License Expression>
- **.dts[i]:** // SPDX-License-Identifier: <SPDX License Expression>

If a specific tool cannot handle the standard comment style, then the appropriate comment mechanism which the tool accepts shall be used. This is the reason for having the "/* */" style comment in C header files. There was build breakage observed with generated .lds files where 'ld' failed to parse the C++ comment. This has been fixed by now, but there are still older assembler tools which cannot handle C++ style comments.

3. Syntax:

A <SPDX License Expression> is either an SPDX short form license identifier found on the SPDX License List, or the combination of two SPDX short form license identifiers separated by "WITH" when a license exception applies. When multiple licenses apply, an expression consists of keywords "AND", "OR" separating sub-expressions and surrounded by "(" , ")" .

License identifiers for licenses like [L]GPL with the 'or later' option are constructed by using a "+" for indicating the 'or later' option:

// SPDX-License-Identifier: GPL-2.0+
// SPDX-License-Identifier: LGPL-2.1+

WITH should be used when there is a modifier to a license needed. For example, the linux kernel UAPI files use the expression:

// SPDX-License-Identifier: GPL-2.0 WITH Linux-syscall-note
// SPDX-License-Identifier: GPL-2.0+ WITH Linux-syscall-note

Other examples using WITH exceptions found in the linux kernel are:

// SPDX-License-Identifier: GPL-2.0 WITH mif-exception
Exceptions can only be used with particular License identifiers. The valid License identifiers are listed in the tags of the exception text file.

OR should be used if the file is dual licensed and only one license is to be selected. For example, some dtsi files are available under dual licenses:

// SPDX-License-Identifier: GPL-2.0 OR BSD-3-Clause

Examples from U-Boot for license expressions in dual licensed files:

// SPDX-License-Identifier: GPL-2.0 OR MIT
// SPDX-License-Identifier: GPL-2.0+ OR BSD-3-Clause

AND should be used if the file has multiple licenses whose terms all apply to use the file. For example, if code is inherited from another project and permission has been given to put it in U-Boot, but the original license terms need to remain in effect:

// SPDX-License-Identifier: (GPL-2.0 WITH Linux-syscall-note) AND MIT

Another other example where both sets of license terms need to be adhered to is:

// SPDX-License-Identifier: GPL-1.0+ AND LGPL-2.1+


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-----------------------------------------------

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- msvcr90.dll
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Version 2, June 1991
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ifndef TOP
 TOP = $(shell while ! test -e make.rules; do cd .. ; done; pwd)
export TOP
endif
include $(TOP)/make.rules

ROOTDIR ?= ../..

LDFLAGS=
ASFLAGS= -I/include -Wa,-mregnames

#NOTE: -DDEBUG only needed for debugging/tracing...
CFLAGS= -UDEBUG -m64 -I. -I/include -I/include/x86emu \
-Is(TOP)/clients/net-snks/include -Is(ROOTDIR)/include \
-Is(ROOTDIR)/lib/libc/include -O3 -nostdinc -fno-builtin \
-freestanding -Wall -Wno-unused

X86EMU_OBJS= debug.o decode.o fpu.o ops2.o ops.o prim_ops.o sys.o

%.o: %.S
  $(CC) $(ASFLAGS) -c -o $@ $^ 

%.o: %.c
  $(CC) $(CFLAGS) -c -o $@ $^ 

all: libx86emu.a

libx86emu.a: $(X86EMU_OBJS)
  $(AR) -rc $@ $^ 
  $(RANLIB) $@

clean:
  $(RM) *.o *.i *.s libx86emu.a

distclean: clean

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=======

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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/* $OpenBSD: syscall.h,v 1.101 2008/03/16 19:43:41 otto Exp $ */

/* System call numbers. */
* created from; OpenBSD: syscalls.master,v 1.90 2008/03/16 19:42:57 otto Exp */

#define TARGET_OPENBSD_NR_syscall 0
#define TARGET_OPENBSD_NR_exit 1
#define TARGET_OPENBSD_NR_fork 2
#define TARGET_OPENBSD_NR_read 3
#define TARGET_OPENBSD_NR_write 4
#define TARGET_OPENBSD_NR_open 5
#define TARGET_OPENBSD_NR_close 6
#define TARGET_OPENBSD_NR_wait4 7
#define TARGET_OPENBSD_NR_link 9
#define TARGET_OPENBSD_NR_unlink 10
#define TARGET_OPENBSD_NR_chdir 12
#define TARGET_OPENBSD_NR_fchdir 13
#define TARGET_OPENBSD_NR_mknod 14
#define TARGET_OPENBSD_NR_chmod 15
#define TARGET_OPENBSD_NR_chown 16
#define TARGET_OPENBSD_NR_break 17
#define TARGET_OPENBSD_NR_getpid 20
#define TARGET_OPENBSD_NR_mount 21
#define TARGET_OPENBSD_NRUnmount 22
#define TARGET_OPENBSD_NR_setuid 23
#define TARGET_OPENBSD_NR_getuid 24
#define TARGET_OPENBSD_NR_geteuid 25
#define TARGET_OPENBSD_NR_ptrace 26
#define TARGET_OPENBSD_NR_recvmsg 27
#define TARGET_OPENBSD_NR_sendmsg 28
#define TARGET_OPENBSD_NR_recvfrom 29
#define TARGET_OPENBSD_NR_accept 30
#define TARGET_OPENBSD_NR_getpeername 31
#define TARGET_OPENBSD_NR_getsockname 32
#define TARGET_OPENBSD_NR_access 33
#define TARGET_OPENBSD_NR_chflags 34
#define TARGET_OPENBSD_NR_fchflags 35
#define TARGET_OPENBSD_NR_sync 36
#define TARGET_OPENBSD_NR_kill 37
#define TARGET_OPENBSD_NR_getppid 39
#define TARGET_OPENBSD_NR_dup 41
#define TARGET_OPENBSD_NR_opipe 42
#define TARGET_OPENBSD_NR_getegid 43
#define TARGET_OPENBSD_NR_profil 44
#define TARGET_OPENBSD_NR_ktrace 45
#define TARGET_OPENBSD_NR_msgrcv      227
#define TARGET_OPENBSD_NR_shmat       228
#define TARGET_OPENBSD_NR_shmdt       230
#define TARGET_OPENBSD_NR_clock_gettime      232
#define TARGET_OPENBSD_NR_clock_settime       233
#define TARGET_OPENBSD_NR_clock_getres        234
#define TARGET_OPENBSD_NR_nanosleep   240
#define TARGET_OPENBSD_NR_minherit    250
#define TARGET_OPENBSD_NR_rfork       251
#define TARGET_OPENBSD_NR_poll        252
#define TARGET_OPENBSD_NR_issetugid   253
#define TARGET_OPENBSD_NR_lchown      254
#define TARGET_OPENBSD_NR_getsid      255
#define TARGET_OPENBSD_NR_pwritev     268
#define TARGET_OPENBSD_NR_poll       252
#define TARGET_OPENBSD_NR___semctl    295
#define TARGET_OPENBSD_NR___getcwd    304
#define TARGET_OPENBSD_NR_adjfreq     305
#define TARGET_OPENBSD_NR_getfsstat   306
#define TARGET_OPENBSD_NR_statfs 307
#define TARGET_OPENBSD_NR_fstatfs 308
#define TARGET_OPENBSD_NR_fhstatfs 309

/* syscall flags from machine/trap.h */

/* $OpenBSD: trap.h,v 1.4 2008/07/04 22:04:37 kettenis Exp $ */
/* $NetBSD: trap.h,v 1.4 1999/06/07 05:28:04 eeh Exp $ */

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 * 
 */
#define TARGET_OPENBSD_SYSCALL_G2RFLAG 0x400 /* on success, return to %g2 rather than npc */
#define TARGET_OPENBSD_SYSCALL_G7RFLAG 0x800 /* use %g7 as above (deprecated) */

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*    @(#)mman.h  8.2 (Berkeley) 1/9/95
*/

#define TARGET_FREEBSD_MAP_RESERVED0080 0x0080 /* previously misimplemented MAP_INHERIT */
#define TARGET_FREEBSD_MAP_RESERVED0100 0x0100 /* previously unimplemented MAP_NOEXTEND */
#define TARGET_FREEBSD_MAP_STACK        0x0400 /* region grows down, like a stack */
#define TARGET_FREEBSD_MAP_NOSYNC       0x0800 /* page to but do not sync underlying file */

#define TARGET_FREEBSD_MAP_FLAGMASK     0x1ff7


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*   @(#)mman.h      8.2 (Berkeley) 1/9/95
*#
#define TARGET_NETBSD_MAP_INHERIT   0x0080 /* region is retained after exec */
#define TARGET_NETBSD_MAP_TRYFIXED   0x0400 /* attempt hint address, even within break */
#define TARGET_NETBSD_MAP_WIRED      0x0800 /* mlock() mapping when it is established */
#define TARGET_NETBSD_MAP_STACK      0x2000 /* allocated from memory, swap space (stack) */
#define TARGET_NETBSD_MAP_FLAGMASK   0x3ff7

/*  $NetBSD: mman.h,v 1.11 1995/03/26 20:24:23 jtc Exp $    */

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 *
#define TARGET_OPENBSD_MAP_INHERIT 0x0080 /* region is retained after exec */
#define TARGET_OPENBSD_MAP_NOEXTEND 0x0100 /* for MAP_FILE, don't change file size */
#define TARGET_OPENBSD_MAP_TRYFIXED 0x0400 /* attempt hint address, even within heap */

#define TARGET_OPENBSD_MAP_FLAGMASK 0x17f7

// XXX
#define TARGET_BSD_MAP_FLAGMASK 0x3ff7

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A. HISTORY OF THE SOFTWARE
==========================

Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands
as a successor of a language called ABC. Guido remains Python’s
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us)
in Reston, Virginia where he released several versions of the
software.

In May 2000, Guido and the Python core development team moved to
BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope
Corporation, see http://www.zope.com). In 2001, the Python Software
Foundation (PSF, see http://www.python.org/psf/) was formed, a
non-profit organization created specifically to own Python-related
Intellectual Property. Zope Corporation is a sponsoring member of
the PSF.

All Python releases are Open Source (see http://www.opensource.org for
the Open Source Definition). Historically, most, but not all, Python
releases have also been GPL-compatible; the table below summarizes
the various releases.

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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\ *
void X86EMU_trace_regs (void) {
    if (DEBUG_TRACE()) {
        x86emu_dump_regs();
    }
    else {
        x86emu_dump_xregs();
    }
}

static void print_decoded_instruction (void) {
    if (DEBUG_DECODE() && ! DEBUG_DECODE_NOPRINT()) {
        printk("%-20s",buf1);
    }
    else {
        printk("%-20s",buf1);
    }
}

Index: debug.c

static void print_decoded_instruction (void) {
    if (DEBUG_DECODE() && ! DEBUG_DECODE_NOPRINT()) {
        printk("%-20s",buf1);
    }
    else {
        printk("%-20s",buf1);
    }
}

Index: ops2.c
DECODE_PRINTF2("%04x\n", target);
TRACE_AND_STEP();
-    if (cond)
+    if (cond) {
        M.x86.R_IP = (u16)target;
+}JMPPTRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " LONG COND ");
+    }
+    DECODE_CLEAR_SEGOVR();
+    END_OF_INSTR();
+}
+
+****************************************************************************
+REMARKS:
+Handles opcode 0x0f,0xC8-0xCF
+****************************************************************************/
+s32 x86emu_bswap(s32 reg)
+{
+    // perform the byte swap
+    s32 temp = reg;
+    reg = (temp & 0xFF000000) >> 24;
+    reg |= (temp & 0xFF0000) >> 8;
+    reg |= (temp & 0xFF00) << 8;
+    reg |= (temp & 0xFF) << 24;
+    return reg;
+}
+
+void x86emuOp2_bswap(u8 op2)
+{
+    /* byte swap 32 bit register */
+    START_OF_INSTR();
+    DECODE_PRINTF("BSWAPt");
+    switch (op2) {
+        case 0xc8:
+            DECODE_PRINTF("EAX\n");
+            M.x86.R_EAX = x86emu_bswap(M.x86.R_EAX);
+            break;
+        case 0xc9:
+            DECODE_PRINTF("ECX\n");
+            M.x86.R_ECX = x86emu_bswap(M.x86.R_ECX);
+            break;
+        case 0xca:
+            DECODE_PRINTF("EDX\n");
+            M.x86.R_EDX = x86emu_bswap(M.x86.R_EDX);
+            break;
+        case 0xcb:
+            DECODE_PRINTF("EBX\n");
+            M.x86.R_EBX = x86emu_bswap(M.x86.R_EBX);
+            break;
+        case 0xcc:
+            DECODE_PRINTF("ESI\n");
+            M.x86.R_ESI = x86emu_bswap(M.x86.R_ESI);
+            break;
+        case 0xcd:
+            DECODE_PRINTF("EDI\n");
+            M.x86.R_EDI = x86emu_bswap(M.x86.R_EDI);
+            break;
+    }
+ case 0xcc:
+   DECODE_PRINTF("ESP\n");
+   M.x86.R_ESP = x86emu_bswap(M.x86.R_ESP);
+   break;
+ case 0xcd:
+   DECODE_PRINTF("EBP\n");
+   M.x86.R_EBP = x86emu_bswap(M.x86.R_EBP);
+   break;
+ case 0xce:
+   DECODE_PRINTF("ESI\n");
+   M.x86.R_ESI = x86emu_bswap(M.x86.R_ESI);
+   break;
+ case 0xcf:
+   DECODE_PRINTF("EDI\n");
+   M.x86.R_EDI = x86emu_bswap(M.x86.R_EDI);
+   break;
+ }
+ TRACE_AND_STEP();
+ DECODE_CLEAR_SEGOVR();
+ END_OF_INSTR();

@@ -1702,14 +1763,14 @@
/*  0xc5 */ x86emuOp2_illegal_op,
/*  0xc6 */ x86emuOp2_illegal_op,
/*  0xc7 */ x86emuOp2_illegal_op,
-/*  0xc8 */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xc9 */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xca */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xcb */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xcc */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xcd */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xce */ x86emuOp2_illegal_op,  /* TODO: bswap */
-/*  0xcf */ x86emuOp2_illegal_op,  /* TODO: bswap */
+/*  0xc8 */ x86emuOp2_bswap,
+/*  0xc9 */ x86emuOp2_bswap,
+/*  0xca */ x86emuOp2_bswap,
+/*  0xcb */ x86emuOp2_bswap,
+/*  0xcc */ x86emuOp2_bswap,
+/*  0xcd */ x86emuOp2_bswap,
+/*  0xce */ x86emuOp2_bswap,
+/*  0xcf */ x86emuOp2_bswap,

/*  0xd0 */ x86emuOp2_illegal_op,
/*  0xd1 */ x86emuOp2_illegal_op,
Index: ops.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops.c,v
retrieving revision 1.1
diff -u -u -r1.1 ops.c
--- ops.c7 Sep 2007 10:01:21 -0000
+++ ops.c20 Mar 2008 16:52:00 -0000
@@ -1061,7 +1061,11 @@
    imm = (s8)fetch_byte_imm();
    DECODE_PRINTF2("PUSH\%d\n", imm);
    TRACE_AND_STEP();
-    push_word(imm);
+    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+        push_long(imm);
+    } else {
+        push_word(imm);
+    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -1256,8 +1260,10 @@
    target = (u16)(M.x86.R_IP + (s16)offset);
    DECODE_PRINTF2("%x\n", target);
    TRACE_AND_STEP();
-    if (cond)
+    if (cond) {
        M.x86.R_IP = target;
+    } else {
+        push_long(imm);
+    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -2516,9 +2522,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* move them until CX is ZERO. */
-        count = M.x86.R_CX;
+        /* move them until (E)CX is ZERO. */
+        count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
+        count = 0;
+    if (M.x86.mode & SYSMODE_32BIT_REP)
+        count = 0;
+    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
        while (count--) {
@@ -2526,6 +2534,8 @@
    store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, val);
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
+    if (M.x86.intr & INTR_HALTED)
+        break;
count = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    - /* move them until CX is ZERO. */
    - count = M.x86.R_CX;
    + /* move them until (E)CX is ZERO. */
    + count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
    M.x86.R_CX = 0;
+if (M.x86.mode & SYSMODE_32BIT_REP)
+    M.x86.R_ECX = 0;
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} while (count--) {
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
    + if (M.x86.intr & INTR_HALTED)
    +     break;
} DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2574,6 +2586,8 @@
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
    + if (M.x86.intr & INTR_HALTED)
    +     break;
} DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2598,16 +2612,21 @@
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
    + if (M.x86.intr & INTR_HALTED)
    +     break;
    if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && (ACCESS_FLAG(F_ZF) == 0) ) break;
    if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
+    if (M.x86.intr & INTR_HALTED)
+        break;
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
}
else {
  TRACE_AND_STEP();
  if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* REPE */
      /* move them until CX is ZERO. */
      - while (M.x86.R_CX != 0) {
          /* move them until (E)CX is ZERO. */
          + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
              if (M.x86.mode & SYSMODE_PREFIX_DATA) {
                  val1 = fetch_data_long(M.x86.R_SI);
                  val2 = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
              }
              if (M.x86.mode & SYSMODE_32BIT_REP)
                M.x86.R_ECX -= 1;
              else
                M.x86.R_CX -= 1;
              M.x86.R_SI += inc;
              M.x86.R_DI += inc;
              if (M.x86.intr & INTR_HALTED)
                break;
          }
          /* dont care whether REPE or REPNE */
          - while (M.x86.R_CX != 0) {
              /* move them until CX is ZERO. */
              + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
                  if (M.x86.mode & SYSMODE_PREFIX_REPE) && ACCESS_FLAG(F_ZF) == 0 ) break;
                  if (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
                  if (M.x86.intr & INTR_HALTED)
                    break;
              }
          }
          M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
      }
  }
  TRACE_AND_STEP();
  if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
      - while (M.x86.R_CX != 0) {
          /* move them until (E)CX is ZERO. */
          + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
              store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AL);
          }
        }
  }
}
+ break;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
@@ -2783,9 +2812,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
-    - count = M.x86.R_CX;
-    + /* move them until (E)CX is ZERO. */
+    + count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
+    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
+    }
    while (count--) {
@@ -2795,6 +2826,8 @@
          store_data_word_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AX);
    }
    M.x86.R_DI += inc;
+    if (M.x86.intr & INTR_HALTED)
+        break;
    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -2817,11 +2850,16 @@
    inc = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
-    - /* move them until CX is ZERO. */
-    - while (M.x86.R_CX != 0) {
-    +     /* move them until (E)CX is ZERO. */
+    +     while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
+      M.x86.R_AL = fetch_data_byte(M.x86.R_SI);
-      M.x86.R_CX -= 1;
+      if (M.x86.mode & SYSMODE_32BIT_REP)
+          M.x86.R_ECX -= 1;
+      else
+          M.x86.R_CX -= 1;
+      M.x86.R_SI += inc;
+      if (M.x86.intr & INTR_HALTED)
+          break;
+      }
    } else {
@@ -2859,9 +2897,11 @@
count = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    /* move them until CX is ZERO. */
    count = M.x86.R_CX;
    /* move them until (E)CX is ZERO. */
    count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
    M.x86.R_CX = 0;
+    if (M.x86.mode & SYSMODE_32BIT_REP)
+        M.x86.R_ECX = 0;
M.x86.mode &=- (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} while (count--) {
@@ -2871,6 +2911,8 @@
    M.x86.R_AX = fetch_data_word(M.x86.R_SI);
}
    M.x86.R_SI += inc;
+    if (M.x86.intr & INTR_HALTED)
+        break;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -2894,26 +2936,36 @@
    inc = 1;
    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
        /* REPE */
-        /* move them until CX is ZERO. */
-        while (M.x86.R_CX != 0) {
-            M.x86.R_CX -= 1;
-            if (M.x86.mode & SYSMODE_32BIT_REP)
-                M.x86.R_ECX -= 1;
-            else
-                M.x86.R_CX -= 1;
+        /* move them until (E)CX is ZERO. */
+        while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
+            val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
+            cmp_byte(M.x86.R_AL, val2);
+            if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+            else
+                M.x86.R_CX -= 1;
+            M.x86.R_DI += inc;
+            if (ACCESS_FLAG(F_ZF) == 0)
+                break;
+            if (M.x86.intr & INTR_HALTED)
+                break;
+        } M.x86.mode &=- SYSMODE_PREFIX_REPE;
} else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
    /* REPNE */
    /* move them until CX is ZERO. */
-    while (M.x86.R_CX != 0) {
-        /* move them until (E)CX is ZERO. */
-        while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
-            val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
-            cmp_byte(M.x86.R_AL, val2);
-            if (M.x86.mode & SYSMODE_32BIT_REP)
-                M.x86.R_ECX -= 1;
-            else
-                M.x86.R_CX -= 1;
-            M.x86.R_DI += inc;
-            if (ACCESS_FLAG(F_ZF) == 0)
-                break;
-            if (M.x86.intr & INTR_HALTED)
-                break;
-        }
-    }

/* move them until (E)CX is ZERO. */
while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
    cmp_byte(M.x86.R_AL, val2);
    if (M.x86.mode & SYSMODE_32BIT_REP)
        M.x86.R_ECX -= 1;
    else
        M.x86.R_CX -= 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF))
        break;  /* zero flag set means equal */
    if (M.x86.intr & INTR_HALTED)
        break;
}
M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
}
else {
    TRACE_AND_STEP();
    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
        /* REPE */
        /* move them until CX is ZERO. */
        while (M.x86.R_CX != 0) {
            if (M.x86.mode & SYSMODE_PREFIX_DATA) {
                val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
                cmp_long(M.x86.R_EAX, val);
            }
            M.x86.R_CX -= 1;
        }
        if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX -= 1;
        else
            M.x86.R_CX -= 1;
        M.x86.R_DI += inc;
        if (ACCESS_FLAG(F_ZF) == 0)
            break;
        if (M.x86.intr & INTR_HALTED)
            break;
    }
    M.x86.mode &= ~SYSMODE_PREFIX_REPE;
}
else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
    /* REPNE */
    /* move them until CX is ZERO. */
    while (M.x86.R_CX != 0) {
        if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX -= 1;
        else
            M.x86.R_CX -= 1;
        M.x86.R_DI += inc;
        if (ACCESS_FLAG(F_ZF) == 0)
            break;
        if (M.x86.intr & INTR_HALTED)
            break;
    }
    M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
}
else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
    /* REPNE */
    /* move them until CX is ZERO. */
    while (M.x86.R_CX != 0) {

/* move them until (E)CX is ZERO. */
while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_long(M.x86.R_EAX, val);
    } else {
        val = fetch_data_word_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_word(M.x86.R_AX, (u16)val);
    }
    M.x86.R_CX -= 1;
    if (M.x86.mode & SYSMODE_32BIT_REP)
        M.x86.R_ECX -= 1;
    else
        M.x86.R_CX -= 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF))
        break;          /* zero flag set means equal */
    if (M.x86.intr & INTR_HALTED)
        break;
}
M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
else {
    DECODE_PRINTF("RET\n");
    imm = fetch_word_imm();
    DECODE_PRINTF2("%x\n", imm);
-RETURN_TRACE("RET", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
+RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
    M.x86.R_SP += imm;
    DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
-RETURN_TRACE("RET", M.x86.saved_cs, M.x86.saved_ip);
+RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
    M.x86.R_SP += imm;
    DECODE_CLEAR_SEGOVR();
    DECODEPRINTF2("%x\n", imm);
-RETURN_TRACE("RETF", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
   M.x86.R_IP = pop_word();
   M.x86.R_CS = pop_word();
+RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
   M.x86.R_SP += imm;
   DECODE_CLEAR_SEGOVR();
   END_OF_INSTR();
@@ -3488,10 +3550,10 @@
{
    START_OF_INSTR();
    DECODE_PRINTF("RETFn");
-RETURN_TRACE("RETF", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
   M.x86.R_IP = pop_word();
   M.x86.R_CS = pop_word();
+RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
   DECODE_CLEAR_SEGOVR();
   END_OF_INSTR();
}
@@ -4020,8 +4082,11 @@
ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
- M.x86.R_CX -= 1;
- if (M.x86.R_CX != 0 && !ACCESS_FLAG(F_ZF)) /* CX != 0 and !ZF */
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+    M.x86.R_ECX -= 1;
+ else
+    M.x86.R_CX -= 1;
+ if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
!ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and !ZF */
    M.x86.R_IP = ip;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -4041,8 +4106,11 @@
ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
- M.x86.R_CX -= 1;
- if (M.x86.R_CX != 0 && ACCESS_FLAG(F_ZF)) /* CX != 0 and ZF */
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+    M.x86.R_ECX -= 1;
+ else
+    M.x86.R_CX -= 1;
+ if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and ZF */
    M.x86.R_IP = ip;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -4062,8 +4130,11 @@
ip += (s16) M.x86.R_IP;
DECODE_PRINTF2("%04x\n", ip);
TRACE_AND_STEP();
- M.x86.R_CX -= 1;
- if (M.x86.R_CX != 0)
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+ M.x86.R_ECX -= 1;
+ else
+ M.x86.R_CX -= 1;
+ if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0)  /* (E)CX != 0 */
M.x86.R_IP = ip;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -4085,8 +4156,10 @@
target = (u16)(M.x86.R_IP + offset);
DECODE_PRINTF2("%x\n", target);
TRACE_AND_STEP();
- if (M.x86.R_CX == 0)
+ if (M.x86.R_CX == 0) {
+ jmp_trace(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " CXZ ");
+ }
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
}
@@ -4213,6 +4286,7 @@
ip = (s16)fetch_word_imm();
ip += (s16)M.x86.R_IP;
DECODE_PRINTF2("%04x\n", ip);
+ jmp_trace(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " NEAR ");
TRACE_AND_STEP();
M.x86.R_IP = (u16)ip;
DECODE_CLEAR_SEGOVR();
@@ -4233,6 +4307,7 @@
cs = fetch_word_imm();
DECODE_PRINTF2("%04x:\", cs);
DECODE_PRINTF2("%04x\n", ip);
+ jmp_trace(M.x86.saved_cs, M.x86.saved_ip, cs, ip, " FAR ");
TRACE_AND_STEP();
M.x86.R_IP = ip;
M.x86.R_CS = cs;
@@ -4254,6 +4329,7 @@
offset = (s8)fetch_byte_imm();
target = (u16)(M.x86.R_IP + offset);
DECODE_PRINTF2("%x\n", target);
@@ -4357,6 +4433,8 @@
  DECODE_PRINTF("REPNE\n");
  TRACE_AND_STEP();
  M.x86.mode |= SYSMODE_PREFIX_REPNE;
+  if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+      M.x86.mode |= SYSMODE_32BIT_REP;
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
}
@@ -4371,6 +4449,8 @@
  DECODE_PRINTF("REPE\n");
  TRACE_AND_STEP();
  M.x86.mode |= SYSMODE_PREFIX_REPE;
+  if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+      M.x86.mode |= SYSMODE_32BIT_REP;
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
@@ -5013,12 +5093,14 @@
  break;
 case 4:         /* jmp word ptr ... */
     destval = fetch_data_word(destoffset);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, destval, " WORD ");
  TRACE_AND_STEP();
  M.x86.R_IP = destval;
  break;
 case 5:         /* jmp far ptr ... */
     destval = fetch_data_word(destoffset);
     destval2 = fetch_data_word(destoffset + 2);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, destval2, destval, " FAR ");
  TRACE_AND_STEP();
  M.x86.R_IP = destval;
  M.x86.R_CS = destval2;
Index: prim_ops.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/prim_ops.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- prim_ops.c7 Sep 2007 10:01:21 -0000
+++ prim_ops.c16 Jan 2008 14:18:15 -0000
@@ -1921,7 +1921,7 @@
void imul_long_direct(u32 *res_lo, u32* res_hi,u32 d, u32 s)
{
#ifdef __HAS_LONG_LONG__
-    s64 res = (s64)d * (s64)s;
+    s64 res = (s64)(s32)d * (s64)(s32)s;

    *res_lo = (u32)res;
    *res_hi = (u32)(res >> 32);
#endif

void mul_long(u32 s)
{
    #ifdef __HAS_LONG_LONG__
    -    u64 res = (u32)M.x86.R_EAX * (u32)s;
+    u64 res = (u64)M.x86.R_EAX * s;
    M.x86.R_EAX = (u32)res;
    M.x86.R_EDX = (u32)(res >> 32);
    #endif
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* in until CX is ZERO. */
-        u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
+        /* in until (E)CX is ZERO. */
+        u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
M.x86.R_ECX : M.x86.R_CX);
-        while (count--) {
-            single_in(size);
            M.x86.R_DI += inc;
        }
        M.x86.R_CX = 0;
    -    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+    if (M.x86.mode & SYSMODE_32BIT_REP) {
        M.x86.R_ECX = 0;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    @- -2355,15 +2354,15 @@
    }
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* out until CX is ZERO. */
-        u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
+        /* out until (E)CX is ZERO. */
+        u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
M.x86.R_ECX : M.x86.R_CX);
    while (count--) {
        single_out(size);
        M.x86.R_SI += inc;
    }
}
M.x86.R_CX = 0;
-        if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+        if (M.x86.mode & SYSMODE_32BIT_REP) {
            M.x86.R_ECX = 0;
        }
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);

Index: sys.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/sys.c,v
retrieving revision 1.1
retrieving revision 1.2
diff -u -u -r1.1 -r1.2
--- sys.c 7 Sep 2007 10:01:21 -0000 1.1
+++ sys.c 7 Sep 2007 10:03:13 -0000 1.2
@@ -45,11 +45,6 @@
 #include <x86emu/regs.h>
 #include "debug.h"
 #include "prim_ops.h"
-#ifdef LINUXBIOS_VERSION
-    #include "io.h"
-#else
-    #include <sys/io.h>
-#else
-    #include <sys/io.h>
-    #endif

#ifdef IN_MODULE
    #include "xf86_ansic.h"
@@ -220,7 +215,7 @@
 {
-DB(if (DEBUG_IO_TRACE()))
-printk("inb %#04x \n", addr);
-    return inb(addr);
+    return 0;
 }*/

@@ -235,7 +230,7 @@
 {
-DB(if (DEBUG_IO_TRACE()))
-printk("inw %#04x \n", addr);
-    return inw(addr);
+    return 0;
 }*/

@@ -250,7 +245,7 @@
 {
-DB(if (DEBUG_IO_TRACE()))
-printk("inl %#04x \n", addr);

/**********************************************************************************/
@@ -235,7 +230,7 @@
 {
-DB(if (DEBUG_IO_TRACE()))
-printk("inw %#04x \n", addr);
-    return inw(addr);
+    return 0;
 }*/

/**********************************************************************************/
@@ -250,7 +245,7 @@
 {
-DB(if (DEBUG_IO_TRACE()))
-printk("inl %#04x \n", addr);
return inl(addr);
+ return 0;
}

/***************************************************************************/
@@ -264,7 +259,6 @@
{
 DB(if (DEBUG_IO_TRACE())
 printk("outb %#02x -> %#04x \n", val, addr);
-outb(val, addr);
 return;
}

@@ -279,7 +273,6 @@
{
 DB(if (DEBUG_IO_TRACE())
 printk("outw %#04x -> %#04x \n", val, addr);
-outw(val, addr);
 return;
}

@@ -295,7 +288,6 @@
 DB(if (DEBUG_IO_TRACE())
 printk("outl %#08x -> %#04x \n", val, addr);
-outl(val, addr);
 return;
}

@@ -405,6 +397,6 @@
void X86EMU_setMemBase(void *base, size_t size)
{
- M.mem_base = (int) base;
+ M.mem_base = (unsigned long) base;
 M.mem_size = size;
}
Index: include/x86emu/debug.h
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/debug.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/debug.h 7 Sep 2007 10:01:21 -0000
+++ include/x86emu/debug.h 20 Mar 2008 15:25:27 -0000
@@ -40,8 +40,6 @@
#ifndef __X86EMU_DEBUG_H
#define __X86EMU_DEBUG_H

--- include/x86emu/debug.h 7 Sep 2007 10:01:21 -0000
+++ include/x86emu/debug.h 20 Mar 2008 15:25:27 -0000
@@ -40,8 +40,6 @@
#ifndef __X86EMU_DEBUG_H
#define __X86EMU_DEBUG_H

Index: include/x86emu/debug.h
#define DEBUG 0
#undef DEBUG

/*------------------ Macros and type definitions ------------------*/

/* checks to be enabled for "runtime" */
@@ -78,6 +76,8 @@
#define DEBUG_SYSINT()        (M.x86.debug & DEBUG_SYSINT_F)
#define DEBUG_TRACECALL()     (M.x86.debug & DEBUG_TRACECALL_F)
#define DEBUG_TRACECALLREGS() (M.x86.debug & DEBUG_TRACECALL_REGS_F)
+#define DEBUG_TRACEJMP()       (M.x86.debug & DEBUG_TRACEJMP_F)
+#define DEBUG_TRACEJMPREGS()   (M.x86.debug & DEBUG_TRACEJMP_REGS_F)
#define DEBUG_SYS()           (M.x86.debug & DEBUG_SYS_F)
#define DEBUG_MEM_TRACE()     (M.x86.debug & DEBUG_MEM_TRACE_F)
#define DEBUG_IO_TRACE()      (M.x86.debug & DEBUG_IO_TRACE_F)
@@ -96,6 +96,8 @@
#define DEBUG_SYSINT()        0
#define DEBUG_TRACECALL()     0
#define DEBUG_TRACECALLREGS() 0
+#define DEBUG_TRACEJMP()       0
+#define DEBUG_TRACEJMPREGS()   0
#define DEBUG_SYS()           0
#define DEBUG_MEM_TRACE()     0
#define DEBUG_IO_TRACE()      0
@@ -169,14 +171,20 @@
  x86emu_dump_regs();                                     \
  if (DEBUG_TRACECALL())                                     \
    printk("%04x:%04x: CALL %s%04x:%04x\n", u , v, s, w, x);
-# define RETURN_TRACE(n,u,v)
+## define RETURN_TRACE(u,v,w,x,s)
    if (DEBUG.TraceCALLREGS())									
      x86emu_dump_regs();                                     \
    if (DEBUG_TRACECALL())                                     \
      printf("%04x:%04x: %s\n",u,v,n);
+printf("%04x:%04x: RET %s %04x:%04x\n",u,v,s,w,x);
+## define JMP_TRACE(u,v,w,x,s)
  +  if (DEBUG_TRACEJMPREGS()) \
  +     x86emu_dump_regs();\ 
  +  if (DEBUG_TRACEJMP())\ 
  +     printk("%04x:%04x: JMP %s%04x:%04x\n", u , v, s, w, x);
#else
  # define CALL_TRACE(u,v,w,x,s)
  -# define RETURN_TRACE(n,u,v)
  +# define RETURN_TRACE(u,v,w,x,s)
  += define JMP_TRACE(u,v,w,x,s)
# endif

#endif DEBUG
Index: include/x86emu/regs.h

===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/regs.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/regs.h	7 Sep 2007 10:01:21 -0000	1.1
+++ include/x86emu/regs.h	15 Jan 2008 13:46:40 -0001.4
@@ -231,6 +231,9 @@
#define SYSMODE_PREFIX_REPNE    0x00000100
#define SYSMODE_PREFIX_DATA     0x00000200
#define SYSMODE_PREFIX_ADDR     0x00000400
+//phueper: for REP(E|NE) Instructions, we need to decide whether it should be using
+//the 32bit ECX register as or the 16bit CX register as count register
+#define SYSMODE_32BIT_REP       0x00000800
#define SYSMODE_INTR_PENDING    0x10000000
#define SYSMODE_EXTRN_INTR      0x20000000
#define SYSMODE_HALTED          0x40000000
@@ -250,7 +253,8 @@
SYSMODE_SEGOVR_GS      | \ 
SYSMODE_SEGOVR_SS      | \ 
SYSMODE_PREFIX_DATA    | \ 
- SYSMODE_PREFIX_ADDR)
+ SYSMODE_PREFIX_ADDR    | \ 
+ SYSMODE_32BIT_REP)

#define INTR_SYNCH           0x1
#define INTRASYNCH           0x2
@@ -274,9 +278,9 @@
*/
  u32 mode;
  volatile int intr; /* mask of pending interrupts */
-int debug;
+ volatile int debug;
#endif DEBUG
-int check;
+ int check;
  u16 saved_ip;
  u16 saved_cs;
  int enc_pos;
Index: include/x86emu/x86emu.h

===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/x86emu.h,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- include/x86emu/x86emu.h.7 Sep 2007 10:01:21 -0001.1
+++ include/x86emu/x86emu.h.19 Oct 2007 08:42:15 -0000.3
```c
#include <console.h>
#define printk(x...) printk(BIOS_DEBUG, x)
#else
#define printk printf
#endif

#define DEBUG_TRACECALL_REGS_F  0x004000
#define DEBUG_DECODE_NOPRINT_F  0x008000
#define DEBUG_SAVE_IP_CS_F      0x010000
#define DEBUG_TRACEJMP_REGS_F   0x040000
#define DEBUG_SYS_F             (DEBUG_SVC_F|DEBUG_FS_F|DEBUG_PROC_F)

void X86EMU_trace_regs(void);

/* $NetBSD: syscall.h,v 1.215 2008/06/17 16:07:57 tsutsui Exp $ */

/* System call numbers. */
#define TARGET_NETBSD_NR_syscall     0
#define TARGET_NETBSD_NR_exit        1
#define TARGET_NETBSD_NR_fork        2
#define TARGET_NETBSD_NR_read        3
#define TARGET_NETBSD_NR_write       4
#define TARGET_NETBSD_NR_open        5
#define TARGET_NETBSD_NR_close       6
#define TARGET_NETBSD_NR_wait4       7
#define TARGET_NETBSD_NR_compat_43_ocreat    8
#define TARGET_NETBSD_NR_link        9
#define TARGET_NETBSD_NR_unlink      10
#define TARGET_NETBSD_NR_chdir       12
#define TARGET_NETBSD_NR_fchdir      13
#define TARGET_NETBSD_NR_mknod       14
#define TARGET_NETBSD_NR_chmod       15
#define TARGET_NETBSD_NR_chown       16
#define TARGET_NETBSD_NR_break       17
```
#define TARGET_NETBSD_NR_compat_20_getfsstat 18
#define TARGET_NETBSD_NR_compat_43_olseek 19
#define TARGET_NETBSD_NR_getpid 20
#define TARGET_NETBSD_NR_getpid 20
#define TARGET_NETBSD_NR_compat_40_mount 21
#define TARGET_NETBSD_NRUnmount 22
#define TARGET_NETBSD_NR_setuid 23
#define TARGET_NETBSD_NR_getuid 24
#define TARGET_NETBSD_NR_getuid 24
#define TARGET_NETBSD_NR_compat_43_lstat43 25
#define TARGET_NETBSD_NR_geteuid 26
#define TARGET_NETBSD_NR_recvmsg 27
#define TARGET_NETBSD_NR_recvmsg 28
#define TARGET_NETBSD_NR_recvfrom 29
#define TARGET_NETBSD_NR_accept 30
#define TARGET_NETBSD_NR_getpeername 31
#define TARGET_NETBSD_NR_getsockname 32
#define TARGET_NETBSD_NR_access 33
#define TARGET_NETBSD_NR_chflags 34
#define TARGET_NETBSD_NR_fchflags 35
#define TARGET_NETBSD_NR_sync 36
#define TARGET_NETBSD_NR_kill 37
#define TARGET_NETBSD_NR_compat_43_stat43 38
#define TARGET_NETBSD_NR_getppid 39
#define TARGET_NETBSD_NR_compat_43_lstat43 40
#define TARGET_NETBSD_NR_dup 41
#define TARGET_NETBSD_NR_pipe 42
#define TARGET_NETBSD_NR_getegid 43
#define TARGET_NETBSD_NR_getegid 43
#define TARGET_NETBSD_NR_getegid 43
#define TARGET_NETBSD_NR_getegid 43
#define TARGET_NETBSD_NR_setuid 44
#define TARGET_NETBSD_NR_ktrace 45
#define TARGET_NETBSD_NR_compat_13_sigaction13 46
#define TARGET_NETBSD_NR_getgid 47
#define TARGET_NETBSD_NR_getgid 47
#define TARGET_NETBSD_NR_getgid 47
#define TARGET_NETBSD_NR_getgid 47
#define TARGET_NETBSD_NR___getlogin 48
#define TARGET_NETBSD_NR___setlogin 49
#define TARGET_NETBSD_NR_acct 50
#define TARGET_NETBSD_NR_acct 51
#define TARGET_NETBSD_NR_compat_13_getloginmask 52
#define TARGET_NETBSD_NR_compat_13_sigprocmask13 53
#define TARGET_NETBSD_NR_compat_12_oreboot 54
#define TARGET_NETBSD_NR_revoke 55
#define TARGET_NETBSD_NR_revoke 56
#define TARGET_NETBSD_NR_replace 57
#define TARGET_NETBSD_NR_replace 58
#define TARGET_NETBSD_NR_replace 58
#define TARGET_NETBSD_NR_replace 58
#define TARGET_NETBSD_NR_setuid 59
#define TARGET_NETBSD_NR_umask 60
#define TARGET_NETBSD_NR_chroot 61
#define TARGET_NETBSD_NR_compat_43_fstat43 62
#define TARGET_NETBSD_NR_compat_43_ogetkerninfo 63
#define TARGET_NETBSD_NR_compat_43_ogetpagesize 64
#define TARGET_NETBSD_NR_compat_12_msync 65
#define TARGET_NETBSD_NR_vfork 66
#define TARGET_NETBSD_NR_sbrk 69
#define TARGET_NETBSD_NR_sstk 70
#define TARGET_NETBSD_NR_compat_43_ommap 71
#define TARGET_NETBSD_NR_vadvise 72
#define TARGET_NETBSD_NR_munmap 73
#define TARGET_NETBSD_NR_mprotect 74
#define TARGET_NETBSD_NR_madvise 75
#define TARGET_NETBSD_NR_mincore 78
#define TARGET_NETBSD_NR_getgroups 79
#define TARGET_NETBSD_NR_setgroups 80
#define TARGET_NETBSD_NR_getpgrp 81
#define TARGET_NETBSD_NR_setpgid 82
#define TARGET_NETBSD_NR_setitimer 83
#define TARGET_NETBSD_NR_compat_43_owait 84
#define TARGET_NETBSD_NR_compat_12_oswapon 85
#define TARGET_NETBSD_NR_getitimer 86
#define TARGET_NETBSD_NR_compat_43_owait 87
#define TARGET_NETBSD_NR_compat_43_osethostname 88
#define TARGET_NETBSD_NR_compat_43_ogetdtablesize 89
#define TARGET_NETBSD_NR_dup2 90
#define TARGET_NETBSD_NR_fcntl 92
#define TARGET_NETBSD_NR_select 93
#define TARGET_NETBSD_NR_fsync 95
#define TARGET_NETBSD_NR_setpriority 96
#define TARGET_NETBSD_NR_compat_30_socket 97
#define TARGET_NETBSD_NR_connect 98
#define TARGET_NETBSD_NR_compat_43_accept 99
#define TARGET_NETBSD_NR_getpriority 100
#define TARGET_NETBSD_NR_compat_43_osend 101
#define TARGET_NETBSD_NR_compat_43_orecv 102
#define TARGET_NETBSD_NR_compat_13_sigreturn13 103
#define TARGET_NETBSD_NR_bind 104
#define TARGET_NETBSD_NR_setsockopt 105
#define TARGET_NETBSD_NR_listen 106
#define TARGET_NETBSD_NR_compat_43_osigvec 108
#define TARGET_NETBSD_NR_compat_43_osigblock 109
#define TARGET_NETBSD_NR_compat_43_osigsetmask 110
#define TARGET_NETBSD_NR_compat_13_sigsuspend13 111
#define TARGET_NETBSD_NR_compat_43_osigstack 112
#define TARGET_NETBSD_NR_compat_43_orecvmsg 113
#define TARGET_NETBSD_NR_compat_43_osendmsg 114
#define TARGET_NETBSD_NR_gettimeofday 116
#define TARGET_NETBSD_NR_getrusage 117
#define TARGET_NETBSD_NR_getsockopt 118
#define TARGET_NETBSD_NR_readv 120
#define TARGET_NETBSD_NR_writev 121
#define TARGET_NETBSD_NR_settimeofday 122
#define TARGET_NETBSD_NR_fchown 123
#define TARGET_NETBSD_NR_fchmod 124
#define TARGET_NETBSD_NR_compat_43_orecvfrom 125
#define TARGET_NETBSD_NR_setreuid 126
#define TARGET_NETBSD_NR_setregid 127
#define TARGET_NETBSD_NR_rename 128
#define TARGET_NETBSD_NR_compat_43_otruncate 129
#define TARGET_NETBSD_NR_compat_43_oftruncate 130
#define TARGET_NETBSD_NR_flock 131
#define TARGET_NETBSD_NR_mknod 132
#define TARGET_NETBSD_NR_sendto 133
#define TARGET_NETBSD_NR_shutdown 134
#define TARGET_NETBSD_NR_socketpair 135
#define TARGET_NETBSD_NR_mkdir 136
#define TARGET_NETBSD_NR_rmdir 137
#define TARGET_NETBSD_NR_utimes 138
#define TARGET_NETBSD_NR_adjtime 140
#define TARGET_NETBSD_NR_compat_43_ogetpeername 141
#define TARGET_NETBSD_NRCompat_43_ogethostid 142
#define TARGET_NETBSD_NRCompat_43_osethostid 143
#define TARGET_NETBSD_NRCompat_43_osetrlimit 144
#define TARGET_NETBSD_NRCompat_43_ogetrlimit 145
#define TARGET_NETBSD_NR_compat_43_okillpg 146
#define TARGET_NETBSD_NR_setsid 147
#define TARGET_NETBSD_NR_quotactl 148
#define TARGET_NETBSD_NR_compat_43_oquota 149
#define TARGET_NETBSD_NR_compat_43_ogetsockname 150
#define TARGET_NETBSD_NR_nfssvc 155
#define TARGET_NETBSD_NR_compat_43_ogetdirentries 156
#define TARGET_NETBSD_NR_compat_20_statfs 157
#define TARGET_NETBSD_NR_compat_20_fstatfs 158
#define TARGET_NETBSD_NR_compat_30_getfh 159
#define TARGET_NETBSD_NR_compat_09_ogetdomainname 160
#define TARGET_NETBSD_NR_compat_09_osetdomainname 161
#define TARGET_NETBSD_NR_compat_09_ouname 162
#define TARGET_NETBSD_NR_sysarch 165
#define TARGET_NETBSD_NR_compat_10_osemsys 169
#define TARGET_NETBSD_NR_compat_10_omsgsys 170
#define TARGET_NETBSD_NR_compat_10_oshmsys 171
#define TARGET_NETBSD_NR_pread 173
#define TARGET_NETBSD_NR_pwrite 174
#define TARGET_NETBSD_NR_compat_30_ntp_gettime 175
#define TARGET_NETBSD_NR_ntp_adjtime 176
#define TARGET_NETBSD_NR_setgid 181
#define TARGET_NETBSD_NR_setegid 182
#define TARGET_NETBSD_NR_seteuid 183
#define TARGET_NETBSD_NR_lfs_bmapv 184
#define TARGET_NETBSD_NR_lfs_markv 185
#define TARGET_NETBSD_NR_lfs_segeclean 186
#define TARGET_NETBSD_NR_lfs_segwait 187
#define TARGET_NETBSD_NR_compat_12_stat12 188
#define TARGET_NETBSD_NR_compat_12_fstat12 189
#define TARGET_NETBSD_NR_compat_12_lstat12 190
#define TARGET_NETBSD_NR_pathconf 191
#define TARGET_NETBSD_NR_fpathconf 192
#define TARGET_NETBSD_NR_getrlimit 193
#define TARGET_NETBSD_NR_setrlimit 194
#define TARGET_NETBSD_NR_compat_12_getdirentries 195
#define TARGET_NETBSD_NR_mmap 196
#define TARGET_NETBSD_NR___syscall 197
#define TARGET_NETBSD_NR___syscall 198
#define TARGET_NETBSD_NR_lseek 199
#define TARGET_NETBSD_NR_truncate 200
#define TARGET_NETBSD_NR_ftruncate 201
#define TARGET_NETBSD_NR___sysctl 202
#define TARGET_NETBSD_NR_mlock 203
#define TARGET_NETBSD_NR_munlock 204
#define TARGET_NETBSD_NR_undelete 205
#define TARGET_NETBSD_NR_futimes 206
#define TARGET_NETBSD_NR_getpgid 207
#define TARGET_NETBSD_NR_reboot 208
#define TARGET_NETBSD_NR_poll 209
#define TARGET_NETBSD_NR_compat_14___semctl 220
#define TARGET_NETBSD_NR_semget 221
#define TARGET_NETBSD_NR_semop 222
#define TARGET_NETBSD_NR_semconfig 223
#define TARGET_NETBSD_NR_compat_14_msgctl 224
#define TARGET_NETBSD_NR_msgget 225
#define TARGET_NETBSD_NR_msgsnd 226
#define TARGET_NETBSD_NR_msgrcv 227
#define TARGET_NETBSD_NR_shmat 228
#define TARGET_NETBSD_NR_compat_14_shmctl 229
#define TARGET_NETBSD_NR_shmdt 230
#define TARGET_NETBSD_NR shmget 231
#define TARGET_NETBSD_NR_clock_gettime 232
#define TARGET_NETBSD_NR_clock_settime 233
#define TARGET_NETBSD_NR_clock_getres 234
#define TARGET_NETBSD_NR_timer_create 235
#define TARGET_NETBSD_NR_timer_delete 236
#define TARGET_NETBSD_NR_timer_settime 237
#define TARGET_NETBSD_NR_timer_gettime 238
#define TARGET_NETBSD_NR_timer_getoverrun 239
#define TARGET_NETBSD_NR_nanosleep 240
#define TARGET_NETBSD_NR_fdatasync 241
#define TARGET_NETBSD_NR_mlockall 242
#define TARGET_NETBSD_NR_munlockall 243
#define TARGET_NETBSD_NR___sigtimedwait 244
#define TARGET_NETBSD_NR_modctl 246
#define TARGET_NETBSD_NR___ksem_init 247
#define TARGET_NETBSD_NR___ksem_open 248
#define TARGET_NETBSD_NR___ksem_unlink 249
#define TARGET_NETBSD_NR___ksem_close 250
#define TARGET_NETBSD_NR___ksem_post 251
#define TARGET_NETBSD_NR___ksem_wait 252
#define TARGET_NETBSD_NR___ksem_trywait 253
#define TARGET_NETBSD_NR___ksem_getvalue 254
#define TARGET_NETBSD_NR___ksem_destroy 255
#define TARGET_NETBSD_NR mq_open 257
#define TARGET_NETBSD_NR mq_close 258
#define TARGET_NETBSD_NR mq_unlink 259
#define TARGET_NETBSD_NR mq_getattr 260
#define TARGET_NETBSD_NR mq_setattr 261
#define TARGET_NETBSD_NR mq_notify 262
#define TARGET_NETBSD_NR mq_send 263
#define TARGET_NETBSD_NR mq_receive 264
#define TARGET_NETBSD_NR mq_timedsend 265
#define TARGET_NETBSD_NR mq_timedreceive 266
#define TARGET_NETBSD_NR___posix_rename 270
#define TARGET_NETBSD_NR swapctl 271
#define TARGET_NETBSD_NR compat_30_getdents 272
#define TARGET_NETBSD_NR minherit 273
#define TARGET_NETBSD_NR lchmod 274
#define TARGET_NETBSD_NR lchown 275
#define TARGET_NETBSD_NR lutimes 276
#define TARGET_NETBSD_NR___msync13 277
#define TARGET_NETBSD_NR compat_30_stat 278
#define TARGET_NETBSD_NR compat_30_fstat 279
#define TARGET_NETBSD_NR compat_30_lstat 280
#define TARGET_NETBSD_NR___sigaltstack14 281
#define TARGET_NETBSD_NR___vfork14 282
#define TARGET_NETBSD_NR posix_chown 283
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#define TARGET_NETBSD_NR posix_lchown 285
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 * FreeBSD strace list
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{ TARGET_FREEBSD_NR___acl_check_fd, "__acl_check_fd", "%s(%d, %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_check_file, "__acl_check_file", "%s(""%s\"", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_check_link, "__acl_check_link", "%s(""%s\"", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_fd, "__acl_delete_fd", "%s(%d, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_file, "__acl_delete_file", "%s(""%s\"", %d)", NULL, NULL },
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TARGET_FREEBSD_NR_revoke, "revoke", NULL, NULL },
TARGET_FREEBSD_NR_rfork, "rfork", NULL, NULL },
TARGET_FREEBSD_NR_rmdir, "rmdir", NULL, NULL },
TARGET_FREEBSD_NR_rtprio_thread, "rtprio_thread", "%s(%d, %d, %p)", NULL, NULL },
TARGET_FREEBSD_NR_sbrk, "sbrk", NULL, NULL, NULL },
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TARGET_FREEBSD_NR_semop, "semop", NULL, NULL, NULL },
TARGET_FREEBSD_NR_sendmsg, "sendmsg", NULL, NULL, NULL },
TARGET_FREEBSD_NR_sendto, "sendto", NULL, NULL, NULL },
TARGET_FREEBSD_NR_setcontext, "setcontext", "%s(%#x)", NULL, NULL, NULL },
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TARGET_FREEBSD_NR_setegid, "setegid", NULL, NULL, NULL },
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TARGET_FREEBSD_NR_setitimer, "setitimer", NULL, NULL, NULL },
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TARGET_FREEBSD_NR_setreuid, "setreuid", NULL, NULL, NULL },
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TARGET_FREEBSD_NR_signalstack, "signalstack", "%s(%p,%p)", NULL, NULL },
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TARGET_FREEBSD_NR_sigprocmask, "sigprocmask", NULL, NULL, NULL },
TARGET_FREEBSD_NR_sigreturn, "sigreturn", NULL, NULL, NULL },
TARGET_FREEBSD_NR_sigsuspend, "sigsuspend", NULL, NULL, NULL },
TARGET_FREEBSD_NR_socket, "socket", "%s(%d,%d,%d)", NULL, NULL, NULL },
TARGET_FREEBSD_NR_socketpair, "socketpair", NULL, NULL, NULL },
TARGET_FREEBSD_NR_sstk, "sstk", NULL, NULL, NULL },
TARGET_FREEBSD_NR_stat, "stat", "%s(\"%s\",%p)", NULL, NULL },
TARGET_FREEBSD_NR_stats, "stats", "%s(\"%s\",%p)", NULL, NULL },
TARGET_FREEBSD_NR_symlink, "symlink", "%s(\"%s\",\"%s\")", NULL, NULL, NULL },
TARGET_FREEBSD_NR_sync, "sync", NULL, NULL, NULL },
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# *****************************************************************************/

ifndef TOP
TOP = $(shell while ! test -e make.rules; do cd .. ; done; pwd)
export TOP
endif
include $(TOP)/make.rules

SUBDIRS=
ifeq ($(SNK_BIOSEMU_APPS), 1)
SUBDIRS += x86emu
endif
CLEANSUBDIRS = $(SUBDIRS)

all :
for subdir in $(SUBDIRS) ; do $(MAKE) -C $${subdir} || exit 1 ; done

# Common targets for all subdirectories:
clean distclean depend:
for subdir in $(CLEANSUBDIRS) ; do $(MAKE) -C $${subdir} $@ ; done
/*
 * System call numbers.
 * * created from FreeBSD: releng/9.1/sys/kern/syscalls.master 229723
#define TARGET_FREEBSD_NR_syscall   0
#define TARGET_FREEBSD_NR_exit  1
#define TARGET_FREEBSD_NR_fork  2
#define TARGET_FREEBSD_NR_read  3
#define TARGET_FREEBSD_NR_write 4
#define TARGET_FREEBSD_NR_open  5
#define TARGET_FREEBSD_NR_wait4 7
/* 8 is old creat */
#define TARGET_FREEBSD_NR_link  9
#define TARGET_FREEBSD_NR_unlink 10
   /* 11 is obsolete execv */
#define TARGET_FREEBSD_NR_chdir 12
#define TARGET_FREEBSD_NR_fchdir 13
#define TARGET_FREEBSD_NR_mknod 14
#define TARGET_FREEBSD_NR_chmod 15
#define TARGET_FREEBSD_NR_chown 16
#define TARGET_FREEBSD_NR_break 17
#define TARGET_FREEBSD_NR_freebsd4_getfsstat 18
   /* 19 is old lseek */
#define TARGET_FREEBSD_NR_getpid  20
#define TARGET_FREEBSD_NR_mount 21
#define TARGET_FREEBSD_NR_unmount 22
#define TARGET_FREEBSD_NR_setuid 23
#define TARGET_FREEBSD_NR_getuid 24
#define TARGET_FREEBSD_NR_geteuid 25
#define TARGET_FREEBSD_NR_ptrace 26
#define TARGET_FREEBSD_NR_recvmsg 27
#define TARGET_FREEBSD_NR_sendmsg 28
#define TARGET_FREEBSD_NR_recvfrom 29
#define TARGET_FREEBSD_NR_accept 30
#define TARGET_FREEBSD_NR_getpeername 31
#define TARGET_FREEBSD_NR_getsockname 32
#define TARGET_FREEBSD_NR_access 33
#define TARGET_FREEBSD_NR_chflags 34
#define TARGET_FREEBSD_NR_fchflags 35
#define TARGET_FREEBSD_NR_sync 36
#define TARGET_FREEBSD_NR_kill 37
   /* 38 is old stat */
#define TARGET_FREEBSD_NR_getppid 39
   /* 40 is old lstat */
#define TARGET_FREEBSD_NR_dup 41
#define TARGET_FREEBSD_NR_pipe 42
#define TARGET_FREEBSD_NR_getegid 43
#define TARGET_FREEBSD_NR_profil 44
#define TARGET_FREEBSD_NR_ktrace 45
    /* 46 is old sigaction */
#define TARGET_FREEBSD_NR_getgid 47
    /* 48 is old sigprocmask */
#define TARGET_FREEBSD_NR_getlogin 49
#define TARGET_FREEBSD_NR_setlogin 50
#define TARGET_FREEBSD_NR_acct 51
    /* 52 is old sigpending */
#define TARGET_FREEBSD_NR_sigaltstack 53
#define TARGET_FREEBSD_NR_ioctl 54
    #define TARGET_FREEBSD_NR_reboot 55
#define TARGET_FREEBSD_NR_revoke 56
    #define TARGET_FREEBSD_NR_symlink 57
#define TARGET_FREEBSD_NR_readlink 58
    #define TARGET_FREEBSD_NR_execve 59
#define TARGET_FREEBSD_NR_umask 60
    #define TARGET_FREEBSD_NR_chroot 61
        /* 62 is old fstat */
        /* 63 is old getkerninfo */
        /* 64 is old getpagesize */
#define TARGET_FREEBSD_NR_msync 65
#define TARGET_FREEBSD_NR_vfork 66
    /* 67 is obsolete vread */
    /* 68 is obsolete vwrite */
#define TARGET_FREEBSD_NR_sbrk 69
#define TARGET_FREEBSD_NR_sstk 70
    /* 71 is old mmap */
#define TARGET_FREEBSD_NR_vadvise 72
#define TARGET_FREEBSD_NR_munmap 73
    #define TARGET_FREEBSD_NR_mprotect 74
    #define TARGET_FREEBSD_NR_madvise 75
        /* 76 is obsolete vhangup */
        /* 77 is obsolete vlimit */
#define TARGET_FREEBSD_NR_mincore 78
#define TARGET_FREEBSD_NR_getgroups 79
    #define TARGET_FREEBSD_NR_setgroups 80
    #define TARGET_FREEBSD_NR_getpgrp 81
    #define TARGET_FREEBSD_NR_setpgid 82
    #define TARGET_FREEBSD_NR_setitimer 83
        /* 84 is old wait */
    #define TARGET_FREEBSD_NR_swapon 85
    #define TARGET_FREEBSD_NR_getitimer 86
        /* 87 is old gethostname */
        /* 88 is old sethostname */
#define TARGET_FREEBSD_NR_getdtablesize 89
#define TARGET_FREEBSD_NR_dup2 90
    #define TARGET_FREEBSD_NR_fcntl 92
    #define TARGET_FREEBSD_NR_select 93
#define TARGET_FREEBSD_NR_fsync 95
#define TARGET_FREEBSD_NR_setpriority 96
#define TARGET_FREEBSD_NR_socket 97
#define TARGET_FREEBSD_NR_connect 98
/* 99 is old accept */
#define TARGET_FREEBSD_NR_getpriority 100
/* 101 is old send */
/* 102 is old recv */
/* 103 is old sigreturn */
#define TARGET_FREEBSD_NR_bind 104
#define TARGET_FREEBSD_NR_setsockopt 105
#define TARGET_FREEBSD_NR_listen 106
/* 107 is obsolete vtimes */
/* 108 is old sigvec */
/* 109 is old sigblock */
/* 110 is old sigsetmask */
/* 111 is old sigsuspend */
/* 112 is old sigstack */
/* 113 is old recvmsg */
/* 114 is old sendmsg */
/* 115 is obsolete vtrace */
#define TARGET_FREEBSD_NR_gettimeofday 116
#define TARGET_FREEBSD_NR_getrusage 117
#define TARGET_FREEBSD_NR_getsockopt 118
#define TARGET_FREEBSD_NR_readv 120
#define TARGET_FREEBSD_NR_writev 121
#define TARGET_FREEBSD_NR_settimeofday 122
#define TARGET_FREEBSD_NR_fchown 123
/* 124 is obsolete vftimeofday */
/* 125 is old recvfrom */
#define TARGET_FREEBSD_NR_setreuid 126
#define TARGET_FREEBSD_NR_setregid 127
#define TARGET_FREEBSD_NR_rename 128
/* 129 is old truncate */
/* 130 is old ftruncate */
#define TARGET_FREEBSD_NR_flock 131
#define TARGET_FREEBSD_NR_mkfifo 132
#define TARGET_FREEBSD_NR_sendto 133
#define TARGET_FREEBSD_NR_shutdown 134
#define TARGET_FREEBSD_NR_socketpair 135
#define TARGET_FREEBSD_NR_mkdir 136
#define TARGET_FREEBSD_NR_rmdir 137
#define TARGET_FREEBSD_NR_utimes 138
/* 139 is obsolete 4.2 sigreturn */
#define TARGET_FREEBSD_NR_adjtime 140
/* 141 is old getpeername */
/* 142 is old gethostid */
/* 143 is old sethostid */
/
/* 144 is old getrlimit */
/* 145 is old setrlimit */
/* 146 is old killpg */
#define TARGET_FREEBSD_NR_killpg    146 /* COMPAT */
#define TARGET_FREEBSD_NR_setsid    147
#define TARGET_FREEBSD_NR_quotactl 148
/* 149 is old quota */
/* 150 is old getsockname */
#define TARGET_FREEBSD_NR_nlm_syscall   154
#define TARGET_FREEBSD_NR_nfssvc    155
/* 156 is old getdirentries */
#define TARGET_FREEBSD_NR_freebsd4_statfs 157
#define TARGET_FREEBSD_NR_freebsd4_fstatfs 158
#define TARGET_FREEBSD_NR_lgetfh    160
#define TARGET_FREEBSD_NR_getfh 161
#define TARGET_FREEBSD_NR_freebsd4_getdomainname 162
#define TARGET_FREEBSD_NR_freebsd4_setdomainname 163
#define TARGET_FREEBSD_NR_freebsd4_uname 164
#define TARGET_FREEBSD_NR_sysarch   165
#define TARGET_FREEBSD_NR_rtprio 166
#define TARGET_FREEBSD_NR_sem sys    169
#define TARGET_FREEBSD_NR_msgsys    170
#define TARGET_FREEBSD_NR_shm sys    171
#define TARGET_FREEBSD_NR_freebsd6_pread    173
#define TARGET_FREEBSD_NR_freebsd6_pwrite 174
#define TARGET_FREEBSD_NR_freebsd6_setfib 175
#define TARGET_FREEBSD_NR_freebsd6_set gid 181
#define TARGET_FREEBSD_NR_freebsd6_set euid 183
#define TARGET_FREEBSD_NR_freebsd6_stat 188
#define TARGET_FREEBSD_NR_freebsd6_fstat 189
#define TARGET_FREEBSD_NR_freebsd6_lstat 190
#define TARGET_FREEBSD_NR_freebsd6_mmap 197
#define TARGET_FREEBSD_NR_freebsd6_mmap 198
#define TARGET_FREEBSD_NR___syscall 198
#define TARGET_FREEBSD_NR_freebsd6___syscall 199
#define TARGET_FREEBSD_NR_freebsd6_truncate 200
#define TARGET_FREEBSD_NR_freebsd6_ftruncate 201
#define TARGET_FREEBSD_NR___sysctl 202
#define TARGET_FREEBSD_NR_mlock 203
#define TARGET_FREEBSD_NR_munlock 204
#define TARGET_FREEBSD_NR_undelete 205
#define TARGET_FREEBSD_NR_futimes 206
#define TARGET_FREEBSD_NR_getpgid 207
#define TARGET_FREEBSD_NR_poll 209
#define TARGET_FREEBSD_NR_freebsd7___semctl 220
#define TARGET_FREEBSD_NR_semget 221
#define TARGET_FREEBSD_NR_semop 222
#define TARGET_FREEBSD_NR_freebsd7_msgctl 224
#define TARGET_FREEBSD_NR_msgget 225
#define TARGET_FREEBSD_NR_msgsnd 226
#define TARGET_FREEBSD_NR_msgrcv 227
#define TARGET_FREEBSD_NR_shmat 228
#define TARGET_FREEBSD_NR_freebsd7_shmctl 229
#define TARGET_FREEBSD_NR_shmdt 230
#define TARGET_FREEBSD_NR_shmget 231
#define TARGET_FREEBSD_NR_clock_gettime 232
#define TARGET_FREEBSD_NR_clock_settime 233
#define TARGET_FREEBSD_NR_clock_getres 234
#define TARGET_FREEBSD_NR_ktimer_create 235
#define TARGET_FREEBSD_NR_ktimer_delete 236
#define TARGET_FREEBSD_NR_ktimer_settime 237
#define TARGET_FREEBSD_NR_ktimer_gettime 238
#define TARGET_FREEBSD_NR_ktimer_getoverrun 239
#define TARGET_FREEBSD_NR_nanosleep 240
#define TARGET_FREEBSD_NR_ntp_gettime 248
#define TARGET_FREEBSD_NR_minherit 250
#define TARGET_FREEBSD_NR_rfork 251
#define TARGET_FREEBSD_NR_openbsd_poll 252
#define TARGET_FREEBSD_NR_issetugid 253
#define TARGET_FREEBSD_NR_lchown 254
#define TARGET_FREEBSD_NR_aio_read 255
#define TARGET_FREEBSD_NR_aio_write 256
#define TARGET_FREEBSD_NR_lio_listio 257
#define TARGET_FREEBSD_NR_getdents 258
#define TARGET_FREEBSD_NR_lchmod 259
#define TARGET_FREEBSD_NR_netbsd_lchown 275
#define TARGET_FREEBSD_NR_lutimes 276
#define TARGET_FREEBSD_NR_netbsd_msync 277
#define TARGET_FREEBSD_NR_nstat 278
#define TARGET_FREEBSD_NR_nfstat 279
#define TARGET_FREEBSD_NR_nlstat 280
#define TARGET_FREEBSD_NR_preadv 289
#define TARGET_FREEBSD_NR_pwritev 290
#define TARGET_FREEBSD_NR_freebsd4_fhstatfs 297
#define TARGET_FREEBSD_NR_fhopen 298
#define TARGET_FREEBSD_NR_fhstat 299
#define TARGET_FREEBSD_NR_modnext 300
#define TARGET_FREEBSD_NR_modstat 301
#define TARGET_FREEBSD_NR_modfnext 302
#define TARGET_FREEBSD_NR_modfind 303
#define TARGET_FREEBSD_NR_kldload   304
#define TARGET_FREEBSD_NR_kldunload 305
#define TARGET_FREEBSD_NR_kldfind  306
#define TARGET_FREEBSD_NR_kldnext   307
#define TARGET_FREEBSD_NR_kldstat   308
#define TARGET_FREEBSD_NR_kldfirstmod 309
#define TARGET_FREEBSD_NR_getsid    310
#define TARGET_FREEBSD_NR_setresuid 311
#define TARGET_FREEBSD_NR_setresgid 312
#define TARGET_FREEBSD_NR_osfengine 313
#define TARGET_FREEBSD_NR_aio_return 314
#define TARGET_FREEBSD_NR_aio_suspend 315
#define TARGET_FREEBSD_NR_aio_cancel 316
#define TARGET_FREEBSD_NR_aio_error 317
#define TARGET_FREEBSD_NR_oaio_read 318
#define TARGET_FREEBSD_NR_oaio_write 319
#define TARGET_FREEBSD_NR_olio_listio 320
#define TARGET_FREEBSD_NR_yield 321
#define TARGET_FREEBSD_NR_mlockall  324
#define TARGET_FREEBSD_NR_munlockall 325
#define TARGET_FREEBSD_NR___getcwd  326
#define TARGET_FREEBSD_NR_sched_setparam 327
#define TARGET_FREEBSD_NR_sched_getparam 328
#define TARGET_FREEBSD_NR_sched_setscheduler 329
#define TARGET_FREEBSD_NR_sched_getscheduler 330
#define TARGET_FREEBSD_NR_sched_yield 331
#define TARGET_FREEBSD_NR_sched_get_priority_max 332
#define TARGET_FREEBSD_NR_sched_get_priority_min 333
#define TARGET_FREEBSD_NR_sched_rr_get_interval 334
#define TARGET_FREEBSD_NR_utrace    335
#define TARGET_FREEBSD_NR_freebsd4_sendfile 336
#define TARGET_FREEBSD_NR_kldsym    337
#define TARGET_FREEBSD_NR_jail  338
#define TARGET_FREEBSD_NR___acl_get_file 347
#define TARGET_FREEBSD_NR___acl_set_file 348
#define TARGET_FREEBSD_NR___acl_get_fd 349
#define TARGET_FREEBSD_NR___acl_set_fd 350
#define TARGET_FREEBSD_NR___acl_delete_file 351
#define TARGET_FREEBSD_NR___acl_delete_fd 352
#define TARGET_FREEBSD_NR___acl_aclcheck_file 353
#define TARGET_FREEBSD_NR___acl_aclcheck_fd 354
#define TARGET_FREEBSD_NR_extattrctl 355
#define TARGET_FREEBSD_NR_extattr_set_file 356
#define TARGET_FREEBSD_NR_extattr_get_file 357
#define TARGET_FREEBSD_NR_extattr_delete_file 358
#define TARGET_FREEBSD_NR_aio_waitcomplete 359
#define TARGET_FREEBSD_NR_getresuid 360
#define TARGET_FREEBSD_NR_getresgid 361
#define TARGET_FREEBSD_NR_kqueue 362
#define TARGET_FREEBSD_NR_kevent 363
#define TARGET_FREEBSD_NR_extattr_set_fd 371
#define TARGET_FREEBSD_NR_extattr_get_fd 372
#define TARGET_FREEBSD_NR_extattr_delete_fd 373
#define TARGET_FREEBSD_NR___setugid 374
#define TARGET_FREEBSD_NR_eaccess 376
#define TARGET_FREEBSD_NR_afs3_syscall 377
#define TARGET_FREEBSD_NR_nmount 378
#define TARGET_FREEBSD_NR___mac_get_proc 384
#define TARGET_FREEBSD_NR___mac_set_proc 385
#define TARGET_FREEBSD_NR___mac_get_fd 386
#define TARGET_FREEBSD_NR___mac_get_file 387
#define TARGET_FREEBSD_NR___mac_set_fd 388
#define TARGET_FREEBSD_NR___mac_set_file 389
#define TARGET_FREEBSD_NR_kenv 390
#define TARGET_FREEBSD_NR_lchflags 391
#define TARGET_FREEBSD_NR_uuidgen 392
#define TARGET_FREEBSD_NR_sendfile 393
#define TARGET_FREEBSD_NR_mac_syscall 394
#define TARGET_FREEBSD_NR_getfsstat 395
#define TARGET_FREEBSD_NR_statfs 396
#define TARGET_FREEBSD_NR_fstatfs 397
#define TARGET_FREEBSD_NR_fhstatfs 398
#define TARGET_FREEBSD_NR_ksem_close 400
#define TARGET_FREEBSD_NR_ksem_post 401
#define TARGET_FREEBSD_NR_ksem_wait 402
#define TARGET_FREEBSD_NR_ksem_trywait 403
#define TARGET_FREEBSD_NR_ksem_init 404
#define TARGET_FREEBSD_NR_ksem_open 405
#define TARGET_FREEBSD_NR_ksem_unlink 406
#define TARGET_FREEBSD_NR_ksem_getvalue 407
#define TARGET_FREEBSD_NR_ksem_destroy 408
#define TARGET_FREEBSD_NR___mac_get_pid 409
#define TARGET_FREEBSD_NR___mac_get_link 410
#define TARGET_FREEBSD_NR___mac_get_set_link 411
#define TARGET_FREEBSD_NR_extattr_set_link 412
#define TARGET_FREEBSD_NR_extattr_get_link 413
#define TARGET_FREEBSD_NR_extattr_delete_link 414
#define TARGET_FREEBSD_NR_mac_execve 415
#define TARGET_FREEBSD_NR_sigaction 416
#define TARGET_FREEBSD_NR_sigreturn 417
#define TARGET_FREEBSD_NR_getcontext 421
#define TARGET_FREEBSD_NR_setcontext 422
#define TARGET_FREEBSD_NR_swapcontext 423
#define TARGET_FREEBSD_NR_swapoff 424
#define TARGET_FREEBSD_NR__acl_get_link 425
#define TARGET_FREEBSD_NR__acl_set_link 426
#define TARGET_FREEBSD_NR__acl_delete_link 427
#define TARGET_FREEBSD_NR__acl_aclcheck_link 428
#define TARGET_FREEBSD_NR_sigwait 429
#define TARGET_FREEBSD_NR_thr_create 430
#define TARGET_FREEBSD_NR_thr_exit 431
#define TARGET_FREEBSD_NR_thr_self 432
#define TARGET_FREEBSD_NR_thr_kill 433
#define TARGET_FREEBSD_NR__umtx_lock 434
#define TARGET_FREEBSD_NR__umtx_unlock 435
#define TARGET_FREEBSD_NR_jail_attach 436
#define TARGET_FREEBSD_NR_extattr_list_fd 437
#define TARGET_FREEBSD_NR_extattr_list_file 438
#define TARGET_FREEBSD_NR_extattr_list_link 439
#define TARGET_FREEBSD_NR_ksem_timedwait 441
#define TARGET_FREEBSD_NR_thr_suspend 442
#define TARGET_FREEBSD_NR_thr_wake 443
#define TARGET_FREEBSD_NR_kldunload 444
#define TARGET_FREEBSD_NR_audit 445
#define TARGET_FREEBSD_NR_auditon 446
#define TARGET_FREEBSD_NR_getauid 447
#define TARGET_FREEBSD_NR_setauid 448
#define TARGET_FREEBSD_NR_getaudit 449
#define TARGET_FREEBSD_NR_setaudit 450
#define TARGET_FREEBSD_NR_getaudit_addr 451
#define TARGET_FREEBSD_NR_setaudit_addr 452
#define TARGET_FREEBSD_NR_auditctl 453
#define TARGET_FREEBSD_NR__umtx_op 454
#define TARGET_FREEBSD_NR_thr_new 455
#define TARGET_FREEBSD_NR_sigqueue 456
#define TARGET_FREEBSD_NR_kmq_open 457
#define TARGET_FREEBSD_NR_kmq_setattr 458
#define TARGET_FREEBSD_NR_kmq_timedreceive 459
#define TARGET_FREEBSD_NR_kmq_timedsend 460
#define TARGET_FREEBSD_NR_kmq_notify 461
#define TARGET_FREEBSD_NR_kmq_unlink 462
#define TARGET_FREEBSD_NR_abort2 463
#define TARGET_FREEBSD_NR_thr_set_name 464
#define TARGET_FREEBSD_NR_aio_fsync 465
define TARGET_FREEBSD_NR_rtprio_thread 466
#define TARGET_FREEBSD_NR_sctp_peeloff 471
#define TARGET_FREEBSD_NR_sctp_generic_sendmsg 472
#define TARGET_FREEBSD_NR_sctp_generic_sendmsg_iov 473
#define TARGET_FREEBSD_NR_sctp_generic_recvmsg 474
#define TARGET_FREEBSD_NR_pread 475
#define TARGET_FREEBSD_NR_pwrite 476
#define TARGET_FREEBSD_NR_mmap 477
#define TARGET_FREEBSD_NR_lseek 478
#define TARGET_FREEBSD_NR_truncate 479
#define TARGET_FREEBSD_NR_ftruncate 480
#define TARGET_FREEBSD_NR_thr_kill2 481
#define TARGET_FREEBSD_NR_shm_open 482
#define TARGET_FREEBSD_NR_shm_unlink 483
#define TARGET_FREEBSD_NR_cpuset 484
#define TARGET_FREEBSD_NR_cpuset_setid 485
#define TARGET_FREEBSD_NR_cpuset_getid 486
#define TARGET_FREEBSD_NR_cpuset_getaffinity 487
#define TARGET_FREEBSD_NR_cpuset_setaffinity 488
#define TARGET_FREEBSD_NR_faccessat 489
#define TARGET_FREEBSD_NR_fchmodat 490
#define TARGET_FREEBSD_NR_fchownat 491
#define TARGET_FREEBSD_NR_fexecve 492
#define TARGET_FREEBSD_NR_fstatat 493
#define TARGET_FREEBSD_NR_futimesat 494
#define TARGET_FREEBSD_NR_linkat 495
#define TARGET_FREEBSD_NR_mkdirat 496
#define TARGET_FREEBSD_NR_mknodat 497
#define TARGET_FREEBSD_NR_openat 498
#define TARGET_FREEBSD_NR_readlinkat 500
#define TARGET_FREEBSD_NR_renameat 501
#define TARGET_FREEBSD_NR symlinkat 502
#define TARGET_FREEBSD_NR_unlinkat 503
#define TARGET_FREEBSD_NR_posix_openpt 504
#define TARGET_FREEBSD_NR_gssd_syscall 505
#define TARGET_FREEBSD_NR_jail_get 506
#define TARGET_FREEBSD_NR_jail_set 507
#define TARGET_FREEBSD_NR_jail_remove 508
#define TARGET_FREEBSD_NR_closefrom 509
#define TARGET_FREEBSD_NR___semctl 510
#define TARGET_FREEBSD_NR_msgctl 511
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#define TARGET_FREEBSD_NR__cap_new 514
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#define TARGET_FREEBSD_NR_pdkill 519
#define TARGET_FREEBSD_NR_pdgetpid 520
#define TARGET_FREEBSD_NR_pselect 522
#define TARGET_FREEBSD_NR_setloginclass 524
#define TARGET_FREEBSD_NR_rctl_get_racct 525
#define TARGET_FREEBSD_NR_rctl_get_rules 526
#define TARGET_FREEBSD_NR_rctl_get_limits 527
#define TARGET_FREEBSD_NR_rctl_add_rule 528
#define TARGET_FREEBSD_NR_rctl_remove_rule 529
#define TARGET_FREEBSD_NR_posix_fallocate 530
#define TARGET_FREEBSD_NR_posix_fadvise 531
#define TARGET_FREEBSD_NR_MAXSYSCALL 532

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John R. Hauser
2018 January 20

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John R. Hauser
2018 January 20

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*
* @(#)errno.h 8.5 (Berkeley) 1/21/94
* /

#define TARGET_EPERM 1 /* Operation not permitted */
#define TARGET_ENOENT 2 /* No such file or directory */
#define TARGET_ESRCH 3 /* No such process */
#define TARGET_EINTR 4 /* Interrupted system call */
#define TARGET_EIO 5 /* Input/output error */
#define TARGET_ENXIO 6 /* Device not configured */
#define TARGET_E2BIG 7 /* Argument list too long */
#define TARGET_ENOEXEC 8 /* Exec format error */
#define TARGET_EBADF 9 /* Bad file descriptor */
#define TARGET_ECHILD 10 /* No child processes */
#define TARGET_EDeadLK 11 /* Resource deadlock avoided */
#define TARGET_ENOMEM 12 /* Cannot allocate memory */
#define TARGET_EACCES 13 /* Permission denied */
#define TARGETEFAULT 14 /* Bad address */
#define TARGET_ENOTBLK 15 /* Block device required */
#define TARGET_ENOEXEC 16 /* Device busy */
#define TARGET_EXIST 17 /* File exists */
#define TARGET_FILE 18 /* Cross-device link */
#define TARGET_ENODEV 19 /* Operation not supported by device */
#define TARGET_EISDIR 20 /* Not a directory */
#define TARGET_EINVAL 22 /* Invalid argument */
#define TARGET_ENFILE 23 /* Too many open files in system */
#define TARGET_EMFILE 24 /* Too many open files */
#define TARGET_ENOTTY 25 /* Inappropriate ioctl for device */
#define TARGET_ETXTBSY 26 /* Text file busy */
#define TARGET_FILE 27 /* File too large */
#define TARGET_ENOSPC 28 /* No space left on device */
#define TARGET_ESPIPE 29 /* Illegal seek */
#define TARGET_EREOF 30 /* Read-only file system */
#define TARGET_EMLINK 31 /* Too many links */
#define TARGET_EPIPE 32 /* Broken pipe */

#define TARGET_EDOM 33 /* Numerical argument out of domain */
#define TARGET_ERANGE 34 /* Result too large */

#define TARGET_EAGAIN 35 /* Resource temporarily unavailable */
#define TARGET_EWOULDBLOCK EAGAIN /* Operation would block */
#define TARGET_EINPROGRESS 36 /* Operation now in progress */
#define TARGET_EALREADY 37 /* Operation already in progress */

/* ipc/network software -- argument errors */
#define TARGET_ENOTSOCK 38 /* Socket operation on non-socket */
#define TARGET_EDESTADDRREQ 39 /* Destination address required */
#define TARGET_EMGSIZE 40 /* Message too long */
#define TARGET_EPROTOCPYPE 41 /* Protocol wrong type for socket */
#define TARGET_ENOPROTOOPT 42 /* Protocol not available */
#define TARGET_EPROTONOSUPPORT 43 /* Protocol not supported */
#define TARGET_ESOCKTNOSUPPORT 44 /* Socket type not supported */
#define TARGET_EOPNOTSUPP 45 /* Operation not supported */
#define TARGET_EFNOSUPPORT 46 /* Protocol family not supported */
#define TARGET_EADDRINUSE 48 /* Address already in use */
#define TARGET_EADDRNOTAVAIL 49 /* Can't assign requested address */

/* ipc/network software -- operational errors */
#define TARGET_ENETDOWN 50 /* Network is down */
#define TARGET_ENETUNREACH 51 /* Network is unreachable */
#define TARGET_ENETRESET 52 /* Network dropped connection on reset */
#define TARGET_ECONNABORTED 53 /* Software caused connection abort */
#define TARGET_ECONNRESET 54 /* Connection reset by peer */
#define TARGET_ENOBUFS 55 /* No buffer space available */
#define TARGET_EISCONN 56 /* Socket is already connected */
#define TARGET_ENOTCONN 57 /* Socket is not connected */
#define TARGET_ESHUTDOWN 58 /* Can't send after socket shutdown */
#define TARGET_EHOSTDOWN 64 /* Host is down */
#define TARGET_EHOSTUNREACH 65 /* No route to host */
#define TARGET_ENOTEMPTY 66 /* Directory not empty */
#define TARGET_EPROCLIM 67 /* Too many processes */
#define TARGET_EUSERS 68 /* Too many users */
#define TARGET_EDQUOT 69 /* Disk quota exceeded */

/* Network File System */
#define TARGET_ESTALE 70 /* Stale NFS file handle */
#define TARGET_EREMOTE 71 /* Too many levels of remote in path */
#define TARGET_EBADRPC 72 /* RPC struct is bad */
#define TARGET_ERPCMISMATCH 73 /* RPC version wrong */
#define TARGET_EPROGUNAVAIL 74 /* RPC prog. not avail */
#define TARGET_EPROGMISMATCH 75 /* Program version wrong */
#define TARGET_EPROCUNAVAIL 76 /* Bad procedure for program */

#define TARGET_ENOLCK 77 /* No locks available */
#define TARGET_ENOSYS 78 /* Function not implemented */

#define TARGET_EFTYPE 79 /* Inappropriate file type or format */
#define TARGET_EAUTH 80 /* Authentication error */
#define TARGET_ENEEDAUTH 81 /* Need authenticator */
#define TARGET_EIPSEC 82 /* IPsec processing failure */
#define TARGET_ENOATTR 83 /* Attribute not found */
#define TARGET_EILSEQ 84 /* Illegal byte sequence */
#define TARGET_ENOMEDIUM 85 /* No medium found */
#define TARGET_EMEDIUMTYPE 86 /* Wrong Medium Type */
#define TARGET_EOVERFLOW 87 /* Conversion overflow */
#define TARGET_ECANCELED 88 /* Operation canceled */
#define TARGET_EIDRM 89 /* Identifier removed */
#define TARGET_ENOMSG 90 /* No message of desired type */
#define TARGET_ELAST 90 /* Must be equal largest errno */

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SVN=`which svn`
PATCH=`which patch`
DIFF_FILE=./x86emu_changes.diff

# check whether svn, patch, ... is available...
if [ ! -x $SVN ]; then
echo "subversion executable not found!"
exit -1
fi
if [ ! -x $PATCH ]; then
echo "patch executable not found!"
exit -1
fi
if [ ! -r $DIFF_FILE ]; then
echo "diff file $DIFF_FILE not found!"
exit -1
fi

# download the x86emu sources from LinuxBIOS subversion

#revision known to work...
REV=496

echo "Checking out x86emu from coreboot-v3 repository revision $REV"
$SVN co svn://coreboot.org/repository/coreboot-v3/util/x86emu -r $REV

echo "Copying files..."

mkdir -p include/x86emu
cp -v x86emu/x86emu/*.c .
cp -v x86emu/x86emu/*.h include/x86emu
cp -v x86emu/include/x86emu/*.h include/x86emu

echo "Removing checkedout subversion director..."

rm -rf x86emu

echo "Patching files..."

$PATCH -p0 < x86emu_changes.diff
echo "done"
exit 0

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From friedman@cli.com Thu May 25 12:19:06 1995
Flags: 10
Return-Path: friedman@cli.com
Received: from po.cwru.edu (root@po.CWRU.Edu [129.22.4.2]) by odin.INS.CWRU.Edu with ESMTP (8.6.10+cwru/CWRU-2.1-ins)
id MAA08685; Thu, 25 May 1995 12:19:05 -0400 (from friedman@cli.com for <chet@odin.INS.CWRU.Edu>)
Received: from cli.com (cli.com [192.31.85.1]) by po.cwru.edu with SMTP (8.6.10+cwru/CWRU-2.3)
id MAA11299; Thu, 25 May 1995 12:19:00 -0400 (from friedman@cli.com for <chet@po.cwru.edu>)
Received: from tepui.cli.com by cli.com (4.1/SMI-4.1)
id AA27213; Thu, 25 May 95 11:18:25 CDT
Received: by tepui.cli.com (4.1) id AA16031; Thu, 25 May 95 11:18:23 CDT
Message-Id: <9505251519.AA06424.SM@odin.INS.CWRU.Edu>
From: friedman@gnu.ai.mit.edu (Noah Friedman)
To: chet@po.cwru.edu
Subject: Bash scripts
Reply-To: friedman@gnu.ai.mit.edu
In-Reply-To: <chet@odin.ins.cwru.edu> Thu, 25 May 1995 11:19:59 -0400
References: <9505251519.AA06424.SM@odin.INS.CWRU.Edu>
Date: Thu, 25 May 95 11:18:21 CST

>Hi.  I snagged some of your bash functions from your home directory on
>the FSF machines (naughty, I know), and I was wondering if you'd let
>me distribute them with bash-2.0.  Thanks.

Sure.  I think there's a later copy in
~ftp/friedman/shell-inits/init-4.89.tar.gz. There are also some elisp and
es frobs in that file.

It should serve as a pretty good example of how to get carried away. :-)
From spcecdt@armory.com Wed May 10 10:21:11 1995
Flags: 10
Return-Path: spcecdt@armory.com
Received: from po.cwru.edu (root@po.CWRU.Edul [129.22.4.2]) by odin.INS.CWRU.Edul with ESMTP
(8.6.10+cwru/CWRU-2.1-ins)
id KAA22876; Wed, 10 May 1995 10:21:10 -0400 (from spcecdt@armory.com for <chet@odin.INS.CWRU.Edul>)
Received: from deepthought.armory.com (mmdf@deepthought.armory.com [192.122.209.42]) by po.cwru.edu with
SMTP (8.6.10+cwru/CWRU-2.3)
id BAA16354; Wed, 10 May 1995 01:33:22 -0400 (from spcecdt@armory.com for <chet@po.cwru.edu>)
From: John DuBois <spcecdt@armory.com>
Date: Tue, 9 May 1995 22:33:12 -0700
In-Reply-To: Chet Ramey <chet@odin.ins.cwru.edu>
"ksh scripts" (May 9, 1:36pm)
X-Www: http://www.armory.com/~spcecdt/
X-Mailer: Mail User's Shell (7.2.5 10/14/92)
To: chet@po.cwru.edu
Subject: Re: ksh scripts
Message-ID: <9505092233.aa13001@deepthought.armory.com>

Sure. The canonical versions are available on ftp.armory.com; you might
want to pick up the latest versions before modifying them.

John

On May 9, 1:36pm, Chet Ramey wrote:

} Subject: ksh scripts
} From odin.ins.cwru.edu!chet Tue May 9 10:39:51 1995
} Received: from odin.INS.CWRU.Edul by deepthought.armory.com id aa22336;
} 9 May 95 10:39 PDT
} Received: (chet@localhost) by odin.INS.CWRU.Edul (8.6.10+cwru/CWRU-2.1-ins)
} id NAA20487; Tue, 9 May 1995 13:39:24 -0400 (from chet)
} Date: Tue, 9 May 1995 13:36:54 -0400
} From: Chet Ramey <chet@odin.ins.cwru.edu>
} To: john@armory.com
} Subject: ksh scripts
} Cc: chet@odin.ins.cwru.edu
} Reply-To: chet@po.cwru.edu
} Message-ID: <9505091736.AA20411.SM@odin.INS.CWRU.Edul>
} Read-Receipt-To: chet@po.CWRU.Edul
} MIME-Version: 1.0
} Content-Type: text/plain; charset=us-ascii
} Status: OR
}
} Hi. I'm the maintainer of bash (the GNU `Bourne Again shell') for
I picked up a tar file of ksh scripts you wrote from an anon FTP site a while back. I'd like your permission to include modified versions of some of them in the next major bash distribution (with proper credit given, of course). Is it OK if I do that?

Chet Ramey

"The lyf so short, the craft so long to lerne." - Chaucer

Chet Ramey, Case Western Reserve University
Internet: chet@po.CWRU.Edu

--

That's great!
Go ahead and circulate it; in fact, we should probably grab it and stick it in our ftp archive, and put a reference to it in the book.
(Too late to actually discuss the thing, at least for this edition).

-------
1.268 libpng 1.2.44
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1.269 gobject-introspection 1.56.1 1.el7

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That's all there is to it!

1.272 libdevmapper 1.02.158 2.el7

1.272.1 Available under license:

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medium customarily used for software interchange.

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Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

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If you develop a new program, and you want it to be of the greatest possible use to the public, the best way to achieve this is to make it free software which everyone can redistribute and change under these terms.

To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the program's name and a brief idea of what it does.>
Copyright (C) <year> <name of author>

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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

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You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here is a sample; alter the names:

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<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

This General Public License does not permit incorporating your program into proprietary programs. If your program is a subroutine library, you may consider it more useful to permit linking proprietary applications with the library. If this is what you want to do, use the GNU Library General Public License instead of this License.

1.277 gstreamer 1.10.4-2.el7
1.277.1 Available under license:

This part of the FAQ is based on a series of questions we asked the FSF to understand how the GPL works and how patents affects the GPL. These questions were answered by the <ulink url="http://www.fsf.org/">FSF lawyers</ulink>, so we view them as the final interpretation on how the GPL and LGPL interact with patents in our opinion. This consultancy was paid for by <ulink url="http://www.fluendo.com/">Fluendo</ulink> in order to obtain clear and quotable answers. These answers were certified by the FSF lawyer team and verified by FSF lawyer and law professor Eben Moglen.

Can someone distribute the combination of GStreamer, the LGPL library, MyPlayer, a GPL playback application, and the binary-only Sorenson decoder together in one distribution/operating system? If not, what needs to be changed to make this possible?
This would be a problem, because the GStreamer and MyPlayer licenses would forbid it. In order to link GStreamer to MyPlayer, you need to use section 3 of the LGPL to convert GStreamer to GPL. The GPL version of GStreamer forbids linking to the Sorenson decoder. Anyway, the MyPlayer GPL license forbids this.

If the authors of MyPlayer want to permit this, we have an exception for them: the controlled interface exception from the FAQ. The idea of this is that you can't get around the GPL just by including a LGPL bit in the middle.

Note: MyPlayer is a completely fictitious application at the time of writing.

Suppose Apple wants to write a binary-only proprietary plugin for GStreamer to decode Sorenson video, which will be shipped stand-alone, not part of a package like in the question above. Can Apple distribute this binary-only plugin?

Yes, modulo certain reverse engineering requirements in section 6 of the LGPL.
is LGPL, and this library can dlopen plug-ins at runtime, what are the
requirements for the license of the plug-in?

<para>
</para>

<answer>
<para>
You may not distribute the plug-in with the GPL application. Distributing
the plug-in alone, with the knowledge that it will be used primarily by GPL
software is a bit of an edge case. We will not advise you that it would be
safe to do so, but we also will not advise you that it would be absolutely
forbidden.

</para>
</answer>

<qandaentry>
<question id="legal-safe-countries">
<para>
Can someone in a country that does not have software patents distribute
code covered by US patents under the GPL to people in, for example, Norway?
If he/she visits the US, can he/she be arrested?

</para>
</question>

<answer>
<para>
Yes, he can.
No, there are no criminal penalties for patent infringement in the US.

</para>
</answer>

<qandaentry>
<question id="legal-unsafe-countries">
<para>
Can someone from the US distribute software covered by US patents under
the GPL to people in Norway? To people in the US?

</para>
</question>

<answer>
<para>
This might infringe some patents, but the GPL would not forbid it absent
some actual restriction, such as a court judgement or agreement. The US
government is empowered to refuse importation of patent infringing
devices, including software.

</para>
</answer>
There are a lot of GPL- or LGPL-licensed libraries that handle media codecs which have patents. Take mad, an mp3 decoding library, as an example. It is licensed under the GPL. In countries where patents are valid, does this invalidate the GPL license for this project?

The mere existence of a patent which might read on the program does not change anything. However, if a court judgement or other agreement prevents you from distributing libmad under GPL terms, you can not distribute it at all.

The GPL and LGPL say (sections 7 and 11):

If you cannot distribute so as to satisfy simultaneously your obligations under this License and any other pertinent obligations, then as a consequence you may not distribute the Library at all.

So let's say there is a court judgement. Does this mean that the GPL license is invalid for the project everywhere, or only in the countries where it conflicts with the applicable patents?

The GPL operates on a per-action, not per-program basis. That is, if you are in a country which has software patents, and a court tells you that you cannot distribute (say) libmad in source code form, then you cannot distribute libmad at all. This doesn't affect anyone else.
Patented decoding can be implemented in GStreamer either by having a binary-only plugin do the decoding, or by writing a plugin (with any applicable license) that links to a binary-only library. Does this affect the licensing issues involved in regards to GPL/LGPL?

No.

Is it correct that you cannot distribute the GPL mad library to decode mp3's, *even* in the case where you have obtained a valid license for decoding mp3?

The only GPL-compatible patent licenses are those which are open to all parties possessing copies of GPL software which practices the teachings of the patent.

If you take a license which doesn't allow others to distribute original or modified versions of libmad practicing the same patent claims as the version you distribute, then you may not distribute at all.

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Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

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For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

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Finally, any free program is threatened constantly by software
patents. We wish to avoid the danger that companies distributing free
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we have made it clear that any patent must be licensed for everyone's
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Most GNU software, including some libraries, is covered by the ordinary
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designated libraries. This license is quite different from the ordinary
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the same as in the ordinary license.

The reason we have a separate public license for some libraries is that
they blur the distinction we usually make between modifying or adding to a
program and simply using it. Linking a program with a library, without
changing the library, is in some sense simply using the library, and is
analogous to running a utility program or application program. However, in
a textual and legal sense, the linked executable is a combined work, a
derivative of the original library, and the ordinary General Public License
treats it as such.

Because of this blurred distinction, using the ordinary General
Public License for libraries did not effectively promote software
sharing, because most developers did not use the libraries. We
concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the
users of those programs of all benefit from the free status of the
libraries themselves. This Library General Public License is intended to
permit developers of non-free programs to use free libraries, while
preserving your freedom as a user of such programs to change the free
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changes in the actual functions of the Library.) The hope is that this
will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and
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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.
If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility
is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.
4. You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
link a "work that uses the Library" with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is
normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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1.286 polkit 0.112-22.el7
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Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The
former contains code derived from the library, while the latter only works together with the library.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

   a) The modified work must itself be a software library.

   b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

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   d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

      (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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a) Accompany the work with the complete corresponding
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changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
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```
cmd/krb5/iprop/iprop.x
cmd/krb5/iprop/iprop_hdr.h
```

```
cmd/krb5/kadmin/server/ipropd_svc.c
```

```
cmd/krb5/kproplog/kproplog.c
```

```
cmd/krb5/slave/kpropd_rpc.c
```

```
lib/gss_mechs/mech_krb5/et/kdb5_err.c
```

```
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
```

```
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
```

```
lib/krb5/kadm5/kadm_host_srv_names.c
```

```
lib/krb5/kdb/kdb_convert.c
```

```
lib/krb5/kdb/kdb_hdr.h
```

```
lib/krb5/kdb/kdb_log.c
```

```
lib/krb5/kdb/kdb_log.h
```

```
lib/libgss/g_accept_sec_context.c
```

```
lib/libgss/g_acquire_cred.c
```

```
lib/libgss/g_canon_name.c
```

```
lib/libgss/g_compare_name.c
```

```
lib/libgss/g_context_time.c
```

```
lib/libgss/g_delete_sec_context.c
```

```
lib/libgss/g_dsp_name.c
```

```
lib/libgss/g_dsp_status.c
```

```
lib/libgss/g_dup_name.c
```

```
lib/libgss/g_exp_sec_context.c
```

```
lib/libgss/g_export_name.c
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```
lib/libgss/g_glue.c
```

```
lib/libgss/g_imp_name.c
```

```
lib/libgss/g_imp_sec_context.c
```

```
lib/libgss/g_init_sec_context.c
```

```
lib/libgss/g_initialize.c
```

```
lib/libgss/g_inquire_context.c
```

```
lib/libgss/g_inquire_cred.c
```

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lib/libgss/g_inquire_names.c
```

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lib/libgss/g_process_context.c
```

```
lib/libgss/g_rel_buffer.c
```

```
lib/libgss/g.rel_cred.c
```

```
lib/libgss/g.rel_name.c
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```
lib/libgss/g.rel_oid_set.c
```

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lib/libgss/g.seal.c
```
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## -*-makefile-*-

## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADS CPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ <
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.S$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%..d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$$(SHELL) -ec '$(GEN_DEPS.c) $< \n | sed "\"s/\$(*).o[ :]*/\1.o $@ : /g\" > $@;\n | -s $@ || rm -f $@'  

%..d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$@$(SHELL) -ec '$(GEN_DEPS.cc) $< \n | sed "\"s/\$(*).o[ :]*/\1.o $@ : /g\" > $@;\n | -s $@ || rm -f $@'  

## Versioned libraries rules
%..$(SO).$(SO_TARGET_VERSION_MAJOR): %..$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%..$(SO): %..$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references
# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup
This is a copy of CONTRIBUTORS file for the Pthreads-win32 library, downloaded from http://sourceware.org/cgi-bin/cvsweb.cgi/~checkout~/pthreads/CONTRIBUTORS?rev=1.32&cvsroot=pthreads-win32

Included here to compliment the Pthreads-win32 license header in wtf/ThreadingWin.cpp file.
WebKit is using derived sources of ThreadCondition code from Pthreads-win32.
Contributors (in approximate order of appearance)

[See also the ChangeLog file where individuals are attributed in log entries. Likewise in the FAQ file.]

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Re-implemented and improved read-write locks;
(with Louis Thomas) re-implemented and improved condition variables;
enhancements to semaphores;
enhancements to mutexes;
new mutex implementation in 'futex' style;
suggested a robust implementation of pthread_once similar to that implemented by V.Kliathcko;
system clock change handling re CV timeouts;
bug fixes.
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Changes to make C version usable with C++ applications;
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Gottlob Frege gottlobfrege at gmail dot com
re-implemented pthread_once (version 2)
(pthread_once cancellation added by rpj).
Vladimir Klatchkovvladimir at klatchko dot com
reimplemented pthread_once with the same form
as described by A. Terekhov (later version 2);
implementation of MCS (Mellor-Crummey/Scott) locks.
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2015
1.292 libtiff 4.0.3 32.el7

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1.293 libxext 1.3.3 3.el7

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1.294 libxaw 1.0.13 4.el7

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1.295 pillow 2.0.0 19.gitd1c6db8.el7
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GIMP Palette
Name: badpalettefile
Columns: 4
#
0 0 0   Index 3
0123456789012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789
34567890123456789
103 62 49   Index 6
79 73 72   Index 7
114 101 97   Index 8
208 127 100   Index 9
151 144 142   Index 10
221 207 199   Index 11

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GIMP Palette
Name: Test
Columns: 0
#

GIMP Palette
Name: badpaletteentry
Columns: 4
#

0 0 0  Index 3
65 38
103 62 49  Index 6
79 73 72  Index 7
114 101 97  Index 8
208 127 100  Index 9
151 144 142  Index 10
221 207 199  Index 11
from http://www.color.org/srgbprofiles.xalter

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GIMP Palette
Name: custompalette
Columns: 4
#
 0 0 0  Index 3
 65 38 30  Index 4
103 62 49  Index 6
 79 73 72  Index 7
114 101 97  Index 8
208 127 100  Index 9
151 144 142  Index 10
221 207 199  Index 11

1.296 zlib 1.2.7 18.el7
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1.297 cups 1.6.3 40.el7
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2. You may modify your copy or copies of the Program or any portion 
of it, thus forming a work based on the Program, and copy and 
distribute such modifications or work under the terms of Section 1 
above, provided that you also meet all of these conditions:

   a) You must cause the modified files to carry prominent notices 
estating that you changed the files and the date of any change.

   b) You must cause any work that you distribute or publish, that in 
whole or in part contains or is derived from the Program or any 
part thereof, to be licensed as a whole at no charge to all third 
parties under the terms of this License.

   c) If the modified program normally reads commands interactively
when run, you must cause it, when started running for such interactive use in the most ordinary way, to print or display an announcement including an appropriate copyright notice and a notice that there is no warranty (or else, saying that you provide a warranty) and that users may redistribute the program under these conditions, and telling the user how to view a copy of this License. (Exception: if the Program itself is interactive but does not normally print such an announcement, your work based on the Program is not required to print an announcement.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Program, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Program, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Program.

In addition, mere aggregation of another work not based on the Program with the Program (or with a work based on the Program) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may copy and distribute the Program (or a work based on it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you also do one of the following:

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   b) Accompany it with a written offer, valid for at least three years, to give any third party, for a charge no more than your cost of physically performing source distribution, a complete machine-readable copy of the corresponding source code, to be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange; or,

   c) Accompany it with the information you received as to the offer to distribute corresponding source code. (This alternative is allowed only for noncommercial distribution and only if you
received the program in object code or executable form with such an offer, in accord with Subsection b above.)

The source code for a work means the preferred form of the work for making modifications to it. For an executable work, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the executable. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

If distribution of executable or object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place counts as distribution of the source code, even though third parties are not compelled to copy the source along with the object code.

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If any portion of this section is held invalid or unenforceable under any particular circumstance, the balance of the section is intended to apply and the section as a whole is intended to apply in other circumstances.

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest possible use to the public, the best way to achieve this is to make it free software which everyone can redistribute and change under these terms.

To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the program's name and a brief idea of what it does.>
Copyright (C) 19yy <name of author>
This program is free software; you can redistribute it and/or modify it under the terms of the GNU General Public License as published by the Free Software Foundation; either version 2 of the License, or (at your option) any later version.

This program is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU General Public License for more details.

You should have received a copy of the GNU General Public License along with this program; if not, write to the Free Software Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.

Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

Gnomovision version 69, Copyright (C) 19yy name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it under certain conditions; type `show c' for details.

The hypothetical commands `show w' and `show c' should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w' and `show c'; they could even be mouse-clicks or menu items--whatever suits your program.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the program `Gnomovision' (which makes passes at compilers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

This General Public License does not permit incorporating your program into proprietary programs. If your program is a subroutine library, you may consider it more useful to permit linking proprietary applications with the library. If this is what you want to do, use the GNU Library General Public License instead of this License.

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To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we
want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.
Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a
2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of
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3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a “work that uses the Library”. Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy
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d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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Appendix: How to Apply These Terms to Your New Libraries

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library 'Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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<H2 CLASS="title"><A NAME="INTRO">Introduction</A></H2>

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<OL START="11">

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- If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

- Verify that the user has already received a copy of these materials or that you have already sent this user a copy.
For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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<P><STRONG>16.</STRONG></P>
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<P>To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<pre>
<VAR>one line to give the library's name and an idea of what it does.</VAR>
Copyright (C) <VAR>year</VAR>  <VAR>name of author</VAR>

This library is free software; you can redistribute it and/or modify it under the terms of the GNU Lesser General Public License as published by the Free Software Foundation; either version 2.1 of the License, or (at your option) any later version.

This library is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU Lesser General Public License for more details.

You should have received a copy of the GNU Lesser General Public License along with this library; if not, write to the Free Software Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
</pre>
Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

PRE
Yoyodyne, Inc., hereby disclaims all copyright interest in the library 'Frob' (a library for tweaking knobs) written by James Random Hacker.

VAR>signature of Ty Coon</VAR>, 1 April 1990
Ty Coon, President of Vice
</PRE>

That's all there is to it!

</BODY>
</HTML>

1.298 cracklib 2.9.0-11.el7
1.298.1 Available under license:

--------
EFFECTIVE OCT 2008, LICENSE IS BEING CHANGED TO LGPL-2.1 (though not reflected in released code until Nov 2009 - slow release cycle...)
--------

Discussion thread from mailing list archive, with approval from everyone actively involved or holding original licensing rights included.

[Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:16

Attachments: Message as HTML
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Nathan Neulinger EMail: nneul@um...
University of Missouri - Rolla Phone: (573) 341-6679
UMR Information Technology Fax: (573) 341-4216

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> From: cracklib-devel-bounces@li...
> [mailto:cracklib-devel-bounces@li...] On Behalf Of
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> Sent: Monday, October 01, 2007 8:15 PM
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From: Alec Muffett <alecm@cr...> - 2007-10-02 08:57
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I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be
linked with any code, not just GPL...

-a

Re: [Cracklib-devel] cracklib license
From: Devin Reade <gdr@gn...> - 2007-10-02 15:04
I would like to see it under LGPL as well. I think it is in everyone's
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license in this situation.

Devin
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If it's sinful, it's more fun.

Re: [Cracklib-devel] cracklib license
From: Nalin Dahyabhai <nalin@re...> - 2008-01-28 16:32
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I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be linked with any code, not just GPL...

My apologies for not chiming in in anything resembling a reasonable timeframe.

I'd also suggest the LGPL, for the reason you noted above. Alternately, GPLv2 with the option of using the library under a later version of the GPL would permit applications which were released under version 3 of the GPL to use the library, too, which would be sufficient for the packages which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

Cheers,

Nalin

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-05 21:27

Attachments: Message as HTML
On Monday 28 January 2008, Nalin Dahyabhai wrote:
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looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
the change now?

-mike

Re: [Cracklib-devel] cracklib license
From: Alec Muffett <alecm@cr...> - 2008-10-05 23:18
>> In any case, I thank you both for working on sorting this out.
> 
> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
> the change now?

yes, go for it. thanks++

-a

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-25 22:34

Attachments: Message as HTML
On Sunday 05 October 2008, Alec Muffett wrote:
> >> In any case, I thank you both for working on sorting this out.
> >
> > looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we
> > make the change now?
> >
> > yes, go for it. thanks++

Nathan Neulinger is the only one who can actually make said change ...

-mike

----------
BELOW IS ORIGINAL LICENSING DISCUSSION RE CHANGING TO GPL from Artistic.
----------

CrackLib was originally licensed with a variant of the Artistic license. In the
interests of wider acceptance and more modern licensing, it was switched with
the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed
as of 2007-10-01 with the following email from Alec Muffett.

The below email references nneul@umr.edu address, as that is the address that was used at the time. For any future emails regarding this, please use nneul@neulinger.org.

-------------------------------------

From alecm@crypticide.com Mon Oct  1 12:26:03 2007
Received: from umr-exproto2.cc.umr.edu ([131.151.0.192]) by UMR-CMAIL1.umr.edu with Microsoft SMTPSVC (6.0.3790.3959);
  Mon, 1 Oct 2007 12:26:03 -0500
Received: from scansrv2.srv.mst.edu ([131.151.1.114]) by umr-exproto2.cc.umr.edu with Microsoft SMTPSVC (6.0.3790.3959);
  Mon, 1 Oct 2007 12:26:02 -0500
Received: (qmail 8022 invoked from network); 1 Oct 2007 16:59:55 -0000
Received: from smtp1.srv.mst.edu (131.151.1.43)
  by scanin-ipvs.cc.umr.edu with SMTP; 1 Oct 2007 16:59:55 -0000
Received: from spunkymail-mx8.g.dreamhost.com (mx1.spunky.mail.dreamhost.com [208.97.132.47])
  by smtp1.srv.mst.edu (8.13.1/8.13.1) with ESMTP id l91Gxtpr020623
  for <nneul@umr.edu>; Mon, 1 Oct 2007 11:59:55 -0500
Received: from rutherford.zen.co.uk (rutherford.zen.co.uk [212.23.3.142])
  by spunkymail-mx8.g.dreamhost.com (Postfix) with ESMTP id 2C7734D311
  for <nneul@neulinger.org>; Mon, 01 Oct 2007 16:59:49 +0000
  (PDT)
  Received: from [82.68.43.14] (helo=[192.168.1.3])
  by rutherford.zen.co.uk with esmtp (Exim 4.50)
  id 11cOcX-0004Qt-6L
  for nneul@neulinger.org; Mon, 01 Oct 2007 16:59:49 +0000
Mime-Version: 1.0 (Apple Message framework v752.2)
In-Reply-To: <1b1b3fd80710010908k11dac0afp1f2dd471059f9a4@mail.gmail.com>
References: <1190922867.3457.147.camel@localhost.localdomain>
  <EC90713277D2BE41B7110CCD74E235CEF44F38@UMR-CMAIL1.umr.edu>
  <1b1b3fd80710010908k11dac0afp1f2dd471059f9a4@mail.gmail.com>
Content-Type: text/plain; charset=US-ASCII; delsp=yes; format=flowed
Message-Id: <117A1264-F6DC-4E25-B0DD-56FBFEBE6E9F@crypticide.com>
Content-Transfer-Encoding: 7bit
From: Alec Muffett <alecm@crypticide.com>
Subject: Re: cracklib license
Date: Mon, 1 Oct 2007 17:59:46 +0100
To: Nathan Neulinger <nneul@neulinger.org>
X-Mailer: Apple Mail (2.752.2)
X-Originating-Rutherford-IP: [82.68.43.14]
Return-Path: alecm@crypticide.com
X-OriginalArrivalTime: 01 Oct 2007 17:26:03.0008 (UTC) FILETIME=[2420C000:01C80450]
Status: RO
Content-Length: 585
The license for my code in the Cracklib distribution is henceforth GPL.

Happy now? :-)

-a

This package was debianized by Jean Pierre LeJacq <jplejacq@quoininc.com> on Wed, 25 Feb 1998. Martin Pitt <martin@piware.de> was the package's maintainer up to version 2.7.19-1. The current maintainer is Jan Dittberner <jandd@debian.org>.

It was downloaded from http://sourceforge.net/projects/cracklib

Copyright (c) 1993 Alec Muffett <alecm@crypto.dircon.co.uk>,
Copyright (c) 2005-2009 Nathan Neulinger <nneul@umr.edu>,
Copyright (c) 2008-2009 Jan Dittberner <jan@dittberner.info>

Modifications: Added cronjob, configuration file, and man pages.

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distribution or on the World Wide Web at http://www.gnu.org/licenses/old-licenses/lgpl-2.1.html. You can also obtain it by writing to the Free Software Foundation, Inc., 51 Franklin St, Fifth Floor, Boston, MA 02110-1301, USA.

Copyright information:

CrackLib was originally licensed with a variant of the Artistic license. In the interests of wider acceptance and more modern licensing, it was switched with the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed as of 2007-10-01 with the following email from Alec Muffett. Cracklib's license was changed from the GPL to the LGPL after consensus of all previous developers in October 2008, effective with release 2.8.15 released on 2009-11-19. See the email discussion below for both license changes.

-------------------------------------
-----------
EFFECTIVE OCT 2008, LICENSE IS BEING CHANGED TO LGPL-2.1 (though not reflected in released code until Nov 2009 - slow release cycle...)
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Discussion thread from mailing list archive, with approval from everyone actively involved or holding original licensing rights included.

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Attachments: Message as HTML
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> > much other than cleaning up the packaging and patches and a small
> > bit of
> > additional code, so whatever licensing y'all come up with is fine
> > by me.
I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be linked with any code, not just GPL...

My apologies for not chiming in in anything resembling a reasonable timeframe.

I'd also suggest the LGPL, for the reason you noted above. Alternately, GPLv2 with the option of using the library under a later version of the GPL would permit applications which were released under version 3 of the GPL to use the library, too, which would be sufficient for the packages which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

Cheers,

Nalin
> which are included in Fedora. FWIW, I'd personally lean toward LGPL.
> 
> > In any case, I thank you both for working on sorting this out.
>
> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make the change now?
> -mike

Re: [Cracklib-devel] cracklib license
From: Alec Muffett <alecm@cr...> - 2008-10-05 23:18
>> In any case, I thank you both for working on sorting this out.
>
> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make the change now?
>
yes. go for it. thanks++

-a

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-25 22:34

Attachments: Message as HTML
On Sunday 05 October 2008, Alec Muffett wrote:
  > >> In any case, I thank you both for working on sorting this out.
  > >
  > >> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make the change now?
  > >
  > >> yes. go for it. thanks++

Nathan Neulinger is the only one who can actually make said change ...
- mike

----------
BELOW IS ORIGINAL LICENSING DISCUSSION RE CHANGING TO GPL from Artistic.
----------

CrackLib was originally licensed with a variant of the Artistic license. In the interests of wider acceptance and more modern licensing, it was switched with the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed as of 2007-10-01 with the following email from Alec Muffett.
The below email references nneul@umr.edu address, as that is the address that was used at the time. For any future emails regarding this, please use nneul@neulinger.org.

-------------------------------------
From alecm@crypticide.com Mon Oct  1 12:26:03 2007
Received: from umr-exproto2.cc.umr.edu ([131.151.0.192]) by UMR-CMAIL1.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
   Mon, 1 Oct 2007 12:26:03 -0500
Received: from scansrv2.srv.mst.edu ([131.151.1.114]) by umr-exproto2.cc.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
   Mon, 1 Oct 2007 12:26:02 -0500
Received: (qmail 8022 invoked from network); 1 Oct 2007 16:59:55 -0000
Received: from smtp1.srv.mst.edu (131.151.1.43)
   by scanin-ipvs.cc.umr.edu with SMTP; 1 Oct 2007 16:59:55 -0000
Received: from spunkymail-mx8.g.dreamhost.com (mx1.spunky.mail.dreamhost.com [208.97.132.47])
   by smtp1.srv.mst.edu (8.13.1/8.13.1) with ESMTP id l91Gxtpr020623
for <nneul@umr.edu>; Mon, 1 Oct 2007 11:59:55 -0500
Received: from rutherford.zen.co.uk (rutherford.zen.co.uk [212.23.3.142])
   by spunkymail-mx8.g.dreamhost.com (Postfix) with ESMTP id 2C7734D311
for <nneul@neulinger.org>; Mon, 1 Oct 2007 09:59:50 -0700 (PDT)
Received: from [82.68.43.14] (helo=[192.168.1.3])
   by rutherford.zen.co.uk with essmtp (Exim 4.50)
   id l1cQcX-0004Qt-6L
for nneul@neulinger.org; Mon, 01 Oct 2007 16:59:49 +0000
Mime-Version: 1.0 (Apple Message framework v752.2)
In-Reply-To: <1b1b3fd8071010010908k11dac0afp1f2dd471059ff9a4@mail.gmail.com>
References: <1190922867.3457.147.camel@localhost.localdomain>
   <EC90713277D2BE41B7110CCCD74E235CEF44F38@UMR-CMAIL1.umr.edu>
   <1b1b3fd8071010010908k11dac0afp1f2dd471059ff9a4@mail.gmail.com>
Content-Type: text/plain; charset=US-ASCII; delsp=yes; format=flowed
Message-Id: <117A1264-F6DC-4E25-B0DD-56FBEFEBE6F9F@crypticide.com>
Content-Transfer-Encoding: 7bit
From: Alec Muffett <alecm@crypticide.com>
Subject: Re: cracklib license
Date: Mon, 1 Oct 2007 17:59:46 +0100
To: Nathan Neulinger <nneul@neulinger.org>
X-Mailer: Apple Mail (2.752.2)
X-Originating-Rutherford-IP: [82.68.43.14]
Return-Path: alecm@crypticide.com
X-OriginalArrivalTime: 01 Oct 2007 17:26:03.0008 (UTC) FILETIME=[2420C000:01C80450]
Status: RO
Content-Length: 585
Lines: 21
Any chance you could write me a self-contained email stating clearly
that the license is being changed to GPL, so I could include that
email
in the repository and clean up the repository/tarballs? I have all the
original discussion, but something succinct and self contained
would be
ideal.

The license for my code in the Cracklib distribution is henceforth GPL.

Happy now? :-)

-a

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Version 2.1, February 1999

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^L

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(signature of Ty Coon), 1 April 1990
Ty Coon, President of Vice

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1.299 libcap 219-67.el7

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---

This script updates the list of years in the copyright notices in most files maintained by the GDB project.

Usage: cd src/gdb && python copyright.py

Always review the output of this script before committing it!

A useful command to review the output is:

```bash
% filterdiff -x *.c -x *.cc -x *.h -x *.exp updates.diff
```
This removes the bulk of the changes which are most likely to be correct.
...

import datetime
import os
import os.path
import subprocess


def get_update_list():
    """Return the list of files to update.

    Assumes that the current working directory when called is the root of
    the GDB source tree (NOT the gdb/ subdirectory!). The names of
    the files are relative to that root directory.
    """
    result = []
    for gdb_dir in ('gdb', 'sim', 'include/gdb'):
        for root, dirs, files in os.walk(gdb_dir, topdown=True):
            for dirname in dirs:
                reldirname = "%s/%s" % (root, dirname)
                if (dirname in EXCLUDE_ALL_LIST
                    or reldirname in EXCLUDE_LIST
                    or reldirname in NOT_FSF_LIST
                    or reldirname in BY_HAND):
                    # Prune this directory from our search list.
                    dirs.remove(dirname)
            for filename in files:
                relpath = "%s/%s" % (root, filename)
                if (filename in EXCLUDE_ALL_LIST
                    or relpath in EXCLUDE_LIST
                    or relpath in NOT_FSF_LIST
                    or relpath in BY_HAND):
                    # Ignore this file.
                    pass
                else:
                    result.append(relpath)
    return result


def update_files(update_list):  
    """Update the copyright header of the files in the given list.

    We use gnulib's update-copyright script for that.
    """
    # We want to use year intervals in the copyright notices, and
    # all years should be collapsed to one single year interval,
    # even if there are "holes" in the list of years found in the

# original copyright notice (OK'ed by the FSF, case [gnu.org #719834]).
# os.environ['UPDATE_COPYRIGHT_USE_INTERVALS'] = '2'

# Perform the update, and save the output in a string.
update_cmd = ['bash', 'gdb/gnulib/import/extra/update-copyright']
update_cmd += update_list

p = subprocess.Popen(update_cmd, stdout=subprocess.PIPE,
                      stderr=subprocess.STDOUT)
update_out = p.communicate()[0]

# Process the output. Typically, a lot of files do not have
# a copyright notice :-(. The update-copyright script prints
# a well defined warning when it did not find the copyright notice.
# For each of those, do a sanity check and see if they may in fact
# have one. For the files that are found not to have one, we filter
# the line out from the output, since there is nothing more to do,
# short of looking at each file and seeing which notice is appropriate.
# Too much work! (~4,000 files listed as of 2012-01-03).
update_out = update_out.splitlines()
warning_string = ': warning: copyright statement not found'
warning_len = len(warning_string)

for line in update_out:
    if line.endswith('n'):
        line = line[:-1]
    if line.endswith(warning_string):
        filename = line[:-warning_len]
        if may_have_copyright_notice(filename):
            print line
        else:
            # Unrecognized file format. !?!
            print "*** " + line

def may_have_copyright_notice(filename):
    """Check that the given file does not seem to have a copyright notice.

    The filename is relative to the root directory.
    This function assumes that the current working directory is that root
    directory.

    The algorithm is fairly crude, meaning that it might return
    some false positives. I do not think it will return any false
    negatives... We might improve this function to handle more
    complex cases later...
    """

    # For now, it may have a copyright notice if we find the word
# "Copyright" at the (reasonable) start of the given file, say
# 50 lines...
MAX_LINES = 50

fd = open(filename)

lineno = 1
for line in fd:
    if 'Copyright' in line:
        return True
    lineno += 1
    if lineno > 50:
        return False
return False

def main():
    """The main subprogram.""
    if not os.path.isfile("gnulib/import/extra/update-copyright"):  
        print "Error: This script must be called from the gdb directory."
    root_dir = os.path.dirname(os.getcwd())
    os.chdir(root_dir)

    update_list = get_update_list()
    update_files(update_list)

    # Remind the user that some files need to be updated by HAND...
    if BY_HAND:
        print
        print "\033[31mREMINDER: The following files must be updated by hand.\033[0m"
        for filename in BY_HAND + MULTIPLE_COPYRIGHT_HEADERS:
            print " ", filename

############################################################################
# Some constants, placed at the end because they take up a lot of room.
# The actual value of these constants is not significant to the understanding
# of the script.
#
# Files which should not be modified, either because they are
# generated, non-FSF, or otherwise special (e.g. license text,
# or test cases which must be sensitive to line numbering).
# Filenames are relative to the root directory.
EXCLUDE_LIST = (
'gdb/CONTRIBUTE',
'gdb/gnulib'
)

# Files which should not be modified, either because they are
# generated, non-FSF, or otherwise special (e.g. license text,
# or test cases which must be sensitive to line numbering).
#
# Matches any file or directory name anywhere. Use with caution.
# This is mostly for files that can be found in multiple directories.
# Eg: We want all files named COPYING to be left untouched.

EXCLUDE_ALL_LIST = (
    "COPYING", "COPYING.LIB", "CVS", "configure", "copying.c",
    "fdl.texi", "gpl.texi", "aclocal.m4",
)

# The list of files to update by hand.

BY_HAND = (
    # These files are sensitive to line numbering.
    "gdb/testsuite/gdb.base/step-line.inp",
    "gdb/testsuite/gdb.base/step-line.c",
)

# Files containing multiple copyright headers. This script is only
# fixing the first one it finds, so we need to finish the update
# by hand.

MULTIPLE_COPYRIGHT_HEADERS = (
    "gdb/doc/gdb.texinfo",
    "gdb/doc/refcard.tex",
    "gdb/gdbarch.sh",
)

# The list of file which have a copyright, but not head by the FSF.
# Filenames are relative to the root directory.

NOT_FSF_LIST = (
    "gdb/exc_request.defs",
    "gdb/gdbtk",
    "gdb/testsuite/gdb.gdbtk/",
    "sim/arm/armemu.h", "sim/arm/armos.c", "sim/arm/gdbhost.c",
    "sim/arm/dbg_hif.h", "sim/arm/dbg_conf.h", "sim/arm/communicate.h",
    "sim/arm/armos.h", "sim/arm/armcopro.c", "sim/arm/armemu.c",
    "sim/arm/kid.c", "sim/arm/thumbemu.c", "sim/arm/armdefs.h",
    "sim/arm/armopts.h", "sim/arm/dbg_cp.h", "sim/arm/dbg_rdi.h",
    "sim/arm/parent.c", "sim/arm/armsupp.c", "sim/arm/armrdi.c",
    "sim/arm/bag.c", "sim/arm/armvirt.c", "sim/arm/main.c", "sim/arm/bag.h",
    "sim/arm/communicate.c", "sim/arm/gdbhost.h", "sim/arm/armfpe.h",
    "sim/arm/arminit.c",
)
"sim/common/cgen-fpu.c", "sim/common/cgen-fpu.h",
"sim/common/cgen-acfcp.c",
"sim/erc32/sis.h", "sim/erc32/erc32.c", "sim/erc32/func.c",
"sim/erc32/float.c", "sim/erc32/interf.c", "sim/erc32/sis.c",
"sim/erc32/exec.c",
"sim/mips/m16run.c", "sim/mips/sim-main.c",
"moxie/moxie-gdb.dts",
# Not a single file in sim/ppc/ appears to be copyright FSF :-(.
"sim/ppc/filter.h", "sim/ppc/gen-support.h", "sim/ppc/ld-insn.h",
"sim/ppc/hw_sem.c", "sim/ppc/hw_disk.c", "sim/ppc/decode_branch.h",
"sim/ppc/sim-endian.h", "sim/ppc/table.c", "sim/ppc/hw_core.c",
"sim/ppc/gen-support.c", "sim/ppc/gen-semantics.h", "sim/ppc/cpu.h",
"sim/ppc/sim_callbacks.h", "sim/ppc/ROM", "sim/ppc/Makefile.in",
"sim/ppc/emu_chirp.c", "sim/ppc/hw_nvram.c", "sim/ppc/dc-test.01",
"sim/ppc/hw_phb.c", "sim/ppc/hw_eeprom.c", "sim/ppc/bits.h",
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"sim/ppc/corefile-n.h", "sim/ppc/std-config.h", "sim/ppc/ld-decode.h",
"sim/ppc/filter_filename.h", "sim/ppc/hw_shm.c",
"sim/ppc/pk_disklabel.c", "sim/ppc/dc-simple", "sim/ppc/misc.h",
"sim/ppc/device_table.h", "sim/ppc/ld-insn.c", "sim/ppc/inline.c",
"sim/ppc/emu_bugapi.h", "sim/ppc/hw_cpu.h", "sim/ppc/debug.h",
"sim/ppc/hw_ide.c", "sim/ppc/debug.c", "sim/ppc/gen-itable.h",
"sim/ppc/interrupts.c", "sim/ppc/hw_glue.c", "sim/ppc/emu_unix.c",
"sim/ppc/sim_calls.c", "sim/ppc/dc-complex", "sim/ppc/ld-cache.c",
"sim/ppc/registers.h", "sim/ppc/dc-test.02", "sim/ppc/options.c",
"sim/ppc/igen.h", "sim/ppc/registers.c", "sim/ppc/device.h",
"sim/ppc/emu_chirp.h", "sim/ppc/hw_register.c", "sim/ppc/hw_init.c",
"sim/ppc/sim-endian-n.h", "sim/ppc/filter_filename.c",
"sim/ppc/decode_fields.h", "sim/ppc/hw_memory.c",
"sim/ppc/misc.c", "sim/ppc/decode.c", "sim/ppc/psim.h",
"sim/ppc/hw_trace.c", "sim/ppc/emu_netbsd.h", "sim/ppc/psim.c",
"sim/ppc/ppc-instructions", "sim/ppc/tree.h", "sim/ppc/README",
"sim/ppc/gen-icache.h", "sim/ppc/gen-model.h", "sim/ppc/ld-cache.h",
"sim/ppc/mon.c", "sim/ppc/corefile.h", "sim/ppc/vm.c",
"sim/ppc/INSTALL", "sim/ppc/gen-model.c", "sim/ppc/hw_cpu.c",
"sim/ppc/corefile.c", "sim/ppc/hw_opic.c", "sim/ppc/gen-icache.c",
"sim/ppc/events.h", "sim/ppc/os_emul.c", "sim/ppc/emu_generic.c",
"sim/ppc/main.c", "sim/ppc/hw_com.c", "sim/ppc/gen-semantics.c",
"sim/ppc/emu_bugapi.c", "sim/ppc/device.c", "sim/ppc/emu_generic.h",
"sim/ppc/tree.c", "sim/ppc/mon.h", "sim/ppc/interrupts.h",
"sim/ppc/cap.c", "sim/ppc/cpu.c", "sim/ppc/hw_phb.h",
"sim/ppc/device_table.c", "sim/ppc/lf.c", "sim/ppc/lf.c",
"sim/ppc/dc-stupid", "sim/ppc/hw_pal.c", "sim/ppc/ppc-spr-table",
"sim/ppc/emu_unix.h", "sim/ppc/words.h", "sim/ppc/basics.h",
"sim/ppc/hw_htab.c", "sim/ppc/lf.h", "sim/ppc/ld-decode.c",
"sim/ppc/sim-endian.c", "sim/ppc/gen-itable.c",
"sim/ppc/decode_expression.h", "sim/ppc/table.h", "sim/ppc/dgen.c"
"sim/ppc/events.c", "sim/ppc/gen-idecode.h", "sim/ppc/emul_netbsd.c",
"sim/ppc/igen.c", "sim/ppc/vm_n.h", "sim/ppc/vm.h",
"sim/ppc/hw_iobus.c", "sim/ppc/inline.h",
"sim/testsuite/sim/bfin/s21.s", "sim/testsuite/sim/mips/mips32-dsp2.s",
)

if __name__ == "__main__":
    main()

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   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the
application. Therefore, Subsection 2d requires that any
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is
the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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   b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

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   d) If a facility in the modified Library refers to a function or a
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the facility, other than as an argument passed when the facility
is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
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4. You may copy and distribute the Library (or a portion or
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under the terms of Sections 1 and 2 above provided that you accompany
it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
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5. A program that contains no derivative of any portion of the
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6. As an exception to the Sections above, you may also compile or
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c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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@end enumerate

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first, please read @url{http://www.gnu.org/philosophy/why-not-lgpl.html}.

@c man end
BEGIN{
    FS="";
    print "/** Do not modify this file!! " \ 
        "*- buffer-read-only: t -*vi" \ 
        ":set ro:";
    print " It is created automatically by copying.awk.";
    print " Modify copying.awk instead. <==" \;
    print ""
    print 
#include "defs.h"
#include "command.h"
#include "gdbcmd.h"

    print 
static void show_copying_command (char *, int);

    print 
static void show_warranty_command (char *, int);

    print 
void _initialize_copying (void);

    print 
static void;
    print "show_copying_command (char *ignore, int from_tty)";
    print "\n"
}
NR == 1;/\[ ]*15\. Disclaimer of Warranty\[ ]*$/
if ($0 ~ /\[ ]*15\. Disclaimer of Warranty\[ ]*$/)
    {
        printf " printf_filtered (\n\n);\n"
    }
else if ($0 !~ /\[ ]*15\. Disclaimer of Warranty\[ ]*$/)
    {
        printf " printf_filtered (" ;
        for (i = 1; i < NF; i++)
            printf " %s\", $i;
        printf " %s\n\n);\n", $NF;
    }
}

;/\[ ]*15\. Disclaimer of Warranty\[ ]*$/
    print "\n"
    print "static void";
    print "show_warranty_command (char *ignore, int from_tty)";
    print "\n"
}

;/\[ ]*15\. Disclaimer of Warranty\[ ]*$/
    \*END OF TERMS AND CONDITIONS[ ]*$/
if (! ($0 ~ /\[ ]*END OF TERMS AND CONDITIONS[ ]*$/))
    {
        printf " printf_filtered (" ;

for (i = 1; i < NF; i++)
    printf "%-s\n", Si;
    printf "%-s\n\n", $NF;
}
}
END
print "}"
print ""
print "void"
print "_initialize_copying (void)"
print "{";
print " add_cmd ("copying", no_set_class, show_copying_command,";
print " _("Conditions for redistributing copies of GDB.").\n";
print " &showlist);";
print " add_cmd ("warranty", no_set_class, show_warranty_command,";
print " _("Various kinds of warranty you do not have.").\n";
print " &showlist);";
print ""
print "/* For old-timers, allow "info copying", etc. */";
print " add_info ("copying", show_copying_command,";
print " _("Conditions for redistributing copies of GDB.").\n";
print " add_info ("warranty", show_warranty_command,";
print " _("Various kinds of warranty you do not have.").\n";
print "}";
} @node Library Copying
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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and
distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public
License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

   a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

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We protect your rights with a two-step method: (1) we copyright the library, and (2) we offer you this license, which gives you legal permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that there is no warranty for the free library. Also, if the library is
modified by someone else and passed on, the recipients should know that what they have is not the original version, so that the original author's reputation will not be affected by problems that might be introduced by others.

Finally, software patents pose a constant threat to the existence of any free program. We wish to make sure that a company cannot effectively restrict the users of a free program by obtaining a restrictive license from a patent holder. Therefore, we insist that any patent license obtained for a version of the library must be consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating
Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

   a) The modified work must itself be a software library.

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   d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the
entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.
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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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That's all there is to it!
/* nicstar.c  v0.22  Jawaid Bazyar (bazyar@hypermall.com)
 * nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
 *
 * Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
 * http://www.hypermall.com/
 * 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY interrupts us (except possibly for removal/insertion of the cable?)
 * 10/4/97 - began heavy inline documentation of the code. Corrected typos and spelling mistakes.
 * 10/5/97 - added code to handle PHY interrupts, disable PHY on loss of link, and correctly re-enable PHY when link is re-established. (put back CFG_PHYIE)
 *
 * Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
 *
 * R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
 *
 * Linux driver for the IDT77201 NICStAR PCI ATM controller.
 * PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
 * see init_nicstar() for PHY initialization to change this. This driver expects the Linux ATM stack to support scatter-gather lists.
 * (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
 *
 * Implementing minimal-copy of received data:
 * IDT always receives data into a small buffer, then large buffers as needed. This means that data must always be copied to create the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
 * Fix is simple: make large buffers large enough to hold entire
* SDU, and leave <small_buffer_data> bytes empty at the start. Then
* copy small buffer contents to head of large buffer.
* Trick is to avoid fragmenting Linux, due to need for a lot of large
* buffers. This is done by 2 things:
*   1) skb->destructor / skb->atm.recycle_buffer
*      combined, allow nicstar_free_rx_skb to be called to
*      recycle large data buffers
*   2) skb_clone of received buffers
* See nicstar_free_rx_skb and linearize_buffer for implementation
* details.
*
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*
* M. Welsh, 6 July 1996
*
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bzip2/libbzip2 version 1.0.6 of 6 September 2010

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1.313 brotli 2.22.7-2.el7

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1.314 cryptsetup 2.0.3 5.el7

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(This file is under construction.)*- text -*.

If you've contributed to gas and your name isn't listed here, it is not meant as a slight. I just don't know about it. Email me, nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into the documentation, and info on specific files will go into an AUTHORS file, as requested by the FSF.

++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of
Buffalo University and Torbjorn Granlund of the Swedish Institute of
Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS
back end (tc-mips.c, tc-mips.h), and contributed Rose format support
that hasn't been merged in yet. Ralph Campbell worked with the MIPS
code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors
(tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format
(obj-ieee), was written by Steve Chamberlain of Cygnus Solutions.
Steve also modified the COFF back end (obj-coffbfd) to use BFD for
some low-level operations, for use with the Hitachi, 29k and Zilog
targets.

John Gilmore built the AMD 29000 support, added .include support, and
simplified the configuration of which versions accept which
pseudo-ops. He updated the 68k machine description so that Motorola's
opcodes always produced fixed-size instructions (e.g. jsr), while
synthetic instructions remained shrinkable (jbsr). John fixed many
bugs, including true tested cross-compilation support, and one bug in
relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT
syntaxes for the 68k, completed support for some COFF targets (68k,
i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael
Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support,
and made a few other minor patches. He handled the binutils releases
for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.
Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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1.316 avahi 0.6.31-19.el7
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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a
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c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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fdlimb contains general algorithms useful for runtimes and compilers to support strict double and float mathematical operations.

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1998-05-11

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*m4/ax_func_which_gethostbyname_r.m4*

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   adapted to BSD conventions for symbol names and debugging.)
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<http://www.gnu.org/licenses/>. */

/* Use the Sequent Symmetry assembler syntax. */
/* Define the syntax of pseudo-ops, labels and comments. */

/* Assembler pseudos to introduce constants of various size. */

#define ASM_BYTE  ".byte	"
#define ASM_SHORT  ".word	"
#define ASM_LONG  ".long	"
#define ASM_QUAD  ".quad	" /* Should not be used for 32bit compilation. */

/* This was suggested, but it shouldn't be right for DBX output. -- RMS */
#define ASM_OUTPUT_SOURCE_FILENAME(FILE, NAME) /*

/* Define the syntax of labels and symbol definitions/declarations. */

/* This is how to output an assembler line
that says to advance the location counter by SIZE bytes. */
#define ASM_OUTPUT_SKIP(FILE,SIZE)  \
fprintf (FILE, "\t.space HOST_WIDE_INT_PRINT_UNSIGNED\n", (SIZE))

/* Define the syntax of labels and symbol definitions/declarations. */

/* This says how to output an assembler line
to define a global common symbol. */
#define ASM_OUTPUT_COMMON(FILE, NAME, SIZE, ROUNDED)  \
( fputs (".comm ", (FILE)),\n assemble_name ((FILE), (NAME)),\n fprintf ((FILE), ",%u\n", (int)(ROUNDED)))

/* This says how to output an assembler line
to define a local common symbol. */

#define ASM_OUTPUT_LOCAL(FILE, NAME, SIZE, ROUNDED)  \
( fputs (".lcomm ", (FILE)),\n assemble_name ((FILE), (NAME)),\n fprintf ((FILE), ",%u\n", (int)(ROUNDED)))

#ifndef HAVE_GAS_LCOMM_WITH_ALIGNMENT
#define ASM_OUTPUT_ALIGNED_LOCAL(FILE, NAME, SIZE, ALIGNMENT)  \
#endif
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# By default the script will update all directories for which the
# output has been vetted. You can instead pass the names of individual
# directories, including those that haven't been approved. So:
#
# update-copyright.pl --this-year
#
# is the command that would be used at the beginning of a year to update
# all copyright notices (and possibly at other times to check whether
# new files have been added with old years). On the other hand:
#
# update-copyright.pl --this-year libjava
#
# would run the script on just libjava/.
#
# Note that things like --version output strings must be updated before
# this script is run. There's already a separate procedure for that.

import os
import re
import sys
import time
import subprocess

class Errors:
    def __init__(self):
        self.num_errors = 0

    def report(self, filename, string):

if filename:
    string = filename + ':' + string
sys.stderr.write(string + '\n')
self.num_errors += 1

def ok(self):
    return self.num_errors == 0

class GenericFilter:
    def __init__(self):
        self.skip_files = set()
        self.skip_dirs = set()
        self.skip_extensions = set()
        self.fossilised_files = set()
        self.own_files = set()

        self.skip_files |= set(['
        # Skip licence files.
        'COPYING',
        'COPYING.LIB',
        'COPYING3',
        'COPYING3.LIB',
        'LICENSE',
        'fdl.texi',
        'gpl_v3.texi',
        'fdl-1.3.xml',
        'gpl-3.0.xml',

        # Skip auto- and libtool-related files
        'aclocal.m4',
        'compile',
        'config.guess',
        'config.sub',
        'depcomp',
        'install-sh',
        'libtool.m4',
        'ltmain.sh',
        'ltoptions.m4',
        'ltSugar.m4',
        'ltversion.m4',
        'lt--obsolete.m4',
        'missing',
        'mkdep',
        'mkinstalldirs',
        'move-if-change',
        'shlibpath.m4',
        'symlink-tree',
        'ylwrap',

# Skip FSF mission statement, etc.
'gnu.texi',
'funding.texi',
'appendix_free.xml',

# Skip imported texinfo files.
'texinfo.tex',
)

def get_line_filter (self, dir, filename):
    if filename.startswith ('ChangeLog'):
        # Ignore references to copyright in changelog entries.
        return re.compile ('\t')

    return None

def skip_file (self, dir, filename):
    if filename in self.skip_files:
        return True

    (base, extension) = os.path.splitext (os.path.join (dir, filename))
    if extension in self.skip_extensions:
        return True

    if extension == '.in':
        # Skip .in files produced by automake.
        if os.path.exists (base + '.am'):
            return True

    # Skip files produced by autogen
    if (os.path.exists (base + '.def')
        and os.path.exists (base + '.tpl')):
        return True

    # Skip configure files produced by autoconf
    if filename == 'configure':
        if os.path.exists (base + '.ac'):
            return True
        if os.path.exists (base + '.in'):
            return True

    return False

def skip_dir (self, dir, subdir):
    return subdir in self.skip_dirs
def is_fossilised_file(self, dir, filename):
    if filename in self.fossilised_files:
        return True
    # Only touch current current ChangeLogs.
    if filename != 'ChangeLog' and filename.find('ChangeLog') >= 0:
        return True
    return False

def by_package_author(self, dir, filename):
    return filename in self.own_files

class Copyright:
    def __init__(self, errors):
        self.errors = errors

        # Characters in a range of years. Include '.' for typos.
        ranges = '[0-9][?:-][0-9,\.\s]+[0-9]'

        # Non-whitespace characters in a copyright holder's name.
        name = '\w[-·]'

        # Matches one year.
        self.year_re = re.compile('[0-9]+')

        # Matches part of a year or copyright holder.
        self.continuation_re = re.compile(ranges + '|' + name)

        # Matches a full copyright notice:
        self.copyright_re = re.compile(r'
            ([Cc]opyright\s+)[Cc]opyright\s+\([^\(\)]\)
            ([Cc]opyright\s+\(\[Cc\]\))
            ([Cc]opyright\s+\%s)
            ([Cc]opyright\s+copy;)
            ([Cc]opyright\s+\@copyright\{\})
            \@set\s+\@copyright\{\w-\}\)
          ', re.IGNORECASE)

        # 2: the years. Include the whitespace in the year, so that
        # we can remove any excess.
        '\(\s*?:\s+\)\s+\(\[Cc\]\)\s+\@value\{[^\{}]*\}\s+\)\s*\)'

        # 3: 'by ', if used
        '\(by\s+\)\s*\)'"
A regexp for notices that might have slipped by. Just matching
# 'copyright' is too noisy, and 'copyright.*[0-9]' falls foul of
# HTML header markers, so check for 'copyright' and two digits.
self.other_copyright_re = re.compile('copyright.*[0-9][0-9]',
    re.IGNORECASE)
self.comment_re = re.compile('#[+][*]+|[%]+|//+[c dnl']
s
self.holders = { '@copying': '@copying' }
s
self.holder_prefixes = set()

def add_package_author (self, holder, canon_form = None):
    if not canon_form:
        canon_form = holder
    self.holders[holder] = canon_form
    index = holder.find (' ')
    while index >= 0:
        self.holder_prefixes.add (holder[:index])
        index = holder.find (' ', index + 1)

class BadYear():
    def __init__ (self, year):
        self.year = year

    def __str__ (self):
        return 'unrecognised year: ' + self.year

def parse_year (self, string):
    year = int (string)
    if len (string) == 2:
        if year > 70:
            return year + 1900
    elif len (string) == 4:
        return year
    raise self.BadYear (string)
def year_range (self, years):
    year_list = [self.parse_year (year)
                 for year in self.year_re.findall (years)]
    assert len (year_list) > 0
    return (min (year_list), max (year_list))

def set_use_quilt (self, use_quilt):
    self.use_quilt = use_quilt

def include_year (self, year):
    assert not self.max_year
    self.max_year = year

def canonicalise_years (self, dir, filename, filter, years):
    # Leave texinfo variables alone.
    if years.startswith ('@value '):
        return years
    (min_year, max_year) = self.year_range (years)
    # Update the upper bound, if enabled.
    if self.max_year and not filter.is_fossilised_file (dir, filename):
        max_year = max (max_year, self.max_year)
    # Use a range.
    if min_year == max_year:
        return '%d' % min_year
    else:
        return '%d-%d' % (min_year, max_year)

def strip_continuation (self, line):
    line = line.lstrip()
    match = self.comment_re.match (line)
    if match:
        line = line[match.end():].lstrip()
    return line

def is_complete (self, match):
    holder = match.group (4)
    return (holder
            and (holder not in self.holder_prefixes
                 or holder in self.holders))

def update_copyright (self, dir, filename, filter, file, line, match):
    orig_line = line
    next_line = None
    pathname = os.path.join (dir, filename)
intro = match.group(1)
if intro.startswith('@set'):
    # Texinfo year variables should always be on one line
    after_years = line[match.end(2):].strip()
    if after_years != ':
        self.errors.report(pathname,
                          'trailing characters in @set: ' + after_years)
        return (False, orig_line, next_line)
else:
    # If it looks like the copyright is incomplete, add the next line.
    while not self.is_complete(match):
        try:
            next_line = file.next()
        except StopIteration:
            break
        # If the next line doesn’t look like a proper continuation,
        # assume that what we’ve got is complete.
        continuation = self.strip_continuation(next_line)
        if not self.continuation_re.match(continuation):
            break
        # Merge the lines for matching purposes.
        orig_line += next_line
        line = line.rstrip() + ' ' + continuation
        next_line = None
        # Rematch with the longer line, at the original position.
        match = self.copyright_re.match(line, match.start())
        assert match
        holder = match.group(4)

        # Use the filter to test cases where markup is getting in the way.
        if filter.by_package_author(dir, filename):
            assert holder not in self.holders
        elif not holder:
            self.errors.report(pathname, 'missing copyright holder')
            return (False, orig_line, next_line)
        elif holder not in self.holders:
            self.errors.report(pathname, 'unrecognised copyright holder: ' + holder)
            return (False, orig_line, next_line)
        else:
# See whether the copyright is associated with the package
# author.
canon_form = self.holders[holder]
if not canon_form:
    return (False, orig_line, next_line)

# Make sure the author is given in a consistent way.
line = (line[:match.start (4)]
        + canon_form
        + line[match.end (4):])

# Remove any 'by'
line = line[:match.start (3)] + line[match.end (3):]

# Update the copyright years.
years = match.group (2).strip()
try:
    canon_form = self.canonicalise_years(dir, filename, filter, years)
except self.BadYear as e:
    self.errors.report (pathname, str (e))
    return (False, orig_line, next_line)

line = (line[:match.start (2)]
        + ' ' + canon_form + self.separator
        + line[match.end (2):])

# Use the standard (C) form.
if intro.endswith ('right'):
    intro += '(C)
elif intro.endswith ('(c)'):
    intro = intro[:-3] + '(C')
line = line[:match.start (1)] + intro + line[match.end (1):]

# Strip trailing whitespace
line = line.rstrip() + '\n'

return (line != orig_line, line, next_line)

def process_file (self, dir, filename, filter):
    pathname = os.path.join (dir, filename)
if filename.endswith ('.tmp'):
    # Looks like something we tried to create before.
    try:
        os.remove (pathname)
    except OSError:
        pass
    return
lines = []
changed = False
line_filter = filter.get_line_filter (dir, filename)
with open (pathname, 'r') as file:
    prev = None
    for line in file:
        while line:
            next_line = None
            # Leave filtered-out lines alone.
            if not (line_filter and line_filter.match (line)):
                match = self.copyright_re.search (line)
                if match:
                    res = self.update_copyright (dir, filename, filter,
                                              file, line, match)
                    (this_changed, line, next_line) = res
                    changed = changed or this_changed

            # Check for copyright lines that might have slipped by.
            elif self.other_copyright_re.search (line):
                self.errors.report (pathname,
                                    'unrecognised copyright: %s'
                                    % line.strip())
                lines.append (line)
            line = next_line

        # If something changed, write the new file out.
    if changed and self.errors.ok():
        tmp_pathname = pathname + '.tmp'
        with open (tmp_pathname, 'w') as file:
            for line in lines:
                file.write (line)
        if self.use_quilt:
            subprocess.call (['quilt', 'add', pathname])
        os.rename (tmp_pathname, pathname)

def process_tree (self, tree, filter):
    for (dir, subdirs, filenames) in os.walk (tree):
        # Don't recurse through directories that should be skipped.
        for i in xrange (len (subdirs) - 1, -1, -1):
            if filter.skip_dir (dir, subdirs[i]):
                del subdirs[i]

        # Handle the files in this directory.
        for filename in filenames:
            if filter.skip_file (dir, filename):
                sys.stdout.write ('Skipping %s
' % os.path.join (dir, filename))
            else:
self.process_file (dir, filename, filter)
class CmdLine:
def __init__ (self, copyright = Copyright):
self.errors = Errors()
self.copyright = copyright (self.errors)
self.dirs = []
self.default_dirs = []
self.chosen_dirs = []
self.option_handlers = dict()
self.option_help = []
self.add_option ('--help', 'Print this help', self.o_help)
self.add_option ('--quilt', '"quilt add" files before changing them',
self.o_quilt)
self.add_option ('--this-year', 'Add the current year to every notice',
self.o_this_year)
def add_option (self, name, help, handler):
self.option_help.append ((name, help))
self.option_handlers[name] = handler
def add_dir (self, dir, filter = GenericFilter()):
self.dirs.append ((dir, filter))
def o_help (self, option = None):
sys.stdout.write ('Usage: %s [options] dir1 dir2...\n\n'
'Options:\n' % sys.argv[0])
format = '%-15s %s\n'
for (what, help) in self.option_help:
sys.stdout.write (format % (what, help))
sys.stdout.write ('\nDirectories:\n')
format = '%-25s'
i=0
for (dir, filter) in self.dirs:
i += 1
if i % 3 == 0 or i == len (self.dirs):
sys.stdout.write (dir + '\n')
else:
sys.stdout.write (format % dir)
sys.exit (0)
def o_quilt (self, option):
self.copyright.set_use_quilt (True)
def o_this_year (self, option):
self.copyright.include_year (time.localtime().tm_year)

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def main(self):
    for arg in sys.argv[1:]:
        if arg[1:] != '-':
            self.chosen_dirs.append(arg)
        elif arg in self.option_handlers:
            self.option_handlers[arg](arg)
        else:
            self.errors.report(None, 'unrecognised option: ' + arg)
        if self.errors.ok():
            if len(self.chosen_dirs) == 0:
                self.chosen_dirs = self.default_dirs
            if len(self.chosen_dirs) == 0:
                self.o_help()
            else:
                for chosen_dir in self.chosen_dirs:
                    canon_dir = os.path.join(chosen_dir, '')
                    count = 0
                    for (dir, filter) in self.dirs:
                        if (dir + os.sep).startswith(canon_dir):
                            count += 1
                            self.copyright.process_tree(dir, filter)
                    if count == 0:
                        self.errors.report(None, 'unrecognised directory: ' + chosen_dir)
                sys.exit(0 if self.errors.ok() else 1)

#----------------------------------------

class TopLevelFilter(GenericFilter):
    def skip_dir(self, dir, subdir):
        return True

class ConfigFilter(GenericFilter):
    def __init__(self):
        GenericFilter.__init__(self)
    def skip_file(self, dir, filename):
        if filename.endswith('.m4'):
            pathname = os.path.join(dir, filename)
            with open(pathname) as file:
                # Skip files imported from gettext.
                if file.readline().find('gettext-') >= 0:
                    return True
            return GenericFilter.skip_file(self, dir, filename)

class GCCFilter(GenericFilter):
    def __init__(self):
        ...
GenericFilter.__init__(self)

self.skip_files |= set([
    # Not part of GCC
    'math-68881.h',
])

self.skip_dirs |= set([
    # Better not create a merge nightmare for the GNAT folks.
    'ada',

    # Handled separately.
    'testsuite',
])

self.skip_extensions |= set([
    # Maintained by the translation project.
    '.po',

    # Automatically-generated.
    '.pot',
])

self.fossilised_files |= set([
    # Old news won't be updated.
    'ONEWS',
])

class TestsuiteFilter(GenericFilter):
    def __init__(self):
        GenericFilter.__init__(self)

        self.skip_extensions |= set([
            # Don't change the tests, which could be woend by anyone.
            '.c',
            '.C',
            '.cc',
            '.h',
            '.hs',
            '.f',
            '.f90',
            '.go',
            '.inc',
            '.java',
        ])

    def skip_file(self, dir, filename):
        # g++.niklas/README contains historical copyright information
class LibCppFilter(GenericFilter):
    def __init__(self):
        GenericFilter.__init__(self)

        self.skip_extensions |= set([
            # Maintained by the translation project.
            '.po',

            # Automatically-generated.
            '.pot',
        ])

class LibGCCFilter(GenericFilter):
    def __init__(self):
        GenericFilter.__init__(self)

        self.skip_dirs |= set([
            # Imported from GLIBC.
            'soft-fp',
        ])

class LibJavaFilter(GenericFilter):
    def __init__(self):
        GenericFilter.__init__(self)

        self.skip_dirs |= set([
            # Handled separately.
            'testsuite',

            # Not really part of the library
            'contrib',

            # Imported from upstream
            'classpath',
            'libltdl',
        ])

    def get_line_filter(self, dir, filename):
        if filename == 'NameDecoder.h':
            return re.compile('.*NAME_COPYRIGHT')
        if filename == 'ICC_Profile.h':
            return re.compile('.*icSigCopyrightTag')
        return GenericFilter.get_line_filter(self, dir, filename)
class LibMudflapFilter (GenericFilter):
    def __init__ (self):
        GenericFilter.__init__ (self)

        self.skip_dirs |= set ({
            # Handled separately.
            'testsuite',
        })

class LibStdCxxFilter (GenericFilter):
    def __init__ (self):
        GenericFilter.__init__ (self)

        self.skip_files |= set ({
            # Contains no copyright of its own, but quotes the GPL.
            'intro.xml',
        })

        self.skip_dirs |= set ({
            # Contains automatically-generated sources.
            'html',

            # The testsuite data files shouldn't be changed.
            'data',

            # Contains imported images
            'images',
        })

        self.own_files |= set ({
            # Contains markup around the copyright owner.
            'spine.xml',
        })

    def get_line_filter (self, dir, filename):
        if filename == 'boost_concept_check.h':
            return re.compile ('// \(C\) Copyright Jeremy Siek')
        return GenericFilter.get_line_filter (self, dir, filename)

class GCCCopyright (Copyright):
    def __init__ (self, errors):
        Copyright.__init__ (self, errors)

        canon_fsfs = 'Free Software Foundation, Inc.'
        self.add_package_author ('Free Software Foundation', canon_fsfs)
        self.add_package_author ('Free Software Foundation.', canon_fsfs)
        self.add_package_author ('Free Software Foundation Inc.', canon_fsfs)
self.add_package_author('Free Software Foundation, Inc.', canon_fsf)
self.add_package_author('Free Software Foundation, Inc.', canon_fsf)
self.add_package_author('The Free Software Foundation', canon_fsf)
self.add_package_author('The Free Software Foundation, Inc.', canon_fsf)
self.add_package_author('Software Foundation, Inc.', canon_fsf)

self.add_external_author('ARM')
self.add_external_author('AdaCore')
self.add_external_author('Ami Tavory and Vladimir Dreizin, IBM-HRL.')
self.add_external_author('Cavium Networks.')
self.add_external_author('Faraday Technology Corp.')
self.add_external_author('Florida State University')
self.add_external_author('Greg Colvin and Beman Dawes.')
self.add_external_author('Hewlett-Packard Company')
self.add_external_author('Information Technology Industry Council.')
self.add_external_author('James Theiler, Brian Gough')
self.add_external_author('Makoto Matsumoto and Takuji Nishimura,')
self.add_external_author('National Research Council of Canada.')
self.add_external_author('Peter Dimov and Multi Media Ltd.')
self.add_external_author('Peter Dimov')
self.add_external_author('Pipeline Associates, Inc.')
self.add_external_author('Regents of the University of California.')
self.add_external_author('Silicon Graphics Computer Systems, Inc.')
self.add_external_author('Silicon Graphics')
self.add_external_author('Stephen L. Moshier')
self.add_external_author('Sun Microsystems, Inc. All rights reserved.')
self.add_external_author('The Go Authors. All rights reserved.')
self.add_external_author('The Go Authors. All rights reserved.')
self.add_external_author('The Regents of the University of California.')
self.add_external_author('Unicode, Inc.')
self.add_external_author('University of Toronto.')

class GCCCmdLine (CmdLine):
def __init__ (self):
    CmdLine.__init__ (self, GCCCopyright)

    self.add_dir ('.', TopLevelFilter())
    # boehm-gc is imported from upstream.
    self.add_dir ('config', ConfigFilter())
    # contrib isn't really part of GCC.
    self.add_dir ('fixincludes')
    self.add_dir ('gcc', GCCFilter())
    self.add_dir (os.path.join ('gcc', 'testsuite'), TestsuiteFilter())
    self.add_dir ('gnattools')
    self.add_dir ('include')
    self.add_dir ('libada')
    self.add_dir ('libatomic')
self.add_dir ('libbacktrace')
self.add_dir ('libcpp', LibCppFilter())
self.add_dir ('libdecnumber')
# libffi is imported from upstream.
self.add_dir ('libgcc', LibGCCFilter())
self.add_dir ('libgfortran')
self.add_dir ('libgomp')
self.add_dir ('libiberty')
self.add_dir ('libitm')
self.add_dir ('libjava', LibJavaFilter())
self.add_dir (os.path.join ('libjava', 'testsuite'), TestsuiteFilter())
self.add_dir ('libmudflap', LibMudflapFilter())
self.add_dir (os.path.join ('libmudflap', 'testsuite'), TestsuiteFilter())
self.add_dir ('libobjc')
self.add_dir ('libquadmath')
# libsanitiser is imported from upstream.
self.add_dir ('libssp')
self.add_dir ('libstdc++-v3', LibStdCxxFilter())
self.add_dir ('lto-plugin')
# zlib is imported from upstream.

self.default_dirs = [
    'gcc',
    'libada',
    'libatomic',
    'libbacktrace',
    'libcpp',
    'libdecnumber',
    'libgcc',
    'libgfortran',
    'libgomp',
    'libitm',
    'libmudflap',
    'libobjc',
    'libstdc++-v3',
]

GCCCmdLine().main()
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package java.security.acl;

/**
 * This interface provides information about a permission that can be granted. Note that this is <em>not</em> the same as the class <code>java.security.Permission</code>.
 *
 * @version 0.0
 *
 * @author Aaron M. Renn (arenn@urbanophile.com)
 */

public interface Permission {
    /**
     * This method tests whether or not a specified <code>Permission</code> (passed as an <code>Object</code>) is the same as this permission.
     *
     * @param perm The permission to check for equality
     *
     * @return <code>true</code> if the specified permission is the same as this one, <code>false</code> otherwise
     */
    boolean equals (Object perm);

    /**
     * This method returns this <code>Permission</code> as a <code>String</code>.
     *
     * @return A <code>String</code> representing this permission.
     */
    String toString();
}

GCC RUNTIME LIBRARY EXCEPTION

Version 3.1, 31 March 2009

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/* Permission.java -- The superclass for all permission objects  

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package java.security;  

import gnu.java.lang.CPStringBuilder;  

import java.io.Serializable;
/**
 * This class is the abstract superclass of all classes that implement
 * the concept of a permission. A permission consists of a permission name
 * and optionally a list of actions that relate to the permission. The
 * actual meaning of the name of the permission is defined only in the
 * context of a subclass. It may name a resource to which access permissions
 * are granted (for example, the name of a file) or it might represent
 * something else entirely. Similarly, the action list only has meaning
 * within the context of a subclass. Some permission names may have no
 * actions associated with them. That is, you either have the permission
 * or you don't.
 *<p>The most important method in this class is <code>implies</code>. This
 * checks whether if one has this permission, then the specified
 * permission is also implied. As a conceptual example, consider the
 * permissions "Read All Files" and "Read File foo". The permission
 * "Read All Files" implies that the caller has permission to read the
 * file foo.
 *<p><code>Permission</code>'s must be immutable - do not change their
 * state after creation.
 * @author Aaron M. Renn (arenn@urbanophile.com)
 * @see Permissions
 * @see PermissionCollection
 * @since 1.1
 * @status updated to 1.4
 */
public abstract class Permission implements Guard, Serializable
{
    /**
     * Compatible with JDK 1.1+.
     */
    private static final long serialVersionUID = -5636570222231596674L;

    /**
     * This is the name assigned to this permission object.
     *
     * @serial the name of the permission
     */
    private String name;

    /**
     * Create an instance with the specified name.
     *
     * @param name the permission name
     */
    public Permission(String name)
{  
  this.name = name;
}

/**  
* This method implements the <code>Guard</code> interface for this class.  
* It calls the <code>checkPermission</code> method in  
* <code>SecurityManager</code> with this <code>Permission</code> as its  
* argument. This method returns silently if the security check succeeds  
* or throws an exception if it fails.  
*  
* @param obj the <code>Object</code> being guarded - ignored by this class  
* @throws SecurityException if the security check fails  
* @see GuardedObject  
* @see SecurityManager#checkPermission(Permission)  
*/
public void checkGuard(Object obj)  
{
  SecurityManager sm = System.getSecurityManager();
  if (sm != null)
    sm.checkPermission(this);
}

/**  
* This method tests whether this <code>Permission</code> implies that the  
* specified <code>Permission</code> is also granted.  
*  
* @param perm the <code>Permission</code> to test against  
* @return true if perm is implied by this  
*/
public abstract boolean implies(Permission perm);

/**  
* Check to see if this object equals obj. Use <code>implies</code>, rather  
* than <code>equals</code>, when making access control decisions.  
*  
* @param obj the object to compare to  
* @return a hash value  
*/
public abstract int hashCode();
/**
 * Get the name of this <code>Permission</code>.
 *
 * @return the name
 */
public final String getName()
{
    return name;
}

/**
 * This method returns the list of actions for this <code>Permission</code>
 * as a <code>String</code>. The string should be in canonical order, for
 * example, both <code>new FilePermission(f, "write,read")</code> and
 * <code>new FilePermission(f, "read,write")</code> have the action list
 * "read,write".
 *
 * @return the action list for this <code>Permission</code>
 */
public abstract String getActions();

/**
 * This method returns an empty <code>PermissionCollection</code> object
 * that can store permissions of this type, or <code>null</code> if no
 * such collection is defined. Subclasses must override this to provide
 * an appropriate collection when one is needed to accurately calculate
 * <code>implies</code>.
 *
 * @return a new <code>PermissionCollection</code>
 */
public PermissionCollection newPermissionCollection()
{
    return null;
}

/**
 * This method returns a <code>String</code> representation of this
 * <code>Permission</code> object. This is in the format:
 * <code>'(' + getClass().getName() + ' ' + getName() + ' ' + getActions
 * + ')'</code>.
 *
 * @return this object as a <code>String</code>
 */
public String toString()
{
    CPStringBuilder string = new CPStringBuilder();

    // Add class name, name, and actions to the string builder
    string.append(getClass().getName()).append(' ').append(getName()).append(' ').append(getActions());

    // Return the string
    return string.toString();
}
string = string.append('(');
string = string.append(getClass().getName());
string = string.append(' ');
string = string.append(getName());

if (!(getActions().equals('""')))
{
    string = string.append(' ');
    string = string.append(getActions());
}

string = string.append(')');
return string.toString();
}
} // class Permission

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Version 3, 29 June 2007

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This is an attempt to acknowledge early contributions to the garbage
collector. Later contributions should instead be mentioned in
README.changes.

HISTORY -

Early versions of this collector were developed as a part of research
projects supported in part by the National Science Foundation
and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for
the Russell programming language implementation. The first version of the
The garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser.
The Encore Multimax modifications were supplied by
Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM.
Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood.
Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code.
Al Dosser (dosser@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code.
Jesper Peterson (jep@mtiame.mtia.oz.au), Michel Schinz, and
Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port.
Thomas Funke (thf@zelator.in-berlin.de(?)) and
Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports.
Douglas Steel (doug@wg.icle.co.uk) provided ICL DRS6000 code.
Bill Janssen (jansen@parc.xerox.com) supplied the SunOS dynamic loader specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and
Dave Detlefs (detlefs@src.dec.com) also provided several generic bug fixes.
Alistair G. Crooks (age@uts.amdahl.com) supplied the NetBSD and 386BSD ports.
Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port.
Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to
a Motorola 88K processor running CX/UX (Harris NightHawk).
Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to
nonIBM development environments (a nontrivial task).
Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port.
David Chase, then at Olivetti Research, suggested several improvements.
Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the
code to save and print call stacks for leak detection on a SPARC.
Jesse Hull and John Ellis supplied the C++ interface code.
Zhong Shao performed much of the experimentation that led to the
current typed allocation facility. (His dynamic type inference code hasn’t
made it into the released version of the collector, yet.)
Version 3, 29 June 2007

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package gnu.classpath.tools.taglets;

import java.util.Map;

import com.sun.tools.doclets.Taglet;

import com.sun.javadoc.Tag;

/**
 * A simple Taglet which handles Copyright information.
 */
public class CopyrightTaglet implements Taglet {

    private static final String NAME = "copyright";
    private static final String HEADER = "Copyright:";

    public String getName() {
        return NAME;
    }

    public boolean inField() {
        return true;
    }

    public boolean inConstructor() {
        return true;
    }

    public boolean inMethod() {
        return true;
    }

    public boolean inOverview() {
        return true;
    }

    public boolean inPackage() {
        return true;
    }
}
public boolean inType() {
    return true;
}

public boolean isInlineTag() {
    return false;
}

public static void register(Map tagletMap) {
    CopyrightTaglet copyrightTaglet = new CopyrightTaglet();
    tagletMap.put(copyrightTaglet.getName(), copyrightTaglet);
}

public String toString(Tag tag) {
    return toString(new Tag[] { tag });
}

public String toString(Tag[] tags) {
    if (tags.length == 0) {
        return null;
    } else {
        boolean haveValidTag = false;
        for (int i = 0; i < tags.length && !haveValidTag; ++i) {
            if (tags[i].text().length() > 0) {
                haveValidTag = true;
            }
        }
        if (haveValidTag) {
            StringBuffer result = new StringBuffer();
            result.append("<dl>\n            for (int i = 0; i < tags.length; i++) {
                if (tags[i].text().length() > 0) {
                    result.append("<dt><i>Copyright © " + tags[i].text() + "</i></dt>");
                }
            result.append("</dl>\n            return result.toString();
        } else {
            return null;
        }
    }
}

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Version 2.1, February 1999

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1.320 tdb 1.42.9-16.el7

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
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5. A program that contains no derivative of any portion of the
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However, linking a "work that uses the Library" with the Library
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library". The executable is therefore covered by this License.
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When a "work that uses the Library" uses material from a header file
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derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
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Otherwise, if the work is a derivative of the Library, you may
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whether or not they are linked directly with the Library itself.

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1.323 libxmu 1.1.2 2.el7

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1.324 libx11 3.22.30-3.el7

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above
specified materials from the same place.

e) Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the
Library" must include any data and utility programs needed for
reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
  ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
  ret void
}
define internal void @innerNoAttribute() {
  ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
call void @innerLarge()
ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
call void @innerLarge()
ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
call void @innerSmall()
ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
call void @innerNoAttribute()
ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
; RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]??]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]??]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
define void @foo_i8() {
  entry:
  %0 = load volatile i8, i8* inttopr (i32 805874688 to i8*)
  %1 = load volatile i8, i8* inttopr (i32 805874692 to i8*)
  %2 = load volatile i8, i8* inttopr (i32 805874719 to i8*)
  %3 = load volatile i8, i8* inttopr (i32 805874720 to i8*)
  %4 = load volatile i8, i8* inttopr (i32 805874727 to i8*)
  store i8 %4, i8* inttopr(i32 805873688 to i8*)
  store i8 %3, i8* inttopr(i32 805873719 to i8*)
  store i8 %2, i8* inttopr(i32 805873720 to i8*)
  store i8 %1, i8* inttopr(i32 805873727 to i8*)
  store i8* inttopr(i32 805874800 to i8*), i8** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttopr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
define void @foo_i16() {
    entry:
    %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
    %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
    %2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
    %3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
    %4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
    %5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
    ret void
}

define void @foo_i32() {
    entry:
    %0 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 2
    %1 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 2
    %2 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 2
    %3 = load volatile i32, i32* inttoptr (i32 805874720 to i32*), align 2
    %4 = load volatile i32, i32* inttoptr (i32 805874750 to i32*), align 2
    %5 = load volatile i32, i32* inttoptr (i32 805874752 to i32*), align 2
    %6 = load volatile i32, i32* inttoptr (i32 805874774 to i32*), align 2
    ret void
}
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

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Developed by:

LLVM Team

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<table>
<thead>
<tr>
<th>Program</th>
<th>Directory</th>
</tr>
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<tbody>
<tr>
<td>&lt;none yet&gt;</td>
<td></td>
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<td>llvm/lib/Support/{reg*, COPYRIGHT.regex}</td>
</tr>
<tr>
<td>pyyaml tests</td>
<td>llvm/test/YAMLParser/{*.data, LICENSE.TXT}</td>
</tr>
<tr>
<td>ARM contributions</td>
<td>llvm/lib/Target/ARM/LICENSE.TXT</td>
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<tr>
<td>md5 contributions</td>
<td>llvm/lib/Support/MD5.cpp llvm/include/llvm/Support/MD5.h</td>
</tr>
</tbody>
</table>

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; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512

define void @zext256() "min-legal-vector-width"="256" 

; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'zext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
define void @zext512() "min-legal-vector-width"="512" {
    ; AVX-LABEL: 'zext512'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; SKX256-LABEL: 'zext512'
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; VEC512-LABEL: 'sext512'
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void

} define void @sext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; VEC512-LABEL: 'sext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x
define void @sext512() "min-legal-vector-width"="512" {
    ; AVX-LABEL: 'sext512'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    ; SKX256-LABEL: 'sext512'
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    ; VEC512-LABEL: 'sext512'
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void

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* 8.1 (Berkeley) 3/16/94
*/
; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
  br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
  br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
  %lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
switch i32 undef, label %exit

i32 1, label %sw.bb.i
i32 2, label %sw.bb3.i
]

sw.bb.i: ; preds = %while.body
unreachable

sw.bb3.i: ; preds = %while.body
unreachable

exit: ; preds = %while.body
switch i32 undef, label %exit.2 [
  i32 1, label %sw.bb.i17
  i32 2, label %sw.bb3.i20
]

sw.bb.i17: ; preds = %exit
%0 = bitcast %struct.0* %lsr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lsr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable

exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
ret void
]

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }

; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s ;
; D31946
; Check that we dont end up with the "’LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
  %cmp = fcmp ogt fp128 %a, %b
  %sub = fsub fp128 %a, %b
  %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
}
define fp128 @TestSelect(fp128 %a) {
  %res = call fp128 @llvm.fabs.f128(fp128 %a)
  ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
  %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
  ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
  %mul = fmul fp128 %a, %a
  %res = fsub fp128 0xL00000000000000008000000000000000, %mul
  ret fp128 %res
}

define void @autogen_SD4739(i8*) {
  ...
}

; CHECK-LABEL: TestSelect:
; CHECK movaps 16(%rsp), %xmm1
; CHECK-NEXT callq __subtf3
; CHECK-NEXT testl %ebx, %ebx
; CHECK-NEXT jg .LBB0_2
; CHECK-NEXT # %bb.1:
; CHECK-NEXT movaps .LCPI0_0(%rip), %xmm0
; CHECK-NEXT .LBB0_2:
; CHECK-NEXT addq $32, %rsp
; CHECK-NEXT popq %rbx
; CHECK-NEXT retq

define void @autogen_SD4739(i8*) {
  ...
}

; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right
; register classes.

define void @autogen_SD4739(i8*) {
  ...
}
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
%L34 = load i8, i8* %0
%Cmp56 = icmp sgt i8 undef, %L34
br label %CF246

CF246: ; preds = %CF246, %BB
%S1163 = select i1 %Cmp56, i8 %L34, i8 undef
br i1 undef, label %CF246, label %CF248

CF248: ; preds = %CF248, %CF246
store i8 %S1163, i8* %0
br label %CF248
)
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vl,avx512bw,avx512dq,prefer-256-bit |
FileCheck %s

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the
legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vmovdqa (%rdi), %ymm0
; CHECK-NEXT:   vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:   vpadd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:   vpadd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:   vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:   vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}

define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: add512:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vmovdqa (%rdi), %zmm0
; CHECK-NEXT:   vmovdqa 32(%rdi), %zmm1
; CHECK-NEXT:   vpadd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:   vpadd 32(%rsi), %zmm1, %zmm1
; CHECK-NEXT:   vmovdqa %zmm1, 32(%rdx)
; CHECK-NEXT:   vmovdqa %zmm0, (%rdx)
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
%d = load <16 x i32>, <16 x i32>* %a

%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}

define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
; CHECK-LABEL: avg_v64i8_256:
; CHECK:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpavgb (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpavgb 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vmovdqu %ymm1, (%rax)
; CHECK-NEXT: vmovdqu %ymm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, %3
%6 = add nuw nsw <64 x i32> %4, %4
%7 = lshr <64 x i32> %5, %5
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT: vmovdq464 %zmm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = load <64 x i8>, <64 x i8>* %a
%6 = load <64 x i8>, <64 x i8>* %b
%7 = zext <64 x i8> %5 to <64 x i32>
%8 = zext <64 x i8> %6 to <64 x i32>
%9 = add nuw nsw <64 x i32> %7, %7
%10 = add nuw nsw <64 x i32> %8, %8
%11 = lshr <64 x i32> %9, %9
%12 = trunc <64 x i32> %11 to <64 x i8>
store <64 x i8> %12, <64 x i8>* undef, align 4
ret void
}
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>

%6 = add nuw nsw <64 x i32> %5, %4

%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>

%8 = trunc <64 x i32> %7 to <64 x i8>

store <64 x i8> %8, <64 x i8>* undef, align 4

ret void}

define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: pmaddwd_32_256:
  ; CHECK:
  # %bb.0:
  ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
  ; CHECK-NEXT: vzeroupper
  retq
  %A = load <32 x i16>, <32 x i16>* %APtr
  %B = load <32 x i16>, <32 x i16>* %BPtr
  %a = sext <32 x i16> %A to <32 x i32>
  %b = sext <32 x i16> %B to <32 x i32>
  %m = mul nsw <32 x i32> %a, %b
  %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
  %even = shufflevector <32 x i32> %m, <32 x i32> undef, <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
  %ret = add <16 x i32> %odd, %even
  store <16 x i32> %ret, <16 x i32>* %CPtr
  ret void
}
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; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mull nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}
define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: psubus_64i8_max_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vpsubusb (%rsi), %ymm1, %ymm1
; CHECK-NEXT: vmovdqa %ymm0, 32(%rdx)
; CHECK-NEXT: vmovdqa %ymm1, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}
define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" { }

; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:    movl %edx, %eax
; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:    xorl %ecx, %ecx
; CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT:    .p2align 4, 0x90
; CHECK-NEXT:  .LBB8_1: # %vector.body
; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %ymm3
; CHECK-NEXT:    vpmovsxbw 16(%rdi,%rcx), %ymm4
; CHECK-NEXT:    vpmovsxbw (%rsi,%rcx), %ymm5
; CHECK-NEXT:    vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT:    vpaddd %ymm1, %ymm3, %ymm1
; CHECK-NEXT:    vpmovsxbw 16(%rsi,%rcx), %ymm4
; CHECK-NEXT:    vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT:    vpaddd %ymm2, %ymm3, %ymm2
; CHECK-NEXT:    addq $32, %rcx
; CHECK-NEXT:    cmpq %rcx, %rax
; CHECK-NEXT:    jne .LBB8_1
; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm1
; CHECK-NEXT:    vpaddd %ymm0, %ymm2, %ymm0
; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:    vpaddd %ymm0, %ymm0, %ymm0
; CHECK-NEXT:    vpmovsxbw 16(%rsi,%rcx), %ymm3
; CHECK-NEXT:    vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT:    vpaddd %ymm2, %ymm3, %ymm2
; CHECK-NEXT:    addq $32, %rcx
; CHECK-NEXT:    cmpq %rcx, %rax
; CHECK-NEXT:    jne .LBB8_1
; CHECK-NEXT:    # %bb.2: # %middle.block
; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm1
; CHECK-NEXT:    vpaddd %ymm0, %ymm2, %ymm0
; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:    vpaddd %ymm0, %ymm0, %ymm0
; CHECK-NEXT:    vpmovsxbw 16(%rsi,%rcx), %ymm3
; CHECK-NEXT:    vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT:    vpaddd %ymm2, %ymm3, %ymm2
; CHECK-NEXT:    addq $32, %rcx
; CHECK-NEXT:    cmpq %rcx, %rax
; CHECK-NEXT:    jne .LBB8_1
; CHECK-NEXT:    # %bb.3: # %return
; CHECK-NEXT:    vmovd %xmm0, %eax
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK: # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB9_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpadd %zmm1, %zmm2, %zmm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB9_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %i, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body
middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 16, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> <i32 1, i32 undef, i32 unde
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

@a = global [1024 x i8] zeroinitializer, align 16
@b = global [1024 x i8] zeroinitializer, align 16

define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sad_16i8_256:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT:  movq $-1024, %rax # imm = 0xFC00
    ; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT:  .p2align 4, 0x90
    ; CHECK-NEXT:  .LBB10_1: # %vector.body
    ; CHECK-NEXT:  # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT:  vmovdqu a+1024(%rax), %xmm2
    ; CHECK-NEXT:  vpsadbw b+1024(%rax), %xmm2, %xmm2
    ; CHECK-NEXT:  vpadd %yymm1, %yymm2, %yymm1
    ; CHECK-NEXT:  addq $4, %rax
    ; CHECK-NEXT:  jne .LBB10_1
    ; CHECK-NEXT:  }
entry:
  br label %vector.body

vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
  %2 = zext <16 x i8> %wide.load to <16 x i32>
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = zext <16 x i8> %wide.load1 to <16 x i32>
  %6 = sub nsw <16 x i32> %2, %5
  %7 = icmp sgt <16 x i32> %6, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1
  %8 = sub nsw <16 x i32> %6, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1, %32 -1
  %9 = select <16 x i1> %7, %7, %7
  %10 = add nsw <16 x i32> %9, %vec.phi
  %index.next = add i64 %index, 4
  br i1 %index.next, label %middle.block, label %vector.body

middle.block:
  %.lcssa = phi <16 x i32> [ %10, %vector.body ]
  %rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
  %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
  %rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
  ; CHECK-LABEL: sad_16i8_512:
  ; CHECK:       # %bb.0: # %entry
  ; CHECK-NEXT:   vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT:   movq $-1024, %rax # imm = 0xFC00
  ; CHECK-NEXT:   .p2align 4, 0x90
  ; CHECK-NEXT:   .LBB11_1: # %vector.body
  ; CHECK-NEXT:     # =>This Inner Loop Header: Depth=1
  ; CHECK-NEXT:     vmovdqu a+1024(%rax), %xmm1
  ; CHECK-NEXT:     vpsadbw b+1024(%rax), %xmm1, %xmm1
  ; CHECK-NEXT:     vpaddd %zmm0, %zmm1, %zmm0
  ; CHECK-NEXT:     addq $4, %rax
  ; CHECK-NEXT:     jne .LBB11_1
  ; CHECK-NEXT:     vextracti64x4 $1, %zmm0, %ymm1
  ; CHECK-NEXT:     vpaddd %zmm1, %zmm0, %zmm0
  ; CHECK-NEXT:     vextracti128 $1, %ymm0, %xmm1
  ; CHECK-NEXT:     vpaddd %zmm1, %zmm0, %zmm0
  ; CHECK-NEXT:     vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]  
  ; CHECK-NEXT:     vpaddd %zmm1, %zmm0, %zmm0
  ; CHECK-NEXT:     vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]  
  ; CHECK-NEXT:     vpaddd %xmm1, %xmm0, %xmm0
  ; CHECK-NEXT:     vmovd %xmm0, %eax
  ; CHECK-NEXT:     vzeroupper
  ; CHECK-NEXT:     retq
}

entry:
  br label %vector.body

vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitialize, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
  %2 = zext <16 x i8> to <16 x i32>
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = zext <16 x i8> to <16 x i32>
  %6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovmd %k1, %ymm0
; CHECK-NEXT: vcvtq2ps %ymm0, %ymm0
; CHECK-NEXT: vpmovmd %k0, %ymm1
; CHECK-NEXT: vcvtq2ps %ymm1, %ymm1
; CHECK-NEXT: vmovaps %ymm1, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f32_512:
    ; CHECK:      # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:   vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:   vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbto16f64_256:
    ; CHECK:      # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:   vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:   vcvtdq2pd %xmm0, %ymm1
    ; CHECK-NEXT:   vextracti128 $1, %ymm0, %xmm0
    ; CHECK-NEXT:   vcvtdq2pd %xmm0, %ymm0
    ; CHECK-NEXT:   vpmovm2d %k0, %ymm2
    ; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm3
    ; CHECK-NEXT:   vextracti128 $1, %ymm2, %xmm2
    ; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm2
    ; CHECK-NEXT:   vmovaps %ymm2, 32(%rdi)
    ; CHECK-NEXT:   vmovaps %ymm3, (%rdi)
    ; CHECK-NEXT:   vmovaps %ymm0, 96(%rdi)
    ; CHECK-NEXT:   vmovaps %ymm1, 64(%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f64_512:
    ; CHECK:      # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:   vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:   vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}
; CHECK-NEXT:  vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT:  vmovaps %zmm1, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}
define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f32_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:  vcvtdq2ps %ymm0, %zmm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vpmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vmovaps %zmm0, 32(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT:  vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:  vpmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:     # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpslld $31, %ymm0, %ymm0
; CHECK-NEXT: vcvtqd2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtqd2pd %ymm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vpslld $31, %ymm2, %ymm2
; CHECK-NEXT: vcvtqd2pd %ymm0, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtqd2pd %xmm2, %ymm2
; CHECK-NEXT: vminvd %ymm2, 32(%rdi)
; CHECK-NEXT: vminvd %ymm3, (%rdi)
; CHECK-NEXT: vnmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vnmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f64_512:
; CHECK:     # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpslld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtqd2pd %ymm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtqd2pd %ymm0, %zmm0
; CHECK-NEXT: vnmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vnmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
 ; CHECK-LABEL: test_16f32toub_512:
 ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vcvtpps2dq (%rdi), %ymm1
    ; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k0
    ; CHECK-NEXT:  vcvtpps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:  vcvtpps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 [%k1] {z}
    ; CHECK-NEXT:  retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptoui <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: test_16f32tosb_256:
 ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vcvtpps2dq (%rdi), %ymm1
    ; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k0
    ; CHECK-NEXT:  vcvtpps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:  vcvtpps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 [%k1] {z}
    ; CHECK-NEXT:  retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

; CHECK-LABEL: test_16f32tosb_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vcvtpps2dq (%rdi), %ymm1
; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k0
; CHECK-NEXT:  vcvtpps2dq 32(%rdi), %ymm1
; CHECK-NEXT:  vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k1
; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 [%k1] {z}
; CHECK-NEXT:  retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: test_16f32tosb_512:
  ; CHECK:    # %bb.0:
  ; CHECK-NEXT: vcvtpsn2dq (%rdi), %zmm1
  ; CHECK-NEXT: vpmovd2m %zmm1, %k1
  ; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 [%k1] [z]
  ; CHECK-NEXT: retq
  %a = load <16 x float>, <16 x float>* %ptr
  %mask = fptosi <16 x float> %a to <16 x i1>
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
  ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: mul256:
  ; CHECK:    # %bb.0:
  ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
  ; CHECK-NEXT: vmovdqa (%rsi), %ymm2
  ; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
  ; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm4 = ymm2[8],ymm0[8],ymm2[9],ymm0[9],ymm2[10],ymm0[10],ymm2[11],ymm0[11],ymm2[12],ymm0[12],ymm2[13],ymm0[13],ymm2[14],ymm0[14],ymm2[15],ymm0[15],ymm2[24],ymm0[24],ymm2[25],ymm0[25],ymm2[26],ymm0[26],ymm2[27],ymm0[27],ymm2[28],ymm0[28],ymm2[29],ymm0[29],ymm2[30],ymm0[30],ymm2[31],ymm0[31]
  ; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm5 = ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
  ; CHECK-NEXT: vpmullw %ymm4, %ymm5, %ymm4
  ; CHECK-NEXT: vcvtnsexdqu %ymm5, %ymm4
  ; CHECK-NEXT: vpmullw %ymm2, %ymm0, %ymm0
  ; CHECK-NEXT: vpmullw %ymm3, %ymm3
  ; CHECK-NEXT: vpand %ymm5, %ymm4, %ymm4
  ; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm2 = ymm2[0],ymm0[0],ymm2[1],ymm0[1],ymm2[2],ymm0[2],ymm2[3],ymm0[3],ymm2[4],ymm0[4],ymm2[5],ymm0[5],ymm2[6],ymm0[6],ymm2[7],ymm0[7],ymm2[16],ymm0[16],ymm2[17],ymm0[17],ymm2[18],ymm0[18],ymm2[19],ymm0[19],ymm2[20],ymm0[20],ymm2[21],ymm0[21],ymm2[22],ymm0[22],ymm2[23],ymm0[23]
  ; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm0 = ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
  ; CHECK-NEXT: vpmullw %ymm2, %ymm0, %ymm0
  ; CHECK-NEXT: vpand %ymm5, %ymm0, %ymm0
  ; CHECK-NEXT: vpackuswb %ymm4, %ymm0, %ymm0
  ; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm2 = ymm3[8],ymm0[8],ymm3[9],ymm0[9],ymm3[10],ymm0[10],ymm3[11],ymm0[11],ymm3[12],ymm0[12],ymm3[13],ymm0[13],ymm3[14],ymm0[14],ymm3[15],ymm0[15],ymm3[24],ymm0[24],ymm3[25],ymm0[25],ymm3[26],ymm0[26],ymm3[27],ymm0[27],ymm3[28],ymm0[28],ymm3[29],ymm0[29],ymm3[30],ymm0[30],ymm3[31],ymm0[31]
  ; CHECK-NEXT: vpunpckhbw {{.*#+}} ymm4 =
ymm1[8], ymm0[8], ymm1[9], ymm0[9], ymm1[10], ymm0[10], ymm1[11], ymm0[11], ymm1[12], ymm0[12], ymm1[13], ymm0[13], ymm1[14], ymm0[14], ymm1[15], ymm0[15], ymm1[24], ymm0[24], ymm1[25], ymm0[25], ymm1[26], ymm0[26], ymm1[27], ymm0[27], ymm1[28], ymm0[28], ymm1[29], ymm0[29], ymm1[30], ymm0[30], ymm1[31], ymm0[31]

; CHECK-NEXT:    vpmullw %ymm2, %ymm4, %ymm2
; CHECK-NEXT:    vpand %ymm5, %ymm2, %ymm2
; CHECK-NEXT:    vpuncpcklbw {{.*#}} ymm3 = ymm3[0], ymm0[0], ymm3[1], ymm0[1], ymm3[2], ymm0[2], ymm3[3], ymm0[3], ymm3[4], ymm0[4], ymm3[5], ymm0[5], ymm3[6], ymm0[6], ymm3[7], ymm0[7], ymm3[16], ymm0[16], ymm3[17], ymm0[17], ymm3[18], ymm0[18], ymm3[19], ymm0[19], ymm3[20], ymm0[20], ymm3[21], ymm0[21], ymm3[22], ymm0[22], ymm3[23], ymm0[23]
; CHECK-NEXT:    vpuncpcklbw {{.*#}} ymm1 = ymm1[0], ymm0[0], ymm1[1], ymm0[1], ymm1[2], ymm0[2], ymm1[3], ymm0[3], ymm1[4], ymm0[4], ymm1[5], ymm0[5], ymm1[6], ymm0[6], ymm1[7], ymm0[7], ymm1[16], ymm0[16], ymm1[17], ymm0[17], ymm1[18], ymm0[18], ymm1[19], ymm0[19], ymm1[20], ymm0[20], ymm1[21], ymm0[21], ymm1[22], ymm0[22], ymm1[23], ymm0[23]
; CHECK-NEXT:    vpmullw %ymm3, %ymm1, %ymm1
; CHECK-NEXT:    vpand %ymm5, %ymm1, %ymm1
; CHECK-NEXT:    vpuncpcklbw {{.*#}} ymm2 = zmm1[8], zmm0[8], zmm1[9], zmm0[9], zmm1[10], zmm0[10], zmm1[11], zmm0[11], zmm1[12], zmm0[12], zmm1[13], zmm0[13], zmm1[14], zmm0[14], zmm1[15], zmm0[15], zmm1[24], zmm0[24], zmm1[25], zmm0[25], zmm1[26], zmm0[26], zmm1[27], zmm0[27], zmm1[28], zmm0[28], zmm1[29], zmm0[29], zmm1[30], zmm0[30], zmm1[31], zmm0[31], zmm1[40], zmm0[40], zmm1[41], zmm0[41], zmm1[42], zmm0[42], zmm1[43], zmm0[43], zmm1[44], zmm0[44], zmm1[45], zmm0[45], zmm1[46], zmm0[46], zmm1[47], zmm0[47], zmm1[56], zmm0[56], zmm1[57], zmm0[57], zmm1[58], zmm0[58], zmm1[59], zmm0[59], zmm1[60], zmm0[60], zmm1[61], zmm0[61], zmm1[62], zmm0[62], zmm1[63], zmm0[63]
; CHECK-NEXT:    vpuncpckhbw {{.*#}} zmm3 = zmm0[8], 8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-NEXT:    vpmullw %zmm2, %zmm3, %zmm2
; CHECK-NEXT:  vpandq %zmm3, %zmm2, %zmm2
; CHECK-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
  zmm1[0],zmm0[0],zmm1[1],zmm0[1],zmm1[2],zmm0[2],zmm1[3],zmm0[3],zmm1[4],zmm0[4],zmm1[5],zmm0[5],
  zmm1[6],zmm0[6],zmm1[7],zmm0[7],zmm1[16],zmm0[16],zmm1[17],zmm0[17],zmm1[18],zmm0[18],zmm1[19],
  zmm0[19],zmm1[20],zmm0[20],zmm1[21],zmm0[21],zmm1[22],zmm0[22],zmm1[23],zmm0[23],zmm1[32],zmm0[32],
  zmm1[33],zmm0[33],zmm1[34],zmm0[34],zmm1[35],zmm0[35],zmm1[36],zmm0[36],zmm1[37],zmm0[37],
  zmm1[38],zmm0[38],zmm1[39],zmm0[39],zmm1[48],zmm0[48],zmm1[49],zmm0[49],zmm1[50],zmm0[50],zmm1[51],
  zmm0[51],zmm1[52],zmm0[52],zmm1[53],zmm0[53],zmm1[54],zmm0[54],zmm1[55],zmm0[55]; CHECK-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
  zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,24,24,25,25,26,26,27,27,32,32,33,33,34,34,35,35,36,36,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55,55]; CHECK-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-NEXT:  vpandq %zmm3, %zmm0, %zmm0
; CHECK-NEXT:  vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
  %d = load <64 x i8>, <64 x i8>* %a
  %e = load <64 x i8>, <64 x i8>* %b
  %f = mul <64 x i8> %d, %e
  store <64 x i8> %f, <64 x i8>* %c
  ret void
} ; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
 ; CHECK-LABEL: test_sitofp_fixed:
    ; First, extend each i32 to i64
    ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
    ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
    ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
    ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
    ; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
    ; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
    ; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
    ; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
    ; Next, convert each to double.
    ; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
    ; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
    ; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
    ; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
    ; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
    ; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
    ; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
    ; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
(define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
  ; CHECK-LABEL: test_sitofp_fixed_shortish:
  ; CHECK-DAG: scvtf.2d v0, v0
  ; CHECK-DAG: scvtf.2d v1, v1
  ; CHECK: ret
  %flt = sitofp <4 x i64> %in to <4 x double>
  %res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
  ret <4 x double> %res
})

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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Source Files
-----------

. The file `src/libs/snprintf/snprintf.c', written by Mark Martinec
  <mark.martinec@ijs.si>.

  Please look into snprintf.c for the copyright message.
The complete snprintf package together with documentation is available from
http://www.ijs.si/software/snprintf/.

The `grn' preprocessor, written by Barry Roithblat <barry@rentonww.com>
and David Slattengren <slatteng@Xinet.COM>. These files have been part
of the original Berkeley ditroff distribution, without AT&T code, and
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The original package can be found at

src/preproc/grn/gprint.h
src/preproc/grn/hdb.cpp
src/preproc/grn/hgraph.cpp
src/preproc/grn/hpoint.cpp
src/preproc/grn/main.cpp

The `gxditview' output device. It is based on X11's `xditview' program
and thus has the X license.

src/devices/xditview/DESC.in
src/devices/xditview/Dvi.c
src/devices/xditview/Dvi.h
src/devices/xditview/DviP.h
src/devices/xditview/FontMap
src/devices/xditview/GXditview.ad
src/devices/xditview/Menu.h
src/devices/xditview/ad2c
src/devices/xditview/device.c
src/devices/xditview/device.h
src/devices/xditview/draw.c
src/devices/xditview/font.c
src/devices/xditview/gray1.bm
src/devices/xditview/gray2.bm
src/devices/xditview/gray3.bm
src/devices/xditview/gray4.bm
src/devices/xditview/gray5.bm
src/devices/xditview/gray6.bm
src/devices/xditview/gray7.bm
src/devices/xditview/gray8.bm
src/devices/xditview/gxditview.man
src/devices/xditview/lex.c
src/devices/xditview/page.c
src/devices/xditview/parse.c
src/devices/xditview/xdit.bm
src/devices/xditview/xdit_mask.bm
src/devices/xditview/xditview.c

src/include/DviChar.h
src/include/XFontName.h

src/libs/libxutil/DviChar.c
src/libs/libxutil/XFontName.c

Macro Packages
--------------

. The -mdoc macro set, using the BSD license.

  tmac/doc.tmac
  tmac/doc-old.tmac
  tmac/doc-common
  tmac/doc-ditroff
  tmac/doc-nroff
  tmac/doc-syms
  tmac/groff_mdoc.man

. The -me macro set, using the BSD license.

  tmac/e.tmac
  tmac/groff_me.man
  doc/meintro.me
  doc/meref.me

Hyphenation Patterns
---------------------

. The file `tmac/hyphen.us' is identical to the file `hyphen.tex', part of
the TeX system written by Donald E. Knuth; the master file can be found
at

    ftp://labrea.stanford.edu/pub/tex/dist/lib/hyphen.tex

It has been renamed for consistency, i.e., to make patterns available
under the filenames `hyphen.<language>', e.g. `hyphen.de' or
`hyphen.uk'.

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. The file `tmac/hyphen.fr' contains the same patterns as the file
`frhyph.tex' (for TeX), which can be found at
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. The file `tmac/hyphen.sv' is identical to the file `svhyph.tex', which can be found at

    http://dante.ctan.org/CTAN/language/hyphenation/svhyph.tex

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. The files `tmac/hyphen.det' and `tmac/hyphen.den' contain the same patterns as the files `dehyphn.tex' and `dehyph.tex' (for TeX), which can be found at

    http://dante.ctan.org/CTAN/language/hyphenation/dehyphn.tex
    http://dante.ctan.org/CTAN/language/hyphenation/dehyph.tex

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. The file `tmac/hyphenex.det' is identical to the file `dehyphtex.tex', which can be found at

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. The file `tmac/hyphen.cs' contains the same patterns as the file `czhyphen.tex' (for TeX), which can be found in the archive

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reimplemented pthread_once with the same form
as described by A.Terekhov (later version 2);
implementation of MCS (Mellor-Crummey/Scott) locks.

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*/

## -*-makefile-*-
## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$$(CC) -E -MM $$(DEFNS) $$(CPPFLAGS)
GEN_DEPS.cc=$$(CXX) -E -MM $$(DEFNS) $$(CPPFLAGS)

## Flags for position independent code
SHARREDLIBCFLAGS = -fPIC
SHARREDLIBCXXFLAGS = -fPIC
SHARREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSPPFLAGS = -D_REENTRANT
LIBPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%:o: $(srcdir)/%.c
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%:o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed '\''s/\($*\)\.o[ :]*/\1.o $@ : /g'\'' > $@; \
[ -s $@ ] || rm -f $@'
%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed '\''s/\($*\)\.o[ :]*/\1.o $@ : /g'\'' > $@; \
[ -s $@ ] || rm -f $@'
## Versioned libraries rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@
## Bind internal references
# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic
# Dependencies [i.e. map files] for the final library
BIR_DEPS=
## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =
## End BSD-specific setup
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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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# This file contains a list of people who've made non-trivial contribution to the Google C++ Testing Framework project. People who commit code to the project are encouraged to add their names here. Please keep the list sorted by first names.

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1.338 libx11 0.7.1-3.el7

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1.339 gawk 4.0.2-4.el7_3.1

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@enumerate
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Accompany it with the complete corresponding machine-readable
source code, which must be distributed under the terms of Sections
1 and 2 above on a medium customarily used for software interchange; or,

@item
Accompany it with a written offer, valid for at least three
years, to give any third party, for a charge no more than your
cost of physically performing source distribution, a complete
machine-readable copy of the corresponding source code, to be
distributed under the terms of Sections 1 and 2 above on a medium
customarily used for software interchange; or,

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Accompany it with the information you received as to the offer
to distribute corresponding source code. (This alternative is
allowed only for noncommercial distribution and only if you
received the program in object code or executable form with such
an offer, in accord with Subsection b above.)
@end enumerate

The source code for a work means the preferred form of the work for
making modifications to it. For an executable work, complete source
code means all the source code for all modules it contains, plus any
associated interface definition files, plus the scripts used to
control compilation and installation of the executable. However, as a
special exception, the source code distributed need not include
anything that is normally distributed (in either source or binary
form) with the major components (compiler, kernel, and so on) of the
operating system on which the executable runs, unless that component
itself accompanies the executable.

If distribution of executable or object code is made by offering
access to copy from a designated place, then offering equivalent
access to copy the source code from the same place counts as
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@ifinfo
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@iftex
@heading END OF TERMS AND CONDITIONS
@end iftex
@ifinfo
@center END OF TERMS AND CONDITIONS
@end ifinfo

@end ifinfo

@page
@heading Appendix: How to Apply These Terms to Your New Programs

If you develop a new program, and you want it to be of the greatest
possible use to the public, the best way to achieve this is to make it
free software which everyone can redistribute and change under these terms.

To do so, attach the following notices to the program. It is safest
to attach them to the start of each source file to most effectively
convey the exclusion of warranty; and each file should have at least
the ``copyright'' line and a pointer to where the full notice is found.

@smallexample
@var{one line to give the program's name and a brief idea of what it does.}
Copyright (C) @var{yyyy}  @var{name of author}

This program is free software; you can redistribute it and/or modify
it under the terms of the GNU General Public License as published by
the Free Software Foundation; either version 2 of the License, or
(at your option) any later version.

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along with this program; if not, write to the Free Software
Foundation, Inc., 51 Franklin Street, Fifth Floor, Boston, MA  02110-1301, USA.
@end smallexample

Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this
when it starts in an interactive mode:

@small example
Gnomovision version 69, Copyright (C) @var{year} @var{name of author}
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.
@end smallexample

The hypothetical commands @samp{show w} and @samp{show c} should show
the appropriate parts of the General Public License. Of course, the
commands you use may be called something other than @samp{show w} and
@samp{show c}; they could even be mouse-clicks or menu items---whatever
suits your program.

You should also get your employer (if you work as a programmer) or your
school, if any, to sign a ``copyright disclaimer'' for the program, if
necessary. Here is a sample; alter the names:

@example
Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.
@end example
This General Public License does not permit incorporating your program into proprietary programs. If your program is a subroutine library, you may consider it more useful to permit linking proprietary applications with the library. If this is what you want to do, use the GNU Lesser General Public License instead of this License.

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@center Version 2.1, February 1999

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[This is the first released version of the Lesser GPL. It also counts as the successor of the GNU Library Public License, version 2, hence the version number 2.1.]
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@subheading Preamble

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When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the \textit{Lesser} General Public License because it does \textbf{less} to protect the user's freedom than the ordinary General Public License. It also provides other free software developers \textbf{less} of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many
libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a `work based on the library` and a `work that uses the library`. The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

@subheading TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION

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@item
This License Agreement applies to any software library or other program which contains a notice placed by the copyright holder or other authorized party saying it may be distributed under the terms of this Lesser General Public License (also called `this License`). Each licensee is addressed as `you`.

A `library` means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The `Library`, below, refers to any such software library or work which has been distributed under these terms. A `work based on the Library` means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated
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``Source code'' for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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@item
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If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

@item
A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)
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As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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@item
Verify that the user has already received a copy of these
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@end enumerate

For an executable, the required form of the "work that uses the
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@item
Give prominent notice with the combined library of the fact
that part of it is a work based on the Library, and explaining
where to find the accompanying uncombined form of the same work.
@end enumerate
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@end enumerate

@end subheading

@end subheading

@subheading HOW TO APPLY THESE TERMS TO YOUR NEW LIBRARIES

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To apply these terms, attach the following notices to the library. It is
safest to attach them to the start of each source file to most effectively
convey the exclusion of warranty; and each file should have at least the
``copyright'' line and a pointer to where the full notice is found.

@smallexample
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1.343 gdisk 0.8.10 3.el7

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse
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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on
which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {

; CHECK-OVERRIDE: PR41535:
; CHECK-NEXT:       // %bb.0:
; CHECK-NEXT:       ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT:       mov v0.d[1], v0.d[0]
; CHECK-NEXT:       ret
%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}

; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
; CHECK-OVERRIDE: @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* %arg1.val)
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

}
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}
; This should promote
; CHECK-LABEL: @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* %arg1) #0
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
  bb:
  %tmp = alloc <8 x i64>, align 32
  %tmp2 = alloc <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should not promote
; CHECK-LABEL: @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg)
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
  bb:
  %tmp = alloc <8 x i64>, align 32
  %tmp2 = alloc <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
; CHECK-LABEL: @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1.val)
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}
; This should promote
; CHECK-LABEL: @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1.val)
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}
define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}
; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinethint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinethint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinethint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinethint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinethint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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RUN: opt -consthoist -S -o - %s | FileCheck %s

target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]??]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]??]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
@goo = global i8* undef

define void @foo_i8() {
  entry:
    %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
    %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
    %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
    %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
    %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
    store i8 %4, i8* inttoptr(i32 805873688 to i8*)
    store i8 %3, i8* inttoptr(i32 805873719 to i8*)
    store i8 %2, i8* inttoptr(i32 805873720 to i8*)
    store i8 %1, i8* inttoptr(i32 805873727 to i8*)
    store i8* inttoptr(i32 805874800 to i8*), i8** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* inttoptr (i32 805874752 to i16*)
; CHECK-NEXT: %2 = load volatile i16, i16* inttoptr (i32 805874692 to i16*)
; CHECK-NEXT: %3 = load volatile i16, i16* inttoptr (i32 805874719 to i16*)
; CHECK-NEXT: %4 = load volatile i16, i16* inttoptr (i32 805874720 to i16*)
; CHECK-NEXT: %5 = load volatile i16, i16* inttoptr (i32 805874727 to i16*)
; CHECK-NEXT: %6 = load volatile i16, i16* inttoptr (i32 805874728 to i16*)
; CHECK-NEXT: %7 = load volatile i16, i16* inttoptr (i32 805874800 to i16*)
; CHECK-NEXT: %8 = load volatile i16, i16* inttoptr (i32 805874801 to i16*)
; CHECK-NEXT: %9 = load volatile i16, i16* inttoptr (i32 805874802 to i16*)
; CHECK-NEXT: %10 = load volatile i16, i16* inttoptr (i32 805874803 to i16*)
; CHECK-NEXT: %11 = load volatile i16, i16* inttoptr (i32 805874804 to i16*)
; CHECK-NEXT: %12 = load volatile i16, i16* inttoptr (i32 805874805 to i16*)
; CHECK-NEXT: %13 = load volatile i16, i16* inttoptr (i32 805874806 to i16*)
; CHECK-NEXT: %14 = load volatile i16, i16* inttoptr (i32 805874807 to i16*)
; CHECK-NEXT: %15 = load volatile i16, i16* inttoptr (i32 805874808 to i16*)
; CHECK-NEXT: %16 = load volatile i16, i16* inttoptr (i32 805874809 to i16*)
; CHECK-NEXT: %17 = load volatile i16, i16* inttoptr (i32 805874810 to i16*)
; CHECK-NEXT: %18 = load volatile i16, i16* inttoptr (i32 805874811 to i16*)
; CHECK-NEXT: %19 = load volatile i16, i16* inttoptr (i32 805874812 to i16*)
; CHECK-NEXT: %20 = load volatile i16, i16* inttoptr (i32 805874813 to i16*)
; CHECK-NEXT: %21 = load volatile i16, i16* inttoptr (i32 805874814 to i16*)
; CHECK-NEXT: %22 = load volatile i16, i16* inttoptr (i32 805874815 to i16*)
; CHECK-NEXT: %23 = load volatile i16, i16* inttoptr (i32 805874816 to i16*)
; CHECK-NEXT: %24 = load volatile i16, i16* inttoptr (i32 805874817 to i16*)
; CHECK-NEXT: %25 = load volatile i16, i16* inttoptr (i32 805874818 to i16*)
; CHECK-NEXT: %26 = load volatile i16, i16* inttoptr (i32 805874819 to i16*)
; CHECK-NEXT: %27 = load volatile i16, i16* inttoptr (i32 805874820 to i16*)
; CHECK-NEXT: %28 = load volatile i16, i16* inttoptr (i32 805874821 to i16*)
; CHECK-NEXT: %29 = load volatile i16, i16* inttoptr (i32 805874822 to i16*)
; CHECK-NEXT: %30 = load volatile i16, i16* inttoptr (i32 805874823 to i16*)
; CHECK-NEXT: %31 = load volatile i16, i16* inttoptr (i32 805874824 to i16*)
; CHECK-NEXT: %32 = load volatile i16, i16* inttoptr (i32 805874825 to i16*)
; CHECK-NEXT: %33 = load volatile i16, i16* inttoptr (i32 805874826 to i16*)
; CHECK-NEXT: %34 = load volatile i16, i16* inttoptr (i32 805874827 to i16*)
; CHECK-NEXT: %35 = load volatile i16, i16* inttoptr (i32 805874828 to i16*)
; CHECK-NEXT: %36 = load volatile i16, i16* inttoptr (i32 805874829 to i16*)
; CHECK-NEXT: %37 = load volatile i16, i16* inttoptr (i32 805874830 to i16*)
; CHECK-NEXT: %38 = load volatile i16, i16* inttoptr (i32 805874831 to i16*)
; CHECK-NEXT: %39 = load volatile i16, i16* inttoptr (i32 805874832 to i16*)
; CHECK-NEXT: %40 = load volatile i16, i16* inttoptr (i32 805874833 to i16*)
; CHECK-NEXT: %41 = load volatile i16, i16* inttoptr (i32 805874834 to i16*)
; CHECK-NEXT: %42 = load volatile i16, i16* inttoptr (i32 805874835 to i16*)
; CHECK-NEXT: %43 = load volatile i16, i16* inttoptr (i32 805874836 to i16*)
; CHECK-NEXT: %44 = load volatile i16, i16* inttoptr (i32 805874837 to i16*)
; CHECK-NEXT: %45 = load volatile i16, i16* inttoptr (i32 805874838 to i16*)
; CHECK-NEXT: %46 = load volatile i16, i16* inttoptr (i32 805874839 to i16*)
; CHECK-NEXT: %47 = load volatile i16, i16* inttoptr (i32 805874840 to i16*)
; CHECK-NEXT: %48 = load volatile i16, i16* inttopr...
define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
  ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
  ret void
}

define internal void @innerNoAttribute() {
  ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this function.
define void @outerNoAttribute() {
  call void @innerLarge()
  ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
  call void @innerLarge()
  ret void
define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
    call void @innerSmall()
    ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
    call void @innerNoAttribute()
    ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() #0
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512v1,avx512bw,avx512dq,prefer-256-bit | FileCheck %s

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: add512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT:    vpaddd (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT:    vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %d = load <16 x i32>, <16 x i32>* %a
    %e = load <16 x i32>, <16 x i32>* %b
    %f = add <16 x i32> %d, %e
    store <16 x i32> %f, <16 x i32>* %c
    ret void
}

define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: avg_v64i8_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rsi), %ymm0
    ; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm1
    ; CHECK-NEXT:    vpavgb (%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:    vpavgb 32(%rdi), %ymm1, %ymm1
    ; CHECK-NEXT:    vmovdqu %ymm1, (%rax)
    ; CHECK-NEXT:    vmovdqu %ymm0, (%rax)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %1 = load <64 x i8>, <64 x i8>* %a
    %2 = load <64 x i8>, <64 x i8>* %b
    %3 = zext <64 x i8> %1 to <64 x i32>
    %4 = zext <64 x i8> %2 to <64 x i32>
    %5 = add nuw nsw <64 x i32> %3, %4
    store <64 x i32> %5, <64 x i32>* %c
    ret void
}
```assembly
define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT:  vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT:  vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
    %1 = load <64 x i8>, <64 x i8>* %a
    %2 = load <64 x i8>, <64 x i8>* %b
    %3 = zext <64 x i8> %1 to <64 x i32>
    %4 = zext <64 x i8> %2 to <64 x i32>
    %5 = add nuw nsw <64 x i32> %3, %4
    %6 = add nuw nsw <64 x i32> %5, %4
    %7 = lshr <64 x i32> %6, %5, %4
    %8 = lshr <64 x i32> %6, %5, %4
    %9 = trunc <64 x i32> %7 to <64 x i8>
    store <64 x i8> %8, <64 x i8>* undef, align 4
    ret void
}
```

```assembly
define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpmaddwd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vpmaddwd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
    %1 = load <32 x i16>, <32 x i16>* %APtr
    %2 = load <32 x i16>, <32 x i16>* %BPtr
    %3 = load <16 x i32>, <16 x i32>* %CPtr
    %4 = add nuw nsw <32 x i32> %1, %2
    %5 = add nuw nsw <32 x i32> %4, %3
    %6 = add nuw nsw <32 x i32> %5, %4
    %7 = add nuw nsw <32 x i32> %6, %5, %4
    %8 = trunc <32 x i32> %7 to <32 x i16>
    store <32 x i16> %8, <32 x i16>* undef, align 4
    ret void
}
```
define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: pmaddwd_32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT:  vpmaddwd (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: psubus_64i8_max_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa64 (%rdi), %ymm0
    ; CHECK-NEXT:  vpmaddwd (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT:  vmovdqa64 %ymm0, (%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}
define void @psubus_64i8_max_512(%64 x i8)* %xptr, %64 x i8)* %yptr, %64 x i8)* %zptr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: psubus_64i8_max_512:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %x = load %64 x i8>, %64 x i8>* %xptr
    %y = load %64 x i8>, %64 x i8>* %yptr
    %cmp = icmp ult %64 x i8> %x, %y
    %max = select %64 x i1> %cmp, %64 x i8> %y, %64 x i8> %x
    %res = sub %64 x i8> %max, %y
    store %64 x i8> %res, %64 x i8>* %zptr
    ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: _Z9test_charPcS_i_256:
    ; CHECK: # %bb.0: # %entry
    ; CHECK-NEXT: movl %edx, %eax
    ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT: xorl %ecx, %ecx
    ; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
    ; CHECK-NEXT: .p2align 4, 0x90
    ; CHECK-NEXT: .LBB8_1: # %vector.body
    ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm3
    ; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm4
    ; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm5
    ; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm3
    ; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm4
    ; CHECK-NEXT: vpdada %ymm1, %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm1, %ymm3, %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm1, %ymm3, %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm1, %ymm3, %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm1, %ymm3, %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm1, %ymm3, %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm1, %ymm3, %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
entry:
  %3 = zext i32 %2 to i64
br label %vector.body

vector.body:
  %index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
  %vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
  %4 = getelementptr inbounds i8, i8* %0, i64 %index
  %5 = bitcast i8* %4 to <32 x i8>*
  %wide.load = load <32 x i8>, <32 x i8>* %5, align 1
  %6 = sext <32 x i8> %wide.load to <32 x i32>
  %7 = getelementptr inbounds i8, i8* %1, i64 %index
  %8 = bitcast i8* %7 to <32 x i8>*
  %wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
  %9 = sext <32 x i8> %wide.load14 to <32 x i32>
  %10 = mul nsw <32 x i32> %9, %6
  %11 = add nsw <32 x i32> %10, %vec.phi
  %index.next = add i64 %index, 32
  %12 = icmp eq i64 %index.next, %3
  br i1 %12, label %middle.block, label %vector.body

middle.block:
  %rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde...
i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>

%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

define i32 @_Z9test_charPcs_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" { ; CHECK-LABEL: _Z9test_charPcs_i_512:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:   movl %edx, %eax
; CHECK-NEXT:   vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:   xorl %ecx, %ecx
; CHECK-NEXT:   vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:   p2align 4, 0x90
; CHECK-NEXT:   .LBB9_1: # %vector.body
; CHECK-NEXT:   # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:   vpmovsxzbw (%rdi,%rcx), %zmm2
; CHECK-NEXT:   vpmovsxzbw (%rsi,%rcx), %zmm3
; CHECK-NEXT:   vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT:   vextracti128 $1, %zmm0, %zmm1
; CHECK-NEXT:   addq $32, %rcx
; CHECK-NEXT:   cmpq %rcx, %rax
; CHECK-NEXT:   jne .LBB9_1
; CHECK-NEXT:   ; %bb.2: # %middle.block
; CHECK-NEXT:   vpaddd %zmm1, %zmm2, %zmm1
; CHECK-NEXT:   ; %bb.3: # %exit.block
; CHECK-NEXT:   vshufd {.*#+} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:   vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:   vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:   vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:   vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:   vshufd {.*#+} xmm1 = xmm0[1,1,2,3]
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> %i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sad_16i8_256:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT:    movq $-1024, %rax # imm = 0xFC00
    ; CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT:    .p2align 4, 0x90
    ; CHECK-NEXT:  .LBB10_1: # %vector.body
    ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm2
    ; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm2, %xmm2
    ; CHECK-NEXT:    vpaddd %ymm1, %ymm2, %ymm1
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
    ; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
    ; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
    ; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
    ; CHECK-NEXT:    vmovd %xmm0, %eax
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    
    entry:
    br label %vector.body
}

vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
 ; CHECK-LABEL: sad_16i8_512:
 ; CHECK: # %bb.0: # %entry
 ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
 ; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
 ; CHECK-NEXT: .p2align 4, 0x90
 ; CHECK-NEXT: .LBB11_1: # %vector.body
 ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
 ; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm1
 ; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm1, %xmm1
 ; CHECK-NEXT: vpaddb %zmm0, %zmm1, %zmm0
 ; CHECK-NEXT: addq $4, %rax
 ; CHECK-NEXT: jne .LBB11_1
 ; CHECK-NEXT: # %bb.2: # %middle.block
 ; CHECK-NEXT: vextracti64x4 $1, %zmm0, %zmm1
 ; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
entry:
  br label %vector.body

vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
  %2 = zext <16 x i8> to <16 x i32>
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = zext <16 x i8> to <16 x i32>
  %6 = sub nsw <16 x i32> %2, %5
  %7 = icmp sgt <16 x i32> %6, undef
  %8 = sub nsw <16 x i32> zeroinitializer, %6
  %9 = select <16 x i1> %7, %6, undef
  %index.next = add i64 %index, 4
  %11 = icmp eq i64 %index.next, 1024
  br i1 %11, label %middle.block, label %vector.body

middle.block:
  %.lcssa = phi <16 x i32> [ %10, %vector.body ]
  %.rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef
  %bin.rdx = add <16 x i32> %.lcssa, %.rdx.shuf
  %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef
  %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
  %rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef
  %bin.rdx3 = add <16 x i32> %bin.rdx3, %rdx.shuf3
  %rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef

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define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK:    # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtqdps %ymm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm1
; CHECK-NEXT: vcvtqdps %ymm1, %ymm1
; CHECK-NEXT: vmovaps %ymm1, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitialize
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vcvtqd2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitialize
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK:    # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res)  "min-legal-vector-width"="512" {  
    ; CHECK-LABEL: sbto16f64_512:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT: vpmovw2m %ymm0, %k0  
    ; CHECK-NEXT: vpmovm2d %k0, %ymm0  
    ; CHECK-NEXT: vcvtqd2pd %ymm0, %zmm0  
    ; CHECK-NEXT: vcvtqd2pd %ymm0, %zmm1  
    ; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0  
    ; CHECK-NEXT: vcvtqd2pd %ymm0, %zmm0  
    ; CHECK-NEXT: vpsrld $31, %ymm0, %ymm0  
    ; CHECK-NEXT: vpmovaps %zmm0, 64(%rdi)  
    ; CHECK-NEXT: vpmovaps %zmm1, (%rdi)  
    ; CHECK-NEXT: vzeroupper  
    ; CHECK-NEXT: retq  
    %mask = icmp slt <16 x i16> %a, zeroinitializer  
    %1 = sitofp <16 x i1> %mask to <16 x double>  
    store <16 x double> %1, <16 x double>* %res  
    ret void  
}  

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: ubto16f32_256:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT: vpmovw2m %ymm0, %k0  
    ; CHECK-NEXT: vpmovm2d %k0, %ymm0  
    ; CHECK-NEXT: kshiftrw $8, %k0, %k1  
    ; CHECK-NEXT: vpsrlq $31, %ymm0, %ymm0  
    ; CHECK-NEXT: vcvtqd2ps %ymm0, %ymm0  
    ; CHECK-NEXT: vpmovm2d %k0, %ymm1  
    ; CHECK-NEXT: vpsrlq $31, %ymm1, %ymm1  
    ; CHECK-NEXT: vcvtqd2ps %ymm1, %ymm1  
    ; CHECK-NEXT: vpmovaps %ymm1, (%rdi)  
    ; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)  
    ; CHECK-NEXT: retq  
    %mask = icmp slt <16 x i16> %a, zeroinitializer  
    %1 = sitofp <16 x i1> %mask to <16 x double>  
    store <16 x double> %1, <16 x double>* %res  
    ret void  
}
define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: ubto16f32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vpsrld $31, %zmm0, %zmm0
    ; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: ubto16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %zmm0, %zmm1
    ; CHECK-NEXT:    vextracti128 $1, %zmm0, %xmm0
    ; CHECK-NEXT:    vcvtdq2pd %zmm2, %zmm0
    ; CHECK-NEXT:    vextracti128 $1, %zmm2, %xmm2
    ; CHECK-NEXT:    vmovaps %zmm0, 32(%rdi)
    ; CHECK-NEXT:    vmovaps %zmm2, 64(%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
}
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f64_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtq2pd %ymm0, %zmm1
; CHECK-NEXT: vextractf64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtq2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
; CHECK-LABEL: test_16f32toub_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vcvtq2dq (%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k0
; CHECK-NEXT: vcvtq2dq 32(%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k1
; CHECK-NEXT: kunpckbw %k0, %k1, %k1
; CHECK-NEXT: vmovdq16 %ymm0, %ymm0 {%k1} {z}
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
; CHECK-LABEL: test_16f32toub_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vcvtq2dq (%rdi), %zmm1
; CHECK-NEXT: vpslld $31, %zmm1, %zmm1
; CHECK-NEXT: vpmovd2m %zmm1, %k1
define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptoui <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
    %a = load <64 x i8>, <64 x i8>* %ptr
    %mask = fptosi <64 x float> %a to <64 x i1>
    %select = select <64 x i1> %mask, <64 x i8> %passthru, <64 x i8> zeroinitializer
    ret <64 x i8> %select

    ymm3[8],ymm0[8],ymm3[9],ymm0[9],ymm3[10],ymm0[10],ymm3[11],ymm0[11],ymm3[12],ymm0[12],ymm3[13]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm5 =
ymm1[8],ymm0[8],ymm1[9],ymm0[9],ymm1[10],ymm0[10],ymm1[11],ymm0[11],ymm1[12],ymm0[12],ymm1[13 ],ymm0[13],ymm1[14],ymm0[14],ymm1[15],ymm0[15],ymm1[24],ymm0[24],ymm1[25],ymm0[25],ymm1[26],ym m0[26],ymm1[27],ymm0[27],ymm1[28],ymm0[28],ymm1[29],ymm0[29],ymm1[30],ymm0[30],ymm1[31],ymm0[3 1 ]

; CHECK-NEXT:  vpmullw %ymm4, %ymm5, %ymm4

; CHECK-NEXT:  vmovdqa {{.*#+}} ymm5 =

; CHECK-NEXT:  vpand %ymm5, %ymm4, %ymm4

; CHECK-NEXT:  vpunpcklwb {{.*#+}} ymm3 =
ymm3[0],ymm0[0],ymm3[1],ymm0[1],ymm3[2],ymm0[2],ymm3[3],ymm0[3],ymm3[4],ymm0[4],ymm3[5],ymm0[5],ymm3[6],ymm0[6],ymm3[7],ymm0[7],ymm3[16],ymm0[16],ymm3[17],ymm0[17],ymm3[18],ymm0[18],ymm3[1 9],ymm0[19],ymm3[20],ymm0[20],ymm3[21],ymm0[21],ymm3[22],ymm0[22],ymm3[23],ymm0[23]

; CHECK-NEXT:  vpmullw %ymm3, %ymm4, %ymm1

; CHECK-NEXT:  vpand %ymm5, %ymm1, %ymm1

; CHECK-NEXT:  vpackuswb %ymm4, %ymm1, %ymm1

; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm3 =
ymm2[8],ymm0[8],ymm2[9],ymm0[9],ymm2[10],ymm0[10],ymm2[11],ymm0[11],ymm2[12],ymm0[12],ymm2[13 ],ymm0[13],ymm2[14],ymm0[14],ymm2[15],ymm0[15],ymm2[24],ymm0[24],ymm2[25],ymm0[25],ymm2[26],ym m0[26],ymm2[27],ymm0[27],ymm2[28],ymm0[28],ymm2[29],ymm0[29],ymm2[30],ymm0[30],ymm2[31],ymm0[3 1 ]

; CHECK-NEXT:  vpmullw %ymm3, %ymm1, %ymm1

; CHECK-NEXT:  vpackuswb %ymm4, %ymm1, %ymm1

; CHECK-NEXT:  vpunpcklwb {{.*#+}} ymm3 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]

; CHECK-NEXT:  vpmullw %ymm2, %ymm0, %ymm0

; CHECK-NEXT:  vpand %ymm5, %ymm3, %ymm3

; CHECK-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0

; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)

; CHECK-NEXT:  vzeroupper

; CHECK-NEXT:  retq

%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8> %c  
ret void
}

define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: mul512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm1
; CHECK-NEXT: vpunpckhbw [[...]] zmm2 = 
  zmm1[8],zmm0[8],zmm1[9],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13],
  zmm0[13],zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[24],zmm0[24],zmm1[25],zmm0[25],zmm1[26],zmm0
  [26],zmm1[27],zmm0[27],zmm1[28],zmm0[28],zmm1[29],zmm0[29],zmm1[30],zmm0[30],zmm1[31],zmm0[31],z
  mm1[40],zmm0[40],zmm1[41],zmm0[41],zmm1[42],zmm0[42],zmm1[43],zmm0[43],zmm1[44],zmm0[44],zmm1[45],
  zmm0[45],zmm1[46],zmm0[46],zmm1[47],zmm0[47],zmm1[56],zmm0[56],zmm1[57],zmm0[57],zmm1[58],z
  mm0[58],zmm1[59],zmm0[59],zmm1[60],zmm0[60],zmm1[61],zmm0[61],zmm1[62],zmm0[62],zmm1[63],zmm0[6
  3]
; CHECK-NEXT: vpunpckhbw [[...]] zmm3 = 
  zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
  2,42,43,43,44,44,45,45,46,46,47,47,47,47,56,56,57,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-NEXT: vpmullw %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpmullw %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%d = load <64 x i8>, <64 x i8> %a
%e = load <64 x i8>, <64 x i8> %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8> %c  
ret void
}
; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: mload_v4i32:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vptestmd %xmm0, %xmm0, %k1
    ; CHECK-NEXT: vpbldmd (%rdi), %xmm1, %xmm0 {%k1}
    ; CHECK-NEXT: retq
    %mask = icmp eq <4 x i32> %trigger, zeroinitializer
    %res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>* %addr, i32 4, <4 x i1> %mask, <4 x i32> %dst)
    ret <4 x i32> %res
}
declare <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32, <4 x i1>, <4 x i32>)

; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;    ; Test that early if conversion produces LOCR with operands of the right
;    ; register classes.

define void @autogen_SD4739(i8*) {
    ; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
    BB:
    %L34 = load i8, i8* %0
    %Cmp56 = icmp sgt i8 undef, %L34
    br label %CF246
    CF246:                                            ; preds = %CF246, %BB
    %SI163 = select i1 %Cmp56, i8 %L34, i8 undef
    br i1 undef, label %CF246, label %CF248
    CF248:                                            ; preds = %CF248, %CF246
    store i8 %SI163, i8* %0
    br label %CF248
} ; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
;    ; D31946
;    ; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.
;    ; Additionally ensure that the output code actually put fp128 values in SSE registers.
declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128)
define fp128 @TestSelect(fp128 %a, fp128 %b) {
    %cmp = fcmp ogt fp128 %a, %b
    %sub = fsub fp128 %a, %b
    %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
    ret fp128 %res
; CHECK-LABEL: TestSelect:
; CHECK    movaps 16(%rsp), %xmm1
; CHECK-NEXT  callq __subtf3
; CHECK-NEXT  testl %ebx, %ebx
; CHECK-NEXT  jg .LBB0_2
; CHECK-NEXT  # %bb.1:
; CHECK-NEXT  movaps .LCPI0_0(%rip), %xmm0
; CHECK-NEXT .LBB0_2:
; CHECK-NEXT  addq $32, %rsp
; CHECK-NEXT  popq %rbx
; CHECK-NEXT  retq
}

define fp128 @TestFabs(fp128 %a) {
%res = call fp128 @llvm.fabs.f128(fp128 %a)
ret fp128 %res
}

; CHECK-LABEL: TestFabs:
; CHECK    andps .LCPI1_0(%rip), %xmm0
; CHECK-NEXT  retq
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
%res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
ret fp128 %res
}

; CHECK-LABEL: TestCopysign:
; CHECK    andps .LCPI2_1(%rip), %xmm0
; CHECK-NEXT  orps %xmm1, %xmm0
; CHECK-NEXT  retq
}

define fp128 @TestFneg(fp128 %a) {
%mul = fmul fp128 %a, %a
%res = fsub fp128 0xL00000000000000008000000000000000, %mul
ret fp128 %res
}

; CHECK-LABEL: TestFneg:
; CHECK    movaps %xmm0, %xmm1
; CHECK-NEXT  callq __multf3
; CHECK-NEXT  xorps .LCPI3_0(%rip), %xmm0
; CHECK-NEXT  popq %rax
; CHECK-NEXT  retq
}

; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -
define void @zext256() "min-legal-vector-width"="256" {  ; VEC256-LABEL: 'zext256'  ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; VEC512-LABEL: 'zext256'
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
   %A = zext <8 x i16> undef to <8 x i64>
   %B = zext <8 x i32> undef to <8 x i64>
   %C = zext <16 x i8> undef to <16 x i32>
   %D = zext <16 x i16> undef to <16 x i32>
   %E = zext <32 x i8> undef to <32 x i16>
   ret void
}
define void @zext512() "min-legal-vector-width"="512" {  ; AVX-LABEL: 'zext512'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; SKX256-LABEL: 'zext512'
define void @sext256() "min-legal-vector-width"="256" {
    ; VEC256-LABEL: 'sext256'
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

    %A = sext <8 x i16> undef to <8 x i64>
    %B = sext <8 x i32> undef to <8 x i64>
    %C = sext <16 x i8> undef to <16 x i32>
    %D = sext <16 x i16> undef to <16 x i32>
    %E = sext <32 x i8> undef to <32 x i16>
    ret void
}
; VEC512-LABEL: 'sext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void

define void @sext512() "min-legal-vector-width"="512" {
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
The two loads based on %struct.0, loading two different data types cause LSR to assume type "void" for the memory type. This would then cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
    br i1 undef, label %while.end, label %while.body.lr.ph
  
  while.body.lr.ph: ; preds = %entry
    br label %while.body
  
  while.body: ; preds = %exit.2, %while.body.lr.ph
    %lsr.iv = phi %struct.0* [%cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
    switch i32 undef, label %exit [
      i32 1, label %sw.bb.i
      i32 2, label %sw.bb3.i
    ]
sw.bb.i: ; preds = %while.body
unreachable

sw.bb3.i: ; preds = %while.body
unreachable

exit: ; preds = %while.body
switch i32 undef, label %exit.2 {
  i32 1, label %sw.bb.i17
  i32 2, label %sw.bb3.i20
}

sw.bb.i17: ; preds = %.exit
%0 = bitcast %struct.0* %lsr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lsr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable

exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
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@ define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
    ; First, extend each i32 to i64
    ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
    ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
    ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
    ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
    ; Next, convert each to double.
    ; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
    ; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
    ; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
    ; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
    ; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
    ; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
    ; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
    ; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
    ; CHECK: ret
    %flt = sitofp <16 x i32> %in to <16 x double>
    %res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}

1.351 libcroco 0.6.12 4.el7
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The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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c) If distribution of the work is made by offering access to copy
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That's all there is to it!

1.352 smartmontools 7.0 1.el7

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1.353 at-spi2-core 2.28.0 1.el7

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The End

abstract: 'Build and install Perl modules'
author:
  - 'Ken Williams <kwilliams@cpan.org>'
  - "Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-build@perl.org>,"
build_requires:
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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

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```perl
#!perl

=head1 NAME

copyright.t

=head1 DESCRIPTION

Tests that the latest copyright years in the top-level README file and the C<perl -v> output match each other.
```
If the test fails, update at least one of README and perl.c so that they match reality.

Optionally you can pass the C<--now> option to check they are at the current year. This isn't checked by default, so that it doesn't fail for people working on older releases. It should be run before making a new release.

=cut

use strict;
use Config;
BEGIN { require './test.pl' }

if ( $Config{usecrosscompile} ) {
    skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so
# that tests still pass for people intentionally working on older versions:
if ($opt eq '--now')
{
    my $current_year = (gmtime)[5] + 1900;
    is $v_year, $current_year, 'perl -v copyright includes current year';
    is $readme_year, $current_year, 'README copyright includes current year';
}

# Otherwise simply check that the two copyright dates match each other:
else
{
    is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;

sub readme_year
# returns the latest copyright year from the top-level README file
{
    open my $readme, '<', '../README' or die "Opening README failed: $!";

    # The copyright message is the first paragraph:
1.357 glib-networking 2.56.1 1.el7
1.357.1 Available under license :

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Version 2.1, February 1999

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s
A shuffle mask with all undef elements is always legal.

```c
define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK: // %bb.0:
; CHECK-NEXT: ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT: mov v0.d[1], v0.d[0]
; CHECK-NEXT: ret

%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}
```

; RUN: llc -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right
; register classes.

```c
define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
%L34 = load i8, i8* %0
%Cmp56 = icmp sgt i8 undef, %L34
br label %CF246

CF246: ; preds = %CF246, %BB
%SI163 = select i1 %Cmp56, i8 %L34, i8 undef
br i1 undef, label %CF246, label %CF248

CF248: ; preds = %CF248, %CF246
store i8 %SI163, i8* %0
br label %CF248
}
; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,SKX256
; RUN: opt -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512
```

```c
define void @zext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'zext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
```
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; VEC512-LABEL: 'zext256'
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> unde to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

define void @zext512() "min-legal-vector-width"="512" {
 ; AVX-LABEL: 'zext512'
 ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> unde to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> unde to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> unde to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> unde to <16 x
define void @sext256() "min-legal-vector-width"="256" {  
  ; VEC256-LABEL: 'sext256'  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>  
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>  
  ret void
}
i64>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

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; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

 target triple = "x86_64-unknown-linux-gnu"

; This should promote
; CHECK-LABEL: @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64> %arg1.val)
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64> readonly %arg1) #0 {
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
; CHECK-LABEL: @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64> %arg1.val)
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
; CHECK-LABEL: @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64> %arg1.val)
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote
; CHECK-LABEL: @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* %arg1.val)
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should not promote
; CHECK-LABEL: @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1)
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
; CHECK-LABEL: @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1)
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
; CHECK-LABEL: @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64> %arg1.val)
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
; CHECK-LABEL: @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1.val)
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

LLVM System Interface Library

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; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
  br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
  br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
  %lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
  switch i32 undef, label %exit [
    i32 1, label %sw.bb.i
    i32 2, label %sw.bb3.i
  ]

  sw.bb.i: ; preds = %while.body
    unreachable

  sw.bb3.i: ; preds = %while.body
    unreachable

  exit: ; preds = %while.body
switch i32 undef, label %exit.2 {
  i32 1, label %sw.bb.i17
  i32 2, label %sw.bb3.i20
}

sw.bb.i17:  ; preds = %exit
  %0 = bitcast %struct.0* %lsr.iv to i32*
  %1 = load i32, i32* %0, align 4
  unreachable

sw.bb3.i20:  ; preds = %exit
  %2 = bitcast %struct.0* %lsr.iv to i8**
  %3 = load i8*, i8** %2, align 4
  unreachable

exit.2:  ; preds = %exit
  %cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
  br label %while.body

while.end:  ; preds = %entry
  ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s

define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
  ; CHECK-LABEL: test_sitofp_fixed:
  ; First, extend each i32 to i64
  ; CHECK-DAG: sshll2.2d [[[BLOCK0_HI:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK1_HI:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK2_HI:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK3_HI:v[0-9]+]], v3, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK0_LO:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK1_LO:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK2_LO:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll2.2d [[[BLOCK3_LO:v[0-9]+]], v3, #0

  ; Next, convert each to double.
  ; CHECK-DAG: scvtf.2d v0, [[[BLOCK0_LO]]]
  ; CHECK-DAG: scvtf.2d v1, [[[BLOCK0_HI]]]
  ; CHECK-DAG: scvtf.2d v2, [[[BLOCK1_LO]]]
  ; CHECK-DAG: scvtf.2d v3, [[[BLOCK1_HI]]]
  ; CHECK-DAG: scvtf.2d v4, [[[BLOCK2_LO]]]
  ; CHECK-DAG: scvtf.2d v5, [[[BLOCK2_HI]]]
  ; CHECK-DAG: scvtf.2d v6, [[[BLOCK3_LO]]]
  ; CHECK-DAG: scvtf.2d v7, [[[BLOCK3_HI]]]
; CHECK: ret
%flt = sitofp <16 x i32> %in to <16 x double>
%res = fdiv <16 x double> %flt, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0
ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:
; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, double 64.0, double 64.0, double 64.0, double 64.0
ret <4 x double> %res
}
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define internal void @innerSmall() "min-legal-vector-width"="128" {
    ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
    ret void
}

define internal void @innerNoAttribute() {
    ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
    call void @innerSmall()
    ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
    call void @innerNoAttribute()
    ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = [ "min-legal-vector-width"="512" ]
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
;
; D31946
; Check that we don't end up with the "LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copySign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
  %cmp = fcmp ogt fp128 %a, %b
  %sub = fsub fp128 %a, %b
  %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
  ret fp128 %res
}

define fp128 @TestFabs(fp128 %a) {
  %res = call fp128 @llvm.fabs.f128(fp128 %a)
  ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
  %res = call fp128 @llvm.copySign.f128(fp128 %a, fp128 %b)
  ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {

%mul = fmul fp128 %a, %a
%res = fsub fp128 0xL00000000000000008000000000000000, %mul
ret fp128 %res
 ; CHECK-LABEL: TestFneg:
 ; CHECK  movaps %xmm0, %xmm1
 ; CHECK-NEXT callq __multf3
 ; CHECK-NEXT xorps .LCPI3_0(%rip), %xmm0
 ; CHECK-NEXT popq %rax
 ; CHECK-NEXT retq
{
 ; RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
 ; Also check that an constant used as value to be stored rather than
 ; pointer in a store instruction is hoisted.
 ; CHECK: foo_i8
 ; CHECK-DAG:  %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
 ; CHECK-DAG:  %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
 ; CHECK-DAG:  %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
 ; CHECK-DAG:  %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
 ; CHECK:  %0 = inttoptr i32 %[[C2]] to i8*
 ; CHECK-NEXT:  %1 = load volatile i8, i8* %0
 ; CHECK-NEXT:  %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
 ; CHECK-NEXT:  %2 = inttoptr i32 %[[M1]] to i8*
 ; CHECK-NEXT:  %3 = load volatile i8, i8* %2
 ; CHECK-NEXT:  %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 31
 ; CHECK-NEXT:  %4 = inttoptr i32 %[[M2]] to i8*
 ; CHECK-NEXT:  %5 = load volatile i8, i8* %4
 ; CHECK-NEXT:  %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 7
 ; CHECK-NEXT:  %6 = inttoptr i32 %[[M3]] to i8*
 ; CHECK-NEXT:  %7 = load volatile i8, i8* %6
 ; CHECK-NEXT:  %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 7
 ; CHECK-NEXT:  %8 = inttoptr i32 %[[M4]] to i8*
 ; CHECK-NEXT:  %9 = load volatile i8, i8* %8
 ; CHECK-NEXT:  %[[M5:const_mat[0-9]?]] = add i32 %[[C3]], 7
 ; CHECK-NEXT:  %10 = inttoptr i32 %[[M5]] to i8*
 ; CHECK-NEXT:  store i8 %9, i8* %10
 ; CHECK-NEXT:  %[[M6:const_mat[0-9]?]] = add i32 %[[C1]], 80
 ; CHECK-NEXT:  %11 = inttoptr i32 %[[M6]] to i8*
 ; CHECK-NEXT:  store i8 %11, i8* %11
 ; CHECK-NEXT:  %12 = inttoptr i32 %[[C3]] to i8*
 ; CHECK-NEXT:  store i8 %7, i8* %12
 ; CHECK-NEXT:  %13 = inttoptr i32 %[[C3]] to i8*
 ; CHECK-NEXT:  store i8 %3, i8* %13
 ; CHECK-NEXT:  %14 = inttoptr i32 %[[M6]] to i8*
 ; CHECK-NEXT:  store i8* %14, i8** @goo
@goo = global i8* undef

define void @foo_i8() {
  entry:
  %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
  %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
  %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
  %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
  %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
  store i8 %4, i8* inttoptr(i32 805873688 to i8*)
  store i8 %3, i8* inttoptr(i32 805873719 to i8*)
  store i8 %2, i8* inttoptr(i32 805873720 to i8*)
  store i8 %1, i8* inttoptr(i32 805873727 to i8*)
  store i8* inttoptr(i32 805874800 to i8*), i8** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: [%[C1:const[0-9]??]] = bitcast i32 805874752 to i32
; CHECK-DAG: [%[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[C2] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[M1:const_mat[0-9]??] = add i32 %[C2], 4
; CHECK-NEXT: %2 = inttoptr i32 %[M1] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %[M2:const_mat[0-9]??] = add i32 %[C2], 32
; CHECK-NEXT: %4 = inttoptr i32 %[M2] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %[M3:const_mat[0-9]??] = add i32 %[C2], 62
; CHECK-NEXT: %6 = inttoptr i32 %[M3] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[C1] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %[M4:const_mat[0-9]??] = add i32 %[C1], 22
; CHECK-NEXT: %10 = inttoptr i32 %[M4] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2

define void @foo_i16() {
  entry:
  %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
  %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
  %2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
  %3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
  %4 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
  ret void
; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874816 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK:  %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %[[M2:const_mat[0-9]?]] = add i32 %[[C2]], 124
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %[[M3:const_mat[0-9]?]] = add i32 %[[C1]], 8
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %[[M4:const_mat[0-9]?]] = add i32 %[[C1]], 12
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i32*
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

define void @foo_i32() {
  entry:
  %0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
  %1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
  %2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
  %3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
  %4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
  %5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
  ret void
}

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vl,avx512bw,avx512dq,prefer-256-bit | FileCheck %s

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpadd 32(%rsi), %ymm0, %ymm0
; CHECK-NEXT: vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, 32(%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}

define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: add512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpadd (%rsi), %zmm0, %zmm0
define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {  
  define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {  

%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, %4
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, %4
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" { ; CHECK-LABEL: pmaddwd_32_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm0
; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="512" { ; CHECK-LABEL: pmaddwd_32_512:
; CHECK: # %bb.0:
define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: psubus_64i8_max_256:  
    ; CHECK:   # %bb.0:  
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0  
    ; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1  
    ; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0  
    ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)  
    ; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)  
    ; CHECK-NEXT: vzeroupper  
    ; CHECK-NEXT: retq  
    %x = load <64 x i8>, <64 x i8>* %xptr  
    %y = load <64 x i8>, <64 x i8>* %yptr  
    %cmp = icmp ult <64 x i8> %x, %y  
    %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x  
    %res = sub <64 x i8> %max, %y  
    store <64 x i8> %res, <64 x i8>* %zptr  
    ret void
}

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {  
    ; CHECK-LABEL: psubus_64i8_max_512:  
    ; CHECK:   # %bb.0:  
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0  
    ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0  
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)  
    ; CHECK-NEXT: vzeroupper  
    ; CHECK-NEXT: retq  
    %A = load <32 x i16>, <32 x i16>* %APtr  
    %B = load <32 x i16>, <32 x i16>* %BPtr  
    %a = sext <32 x i16> %A to <32 x i32>  
    %b = sext <32 x i16> %B to <32 x i32>  
    %m = mul nsw <32 x i32> %a, %b  
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>  
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>  
    %ret = add <16 x i32> %odd, %even  
    store <16 x i32> %ret, <16 x i32>* %CPtr  
    ret void  
}
define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
	.entry:
		%3 = zext i32 %2 to i64

; CHECK-NEXT: retq
		%x = load <64 x i8>, <64 x i8>* %xptr
		%y = load <64 x i8>, <64 x i8>* %yptr
		%cmp = icmp ult <64 x i8> %x, %y
		%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
		%res = sub <64 x i8> %max, %y
		store <64 x i8> %res, <64 x i8>* %zptr
		ret void
}
br label %vector.body

vector.body:
  %index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
  %vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
  %4 = getelementptr inbounds i8, i8* %0, i64 %index
  %5 = bitcast i8* %4 to <32 x i8>*
  %wide.load = load <32 x i8>, <32 x i8>* %5, align 1
  %6 = sext <32 x i8> %wide.load to <32 x i32>
  %7 = getelementptr inbounds i8, i8* %1, i64 %index
  %8 = bitcast i8* %7 to <32 x i8>* %8, align 1
  %9 = sext <32 x i8> %wide.load14 to <32 x i32>
  %10 = mul nsw <32 x i32> %9, %6
  %11 = add nsw <32 x i32> %10, %vec.phi
  %index.next = add i64 %index, 32
  %12 = icmp eq i64 %index.next, %3
  br i1 %12, label %middle.block, label %vector.body

middle.block:
  %rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
  %rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
  %rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
  %rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
  %rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
  %13 = extractelement <32 x i32> %bin.rdx20, i32 0
  ret i32 %13
}
define i32 @__Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: __Z9test_charPcS_i_512:
  ; CHECK:       # %bb.0: # %entry
  ; CHECK-NEXT:  movl %edx, %eax
  ; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT:  xorl %ecx, %ecx
  ; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
  ; CHECK-NEXT:  .p2align 4, 0x90
  ; CHECK-NEXT:  .LBB9_1: # %vector.body
  ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
  ; CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
  ; CHECK-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
  ; CHECK-NEXT:    vpmaddwd %zmm2, %zmm3, %zmm2
  ; CHECK-NEXT:    cmpq %rcx, %rax
  ; CHECK-NEXT:    jne .LBB9_1
  ; CHECK-NEXT:  # %bb.2: # %middle.block
  ; CHECK-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
  ; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
  ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
  ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
  ; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
  ; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
  ; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
  ; CHECK-NEXT:    vmovd %xmm0, %eax
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
entry:
  %3 = zext i32 %2 to i64
  br label %vector.body
}

vector.body:

%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %i, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> undef, <32 x i32> unde...
; CHECK-NEXT:  vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-NEXT:  vpaddd %ymm1, %ymm2, %ymm1
; CHECK-NEXT:  addq $4, %rax
; CHECK-NEXT:  jne .LBB10_1
; CHECK-NEXT:  # %bb.2: # %middle.block
; CHECK-NEXT:  vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:  vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT:  vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT:  vpaddd %xmm0, %xmm1, %xmm0
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
entry:
  br label %vector.body

vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitialize, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %2 = addq $4, %rax
  %widen = load <16 x i8>, <16 x i8>* %1, align 4
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %widen1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = sub nsw <16 x i32> %2, %5
  %6 = add nsw <16 x i32> %4, %5
  %7 = icmp sgt <16 x i32> %6, %i32 -1
  %8 = add nsw <16 x i32> %7, %i32 -1
  %9 = add nsw <16 x i32> %8, %vec.phi
  %index.next = add i64 %index, 4
  %11 = icmp eq i64 %index.next, 1024
  br i1 %11, label %middle.block, label %vector.body

middle.block:
  %lcssa = phi <16 x i32> [ %10, %vector.body ]
  %rdx.shuf = shufflevector <16 x i32> %lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx = add <16 x i32> %lcssa, %rdx.shuf
  %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 8, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> 
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3 
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> 
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4 
%12 = extractelement <16 x i32> %bin.rdx4, i32 0 
ret i32 %12 
} 
define i32 @sad_16i8_512() "min-legal-vector-width"="512" { 
; CHECK-LABEL: sad_16i8_512: 
; CHECK: # %bb.0: # %entry 
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0 
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00 
; CHECK-NEXT: .p2align 4, 0x90 
; CHECK-NEXT: .LBB11_1: # %vector.body 
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1 
; CHECK-NEXT: vmovdquad a+1024(%rax), %xmm1 
; CHECK-NEXT: vpsadbwb b+1024(%rax), %xmm1, %xmm1 
; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0 
; CHECK-NEXT: addq $4, %rax 
; CHECK-NEXT: jne .LBB11_1 
; CHECK-NEXT: # %bb.2: # %middle.block 
; CHECK-NEXT: vextracti64x4 %1, %zmm0, %ymm1 
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0 
; CHECK-NEXT: vextracti128 %1, %zmm0, %zmm1 
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0 
; CHECK-NEXT: vpshufd {{.*#+}} %xmm1 = %xmm0[2,3,0,1] 
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0 
; CHECK-NEXT: vextracti64x4 %1, %zmm0, %ymm1 
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0 
; CHECK-NEXT: vextracti128 %1, %zmm0, %zmm1 
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0 
; CHECK-NEXT: vpshufd {{.*#+}} %xmm1 = %xmm0[1,1,2,3] 
; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0 
; CHECK-NEXT: vmovd %zmm0, %eax 
; CHECK-NEXT: vzeroupper 
; CHECK-NEXT: retq 
entry: 
br label %vector.body 
vector.body: 
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ] 
%vec.phi = phi i64 [ <16 x i32> ] [ zeroinitializer, %entry ], [ %10, %vector.body ] 
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index 
%1 = bitcast i8* %0 to <16 x i8>* 
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4 
%2 = zext <16 x i8> %wide.load to <16 x i32> 
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 1024
br i1 %11, label %middle.block, label %vector.body
middle.block:
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> %i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> %i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> %i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> %i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define void @sbtol6f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: sbtol6f32_256:
 ; CHECK:  # %bb.0:
 ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
 ; CHECK-NEXT:  kshiftrw $8, %k0, %k1
 ; CHECK-NEXT:  vpmovvm2d %k1, %ymm0
 ; CHECK-NEXT:  vcvtqd2ps %ymm0, %ymm0
 ; CHECK-NEXT:  vpmovmd2 %k0, %ymm1
 ; CHECK-NEXT:  vcvtqd2ps %ymm1, %ymm1
 ; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
 ; CHECK-NEXT:  vmovaps %ymm0, 32(%rdi)
 ; CHECK-NEXT:  vzeroupper
 ; CHECK-NEXT:  retq
 %mask = icmp slt <16 x i16> %a, zeroinitializer

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitialzer
    %l = sitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %l, <16 x float>* %res
    ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbto16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    vpmovm2d %k1, %ymmm0
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymmm1
    ; CHECK-NEXT:    vextracti128 $1, %ymmm0, %xmm0
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymmm0
    ; CHECK-NEXT:    vpmovm2d %k0, %ymmm1
    ; CHECK-NEXT:    vcvtdq2pd %ymm2, %ymmm3
    ; CHECK-NEXT:    vextracti128 $1, %ymmm2, %xmm2
    ; CHECK-NEXT:    vcvtdq2pd %ymm2, %ymmm2
    ; CHECK-NEXT:    vmovaps %ymmm2, 32(%rdi)
    ; CHECK-NEXT:    vmovps %ymmm3, (%rdi)
    ; CHECK-NEXT:    vmovaps %ymmm0, 96(%rdi)
    ; CHECK-NEXT:    vmovaps %ymmm1, 64(%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitialzer
    %l = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %l, <16 x double>* %res
    ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f64_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitialzer
    %l = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %l, <16 x double>* %res
    ret void
}
define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" 
  ; CHECK-LABEL: ubto16f32_256:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:    vpmovw2m %y whole, %k0
  ; CHECK-NEXT:    vpmovm2d %k0, %z whole
  ; CHECK-NEXT:    vcvtdq2pd %y whole, %z whole
  ; CHECK-NEXT:    vextracti64x4 %1, %z whole, %y whole
  ; CHECK-NEXT:    vcvtdq2pd %y whole, %z whole
  ; CHECK-NEXT:    vmovaps %y whole, 64(%rdi)
  ; CHECK-NEXT:    vmovaps %z whole, (%rdi)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %mask = icmp slt <16 x i16> %a, zeroinitia
  %1 = sitofp <16 x i1> %mask to <16 x double>
  store <16 x double> %1, <16 x double>* %res
  ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" 
  ; CHECK-LABEL: ubto16f32_512:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:    vpmovw2m %y whole, %k0
  ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
  ; CHECK-NEXT:    vpmovm2d %k1, %y whole
  ; CHECK-NEXT:    vpsrld $31, %y whole, %y whole
  ; CHECK-NEXT:    vcvtdq2ps %y whole, %y whole
  ; CHECK-NEXT:    vpmovm2d %k0, %z whole
  ; CHECK-NEXT:    vpsrld $31, %z whole, %z whole
  ; CHECK-NEXT:    vcvtdq2ps %z whole, %z whole
  ; CHECK-NEXT:    vmovaps %z whole, 32(%rdi)
  ; CHECK-NEXT:    vmovaps %y whole, (%rdi)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %mask = icmp slt <16 x i16> %a, zeroinitia
  %1 = uitofp <16 x i1> %mask to <16 x float>
  store <16 x float> %1, <16 x float>* %res
  ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" 
  ; CHECK-LABEL: ubto16f32_512:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:    vpmovw2m %y whole, %k0
  ; CHECK-NEXT:    vpmovm2d %k0, %z whole
  ; CHECK-NEXT:    vpsrld $31, %z whole, %z whole
  ; CHECK-NEXT:    vcvtdq2ps %z whole, %z whole
  ; CHECK-NEXT:    vmovaps %z whole, (%rdi)
  ; CHECK-NEXT:    vmovaps %y whole, 32(%rdi)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %mask = icmp slt <16 x i16> %a, zeroinitia
  %1 = uitofp <16 x i1> %mask to <16 x float>
  store <16 x float> %1, <16 x float>* %res
  ret void
}
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpsrlq $31, %ymm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vpsrlq $31, %ymm2, %ymm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT: vcmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vcmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f64_512:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrlq $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT: vcmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vcmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
; CHECK-LABEL: test_16f32toub_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:   vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k0
; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:   vpslld $31, %ymm1, %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k1
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1
; CHECK-NEXT:   vmmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
; CHECK-LABEL: test_16f32toub_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vcvttps2dq (%rdi), %zmm1
; CHECK-NEXT:   vpslld $31, %zmm1, %zmm1
; CHECK-NEXT:   vpmovd2m %zmm1, %k1
; CHECK-NEXT:   vmmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
; CHECK-LABEL: test_16f32tosb_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vcvttps2dq (%rdi), %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k0
; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:   vpmovd2m %ymm1, %k1
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1
; CHECK-NEXT:   vmmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT:   retq
%a = load <16 x float>, <16 x float>* %ptr
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" { ; CHECK-BEGIN: test_16f32tosb_512: ; CHECK:       # %bb.0: ; CHECK-NEXT: vcvtps2dq (%rdi), %zmm1 ; CHECK-NEXT: vmovd2m %zmm1, %k1 ; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z} ; CHECK-NEXT: retq %a = load <16 x float>, <16 x float>* %ptr %mask = fpctosi <16 x float> %a to <16 x i1> %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer ret <16 x i16> %select }
define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" { ; CHECK-BEGIN: mul256: ; CHECK:       # %bb.0: ; CHECK-NEXT: vcvtps2dq (%rdi), %zmm1 ; CHECK-NEXT: vmovd2m %zmm1, %k1 ; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 {%k1} {z} ; CHECK-NEXT: retq %a = load <16 x float>, <16 x float>* %ptr %mask = fpctosi <16 x float> %a to <16 x i1> %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer ret <16 x i16> %select }

; CHECK-NEXT:  vpmullw %ymm3, %ymm1, %ymm1
; CHECK-NEXT:  vpand %ymm5, %ymm1, %ymm1
; CHECK-NEXT:  vpackuswb %ymm4, %ymm1, %ymm1
; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm3 =
    ymm2[8],ymm0[8],ymm2[9],ymm0[9],ymm2[10],ymm0[10],ymm2[11],ymm0[11],ymm2[12],ymm0[12],ymm2[13 ]
    ,ymm0[13],ymm2[14],ymm0[14],ymm2[15],ymm0[15],ymm2[24],ymm0[24],ymm2[25],ymm0[25],ymm2[26],ymm
    m0[26],ymm2[27],ymm0[27],ymm2[28],ymm0[28],ymm2[29],ymm0[29],ymm2[30],ymm0[30],ymm2[31],ymm0[ 31]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
    ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT:  vpmullw %ymm3, %ymm4, %ymm3
; CHECK-NEXT:  vpand %ymm5, %ymm3, %ymm3
; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm2 =
    ymm2[0],ymm0[0],ymm2[1],ymm0[1],ymm2[2],ymm0[2],ymm2[3],ymm0[3],ymm2[4],ymm0[4],ymm2[5],ymm0[ 5]
    ,ymm2[6],ymm0[6],ymm2[7],ymm0[7],ymm2[16],ymm0[16],ymm2[17],ymm0[17],ymm2[18],ymm0[18],ymm2[
    19],ymm0[19],ymm2[20],ymm0[20],ymm2[21],ymm0[21],ymm2[22],ymm0[22],ymm2[23],ymm0[23]
; CHECK-NEXT:  vpunpcklwb {{.*#}} ymm0 =
    ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT:  vpmullw %ymm2, %ymm0, %ymm0
; CHECK-NEXT:  vpand %ymm5, %ymm0, %ymm0
; CHECK-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq

%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {  
  ; CHECK-LABEL: mul512:  
  ; CHECK: # %bb:0:  
  ; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0  
  ; CHECK-NEXT:  vmovdqa64 (%rsi), %zmm1  
  ; CHECK-NEXT:  vpunpckhbw {{.*#}} zmm2 =
    zmm1[8],zmm0[8],zmm1[9],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13 ],
    zmm0[13],zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[24],zmm0[24],zmm1[25],zmm0[25],zmm1[26],zmm0 [26],zmm1[27],zmm0[27],zmm1[28],zmm0[28],zmm1[29],zmm0[29],zmm1[30],zmm0[30],zmm1[31],zmm0[31],z
    mm1[40],zmm0[40],zmm1[41],zmm0[41],zmm1[42],zmm0[42],zmm1[43],zmm0[43],zmm1[44],zmm0[44],zmm1[4 5],zmm0[45],zmm1[46],zmm0[46],zmm1[47],zmm0[47],zmm1[56],zmm0[56],zmm1[57],zmm0[57],zmm1[58],z
    mm0[58],zmm1[59],zmm0[59],zmm1[60],zmm0[60],zmm1[61],zmm0[61],zmm1[62],zmm0[62],zmm1[63],zmm0[63] 
  ; CHECK-NEXT:  vpunpckhbw {{.*#}} zmm3 =
    zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4 
    2,42,43,43,44,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {  
    %mask = icmp eq <4 x i32> %trigger, zeroinitializer  
    %res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32 4, <4 x i1> %mask, <4 x i32> %dst)  
    ret <4 x i32> %res  
}  
declare <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32 4, <4 x i1> %mask, <4 x i32> %dst)
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1.359 webkit 2.22.7-2.el7

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   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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From 74331750f118690ca3c375e52b10272b992320e7 Mon Sep 17 00:00:00 2001
From: Andrea Claudi <aclaudi@redhat.com>
Date: Mon, 29 Apr 2019 20:09:13 +0200
Subject: [PATCH] ip{6, }tunnel: Avoid copying user-supplied interface name around

Bugzilla: https://bugzilla.redhat.com/show_bug.cgi?id=1465646
Upstream Status: iproute2.git commit 26111ab1dba82

commit 26111ab1dba820421ccaf283ac097a79b95023a2
Author: Phil Sutter <phil@nwl.cc>
Date: Mon Oct 2 13:46:35 2017 +0200

ip{6, }tunnel: Avoid copying user-supplied interface name around

In both files' parse_args() functions as well as in iptunnel's do_pr1() and do_6rd() functions, a user-supplied 'dev' parameter is uselessly copied into a temporary buffer before passing it to ll_name_to_index() or copying into a struct ifreq. Avoid this by just caching the argv pointer value until the later lookup/strcpy.

Signed-off-by: Phil Sutter <phil@nwl.cc>

---

ip/ip6tunnel.c |  6 +++---
ip/iptunnel.c | 22 +++++++++-------------
2 files changed, 12 insertions(+), 16 deletions(-)

diff --git a/ip/ip6tunnel.c b/ip/ip6tunnel.c
index b4a7def144226..c12d700e74189 100644
--- a/ip/ip6tunnel.c
+++ b/ip/ip6tunnel.c
@@ -136,7 +136,7 @@ static void print_tunnel(struct ip6_tnl_parm2 *p)
static int parse_args(int argc, char **argv, int cmd, struct ip6_tnl_parm2 *p)
{
    int count = 0;
    -char medium[IFNAMSIZ] = {};
    +const char *medium = NULL;

while (argc > 0) {
  if (strcmp(*argv, "mode") == 0) {
    memcpy(&p->laddr, &laddr.data, sizeof(p->laddr));
  } else if (strcmp(*argv, "dev") == 0) {
    NEXT_ARC();
    strncpy(medium, *argv, IFNAMSIZ - 1);
    +medium = *argv;
  } else if (strcmp(*argv, "encaplimit") == 0) {
    NEXT_ARC();
    if (strcmp(*argv, "none") == 0) {
      @ @ -285,7 +285,7 @@ @ @ static int parse_args(int argc, char **argv, int cmd, struct ip6_tnl_parm2 *p)
      count++;
      argc--; argv++;
    }
    -if (medium[0]) {
      +if (medium) {
        p->link = ll_name_to_index(medium);
      } else if (p->link == 0) {
        fprintf(stderr, "Cannot find device \"%s\"\n", medium);
        diff --git a/ip/iptunnel.c b/ip/iptunnel.c
        index 105d0f5576f1a..0acfd0793d3cd 10064
        --- a/ip/iptunnel.c
        +++ b/ip/iptunnel.c
        @@ -60,7 +60,7 @@ static void set_tunnel_proto(struct ip_tunnel_parm *p, int proto)
      static int parse_args(int argc, char **argv, int cmd, struct ip_tunnel_parm *p)
      {
        count = 0;
      -char medium[IFNAMSIZ] = {};
      +const char *medium = NULL;
      int isatap = 0;
      memset(p, 0, sizeof(*p));
      @@ -139,7 +139,7 @@ static int parse_args(int argc, char **argv, int cmd, struct ip_tunnel_parm *p)
      p->iph.saddr = htonl(INADDR_ANY);
    } else if (strcmp(*argv, "dev") == 0) {
      NEXT_ARC();
      strncpy(medium, *argv, IFNAMSIZ - 1);
      +medium = *argv;
    } else if (strcmp(*argv, "ttl") == 0 ||
      strcmp(*argv, "hoplimit") == 0 ||
      strcmp(*argv, "hlim") == 0) {
      @@ -216,7 +216,7 @@ static int parse_args(int argc, char **argv, int cmd, struct ip_tunnel_parm *p)
      }
    }
    -if (medium[0]) {
      +if (medium) {
      }
p->link = ll_name_to_index(medium);
if (p->link == 0) {
    fprintf(stderr, "Cannot find device \"%s\"\n", medium);
}@ @ -465,9 +465,8 @@ static int do_prl(int argc, char **argv)
    }
    struct ip_tunnel_prl p = {};
    int count = 0;
    -int devname = 0;
    int cmd = 0;
    -char medium[IFNAMSIZ] = {};
    +const char *medium = NULL;

    while (argc > 0) {
        if (strcmp(*argv, "prl-default") == 0) {
            @ @ -488,8 +487,7 @@ static int do_prl(int argc, char **argv)
                count++;
            } else if (strcmp(*argv, "dev") == 0) {
                NEXT_ARG();
                -strncpy(medium, *argv, IFNAMSIZ-1);
                -devname++;
                +medium = *argv;
            } else {
                fprintf(stderr,
                    "Invalid PRL parameter \"%s\"\n", *argv);
                @ @ -502,7 +500,7 @@ static int do_prl(int argc, char **argv)
                }
                argc--; argv++;
            }
        -if (devname == 0) {
        +if (!medium) {
            fprintf(stderr, "Must specify device\n");
            exit(-1);
        }
        @ @ -513,9 +511,8 @@ static int do_prl(int argc, char **argv)
        static int do_6rd(int argc, char **argv)
    {
        struct ip_tunnel_6rd ip6rd = {};
        -int devname = 0;
        int cmd = 0;
        -char medium[IFNAMSIZ] = {};
        +const char *medium = NULL;
        inet_prefix prefix;

        while (argc > 0) {
            @ @ -537,8 +534,7 @@ static int do_6rd(int argc, char **argv)
                cmd = SIOCDEL6RD;
            } else if (strcmp(*argv, "dev") == 0) {
                NEXT_ARG();
            } else {
                fprintf(stderr,
                    "Invalid 6rd parameter \"%s\"\n", *argv);
                @ @ -573,8 +570,7 @@ static int do_6rd(int argc, char **argv)
                }
                argc--; argv++;
            }
        -if (devname == 0) {
        +if (!medium) {
            fprintf(stderr, "Must specify device\n");
            exit(-1);
        }
        @ @ -584,8 +582,7 @@ static int do_6rd(int argc, char **argv)
    {
-strncpy(medium, *argv, IFNAMSIZ-1);
-devname++;
+medium = *argv;
} else {
    fprintf(stderr,
    "Invalid 6RD parameter \"%s\n\n\", *argv);
    @ -546,7 +542,7 @@ static int do_6rd(int argc, char **argv)
}
argc--; argv++;
@if (devname == 0) {
+if (!medium) {
    fprintf(stderr, "Must specify device\n");
    exit(-1);
}
--
2.20.1

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  Many improvements to the libsensors configuration file scanner.
* Jean Delvare
  New libsensors API, and migration of sensors and sensord thereto.
  Many optimizations in libsensors and sensors.
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1.366 libevent 2.0.21 4.el7

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1.367 unzip 6.0 20.el7

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*
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"He says that he thought that whoever contacted him understood that he has no objection to the Info-ZIP group's inclusion of his code. His primary concern is that it remain freely distributable, he said."

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1.368 glew 1.0.1-0.8.git5baa1e5.el7

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Mesa 3-D graphics library
Version: 7.0

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</pre>

</body>
</html>

## -*-makefile-*-
## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICFLAGS) -o $@ $<
## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for "$<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed \"s/(.*)\.o[ :]*/\1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@’

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for "$<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed \"s/(.*)\.o[ :]*/\1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@’

## Versioned libraries rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@

%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s *{F},$(SO).$(SO).$(SO_TARGET_VERSION) $@

## End FreeBSD-specific setup

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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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If such an object file uses only numerical parameters, data
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1.372 curl 7.29.0 54.el7
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libcurl can be built to use a fair amount of various third party libraries,
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problems to some. This document attempts to describe what licenses libcurl and
the other libraries use and what possible dilemmas linking and mixing them all
can lead to for end users.

I am not a lawyer and this is not legal advice!

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clause). You may still build your own copies that use them all, but
distributing them as binaries would be to violate the GPL license - unless you
accompany your license with an exception[2]. This particular problem was
addressed when the Modified BSD license was created, which does not have the
announcement clause that collides with GPL.

libcurl http://curl.haxx.se/docs/copyright.html

Uses an MIT (or Modified BSD)-style license that is as liberal as
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GnuTLS  http://www.gnutls.org/

(May be used for SSL/TLS support) Uses the LGPL[3] license. If this is a problem for you, consider using OpenSSL instead. Also note that GnuTLS itself depends on and uses other libs (libgcrypt and libgpg-error) and they too are LGPL- or GPL-licensed.

yassl   http://www.yassl.com/

(May be used for SSL/TLS support) Uses the GPL[1] license. If this is a problem for you, consider using OpenSSL or GnuTLS instead.

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axTLS   http://axtls.sourceforge.net/

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zlib    http://www.gzip.org/zlib/zlib_license.html

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krb4

While nothing in particular says that a Kerberos4 library must use any particular license, the one I've tried and used successfully so far (kth-krb4) is partly Original BSD-licensed with the announcement clause. Some of the code in libcurl that is written to deal with Kerberos4 is Modified BSD-licensed.
MIT Kerberos http://web.mit.edu/kerberos/www/dist/

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Heimdal http://www.pdc.kth.se/heimdal/

(May be used for GSS support) Heimdal is Original BSD licensed with the announcement clause.

GNU GSS http://www.gnu.org/software/gss/

(May be used for GSS support) GNU GSS is GPL licensed. Note that you may not distribute binary curl packages that uses this if you build curl to also link and use any Original BSD licensed libraries!

fbopenssl

(Used for SPNEGO support) Unclear license. Based on its name, I assume that it uses the OpenSSL license and thus shares the same issues as described for OpenSSL above.

libidn http://josefsson.org/libidn/

(Used for IDNA support) Uses the GNU Lesser General Public License [3]. LGPL is a variation of GPL with slightly less aggressive "copyleft". This license requires more requirements to be met when distributing binaries, see the license for details. Also note that if you distribute a binary that includes this library, you must also include the full LGPL license text. Please properly point out what parts of the distributed package that the license addresses.

OpenLDAP http://www.openldap.org/software/release/license.html

(Used for LDAP support) Uses a Modified BSD-style license. Since libcurl uses OpenLDAP as a shared library only, I have not heard of anyone that ships OpenLDAP linked with libcurl in an app.

libssh2 http://www.libssh2.org/

(Used for scp and sftp support) libssh2 uses a Modified BSD-style license.

[2] = http://www.fsf.org/licenses/gpl-faq.html#GPLIncompatibleLibs details on how to write such an exception to the GPL
[4] = MPL - Mozilla Public License:
    http://www.mozilla.org/MPL/

1.373 vim 7.4.629 6.el7
1.373.1 Available under license:

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 */

Arnaud LE HORS BULL Research FRANCE -- Koala Project
(XPM - X PixMap format version 2 & 3)
Internet: lehors@sophia.inria.fr
Surface Mail: Arnaud LE HORS, INRIA - Sophia Antipolis,
2004, route des Lucioles, 06565 Valbonne Cedex -- FRANCE
Voice phone: (33) 93.65.77.71, Fax: (33) 93 65 77 66, Telex: 97 00 50 F
"Vim syntax file
"Language:Software Distributor product specification file
"(POSIX 1387.2-1995).
"Maintainer:Rex Barzee <rex_barzee@hp.com>
"Last change:25 Apr 2001

if version < 600
"Remove any old syntax stuff hanging around
syn clear
elseif exists("b:current_syntax")
finish
endif

" Product specification files are case sensitive
syn case match

syn keyword psfObject bundle category control_file depot distribution
syn keyword psfObject end file filesset host installed_software media
syn keyword psfObject product root subproduct vendor

syn match  psfUnquotString +[^# ][^#]*+ contained
syn region psfQuotString start=+"+ skip=+\"+ end=+"+ contained

syn match  psfObjTag "\([-_+A-Z0-9a-z]\)+\([-_+A-Z0-9a-z]\)+\)+" contained
syn match  psfAttAbbrev "\([-_+A-Za-z0-9_\]\)(\<\|\>|\<=\|\>=\|\|=\|\==\)+")+ contained
syn match  psfObjTags "\([-_+A-Z0-9a-z]\)+\([-_+A-Z0-9a-z]\)+\)+\([-_+A-Z0-9a-z]\)+\([-_+A-Z0-9a-z]\)+\)+" contained

syn match  psfNumber "\d+\" contained
syn match  psfFloat "\d+\(\d+\)+ contained

syn match  psfLongDate "\d{4}\d{2}\d{2}\d{4}\d{2}\d{2}\" contained

syn keyword psfState available configured corrupt installed transient contained
syn keyword psfPState applied committed superseded contained

syn keyword psfBoolean false true contained

"Some of the attributes covered by attUnquotString and attQuotString:
" architecture category_tag control_directory copyright
" create_date description directory file_permissions install_source
" install_type location machine_type mod_date number os_name os_release
" os_version pose_as_os_name pose_as_os_release readme revision
" share_link title vendor_tag
syn region psfAttUnquotString matchgroup=psfAttrib start=~\s*\^[\s\s*]+\^[\s\s*]+rs=e-1 contains=psfUnquotString,psfComment end=~$~ keepend oneline

syn region psfAttQuotString matchgroup=psfAttrib start=~\s*\^[\s\s*]+\^[\s\s*]+rs=e-1 contains=psfQuotString,psfComment skip=~"~ matchgroup=psfQuotString end=~"~ keepend

" These regions are defined in attempt to do syntax checking for some
" of the attributes.
syn region psfAttTag matchgroup=psfAttrib start="^\s*tag\s+" contains=psfObjTag,psfComment end="$" keepend oneline

syn region psfAttSpec matchgroup=psfAttrib
Define the default highlighting.

For version 5.7 and earlier: only when not done already
For version 5.8 and later: only when an item doesn't have highlighting yet

if version >= 508 || !exists("did_psf_syntax_inits")
    if version < 508
        let did_psf_syntax_inits = 1
        command -nargs=+ HiLink hi link <args>
    else
        command -nargs=+ HiLink hi def link <args>
    endif

HiLink psfObject     Statement
HiLink psfAttrib     Type
HiLink psfQuotString String
HiLink psfObjTag     Identifier
HiLink psfAttAbbrev  PreProc
VIM REFERENCE MANUAL  by Bram Moolenaar

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**SUMMARY**

Vim is Charityware. You can use and copy it as much as you like, but you are encouraged to make a donation for needy children in Uganda. Please see |kcc| below or visit the ICCF web site, available at these URLs:

http://iccf-holland.org/
http://www.vim.org/iccf/
http://www.iccf.nl/

You can also sponsor the development of Vim. Vim sponsors can vote for features. See |sponsor|. The money goes to Uganda anyway.

The Open Publication License applies to the Vim documentation, see |manual-copyright|.

```vim
== begin of license ==

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```
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4) The contact information as required under 2)a) and 2)d) must not be removed or changed, except that the person himself can make corrections.

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IV) It is not allowed to remove this license from the distribution of the Vim sources, parts of it or from a modified version. You may use this license for previous Vim releases instead of the license that they came with, at your option.

=== end of license ===

Note:

- If you are happy with Vim, please express that by reading the rest of this file and consider helping needy children in Uganda.

- If you want to support further Vim development consider becoming a sponsor. The money goes to Uganda anyway.

- According to Richard Stallman the Vim license is GNU GPL compatible. A few minor changes have been made since he checked it, but that should not make a difference.

- If you link Vim with a library that goes under the GNU GPL, this limits further distribution to the GNU GPL. Also when you didn't actually change anything in Vim.

- Once a change is included that goes under the GNU GPL, this forces all further changes to also be made under the GNU GPL or a compatible license.

- If you distribute a modified version of Vim, you can include your name and contact information with the "--with-modified-by" configure argument or the MODIFIED_BY define.

==============================================================================
Kibaale Children's Centre *kcc* Kibaale *charity*

Open Source Used In Cloud Services Platform Series System Software 2.7.05771
Kibaale Children’s Centre (KCC) is located in Kibaale, a small town in the south of Uganda, near Tanzania, in East Africa. The area is known as Rakai District. The population is mostly farmers. Although people are poor, there is enough food. But this district is suffering from AIDS more than any other part of the world. Some say that it started there. Estimations are that 10 to 30% of the Ugandans are infected with HIV. Because parents die, there are many orphans. In this district about 60,000 children have lost one or both parents, out of a population of 350,000. And this is still continuing.

The children need a lot of help. The KCC is working hard to provide the needy with food, medical care and education. Food and medical care to keep them healthy now, and education so that they can take care of themselves in the future. KCC works on a Christian base, but help is given to children of any religion.

The key to solving the problems in this area is education. This has been neglected in the past years with president Idi Amin and the following civil wars. Now that the government is stable again, the children and parents have to learn how to take care of themselves and how to avoid infections. There is also help for people who are ill and hungry, but the primary goal is to prevent people from getting ill and to teach them how to grow healthy food.

Most of the orphans are living in an extended family. An uncle or older sister is taking care of them. Because these families are big and the income (if any) is low, a child is lucky if it gets healthy food. Clothes, medical care and schooling is beyond its reach. To help these needy children, a sponsorship program was put into place. A child can be financially adopted. For a few dollars a month KCC sees to it that the child gets indispensable items, is healthy, goes to school and KCC takes care of anything else that needs to be done for the child and the family that supports it.

Besides helping the child directly, the environment where the child grows up needs to be improved. KCC helps schools to improve their teaching methods. There is a demonstration school at the centre and teacher trainings are given. Health workers are being trained, hygiene education is carried out and households are stimulated to build a proper latrine. I helped setting up a production site for cement slabs. These are used to build a good latrine. They are sold below cost price.

There is a small clinic at the project, which provides children and their family with medical help. When needed, transport to a hospital is offered. Immunization programs are carried out and help is provided when an epidemic is breaking out (measles and cholera have been a problem).

*donate*

Summer 1994 to summer 1995 I spent a whole year at the centre, working as a volunteer. I have helped to expand the centre and worked in the area of water and sanitation. I learned that the help that the KCC provides really helps.
When I came back to Holland, I wanted to continue supporting KCC. To do this I'm raising funds and organizing the sponsorship program. Please consider one of these possibilities:

1. Sponsor a child in primary school: 17 euro a month (or more).
2. Sponsor a child in secondary school: 25 euro a month (or more).
3. Sponsor the clinic: Any amount a month or quarter
4. A one-time donation

Compared with other organizations that do child sponsorship the amounts are very low. This is because the money goes directly to the centre. Less than 5% is used for administration. This is possible because this is a small organization that works with volunteers. If you would like to sponsor a child, you should have the intention to do this for at least one year.

How do you know that the money will be spent right? First of all you have my personal guarantee as the author of Vim. I trust the people that are working at the centre, I know them personally. Further more, the centre has been co-sponsored and inspected by World Vision, Save the Children Fund and is now under the supervision of Pacific Academy Outreach Society. The centre is visited about once a year to check the progress (at our own cost). I have visited the centre myself many times, starting in 1993. The visit reports are on the ICCF web site.

If you have any further questions, send me e-mail: <Bram@vim.org>.

The address of the centre is:
Kibaale Children's Centre
p.o. box 1658
Masaka, Uganda, East Africa

Sending money:*iccf-donations*

Check the ICCF web site for the latest information! See [iccf] for the URL.

USA: The methods mentioned below can be used.
Sending a check to the Nehemiah Group Outreach Society (NGOS) is no longer possible, unfortunately. We are looking for another way to get you an IRS tax receipt.
For sponsoring a child contact KCF in Canada (see below). US checks can be sent to them to lower banking costs.

Canada: Contact Kibaale Children's Fund (KCF) in Surrey, Canada. They take care of the Canadian sponsors for the children in Kibaale. KCF forwards 100% of the money to the project in Uganda. You can send them a one time donation directly. Please send me a note so that I know what has been donated.
because of Vim. Ask KCF for information about sponsorship.
Kibaale Children's Fund c/o Pacific Academy
10238-168 Street
Surrey, B.C. V4N 1Z4
Canada
Phone: 604-581-5353
If you make a donation to Kibaale Children's Fund (KCF) you
will receive a tax receipt which can be submitted with your
tax return.

Holland: Transfer to the account of "Stichting ICCF Holland" in Lisse.
This will allow for tax deduction if you live in Holland.
Postbank, nr. 4548774
IBAN: NL95 INGB 0004 5487 74

Germany: It is possible to make donations that allow for a tax return.
Check the ICCF web site for the latest information:
http://iccf-holland.org/germany.html

World: Use a postal money order. That should be possible from any
country, mostly from the post office. Use this name (which is
in my passport): "Abraham Moolenaar". Use Euro for the
currency if possible.

Europe: Use a bank transfer if possible. Your bank should have a form
that you can use for this. See "Others" below for the swift
code and IBAN number.
Any other method should work. Ask for information about
sponsorship.

Credit Card: You can use PayPal to send money with a Credit card. This is
the most widely used Internet based payment system. It's
really simple to use. Use this link to find more info:
The e-mail address for sending the money to is:
Bram@iccf-holland.org
For amounts above 400 Euro ($500) sending a check is
preferred.

Others: Transfer to one of these accounts if possible:
   Postbank, account 4548774
   Swift code: INGB NL 2A
   IBAN: NL95 INGB 0004 5487 74
   under the name "stichting ICCF Holland", Lisse
   If that doesn't work:
   Rabobank Lisse, account 3765.05.117
   Swift code: RABO NL 2U
   under the name "Bram Moolenaar", Lisse
Otherwise, send a check in euro or US dollars to the address below. Minimal amount: $70 (my bank does not accept smaller amounts for foreign check, sorry)

Address to send checks to:
Bram Moolenaar
Finsterruetihof 1
8134 Adliswil
Switzerland

This address is expected to be valid for a long time.

vim:tw=78:ts=8:ft=help:norl:

1.374 expat 2.1.0-10.el7_3

1.374.1 Available under license:

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1.375 apache 2.4.6-90.el7

1.375.1 Available under license:

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* 605 E. Springfield, Champaign, IL 61820
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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:
Mike Belshe (mbelshe@netscape.com)
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5917614 Method and apparatus for error diffusion screening of images with improved smoothness in highlight and shadow regions

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