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In your requests please include the following reference number 78EE117C99-1228104976

Contents

1.1 source-map 0.5.7
   1.1.1 Available under license
1.2 pcre 8.39-9
   1.2.1 Available under license
1.3 azure-event-hubs-go 3.2.0
   1.3.1 Available under license
1.4 pytzdata 2020.1
   1.4.1 Available under license
1.5 docker-ce 20.10.12~3-0~debian-buster
   1.5.1 Available under license
1.6 django-fsm-log 2.0.1
   1.6.1 Available under license
1.7 spdx-expression-parse 3.0.0
   1.7.1 Available under license
1.8 is-regex 1.0.5
   1.8.1 Available under license
1.9 goprotobuf 1.5.0
   1.9.1 Available under license
1.10 discover 2.1.2-8
   1.10.1 Available under license
1.11 lsb 10.2019051400
   1.11.1 Available under license
1.12 less 487 0.1+b1
   1.12.1 Available under license
1.13 postgresql 13.5-0+deb11u1
   1.13.1 Available under license
1.14 xkeyboard-config 2.26-2
1.14.1 Available under license
1.15 mysql-defaults 5.8+1.0.5
1.15.1 Available under license
1.16 yaml 1.9.2
1.16.1 Available under license
1.17 packaging 21.0
1.17.1 Available under license
1.18 libsemanage 3.1-1
1.18.1 Available under license
1.19 gopkg.in-yaml 3.0.0-20200615113413-eecca48fe776
1.19.1 Available under license
1.20 dmidecode 3.2 1
1.20.1 Available under license
1.21 vpp 21.06.0-7~g814e456e0
1.21.1 Available under license
1.22 distro-info-data 0.41+deb10u4
1.22.1 Available under license
1.23 pendulum 2.1.2
1.23.1 Available under license
1.24 go-sysconf 0.3.5
1.24.1 Available under license
1.25 tar 1.29b 2ubuntu0.2
1.25.1 Available under license
1.26 acl 2.2.53-4
1.26.1 Available under license
1.27 opencontainers-go-digest 1.0.0
1.27.1 Available under license
1.28 systemd 237-3ubuntu10.52
1.28.1 Available under license
1.29 unattended-upgrades 1.11.2
1.29.1 Available under license
1.30 sys-mount 0.2.0
1.30.1 Available under license
1.31 stack 1.8.0
1.31.1 Available under license
1.32 @babel/runtime 7.9.6
1.32.1 Available under license
1.33 perl 5.32.1-4+deb11u2
1.33.1 Available under license
1.34 nghttp2 1.36.0-2+deb10u1
   1.34.1 Available under license
1.35 numactl 2.0.12-1
   1.35.1 Available under license
1.36 django 3.2.10
   1.36.1 Available under license
1.37 p-limit 2.3.0
   1.37.1 Available under license
1.38 libsepol 3.1-1
   1.38.1 Available under license
1.39 ca-certificates 20210119
   1.39.1 Available under license
1.40 xz 5.2.5-r0
   1.40.1 Available under license
1.41 subunit 1.2.0 0ubuntu2
   1.41.1 Available under license
1.42 libseccomp 2.5.1-1
   1.42.1 Available under license
1.43 asn1crypto 0.24.0
   1.43.1 Available under license
1.44 libtasn 4.16.0 2
   1.44.1 Available under license
1.45 logr 0.4.0
   1.45.1 Available under license
1.46 object-assign 4.1.1
   1.46.1 Available under license
1.47 attrs 20.3.0
   1.47.1 Available under license
1.48 audit 2.8.4 3
   1.48.1 Available under license
1.49 python-webencodings 0.5.1-1
   1.49.1 Available under license
1.50 bash 5.1-2+b3
   1.50.1 Available under license
1.51 libxslt 1.1.32 2.2~deb10u1
   1.51.1 Available under license
1.52 go-uber-org-mult ierr 1.7.0
   1.52.1 Available under license
1.53 follow-redirects 1.5.10
   1.53.1 Available under license
1.54 babel-pluginStyled-components 1.12.0
   1.54.1 Available under license
1.55 postgresql 12.8
   1.55.1 Available under license
1.56 fdisk 2.33.1
   1.56.1 Available under license
1.57 object-inspect 1.7.0
   1.57.1 Available under license
1.58 resource-agents 4.2.0-2+deb10u2
   1.58.1 Available under license
1.59 karrick-godirwalk 1.16.1
   1.59.1 Available under license
1.60 is-arrayish 0.2.1
   1.60.1 Available under license
1.61 dash 0.5.8 2.10
   1.61.1 Available under license
1.62 python-rsa 4.0-2
   1.62.1 Available under license
1.63 corosync 3.1.2-2
   1.63.1 Available under license
1.64 follow-redirects 1.14.4
   1.64.1 Available under license
1.65 python-defaults 3.7.3-1
   1.65.1 Available under license
1.66 kerberos 1.17-3+deb10u2
   1.66.1 Available under license
1.67 lxml 4.3.2-1+deb10u3
   1.67.1 Available under license
1.68 @babel/helper-split-export-declaration 7.8.3
   1.68.1 Available under license
1.69 typescript-logic 0.0.0
1.70 miekg-dns 1.1.31
   1.70.1 Available under license
1.71 bash 5.1.4-r0
   1.71.1 Available under license
1.72 apt 1.6.14
   1.72.1 Available under license
1.73 go-xerial-snappy 0.0.0-20180814174437-776d5712da21
   1.73.1 Available under license
1.74 docker-compose-on-kubernetes 0.4.19-0.20190128150448-356b2919c496
   1.74.1 Available under license
1.75 libnet-telnet-perl 3.04-1
   1.75.1 Available under license
1.76 isl 0.20-2
   1.76.1 Available under license
1.77 redux-immutable 4.0.0
1.78 queue 1.1.0
   1.78.1 Available under license
1.79 modern-go-concurrent 0.0.0-2018030612644-bacd9c7ef1dd
   1.79.1 Available under license
1.80 findutils 4.6.0+git+20190209 2
   1.80.1 Available under license
1.81 xml-core 0.18+nmu1
   1.81.1 Available under license
1.82 futures 3.3.0
   1.82.1 Available under license
1.83 iconv 2.31
   1.83.1 Available under license
1.84 libedit 20210216.3.1-r0
   1.84.1 Available under license
1.85 init-systemhelpers 1.56+nmu1
   1.85.1 Available under license
1.86 pam 1.4.0-9+deb11u1
   1.86.1 Available under license
1.87 csstype 2.6.10
   1.87.1 Available under license
1.88 klibc 2.0.6-1+deb10u1
   1.88.1 Available under license
1.89 django-rq 2.4.1
   1.89.1 Available under license
1.90 docker-spdystream 0.0.0-20181023171402-6480d4af844c
   1.90.1 Available under license
1.91 otel-exporters-otlp-otlptrace-otlptracehttp 1.0.0-RC1
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1.92 libgutils 1.44-1
   1.92.1 Available under license
<table>
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<tr>
<th>Version</th>
<th>Component</th>
<th>Version Details</th>
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<tbody>
<tr>
<td>1.93</td>
<td>jsbn 0.1.1</td>
<td>1.93.1 Available under license</td>
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<td>1.94</td>
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<td>1.95.1 Available under license</td>
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<td>1.96.1 Available under license</td>
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<td>1.99.1 Available under license</td>
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<td>1.100.1 Available under license</td>
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<td>libgcrypt 1.8.4 5+deb10u1</td>
<td>1.101.1 Available under license</td>
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<tr>
<td>1.102</td>
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<td>1.104.1 Available under license</td>
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<td>1.111.1 Available under license</td>
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<td>1.112</td>
<td>sensible-utils 0.0.12</td>
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</tbody>
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1.112.1 Available under license
1.113 zap 1.19.0
1.113.1 Available under license
1.114 libfile-fcntllock-perl 0.22 3+b5
1.114.1 Available under license
1.115 gdbm 1.18.1-4
1.115.1 Available under license
1.116 cpp 8.4.0 1ubuntu1~18.04
1.116.1 Available under license
1.117 go-yaml 2.0.0-20160715033755-e4d366fc3c79
1.117.1 Available under license
1.118 mgo 2.0.0-20190816093944-a6b53ec6cb22
1.118.1 Available under license
1.119 urlib3 1.26.6
1.119.1 Available under license
1.120 go-uber-org-multier 1.6.0
1.120.1 Available under license
1.121 cpprest 2.10.18
1.121.1 Available under license
1.122 boost 1.65.1+dfsg-0ubuntu5
1.122.1 Available under license
1.123 libdrm 2.4.97-1
1.123.1 Available under license
1.124 file 5.35-4+deb10u2
1.124.1 Available under license
1.125 libxcrypt 4.4.18-5
1.125.1 Available under license
1.126 react-cookies 0.1.1
1.126.1 Available under license
1.127 doctrine 1.5.0
1.127.1 Available under license
1.128 oniguruma 6.9.1-1
1.128.1 Available under license
1.129 go-cty 1.7.1
1.129.1 Available under license
1.130 ethtool 0.0.0-20200218184317-f459e2d13664
1.130.1 Available under license
1.131 python-dateutil 2.8.2
1.131.1 Available under license
1.132 llvm 11.0.1-2
   1.132.1 Available under license
1.133 ms 2.1.2
   1.133.1 Available under license
1.134 redux-saga-is 1.1.2
   1.134.1 Available under license
1.135 blackfriday 2.0.1
   1.135.1 Available under license
1.136 gzip 1.6 5ubuntu1.1
   1.136.1 Available under license
1.137 libsemanage 3.1-1+b2
   1.137.1 Available under license
1.138 nss 3.42.1-1+deb10u3
   1.138.1 Available under license
1.139 ncurses 6.1+20181013 2+deb10u2
   1.139.1 Available under license
1.140 color-convert 1.9.3
   1.140.1 Available under license
1.141 python 3.7.3 2+deb10u3
   1.141.1 Available under license
1.142 @emotion/memoize 0.7.5
   1.142.1 Available under license
1.143 urllib3 1.26.6-1.ph1
   1.143.1 Available under license
1.144 procps 3.3.15-2
   1.144.1 Available under license
1.145 pinentry 1.1.0-4
   1.145.1 Available under license
1.146 pygobject 3.30.4-1
   1.146.1 Available under license
1.147 kmod 24 1ubuntu3.5
   1.147.1 Available under license
1.148 audit 3.0-2
   1.148.1 Available under license
1.149 sarama 1.27.2
   1.149.1 Available under license
1.150 util-linux 2.31.1 0.4ubuntu3.7
   1.150.1 Available under license
1.151 cilium-ebpf 0.6.2
1.151.1 Available under license
1.152 redux-saga-types 1.1.0
   1.152.1 Available under license
1.153 jmespath 0.10.0
   1.153.1 Available under license
1.154 icu 67.1-7
   1.154.1 Available under license
1.155 sysv-init 2.88dsf 59.10ubuntu1
   1.155.1 Available under license
1.156 glib 2.58.3-2+deb10u3
   1.156.1 Available under license
1.157 defusedxml 0.7.1
   1.157.1 Available under license
1.158 python-s3transfer 0.2.0-1
   1.158.1 Available under license
1.159 @babel/generator 7.9.6
   1.159.1 Available under license
1.160 godotenv 1.3.0
   1.160.1 Available under license
1.161 property-expr 2.0.4
   1.161.1 Available under license
1.162 readline 7.0 5
   1.162.1 Available under license
1.163 attr 2.4.48 4
   1.163.1 Available under license
1.164 pflag 1.0.5
   1.164.1 Available under license
1.165 cdebconf 0.260
   1.165.1 Available under license
1.166 lzo 2.10-0.1
   1.166.1 Available under license
1.167 go-autorest-tracing 0.6.0
   1.167.1 Available under license
1.168 taskssel 3.53
   1.168.1 Available under license
1.169 @babel/traverse 7.9.6
   1.169.1 Available under license
1.170 dpkg 1.19.0.5ubuntu2.3
   1.170.1 Available under license
<table>
<thead>
<tr>
<th>Package</th>
<th>Version</th>
<th>Available under license</th>
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<tbody>
<tr>
<td>libffi</td>
<td>3.3</td>
<td>Available under license</td>
</tr>
<tr>
<td>es-abstract</td>
<td>1.17.5</td>
<td>Available under license</td>
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<td>discover-data</td>
<td>2.2013.01.11</td>
<td>Available under license</td>
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<td>python-docutils</td>
<td>0.14+dfsg-4</td>
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<td>1.2.3</td>
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<td>find-up</td>
<td>2.1.0</td>
<td>Available under license</td>
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<tr>
<td>init-system helpers</td>
<td>1.60</td>
<td>Available under license</td>
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<tr>
<td>pcre</td>
<td>8.39 12</td>
<td>Available under license</td>
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<tr>
<td>util-linux</td>
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<td>Available under license</td>
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<td>shadow</td>
<td>4.5-1.1</td>
<td>Available under license</td>
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<td>lm-sensors</td>
<td>3.5.0-3</td>
<td>Available under license</td>
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<tr>
<td>python-setuptools</td>
<td>57.5.0</td>
<td>Available under license</td>
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<td>bzip2</td>
<td>1.0.6 8.1ubuntu0.2</td>
<td>Available under license</td>
</tr>
<tr>
<td>ip-address</td>
<td>7.1.0</td>
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<td>0.0.0-20191202100458-e7afc7fbc510</td>
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<td>1.1.34 4</td>
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<td>5.2.1</td>
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<td>openssh</td>
<td>7.9p1-10+deb10u1</td>
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1.192 redux-thunk 2.3.0
1.192.1 Available under license
1.193 git 2.20.1-2+deb10u3
1.193.1 Available under license
1.194 diffutils 3.7-5
1.194.1 Available under license
1.195 x-text 0.0.0-20170706130353-cfdff0222e86b4
1.195.1 Available under license
1.196 eslint-plugin-import 2.20.2
1.196.1 Available under license
1.197 lib-pq 1.2.0
1.197.1 Available under license
1.198 beautiful-soup 4.7.1-1
1.198.1 Available under license
1.199 @reduxjs/toolkit 1.6.2
1.199.1 Available under license
1.200 immutable 4.0.0-rc.12
1.200.1 Available under license
1.201 openssl 1.1.1l
1.201.1 Available under license
1.202 libnetfilter-cttimeout 1.0.7-1
1.202.1 Available under license
1.203 libvmtools 10.3.10-1+deb10u2
1.203.1 Available under license
1.204 python-dateutil 2.7.3-1.ph3
1.204.1 Available under license
1.205 popt 1.16-12
1.205.1 Available under license
1.206 read-pkg 3.0.0
1.206.1 Available under license
1.207 cryptsetup 2.1.0-5+deb10u2
1.207.1 Available under license
1.208 msrest 0.6.21
1.208.1 Available under license
1.209 libksba 1.5.0-3
1.209.1 Available under license
1.210 libgpg-error 1.38-2
1.210.1 Available under license
1.211 eslint-import-resolver-node 0.3.3
   1.211.1 Available under license
1.212 cpio 2.12+dfsg 9
   1.212.1 Available under license
1.213 apt 2.2.4
   1.213.1 Available under license
1.214 require-main-filename 2.0.0
   1.214.1 Available under license
1.215 goprotobuf 1.5.1
   1.215.1 Available under license
1.216 click 8.0.3
   1.216.1 Available under license
1.217 libjson-perl 4.03000-1
   1.217.1 Available under license
1.218 python-setuptools 59.6.0
   1.218.1 Available under license
1.219 patch 2.7.6 3+deb10u1
   1.219.1 Available under license
1.220 perl 5.28.1-6+deb10u1
   1.220.1 Available under license
1.221 suds-jurko 0.7.dev0
   1.221.1 Available under license
1.222 golang-sql-civil 0.0.0-20190719163853-cb61b32ac6fe
   1.222.1 Available under license
1.223 go-runtime 1.14
   1.223.1 Available under license
1.224 ucf 3.0038+nmu1
   1.224.1 Available under license
1.225 go-autorest-date 0.3.0
   1.225.1 Available under license
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   1.227.1 Available under license
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   1.228.1 Available under license
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<td>0.2.1</td>
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<td>lodash-repeat</td>
<td>4.1.0.1</td>
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<td>python-cffi</td>
<td>1.12.2.1</td>
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1.2 pcre 8.39-9

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1.3 azure-event-hubs-go 3.2.0

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1.4 pytzdata 2020.1

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1.6 django-fsm-log 2.0.1

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1.7 spdx-expression-parse 3.0.0

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1.8 is-regex 1.0.5
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1.9 goprotobuf 1.5.0
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1.11 lsb 10.2019051400

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1.12 less 487 0.1+b1

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1.19 gopkg.in-yaml 3.0.0-20200615113413-eeeca48fe776

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1.26 acl 2.2.53-4

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1.34 nghttp2 1.36.0-2+deb10u1

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set yrange [0:]
set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
set xtics rotate by -45
set style histogram errorbars gap 2 lw 1
set style fill solid border -1

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1.35 numactl 2.0.12-1

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===========================
Advice for new contributors
===========================

New contributor and not sure what to do? Want to help but just don't know how
to get started? This is the section for you.

.. admonition:: Get up and running!

   If you are new to contributing to Django, the :doc:`/intro/contributing`
tutorial will give you an introduction to the tools and the workflow.

This page contains more general advice on ways you can contribute to Django, and how to approach that.

If you are looking for a reference on the details of making code contributions, see the :doc:`/internals/contributing/writing-code/index` documentation.

First steps
============

Start with these steps to discover Django's development process.

* **Triage tickets**

  If an `unreviewed ticket`_ reports a bug, try and reproduce it. If you can reproduce it and it seems valid, make a note that you confirmed the bug and accept the ticket. Make sure the ticket is filed under the correct component area. Consider writing a patch that adds a test for the bug's behavior, even if you don't fix the bug itself. See more at :ref:`how-can-i-help-with-triaging`

* **Look for tickets that are accepted and review patches to build familiarity with the codebase and the process**

  Mark the appropriate flags if a patch needs docs or tests. Look through the changes a patch makes, and keep an eye out for syntax that is incompatible with older but still supported versions of Python. :doc:`Run the tests</internals/contributing/writing-code/unit-tests>` and make sure they pass. Where possible and relevant, try them out on a database other than SQLite. Leave comments and feedback!

* **Keep old patches up to date**

  Oftentimes the codebase will change between a patch being submitted and the time it gets reviewed. Make sure it still applies cleanly and functions as expected. Updating a patch is both useful and important! See more on :doc:`writing-code/submitting-patches`.

* **Write some documentation**

  Django's documentation is great but it can always be improved. Did you find a typo? Do you think that something should be clarified? Go ahead and suggest a documentation patch! See also the guide on :doc:`writing-documentation`.

.. note::
The `reports page`_ contains links to many useful Trac queries, including several that are useful for triaging tickets and reviewing patches as suggested above.

.. _reports page: https://code.djangoproject.com/wiki/Reports

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Guidelines
==========

As a newcomer on a large project, it's easy to experience frustration. Here's some advice to make your work on Django more useful and rewarding.

* **Pick a subject area that you care about, that you are familiar with, or that you want to learn about**

You don't already have to be an expert on the area you want to work on; you become an expert through your ongoing contributions to the code.

* **Analyze tickets' context and history**

Trac isn't an absolute; the context is just as important as the words. When reading Trac, you need to take into account who says things, and when they were said. Support for an idea two years ago doesn't necessarily mean that the idea will still have support. You also need to pay attention to who *hasn't* spoken -- for example, if an experienced contributor hasn't been recently involved in a discussion, then a ticket may not have the support required to get into Django.

* **Start small**

It's easier to get feedback on a little issue than on a big one. See the `easy pickings`_

* **If you're going to engage in a big task, make sure that your idea has support first**
This means getting someone else to confirm that a bug is real before you fix the issue, and ensuring that there's consensus on a proposed feature before you go implementing it.

* **Be bold! Leave feedback!**

Sometimes it can be scary to put your opinion out to the world and say "this ticket is correct" or "this patch needs work", but it's the only way the project moves forward. The contributions of the broad Django community ultimately have a much greater impact than that of any one person. We can't do it without **you**!

* **Err on the side of caution when marking things Ready For Check-in**

If you're really not certain if a ticket is ready, don't mark it as such. Leave a comment instead, letting others know your thoughts. If you're mostly certain, but not completely certain, you might also try asking on IRC to see if someone else can confirm your suspicions.

* **Wait for feedback, and respond to feedback that you receive**

Focus on one or two tickets, see them through from start to finish, and repeat. The shotgun approach of taking on lots of tickets and letting some fall by the wayside ends up doing more harm than good.

* **Be rigorous**

When we say ":pep:`8`, and must have docs and tests", we mean it. If a patch doesn't have docs and tests, there had better be a good reason. Arguments like "I couldn't find any existing tests of this feature" don't carry much weight--while it may be true, that means you have the extra-important job of writing the very first tests for that feature, not that you get a pass from writing tests altogether.

* **Be patient**

It's not always easy for your ticket or your patch to be reviewed quickly. This isn't personal. There are a lot of tickets and pull requests to get through.

Keeping your patch up to date is important. Review the ticket on Trac to ensure that the *Needs tests*, *Needs documentation*, and *Patch needs improvement* flags are unchecked once you've addressed all review comments.

Remember that Django has an 8 month release cycle, so there's plenty of time for your patch to be reviewed.

Finally, a well-timed reminder can help. See :ref:`contributing code FAQ`
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A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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Footnotes:

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(2) According to Richard Stallman, 1.6.1 is not GPL-compatible, because its license has a choice of law clause. According to CNRI, however, Stallman's lawyer has told CNRI's lawyer that 1.6.1 is "not incompatible" with the GPL.

Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
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6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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1.49 python-webencodings 0.5.1-1
1.49.1 Available under license :
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    from setuptools import setup, find_packages
    import io
    import os
    import re

    VERSION = re.search("VERSION = '([\^\"]+)"", io.open(
        path.join(path.dirname(__file__), 'webencodings', '__init__.py'),
        encoding='utf-8')
    ).read().strip()).group(1)

    LONG_DESCRIPTION = io.open(
        path.join(path.dirname(__file__), 'README.rst'),
        encoding='utf-8')
    ).read()
setup(
    name='webencodings',
    version=VERSION,
    url='https://github.com/SimonSapin/python-webencodings',
    license='BSD',
    author='Simon Sapin',
    author_email='simon.sapin@exyr.org',
    maintainer='Geoffrey Sneddon',
    maintainer_email='me@gsnedders.com',
    description='Character encoding aliases for legacy web content',
    long_description=LONG_DESCRIPTION,
    classifiers=[
        'Development Status :: 4 - Beta',
        'Intended Audience :: Developers',
        'License :: OSI Approved :: BSD License',
        'Programming Language :: Python',
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        'Programming Language :: Python :: 2.6',
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        'Programming Language :: Python :: 3',
        'Programming Language :: Python :: 3.3',
        'Programming Language :: Python :: 3.4',
        'Programming Language :: Python :: 3.5',
        'Programming Language :: Python :: 3.6',
        'Programming Language :: Python :: Implementation :: CPython',
        'Programming Language :: Python :: Implementation :: PyPy',
        'Topic :: Internet :: WWW/HTTP',
    ],
    packages=find_packages(),
)

Found in path(s):
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-0.5.1/setup.py

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Found in path(s):
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-0.5.1/__init__.py
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-0.5.1/webencodings/labels.py
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-0.5.1/webencodings/tests.py
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-
This is a Python implementation of the `WHATWG Encoding standard`_.

* Latest documentation: http://encoding.spec.whatwg.org/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
* PyPI releases: http://pypi.python.org/pypi/webencodings
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* Python 2.6+ and 3.3+

In order to be compatible with legacy web content when interpreting something like `Content-Type: text/html; charset=latin1`, tools need to use a particular set of aliases for encoding labels as well as some overriding rules. For example, `US-ASCII` and `iso-8859-1` on the web are actually aliases for `windows-1252`, and an UTF-8 or UTF-16 BOM takes precedence over any other encoding declaration. The Encoding standard defines all such details so that implementations do not have to reverse-engineer each other.

This module has encoding labels and BOM detection, but the actual implementation for encoders and decoders is Pythons.

Regenerate the `webencodings.labels` module.

```python
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```

```python
import json
try:
```
from urllib import urlopen
except ImportError:
    from urllib.request import urlopen

def assert_lower(string):
    assert string == string.lower()
    return string

def generate(url):
    parts = ['''
    webencodings.labels
    ~~~~~~~~~~~~~~~~~~~~

    Map encoding labels to their name.

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    # XXX Do not edit!
    # This file is automatically generated by mklables.py

    LABELS = {
        '''
    labels = [
        (repr(assert_lower(label)).lstrip('u'),
        repr(encoding['name']).lstrip('u'))
        for category in json.loads(urlopen(url).read().decode('ascii'))
        for encoding in category['encodings']
        for label in encoding['labels']]
    max_len = max(len(label) for label, name in labels)
    parts.extend(
        '%s:%s %s
    for label, name in labels)
    parts.append('}
    return ''.join(parts)

if __name__ == '__main__':
    print(generate('http://encoding.spec.whatwg.org/encodings.json'))

Found in path(s):
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-
This is a Python implementation of the `WHATWG Encoding standard`_
<http://encoding.spec.whatwg.org/>_.

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
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This module has encoding labels and BOM detection,
but the actual implementation for encoders and decoders is Pythons.

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1.64 follow-redirects 1.14.4

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1.65 python-defaults 3.7.3-1

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This is the Debian GNU/Linux prepackaged version of the Python programming language. Python was written by Guido van Rossum <guido@cwi.nl> and others.

This package was put together by Klee Dienes <klee@debian.org> from sources from ftp.python.org:/pub/python, based on the Debianization by the previous maintainers Bernd S. Brentrup <bsb@uni-muenster.de> and Bruce Perens.

Current maintainer is Matthias Klose <doko@debian.org> until the final 2.3 version is released.
A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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3.1  3.0.1  2009  PSF  yes
3.1.1  3.1  2009  PSF  yes

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cmd/krb5/kproplog/kproplog.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
lib/libgss/g_canon_name.c
lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
lib/libgss/g_delete_sec_context.c
lib/libgss/g_dsp_name.c
lib/libgss/g_dsp_status.c
lib/libgss/g_dup_name.c
lib/libgss/g_exp_sec_context.c
lib/libgss/g_export_name.c
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lib/libgss/g_imp_name.c
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lib/libgss/g_store_cred.c
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lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/uts/common/gssapi/include/gssapi_err_generic.h
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lib/gssapi/mechglue/g_accept_sec_context.c
and the initial implementation of incremental propagation, including
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include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
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package spec

// License information for the exposed API.

// For more information: http://goo.gl/8us55a#licenseObject
type License struct {
    Name string `json:"name,omitempty"
    URL  string `json:"url,omitempty"
}

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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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https://github.com/golang/protobuf

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import: ../../../../fossene/db/schema/thing.yml
fields:
  site: string
  author: root

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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* docutils/utils/math/__init__.py,
  docutils/utils/math/latex2mathml.py,
  docutils/writers/xetex/__init__.py,
  docutils/writers/latex2e/docutils-05-compat.sty,
  docs/user/docutils-05-compat.sty.txt,
  docutils/utils/error_reporting.py,
  docutils/test/transforms/test_smartquotes.py:

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* docutils/utils/smartquotes.py

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  docutils/writers/html4css1/math.css

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from CWI was Python 1.2. In 1995, Guido continued his work on Python
at the Corporation for National Research Initiatives (CNRI) in Reston,
Virginia where he released several versions of the software. Python
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This is the Debian GNU/Linux prepackaged version of the GNU groff document formatting system. GNU groff was written by James Clark <jjc@jclark.com>.

This Debian package was previously maintained by Fabrizio Polacco <fpolacco@debian.org>. It is now maintained by Colin Watson <cjwatson@debian.org>.

The original tarball came from <URL:ftp://groff.ffii.org/pub/groff/>:
48fa768dd6fdebe7968041dd5ae8e2b02  groff-1.20.1.tar.gz
and was simply renamed to groff_1.20.1.orig.tar.gz.

Some patches have been applied to groff outside the debian directory.

The Debian diff also appends /usr/share/groff/tmac to the default macro path for compatibility with versions of groff earlier than 1.17 (patch by Colin Watson).

========================================================================

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dvi format, HP LaserJet 4 printers, Canon CAPSL printers, HTML format (still alpha), and typewriter-like devices. Also included is a modified version of the Berkeley -me macros, an enhanced version of the X11 xditview previewer, and an implementation of the -mm macros contributed by Joergen Haegg (jh@axis.se).

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I e-mailed the upstream maintainer to clarify the intent of this. Here is the relevant part of the reply:

From: Werner LEMBERG <wl@gnu.org>
To: cjwatson@debian.org
Subject: Re: Clarifying the scope of groff/LICENSE
Date: Thu, 18 Nov 2004 00:19:52 +0100 (CET)

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This is the intention, of course.

> If not, is the FDL thing from yesteryear going to be resolved some
> other way? I just want to make sure that I don't get forced to
> remove the groff manual, since that would really diminish the
> quality of the Debian package IMHO.

Sigh. I haven't found time to revert the FDL stuff back to GPL. It basically affects only Bernd Warken and me, and we have both agreed to return to GPL in case FDL makes problems.

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=========================================================================
The file tmac/hyphen.fr contains the same patterns as the file frhyph.tex
(for TeX), which can be found at:

http://dante.ctan.org/CTAN/language/hyphenation/frhyph.tex

The patterns have been converted to a format groff can understand.

frhyph.tex % French hyphenation patterns
% % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % %
% % % % % % % %
This file is available for free and can used and redistributed
asis for free. Modified versions should have another name.
% % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % % %
% 'message{frhyph.tex - French hyphenation patterns (V2.11) <2002/01/16>}

=========================================================================
The file tmac/hyphen.sv is identical to the file svhyph.tex, which can be found at:

http://dante.ctan.org/CTAN/language/hyphenation/svhyph.tex

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=========================================================================

The files tmac/hyphen.det and tmac/hyphen.den contain the same patterns as the files ‘dehyphnt-x-2017-03-31.pat’ and ‘dehyphnx-2017-03-31.pat’ (for TeX), respectively, which can be found at:

http://ctan.org/tex-archive/language/hyphenation/dehyph-exptl

The patterns have been converted to latin-1.

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=========================================================================

The file tmac/hyphen.cs contains the same patterns as the file czhyphen.tex (for TeX), which can be found in the archive:

http://dante.ctan.org/CTAN/macros/cstex/base/csplain.tar.gz

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uga_dan.txt For Vim version 8.1. Last change: 2018 May 17

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==============================================================================
Kibaale Children's Centre *kcc* *Kibaale* *charity*

Kibaale Children's Centre (KCC) is located in Kibaale, a small town in the south of Uganda, near Tanzania, in East Africa. The area is known as Rakai
District. The population is mostly farmers. Although people are poor, there is enough food. But this district is suffering from AIDS more than any other part of the world. Some say that it started there. Estimations are that 10 to 30% of the Ugandans are infected with HIV. Because parents die, there are many orphans. In this district about 60,000 children have lost one or both parents, out of a population of 350,000. And this is still continuing.

The children need a lot of help. The KCC is working hard to provide the needy with food, medical care and education. Food and medical care to keep them healthy now, and education so that they can take care of themselves in the future. KCC works on a Christian base, but help is given to children of any religion.

The key to solving the problems in this area is education. This has been neglected in the past years with president Idi Amin and the following civil wars. Now that the government is stable again, the children and parents have to learn how to take care of themselves and how to avoid infections. There is also help for people who are ill and hungry, but the primary goal is to prevent people from getting ill and to teach them how to grow healthy food.

Most of the orphans are living in an extended family. An uncle or older sister is taking care of them. Because these families are big and the income (if any) is low, a child is lucky if it gets healthy food. Clothes, medical care and schooling is beyond its reach. To help these needy children, a sponsorship program was put into place. A child can be financially adopted. For a few dollars a month KCC sees to it that the child gets indispensable items, is healthy, goes to school and KCC takes care of anything else that needs to be done for the child and the family that supports it.

Besides helping the child directly, the environment where the child grows up needs to be improved. KCC helps schools to improve their teaching methods. There is a demonstration school at the centre and teacher trainings are given. Health workers are being trained, hygiene education is carried out and households are stimulated to build a proper latrine. I helped setting up a production site for cement slabs. These are used to build a good latrine. They are sold below cost price.

There is a small clinic at the project, which provides children and their family with medical help. When needed, transport to a hospital is offered. Immunization programs are carried out and help is provided when an epidemic is breaking out (measles and cholera have been a problem).

*donate*

Summer 1994 to summer 1995 I spent a whole year at the centre, working as a volunteer. I have helped to expand the centre and worked in the area of water and sanitation. I learned that the help that the KCC provides really helps. When I came back to Holland, I wanted to continue supporting KCC. To do this I'm raising funds and organizing the sponsorship program. Please consider one of these possibilities:
1. Sponsor a child in primary school: 17 euro a month (or more).
2. Sponsor a child in secondary school: 25 euro a month (or more).
3. Sponsor the clinic: Any amount a month or quarter
4. A one-time donation

Compared with other organizations that do child sponsorship the amounts are very low. This is because the money goes directly to the centre. Less than 5% is used for administration. This is possible because this is a small organization that works with volunteers. If you would like to sponsor a child, you should have the intention to do this for at least one year.

How do you know that the money will be spent right? First of all you have my personal guarantee as the author of Vim. I trust the people that are working at the centre, I know them personally. Furthermore, the centre has been co-sponsored and inspected by World Vision, Save the Children Fund and is now under the supervision of Pacific Academy Outreach Society. The centre is visited about once a year to check the progress (at our own cost). I have visited the centre myself many times, starting in 1993. The visit reports are on the ICCF web site.

If you have any further questions, send me e-mail: <Bram@vim.org>.

The address of the centre is:
Kibaale Children's Centre
p.o. box 1658
Masaka, Uganda, East Africa

Sending money: *iccf-donations*

Check the ICCF web site for the latest information! See [iccf] for the URL.

USA: The methods mentioned below can be used.
Sending a check to the Nehemiah Group Outreach Society (NGOS) is no longer possible, unfortunately. We are looking for another way to get you an IRS tax receipt.
For sponsoring a child contact KCF in Canada (see below). US checks can be sent to them to lower banking costs.

Canada: Contact Kibaale Children's Fund (KCF) in Surrey, Canada. They take care of the Canadian sponsors for the children in Kibaale. KCF forwards 100% of the money to the project in Uganda. You can send them a one time donation directly.
Please send me a note so that I know what has been donated because of Vim. Ask KCF for information about sponsorship.
Kibaale Children's Fund c/o Pacific Academy
10238-168 Street
If you make a donation to Kibaale Children's Fund (KCF) you will receive a tax receipt which can be submitted with your tax return.

Holland: Transfer to the account of "Stichting ICCF Holland" in Lisse. This will allow for tax deduction if you live in Holland.
  Postbank, nr. 4548774
  IBAN: NL95 INGB 0004 5487 74

Germany: It is possible to make donations that allow for a tax return.
  Check the ICCF web site for the latest information:
  http://iccf-holland.org/germany.html

World: Use a postal money order. That should be possible from any country, mostly from the post office. Use this name (which is in my passport): "Abraham Moolenaar". Use Euro for the currency if possible.

Europe: Use a bank transfer if possible. Your bank should have a form that you can use for this. See "Others" below for the swift code and IBAN number.
  Any other method should work. Ask for information about sponsorship.

Credit Card: You can use PayPal to send money with a Credit card. This is the most widely used Internet based payment system. It's really simple to use. Use this link to find more info:
  The e-mail address for sending the money to is:
  Bram@iccf-holland.org
  For amounts above 400 Euro ($500) sending a check is preferred.

Others: Transfer to one of these accounts if possible:
  Postbank, account 4548774
  Swift code: INGB NL 2A
  IBAN: NL95 INGB 0004 5487 74
  under the name "stichting ICCF Holland", Lisse
  If that doesn't work:
    Rabobank Lisse, account 3765.05.117
    Swift code: RABO NL 2U
    under the name "Bram Moolenaar", Lisse
  Otherwise, send a check in euro or US dollars to the address below. Minimal amount: $70 (my bank does not accept smaller amounts for foreign check, sorry)
Address to send checks to:
Bram Moolenaar
Finsterrueihof 1
8134 Adliswil
Switzerland

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Version 2.1, February 1999

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" Vim syntax file
" Language:Software Distributor product specification file
"(POSIX 1387.2-1995).
" Maintainer:Rex Barzee <rex_barzee@hp.com>
" Last change:25 Apr 2001

" quit when a syntax file was already loaded
if exists("b:current_syntax")
    finish
endif

" Product specification files are case sensitive
syn case match

syn keyword psfObject bundle category control_file depot distribution
syn keyword psfObject end file fileset host installed_software media
syn keyword psfObject product root subproduct vendor

syn match  psfUnquotString +[^" ][^#]*+ contained
syn region psfQuotString   start=+"+ skip=+\"+ end=+"+ contained

syn match  psfObjTag    "\<[\-_A-Z0-9a-z]*/\<[\-_A-Z0-9a-z]>[++^]+*" contained
syn match  psfAttAbbrev "\<[fa]\b\[aclqr]\>[\<\>|\=\+=\=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+=\+...
These regions are defined in attempt to do syntax checking for some
of the attributes.

Define the default highlighting.
Only when an item doesn’t have highlighting yet

hi def link psfObject Statement
hi def link psfAttrib Type
" Long descriptions and copyrights confuse the syntax highlighting, so
" force vim to backup at least 100 lines before the top visible line
" looking for a sync location.
syn sync lines=100

let b:current_syntax = "psf"

1.99 python27-botocore 1.12.103+repack-1

1.99.1 Available under license:
Format: https://www.debian.org/doc/packaging-manuals/copyright-format/1.0/
Upstream-Name: python-botocore
Source: https://pypi.python.org/pypi/botocore
Files-Excluded: botocore/vendored

Files: *
Copyright: Mitch Garnaat <garnaat@amazon.com>
License: Apache-2.0

Files: debian/*
Copyright: 2013 TANIGUCHI Takaki <takaki@debian.org>
License: Apache-2.0

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On Debian systems, the complete text of the Apache version 2.0 license
can be found in "/usr/share/common-licenses/Apache-2.0".
# This is the official list of people who can contribute
# (and typically have contributed) code to the Go repository.
# The AUTHORS file lists the copyright holders; this file
# lists people. For example, Google employees are listed here
# but not in AUTHORS, because Google holds the copyright.
#
# Names should be added to this file only after verifying that
# the individual or the individual's organization has agreed to
# the appropriate Contributor License Agreement, found here:
#
#     http://code.google.com/legal/individual-cla-v1.0.html
#
# The agreement for individuals can be filled out on the web.

# Names should be added to this file like so:
#
# Individual's name <submission email address>
#
# An entry with multiple email addresses specifies that the
# first address should be used in the submit logs and
# that the other addresses should be recognized as the
# same person when interacting with Gerrit.

# Please keep the list sorted.

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

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DRuntime: Runtime Library for the D Programming Language

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1.105 jcmturner-gokrb 7.5.0

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in "src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi_err_generic.et  
lib/gssapi/mechglue/g_accept_sec_context.c  
lib/gssapi/mechglue/g_acquire_cred.c  
lib/gssapi/mechglue/g_canon_name.c  
lib/gssapi/mechglue/g_compare_name.c  
lib/gssapi/mechglue/g_context_time.c  
lib/gssapi/mechglue/g_delete_sec_context.c  
lib/gssapi/mechglue/g_dsp_name.c  
lib/gssapi/mechglue/g_dsp_status.c  
lib/gssapi/mechglue/g_dup_name.c  
lib/gssapi/mechglue/g_exp_sec_context.c  
lib/gssapi/mechglue/g_export_name.c  
lib/gssapi/mechglue/g_glue.c  
lib/gssapi/mechglue/g_imp_name.c  
lib/gssapi/mechglue/g_imp_sec_context.c  
lib/gssapi/mechglue/g_init_sec_context.c  
lib/gssapi/mechglue/g_initialize.c  
lib/gssapi/mechglue/g_inquire_context.c  
lib/gssapi/mechglue/g_inquire_cred.c  
lib/gssapi/mechglue/g_inquire_names.c  
lib/gssapi/mechglue/g_process_context.c  
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
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lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c

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1.111 python-certifi 2018.08.24-1.ph3
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c-a-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov  3 19:04:19 2011#
This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree: http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#

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1.117 go-yaml 2.0.0-20160715033755-e4d366fc3c79

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1.118 mgo 2.0.0-20190816093944- 
a6b53ec6cb22 

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mgo - MongoDB driver for Go

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1.119 urllib3 1.26.6
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1. Websocket++ (http://www.zaphoyd.com/websocketpp/)

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base64.cpp and base64.h

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1.122 boost 1.65.1+dfsg-0ubuntu5

1.122.1 Available under license :

class boost {
namespace intrusive {

namespace detail {

using boost::move_detail::is_same;
using boost::move_detail::add_const;
using boost::move_detail::remove_const;
using boost::move_detail::remove_cv;
using boost::move_detail::remove_reference;
using boost::move_detail::add_reference;
using boost::move_detail::remove_pointer;
using boost::move_detail::add_pointer;
using boost::move_detail::true_type;
using boost::move_detail::false_type;
using boost::move_detail::enable_if_c;
using boost::move_detail::enable_if;
using boost::move_detail::disable_if_c;
using boost::move_detail::disable_if;
using boost::move_detail::is_convertible;
using boost::move_detail::if_c;
using boost::move_detail::if_;
using boost::move_detail::is_const;
using boost::move_detail::identity;
using boost::move_detail::alignment_of;
using boost::move_detail::is_empty;
using boost::move_detail::addressof;
using boost::move_detail::integral_constant;
using boost::move_detail::enable_if_convertible;
using boost::move_detail::disable_if_convertible;
using boost::move_detail::bool_;
using boost::move_detail::true_;
using boost::move_detail::false_;
using boost::move_detail::yes_type;
using boost::move_detail::no_type;
using boost::move_detail::apply;
using boost::move_detail::eval_if_c;
using boost::move_detail::eval_if;
using boost::move_detail::unvoid_ref;
using boost::move_detail::add_const_if_c;

template<std::size_t S>
struct ls_zeros
{
    static const std::size_t value = (S & std::size_t(1)) ? 0 : (1 + ls_zeros<(S>>1u)>::value);
};

 template<>
 struct ls_zeros<0>
 {


static const std::size_t value = 0;
};

template<>
struct ls_zeros<1>
{
    static const std::size_t value = 0;
};

// Infrastructure for providing a default type for T::TNAME if absent.
#define BOOST_INTRUSIVE_INSTANTIATE_DEFAULT_TYPE_TMPLT(TNAME)     \
    template <typename T, typename DefaultType>                    \
    struct boost_intrusive_default_type_##TNAME                    \
    {
        template <typename X>                                       \
        static char test(int, typename X::TNAME*);                  \
        \
        template <typename X>                                       \
        static int test(...);                                       \
        \
        struct DefaultWrap { typedef DefaultType TNAME; };          \
        \
        static const bool value = (1 == sizeof(test<T>(0, 0)));     \
        \
        typedef typename                                            \
            ::boost::intrusive::detail::if_c                         \
            <value, T, DefaultWrap>::type::TNAME type;            \
    };                                                             

#define BOOST_INTRUSIVE_OBTAIN_TYPE_WITH_DEFAULT(INSTANTIATION_NS_PREFIX, T,
    TNAME, TIMPL)   
    typename INSTANTIATION_NS_PREFIX                                                       
        boost_intrusive_default_type_##TNAME< T, TIMPL >::type                            

#define BOOST_INTRUSIVE_INSTANTIATE_EVAL_DEFAULT_TYPE_TMPLT(TNAME)   
    template <typename T, typename DefaultType>                    
    struct boost_intrusive_eval_default_type_##TNAME             
    {
        template <typename X>                                       
        static char test(int, typename X::TNAME*);                  
        \
        template <typename X>                                       
        static int test(...);                                       
        \
        struct DefaultWrap                                          
        { typedef typename DefaultType::type TNAME; };              
    };                                                             

static const bool value = (1 == sizeof(test<T>(0, 0))); \\

typedef typename \\
::boost::intrusive::detail::eval_if_c
< value \\
, ::boost::intrusive::detail::identity<T> \\
, ::boost::intrusive::detail::identity<DefaultWrap> \\
>::type::TNAME type; \\
}; \\
//

#define BOOST_INTRUSIVE_OBTAIN_TYPE_WITH_EVAL_DEFAULT(INSTANTIATION_NS_PREFIX, T, TNAME, TIMPL) \\
    typename INSTANTIATION_NS_PREFIX \\
    boost::intrusive_eval_default_type_##TNAME< T, TIMPL >::type \\
//

#define BOOST_INTRUSIVE_INTERNAL_STATIC_BOOL_IS_TRUE(TRAITS_PREFIX, TYPEDEF_TO_FIND) 
    template <class T>
    struct TRAITS_PREFIX##_bool
    { 
    template<bool Add>
    struct two_or_three {yes_type _[2 + Add];};
    template <class U> static yes_type test(...);
    template <class U> static two_or_three<U::TYPEDEF_TO_FIND> test (int);
    static const std::size_t value = sizeof(test<T>(0));
    };
    
    template <class T>
    struct TRAITS_PREFIX##_bool_is_true
    { 
    static const bool value = TRAITS_PREFIX##_bool<T>::value > sizeof(yes_type)*2;
    };
    
    //

#define BOOST_INTRUSIVE_HAS_STATIC_MEMBER_FUNC_SIGNATURE(TRAITS_NAME, FUNC_NAME) 
    template <typename U, typename Signature> 
    class TRAITS_NAME 
    { 
    private: 
    template<Signature> struct helper;
    template<typename T> 
    static ::boost::intrusive::detail::yes_type test(helper<&T::FUNC_NAME>*,);
    template<typename T> static ::boost::intrusive::detail::no_type test(...); 
    public: 

static const bool value = sizeof(test<U>(0)) == sizeof(::boost::intrusive::detail::yes_type); \
];
//

#define BOOST_INTRUSIVE_HAS_MEMBER_FUNC_CALLED(TRAITS_NAME, FUNC_NAME) \
template<typename Type> \
struct TRAITS_NAME \
{ \
    struct BaseMixin \
    { \
        void FUNC_NAME(); \
    }; \
    struct Base : public Type, public BaseMixin { Base(); }; \
    template<typename T, T t> class Helper {}; \
    template<typename U> \
    static ::boost::intrusive::detail::no_type test(U*, Helper<void(BaseMixin::*)(), &U::FUNC_NAME>* = 0); \
    static ::boost::intrusive::detail::yes_type test(...); \
    static const bool value = sizeof(::boost::intrusive::detail::yes_type) == sizeof(test((Base*)(0))); \
};
//

#define BOOST_INTRUSIVE_HAS_MEMBER_FUNC_CALLED_IGNORE_SIGNATURE(TRAITS_NAME, \
FUNC_NAME) \
BOOST_INTRUSIVE_HAS_MEMBER_FUNC_CALLED(TRAITS_NAME##_ignore_signature, FUNC_NAME) \
\ 
template<typename Type, class> \
struct TRAITS_NAME \
: public TRAITS_NAME##_ignore_signature<Type> \
{ }; \
//namespace detail \
//namespace intrusive \
//namespace boost 

#include <boost/intrusive/detail/config_end.hpp>

#endif //BOOST_INTRUSIVE_DETAIL_MPL_HPP
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Adapters for Boost.MPL containers.

### Classes

- **boost::mpl::integral_c**<br>Adapter for IntegralConstants from the Boost.MPL.  [More...](#)
- **boost::mpl::list**<br>Adapter for Boost.MPL lists.  [More...](#)
- **boost::mpl::vector**<br>Adapter for Boost.MPL vectors.  [More...](#)
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#include <boost/mpl/push_back.hpp>
#include <boost/mpl/quote.hpp>
#include <boost/mpl/transform.hpp>
#include <boost/mpl/vector.hpp>

template <typename X>
struct f { using type = X; };

template <int i>
struct t { };

using vector = mpl_vector((1..input_size).to_a.map { |n| "t<#{n}>>" }) %>

using result = boost::mpl::transform<vector, boost::mpl::quote1<f>>::type;

int main() {

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[section:mpl MPL Interoperability]

All the value based traits in this library conform to MPL's requirements
Please note that these types no longer inherit from `mpl::true_` or `mpl::false_` etc, and the library will no longer implicitly include any MPL header. However there is an implicit conversion from `integral_constant` to the corresponding MPL types, therefore tag-dispatching that uses MPL types in function overloads will still work as before.

[boost]
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All the value based traits in this library conform to MPL's requirements for an integral constant type.

Please note that these types no longer inherit from `mpl::true_` or `mpl::false_` etc, and the library will no longer implicitly include any MPL header. However there is an implicit conversion from `integral_constant` to the corresponding MPL types, therefore tag-dispatching that uses MPL types in function overloads will still work as before.
#include <boost/mpl/push_back.hpp>
#include <boost/mpl/vector.hpp>

template <int i>
struct t { }

using vector = mpl_vector((1..input_size).to_a.map { |n| "t<#{n}>" }) %;

int main() { }

#endif

#endif

namespace boost {
namespace interprocess {
namespace ipcdetail {

template <class T, T val>
struct integral_constant
{
    static const T value = val;
    typedef integral_constant<T,val> type;

}}
}
}

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template< bool C_ >
struct bool_ : integral_constant<bool, C_>
{
    static const bool value = C_; 
};

typedef bool_<true>        true_;
typedef bool_<false>       false_;

typedef true_  true_type;
typedef false_ false_type;

typedef char yes_type;
struct no_type
{
    char padding[8];
};

template <bool B, class T = void>
struct enable_if_c {
    typedef T type;
};
template <class T>
struct enable_if_c<false, T> {};

template <class Cond, class T = void>
struct enable_if : public enable_if_c<Cond::value, T> {};
template <class Cond, class T = void>
struct disable_if : public enable_if_c<!Cond::value, T> {};

template<
    bool C
    , typename T1
    , typename T2
>
struct if_c
{
    typedef T1 type;
};
template<
    typename T1
    , typename T2
>
struct if_c<false,T1,T2>
{
    typedef T2 type;
};

template<
    typename T1
    , typename T2
    , typename T3
>
struct if_
{
    typedef typename if_c<0 != T1::value, T2, T3>::type type;
};

template<std::size_t S>
struct ls_zeros
{
    static const std::size_t value = (S & std::size_t(1)) ? 0 : (1u + ls_zeros<(S >> 1u)>::value);
};

template<>//namespace ipcdetail {
template<>//namespace interprocess {
template<>//namespace boost {

#ifdef //ifndef BOOST_INTERPROCESS_DETAIL_MPL_HPP
#!/usr/bin/env python
from subprocess import check_output as run
from datetime import datetime
from itertools import groupby
from operator import itemgetter
import re
import magic

def authors(filename):
log = run(['git', 'log', '--follow',
    '--date=short', '--format=%aN%x09%ad', filename],
    universal_newlines=True)
for line in log.splitlines():
    author, date = line.split('	')
    if author != 'fix-copyright.py':
        yield author, datetime.strptime(date, '%Y-%m-%d')

def new_copyright(filename, previous):
    def f():
        au = list(authors(filename))
        alldates = map(itemgetter(1), au)
        aup = sorted(au + map(lambda a: (a, None), previous), key=itemgetter(0))
        for author, records in groupby(aup, itemgetter(0)):
            dates = filter(None, map(itemgetter(1), records))
            if not dates: dates = alldates
            start = min(dates)
            end = max(dates)
            fmt = '[0]' if start.year == end.year else '[0]-[1]'
            line = 'Copyright ' + fmt.format(start.year, end.year) + ' ' + author
            key = (start, author)
            yield key, line
        return map(itemgetter(1), sorted(f()))

def fix_copyright(filename):
    # Find copyright block in original file
    prefix = set()
    names = []
    lines = []
    with open(filename, 'r') as f:
        content = list(f)
        for i, line in enumerate(content[:15]):
            m = re.match(r'^\s*\(c\)?\s+#\s+copyright\s*\(c\)?\s+\d{4}(\s+\d{4})?\s+(?P<name>.+?)\s*$', line, re.IGNORECASE)
            if m:
                d = m.groupdict()
                prefix.add(d['prefix'])
                lines.append(i)
                names.append(d['name'].strip())
    if len(prefix) != 1:
        print 'Not found:', filename
        return
    prefix = list(prefix)[0]

    print filename
    new = iter(new_copyright(filename, names))
    with open(filename, 'w') as f:
        for i, line in enumerate(content):
if i in lines:
    for repl in new:
        print >>f, prefix + repl
    else:
        print >>f, line,
pass

def all_files():
    ls = run(['git', 'ls-files'], universal_newlines=True)
    for filename in ls.splitlines():
        if magic.from_file(filename, mime=True).split('/')[0] == 'text':
            yield filename

for f in all_files():
    fix_copyright(f)

[article Invalid copyright
[quickbook 1.5]
[copyright No year]
]

Maybe this should pass?

<?xml version="1.0" encoding="utf-8"?>

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<section id="date_time.license">
<title>Boost Date-Time Library License</title>
<bridgehead renderas="sect2">Boost Date-Time Library License</bridgehead>
<!--
<ulink url="../../../index.htm">
<imagedata align="left"
format="GIF"
fileref="../../../boost.png"
alt="C++ Boost" />
</ulink>-->
<!--hr /-->
<para>
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</para>
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This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser. The Encore Multimax modifications were supplied by Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM. Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood. Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code. Al Dosser (dosser@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code. Jesper Peterson(jep@mtiame.mtia.oz.au), Michel Schinz, and Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port.
Thomas Funke (thf@zelator.in-berlin.de) and Brian D. Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports. Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code. Bill Janssen (janssen@parc.xerox.com) supplied the SunOS dynamic loader specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and Dave Detlefs (detlefs@src.dec.com) also provided several generic bug fixes. Alistair G. Crooks (agc@uts.amdahl.com) supplied the NetBSD and 386BSD ports. Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port. Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to a Motorola 88K processor running CX/UX (Harris NightHawk). Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to nonIBM development environments (a nontrivial task). Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port. David Chase, then at Olivetti Research, suggested several improvements. Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the code to save and print call stacks for leak detection on a SPARC. Jesse Hull and John Ellis supplied the C++ interface code. Zhong Shao performed much of the experimentation that led to the current typed allocation facility. (His dynamic type inference code hasn’t made it into the released version of the collector, yet.)

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EST)
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Gennadiy Rozental (rogeeff -at- mail.com) (Email to Andreas Huber, see change log)
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10:09:19 +0200)
Gottfried Ganusague (Gottfried.Ganssauge -at- HAUFE.DE) (Alternative spelling of Gottfried Ganssauge)
Greg Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)
#define BOOST_CONTAINER_CONTAINER_DETAIL_MPL_HPP

#ifndef BOOST_CONFIG_HPP
  # include <boost/config.hpp>
#endif

#if defined(BOOST_HAS_PRAGMA_ONCE)
  #pragma once
#endif

#include <boost/container/detail/config_begin.hpp>
#include <boost/container/detail/workaround.hpp>
#include <boost/move/detail/mpl.hpp>
#include <cassert>

namespace boost {
namespace container {
namespace container_detail {

using boost::move_detail::integral_constant;
using boost::move_detail::true_type;
using boost::move_detail::false_type;
using boost::move_detail::enable_if_c;
using boost::move_detail::enable_if;
using boost::move_detail::enable_if_convertible;
using boost::move_detail::disable_if_c;
using boost::move_detail::disable_if;
using boost::move_detail::disable_if_convertible;
using boost::move_detail::is_convertible;
using boost::move_detail::if_c;
using boost::move_detail::if_
using boost::move_detail::identity;
using boost::move_detail::bool_;
using boost::move_detail::true_
using boost::move_detail::false_
using boost::move_detail::yes_type;
using boost::move_detail::no_type;
using boost::move_detail::bool_
using boost::move_detail::true_
using boost::move_detail::false_
using boost::move_detail::unvoid_ref;
using boost::move_detail::and_
using boost::move_detail::or_
using boost::move_detail::not_
using boost::move_detail::enable_if_and;
using boost::move_detail::disable_if_and;
using boost::move_detail::enable_if_or;
using boost::move_detail::disable_if_or;

template <class FirstType>
struct select1st
{
  typedef FirstType type;

  template<class T>
  const type& operator()(const T& x) const
  { return x.first;  }

  template<class T>
  type& operator()(T& x)
  { return const_cast<type&>(x.first);  }
};

//namespace container_detail {
//namespace container [  
//namespace boost {

#include <boost/container/detail/config_end.hpp>
#endif   //#ifndef BOOST_CONTAINER_CONTAINER_DETAIL_MPL_HPP

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#include <boost/mpl/fold.hpp>
#include <boost/mpl/push_back.hpp>
#include <boost/mpl/quote.hpp>
#include <boost/mpl/vector.hpp>

template <typename State, typename X>
struct f { using type = X; };  

struct state [ ];

template <int i>
struct t [ ];

using vector = //= mpl_vector((1..input_size).to_a.map { [n] "t<#{n}>" }) %>

using result = boost::mpl::fold<vector, state, boost::mpl::quote2<f>::type>

int main() {  

Boost.Geometry (aka GGL, Generic Geometry Library)

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*/

#include "licence_info.hpp"
#include "bcp_imp.hpp"
#include "fileview.hpp"
#include <fstream>
#include <iomanip>
#include <cstring>
#include <stdexcept>
#include <boost/lexical_cast.hpp>
#include <boost/filesystem/operations.hpp>
#include <boost/throw_exception.hpp>

// split_path is a small helper for outputting a path name,
// complete with a link to that path:
//
struct split_path
{
    const fs::path& root;
    const fs::path& file;
    split_path(const fs::path& r, const fs::path& f)
    : root(r), file(f){}
private:
    split_path& operator=(const split_path&);
};

std::ostream& operator << (std::ostream& os, const split_path& p)
{
    os << "<a href="" << (p.root / p.file).string() << "]""" << p.file.string() << ">
};
return os;
}

std::string make_link_target(const std::string& s)
{
    // convert an arbitrary string into something suitable
    // for an <a> name:
    std::string result;
    for(unsigned i = 0; i < s.size(); ++i)
    {
        result.append(1, static_cast<std::string::value_type>(std::isalnum(s[i]) ? s[i] : '_'));
    }
    return result;
}

void bcp_implementation::output_license_info()
{

    std::pair<const license_info*, int> licenses = get_licenses();

    std::map<int, license_data>::const_iterator i, j;
    i = m_license_data.begin();
    j = m_license_data.end();

    std::ofstream os(m_dest_path.string().c_str());
    if(!os)
    {
        std::string msg("Error opening ");
        msg += m_dest_path.string();
        msg += " for output."
        std::runtime_error e(msg);
        boost::throw_exception(e);
    }

    os <<
        "<!DOCTYPE HTML PUBLIC "-//W3C//DTD HTML 4.0 Transitional//EN" \n" "html>\n" "<head>\n" "<title>Boost Licence Dependency Information";

    if(m_module_list.size() == 1)
    {
        os << " for " << *(m_module_list.begin());
    }

    os <<
        "</title>\n" "</head>\n" "<body>\n" "<H1>Boost Licence Dependency Information";

    if(m_module_list.size() == 1)
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```cpp
{
  os << " for " << *(m_module_list.begin());
}

os << 
"</H1>\n"
"<H2>Contents</H2>\n"
"<pre><a href="#input">Input Information</a>
if(!m_bsl_summary_mode)
  os << " <a href="#summary">Licence Summary</a>\n";
os << " <a href="#details">Licence Details</a>\n";
}

while(i != j)
{
  // title:
  os << " <A href="#" " << make_link_target(licenses.first[i->first].license_name) << "" " " << licenses.first[i->first].license_name << " </a>\n";
  +++i;
}

os << " <a href="#files">Files with no recognised license</a>\n";
" <a href="#authors">Files with no recognised copyright holder</a>\n";
if(!m_bsl_summary_mode)
{
  os << "Moving to the Boost Software License...\n"
  " <a href="#bsl-converted">Files that can be automatically converted to the Boost Software License</a>\n"
  " <a href="#to-bsl">Files that can be manually converted to the Boost Software License</a>\n"
  " <a href="#not-to-bsl">Files that can NOT be moved to the Boost Software License</a>\n"
  " <a href="#need-bsl-authors">Authors we need to move to the Boost Software License</a>\n"
  " <a href="#copyright">Copyright Holder Information</a>\n";
}

os << " <a href="#depend">File Dependency Information</a>\n";
"</pre>";

//
// input Information:
//

os << "<a name="input"></a><h2>Input Information</h2>
if(m_scan_mode)
  os << "<P>The following files were scanned for boost dependencies:<BR>";
else
  os << "<P>The following Boost modules were checked:<BR>";
std::list<std::string>::const_iterator si = m_module_list.begin();
std::list<std::string>::const_iterator sj = m_module_list.end();
while(si != sj)
{

```
The Boost path was: `<code>` << m_boost_path.string() << `</code>`;

// extract the boost version number from the boost directory tree,
// not from this app (which may have been built from a previous
// version):
//
fileview version_file(m_boost_path / "boost/version.hpp");
static const boost::regex version_regex("^[[:blank:]]*#\[[:blank:]]*define[[:blank:]]*BOOST_VERSION[[:blank:]]*[\d]+(\d+)\); boost::cmatch what;
if(boost::regex_search(version_file.begin(), version_file.end(), what, version_regex))
{
    int version = boost::lexical_cast<int>(what.str(1));
    os << "The Boost version is: " << version / 100000 << "." << version / 100 % 100 << "." << version % 100 << 
"</p>\n";
}

// output each license:
//
i = m_license_data.begin();
j = m_license_data.end();
if(!m_bsl_summary_mode)
{
    // start with the summary:
    //
    os << "<a name="summary">\n<h2>Licence Summary</h2>\n";
    while(i != j)
    {
        // title:
        os << 
"<H3>" << licenses.first[i->first].license_name << "</H3>\n";
        // license text:
        os << "<BLOCKQUOTE>" << licenses.first[i->first].license_text << "</BLOCKQUOTE>";
        // Copyright holders:
        os << "<P>This license is used by " << i->second.authors.size() << " authors and " << i->second.files.size() << " files <a href=""<<(licenses.first[i->first].license_name) << ">")(see details)</a>";
        os << "</P>";/BLOCKQUOTE>\n";
        ++i;
    }
}
//
// and now the details:
i = m_license_data.begin();
j = m_license_data.end();
int license_index = 0;

os << "<a name="details"></a><h2>Licence Details</h2>
{
    // title:
    os << "<H3><A name="" << m_license_data.first.m_license_name << ""></A></H3>
    // license text:
    os << "<BLOCKQUOTE>" << m_license_data.first.m_license_text << "</BLOCKQUOTE>";
    if(!m_bsl_summary_mode || (license_index >= 3))
    {
        // Copyright holders:
        std::set<std::string>::const_iterator x, y;
        x = m_license_data.first.m_authors.begin();
        y = m_license_data.first.m_authors.end();
        while(x != y)
        {
            os << *x << "<BR>";
            ++x;
        }
        os << "</P></BLOCKQUOTE>";
        // Files using this license:
        std::set<fs::path, path_less>::const_iterator m, n;
        m = m_license_data.first.m_files.begin();
        n = m_license_data.first.m_files.end();
        while(m != n)
        {
            os << split_path(m_boost_path, *m) << "<BR>";
            ++m;
        }
        os << "</P></BLOCKQUOTE>";
    }
    else
    {
        os << "<P>This license is used by " << m_license_data.first.m_authors.size() << " authors (list omitted for brevity).</P>
        os << "<P>This license applies to " << m_license_data.first.m_files.size() << " files (list omitted for brevity).</P>"
        ++license_index;
        ++i;
    }
}
// Output list of files not found to be under license control:
//
// os << "<h2><a name="files"></a>Files With No Recognisable Licence</h2>\n"
"<P>The following " << m_unknown_licenses.size() << " files had no recognisable license information:"<<</p><BLOCKQUOTE><p>
std::set<fs::path, path_less>::const_iterator i2, j2;
i2 = m_unknown_licenses.begin();
j2 = m_unknown_licenses.end();
while(i2 != j2)
{
    os << split_path(m_boost_path, *i2) << "<br>\n";
    ++i2;
}
os << "</p></BLOCKQUOTE>";

// Output list of files with no found copyright holder:
//
// os << "<h2><a name="authors"></a>Files With No Recognisable Copyright Holder</h2>\n"
"<P>The following " << m_unknown_authors.size() << " files had no recognisable copyright holder:"<<</p><BLOCKQUOTE><p>
i2 = m_unknown_authors.begin();
j2 = m_unknown_authors.end();
while(i2 != j2)
{
    os << split_path(m_boost_path, *i2) << "<br>\n";
    ++i2;
}
os << "</p></BLOCKQUOTE>";
if(!m_bsl_summary_mode)
{
    // Output list of files that have been moved over to the Boost
    // Software License, along with enough information for human
    // verification.
    //
    os << "<h2><a name="bsl-converted"></a>Files that can be automatically converted to the Boost Software License</h2>\n"
    "<P>The following " << m_converted_to_bsl.size() << " files can be automatically converted to the Boost Software License, but require manual verification before they can be committed to CVS:"<<</p><BLOCKQUOTE><p>
    typedef std::map<fs::path, std::pair<std::string, std::string>, path_less>::const_iterator conv_iterator;
    conv_iterator i = m_converted_to_bsl.begin(),
ie = m_converted_to_bsl.end();
    int file_num = 1;
    while (i != ie)
// Output list of files that could be moved over to the Boost Software License

// os << "<h2><a name="to-bsl"></a>Files that could be converted to the Boost Software License</h2>

"<P>The following " << m_can_migrate_to_bsl.size() << " files could be manually converted to the Boost Software License, but have not yet been:

"<P>\n<BLOCKQUOTE><P>";  
i2 = m_can_migrate_to_bsl.begin();  
j2 = m_can_migrate_to_bsl.end();  
while(i2 != j2)  
{  
   os << split_path(m_boost_path, *i2) << "<br>\n";  
   ++i2;  
}  
"</P></BLOCKQUOTE>";  
// Output list of files that can not be moved over to the Boost Software License  
// os << "<h2><a name="not-to-bsl"></a>Files that can NOT be converted to the Boost Software License</h2>

"<P>The following " << m_cannot_migrate_to_bsl.size() << " files cannot be converted to the Boost Software License because we need the permission of more authors:

"<P>\n<BLOCKQUOTE><P>";  
i2 = m_cannot_migrate_to_bsl.begin();  
j2 = m_cannot_migrate_to_bsl.end();  
while(i2 != j2)  
{  
   os << split_path(m_boost_path, *i2) << "<br>\n";  
   ++i2;  
}  
"</P></BLOCKQUOTE>";  
// Output list of authors that we need permission for to move to the BSL  
// os << "<h2><a name="need-bsl-authors"></a>Authors we need for the BSL</h2>

"<P>Permission of the following authors is needed before we can convert to the Boost Software License. The list of authors that have given their permission is contained in <code>more/blanket-permission.txt</code>.\n<BLOCKQUOTE><P>";  
std::copy(m_authors_for_bsl_migration.begin(), m_authors_for_bsl_migration.end(),  
   std::ostream_iterator<std::string>(os, "<br>\n"));  
os << "</p></BLOCKQUOTE>";
// output a table of copyright information:
//
os << "<H2><a name="copyright"></a>Copyright Holder Information</H2>\n";
std::map<std::string, std::set<fs::path, path_less> >::const_iterator ad, ead;
ad = m_author_data.begin();
ead = m_author_data.end();
while(ad != ead)
{
    os << "<tr><td>" << ad->first << "</td><td>
    std::set<fs::path, path_less>::const_iterator fi, efi;
    fi = ad->second.begin();
    efi = ad->second.end();
    while(fi != efi)
    {
        os << split_path(m_boost_path, *fi) << " ";
        ++fi;
    }
    os << "</td></tr>
++ad;
}

// output file dependency information:
//
os << "<H2><a name="depend"></a>File Dependency Information</H2><BLOCKQUOTE><pre>
std::map<fs::path, fs::path, path_less>::const_iterator dep, last_dep;
std::set<fs::path, path_less>::const_iterator fi, efi;
fi = m_copy_paths.begin();
efi = m_copy_paths.end();
// if in summary mode, just figure out the "bad" files and print those only:
std::set<fs::path, path_less> bad_paths;
if(m_bsl_summary_mode)
{
    bad_paths.insert(m_unknown_licenses.begin(), m_unknown_licenses.end());
    bad_paths.insert(m_unknown_authors.begin(), m_unknown_authors.end());
    bad_paths.insert(m_can_migrate_to_bsl.begin(), m_can_migrate_to_bsl.end());
    bad_paths.insert(m_cannot_migrate_to_bsl.begin(), m_cannot_migrate_to_bsl.end());
typedef std::map<fs::path, std::pair<std::string, std::string>, path_less>
    ::const_iterator conv_iterator;
    conv_iterator i = m_converted_to_bsl.begin(),
    ie = m_converted_to_bsl.end();
    while(i != ie)
    {
        bad_paths.insert(i->first);
        ++i;
{  
  fi = bad_paths.begin();
  efi = bad_paths.end();
  os << "<P>For brevity, only files not under the BSL are shown</P>\n";
}
while(fi != efi)
{
  os << split_path(m_boost_path, *fi);
  dep = m_dependencies.find(*fi);
  last_dep = m_dependencies.end();
  std::set<fs::path, path_less> seen_deps;
  if (dep != last_dep)
    while(true)
      {
        os << " -> " ;
        if(fs::exists(m_boost_path / dep->second))
          os << split_path(m_boost_path, dep->second);
        else if(fs::exists(dep->second))
          os << split_path(fs::path(), dep->second);
        else
          os << dep->second.string();
        if(seen_deps.find(dep->second) != seen_deps.end())
          {
            os << " <I>(Circular dependency!)</i>";
            break; // circular dependency!!!
          }
        seen_deps.insert(dep->second);
        last_dep = dep;
        dep = m_dependencies.find(dep->second);
        if((dep == m_dependencies.end()) || (0 == compare_paths(dep->second, last_dep->second)))
          break;
      }
  os << "\n";
  ++fi;
}
os << "</pre></BLOCKQUOTE>\n";

os << "</body></html>\n";

if(!os)
{
  std::string msg("Error writing to ");
  msg += m_dest_path.string();
  msg += ": ";
  std::runtime_error e(msg);
  boost::throw_exception(e);
}
1.123 libdrm 2.4.97-1
1.123.1 Available under license:

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1.124 file 5.35-4+deb10u2
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1.125 libxcrypt 4.4.18-5

1.125.1 Available under license:

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK:       // %bb.0:
; CHECK-NEXT:   ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT:   mov v0.d[1], v0.d[0]
; CHECK-NEXT:   ret
%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}
; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s

; Test that early if conversion produces LOCR with operands of the right register classes.

define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
%L34 = load i8, i8* %0
%Cmp56 = icmp sgt i8 undef, %L34
br label %CF246
CF246:
; preds = %CF246, %BB
%Sl163 = select i1 %Cmp56, i8 %L34, i8 undef
br i1 undef, label %CF246, label %CF248

CF248:
; preds = %CF248, %CF246
store i8 %SI163, i8* %0
br label %CF248
}
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; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
; CHECK-LABEL: test_sitofp_fixed:
; First, extend each i32 to i64
; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
; Next, convert each to double.
; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
; CHECK: ret
%flt = sitofp <16 x i32> %in to <16 x double>
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,
double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0,
double 64.0>
ret <16 x double> %res
}

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define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s

; Check that we dont end up with the ""LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128)
declare fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK:       # %bb.0:
; CHECK-NEXT:   pushq %rbx
; CHECK-NEXT:   .cfi_def_cfa_offset 16
; CHECK-NEXT:   subq $32, %rsp
; CHECK-NEXT:   .cfi_def_cfa_offset 48
; CHECK-NEXT:   .cfi_offset %rbx, -16
; CHECK-NEXT:   movaps %xmm1, {{[-0-9]+}}(%r{{[sb]}}p) # 16-byte Spill
; CHECK-NEXT:   movaps %xmm0, (%rsp) # 16-byte Spill
; CHECK-NEXT:   callq __gttf2
; CHECK-NEXT:   movl %eax, %ebx
; CHECK-NEXT:   testl %ebx, %ebx
; CHECK-NEXT:   jg .LBB0_2
; CHECK-NEXT: # %bb.1:
; CHECK-NEXT:   xorps %xmm0, %xmm0
; CHECK-NEXT:   addq $32, %rsp
; CHECK-NEXT:   cfi_def_cfa_offset 16
; CHECK-NEXT:   popq %rbx
; CHECK-NEXT:   cfi_def_cfa_offset 8
; CHECK-NEXT:   retq
%cmp = fcmp ogt fp128 %a, %b
define fp128 @TestFabs(fp128 %a) {
  ; CHECK-LABEL: TestFabs:
  ; CHECK:    # %bb.0:
  ; CHECK-NEXT:  andps [{.*}](%rip), %xmm0
  ; CHECK-NEXT:  retq
  %res = call fp128 @llvm.fabs.f128(fp128 %a)
  ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
  ; CHECK-LABEL: TestCopysign:
  ; CHECK:    # %bb.0:
  ; CHECK-NEXT:  andps [{.*}](%rip), %xmm1
  ; CHECK-NEXT:  andps [{.*}](%rip), %xmm0
  ; CHECK-NEXT:  orps %xmm1, %xmm0
  ; CHECK-NEXT:  retq
  %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
  ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
  ; CHECK-LABEL: TestFneg:
  ; CHECK:    # %bb.0:
  ; CHECK-NEXT:  pushq %rax
  ; CHECK-NEXT:  .cfi_def_cfa_offset 16
  ; CHECK-NEXT:  movaps %xmm0, %xmm1
  ; CHECK-NEXT:  callq __multf3
  ; CHECK-NEXT:  xorps [{.*}](%rip), %xmm0
  ; CHECK-NEXT:  popq %rax
  ; CHECK-NEXT:  .cfi_def_cfa_offset 8
  ; CHECK-NEXT:  retq
  %mul = fmul fp128 %a, %a
  %res = fsub fp128 0xL00000000000000000000000000000000, %mul
  ret fp128 %res
}

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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
    ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
    ret void
}

define internal void @innerNoAttribute() {
    ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
    call void @innerSmall()
    ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
    call void @innerNoAttribute()
    ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = [ "min-legal-vector-width"="512" ]

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; RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]??]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]??]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %[[M1:const_mat[0-9]??]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %[[M2:const_mat[0-9]??]] = add i32 %[[C2]], 31
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttopr i32 [%[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %[[M3:const_mat[0-9]*]] = add i32 %[[C1]], 7
; CHECK-NEXT: %8 = inttopr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttopr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %[[M4:const_mat[0-9]*]] = add i32 %[[C4]], 31
; CHECK-NEXT: %11 = inttopr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %12 = inttopr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
; CHECK-NEXT: %[[M5:const_mat[0-9]*]] = add i32 %[[C3]], 7
; CHECK-NEXT: %13 = inttopr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %14 = inttopr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8* %14, i8** @goo

@goo = global i8* undef
define void @foo_i8() {
  entry:
  %0 = load volatile i8, i8* inttopr (i32 805874688 to i8*)
  %1 = load volatile i8, i8* inttopr (i32 805874692 to i8*)
  %2 = load volatile i8, i8* inttopr (i32 805874719 to i8*)
  %3 = load volatile i8, i8* inttopr (i32 805874720 to i8*)
  %4 = load volatile i8, i8* inttopr (i32 805874727 to i8*)
  store i8 %4, i8* inttopr(i32 805873688 to i8*)
  store i8 %3, i8* inttopr(i32 805873719 to i8*)
  store i8 %2, i8* inttopr(i32 805873720 to i8*)
  store i8 %1, i8* inttopr(i32 805873727 to i8*)
  store i8* inttopr(i32 805874800 to i8*), i8** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]*]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]*]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttopr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %[[M1:const_mat[0-9]*]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttopr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %[[M2:const_mat[0-9]*]] = add i32 %[[C2]], 32
; CHECK-NEXT: %4 = inttopr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
define void @foo_i16() {
  entry:
  %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
  %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
  %2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
  %3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
  %4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
  %5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
  ret void
}

define void @foo_i32() {
  entry:
  %0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
  %1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
  %2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
  %3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
  %4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
  ret void
}
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; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1:VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1:VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:VAL:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:VAL:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:VAL:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT:  call fastcc void @callee_avx512_legal512_prefer128_call_avx512_legal512_prefer128(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
; CHECK-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT:  ret void
;
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
  CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>[[ARG1_VAL:%.*]])
  CHECK-NEXT:  bb:
  CHECK-NEXT:    store <8 x i64> %arg1, <8 x i64>* %arg
  ret void
}
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
  CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
  CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  CHECK-NEXT:  bb:
  CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
  CHECK-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
  CHECK-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  CHECK-NEXT:  call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  CHECK-NEXT:  [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  CHECK-NEXT:  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
  CHECK-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  CHECK-NEXT:  ret void
}

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bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
  CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
  CHECK-NEXT:  bb:
  CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  CHECK-NEXT:    ret void
  
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
  CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  CHECK-NEXT:  bb:
  CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
  CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
  CHECK-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  CHECK-NEXT:    call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  CHECK-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
  CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  CHECK-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  CHECK-NEXT:    ret void
  
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
  CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
  CHECK-NEXT: bb:
  CHECK-NEXT:  store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  CHECK-NEXT:  ret void
  
bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
  CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
  CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  CHECK-NEXT: bb:
  CHECK-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
  CHECK-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
  CHECK-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  CHECK-NEXT:  call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  CHECK-NEXT:  [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  CHECK-NEXT:  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
  CHECK-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  CHECK-NEXT:  ret void
  
bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(%arg, %arg1) #1 {
    %arg1 = load <8 x i64>, %arg1
    store <8 x i64>, %arg1
    ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(%arg) #2 {
    %arg1 = alloca <8 x i64>, align 32
    %arg2 = alloca <8 x i64>, align 32
    %arg3 = bitcast <8 x i64>* %arg1 to i8*
    call void @llvm.memset.p0i8.i64(i8* %arg3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(%arg2, %arg1)
    %arg4 = load <8 x i64>, %arg2, align 32
    store <8 x i64>, %arg4, %arg1
    ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg, %arg1) #2 {
    %arg1 = load <8 x i64>, %arg1
    store <8 x i64>, %arg1
    ret void
}

; This should not promote
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg) #2 {
    %arg1 = alloca <8 x i64>, align 32
    %arg2 = alloca <8 x i64>, align 32
    %arg3 = bitcast <8 x i64>* %arg1 to i8*
    call void @llvm.memset.p0i8.i64(i8* %arg3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg2, %arg1)
    %arg4 = load <8 x i64>, %arg2, align 32
    store <8 x i64>, %arg4, %arg1
    ret void
}
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define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
  ; CHECK-LABEL: define ({{[^@]+}})@avx2_legal256_prefer256_call_avx2_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:.%*]])
  ; CHECK-NEXT: bb:
      ; CHECK-NEXT: [[TMP:.%*]] = alloca <8 x i64>, align 32
      ; CHECK-NEXT: [[TMP2:.%*]] = alloca <8 x i64>, align 32
      ; CHECK-NEXT: [[TMP3:.%*]] = bitcast <8 x i64>* [[TMP]] to i8*
      ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
      ; CHECK-NEXT: [[TMP_VAL:.%*]] = load <8 x i64>, <8 x i64>* [[TMP]]
      ; CHECK-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
      ; CHECK-NEXT: [[TMP4:.%*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
      ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
      ; CHECK-NEXT: ret void
  bb:
      %tmp = alloca <8 x i64>, align 32
      %tmp2 = alloca <8 x i64>, align 32
      %tmp3 = bitcast <8 x i64>* %tmp to i8*
      call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
      call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
      %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
      store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
      ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
  ; CHECK-LABEL: define ({{[^@]+}})@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:.%*]], <8 x i64>* [[ARG1_VAL:.%*]])
  ; CHECK-NEXT: bb:
      ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
      ; CHECK-NEXT: ret void
  bb:
      %tmp = load <8 x i64>, <8 x i64>* %arg1
      store <8 x i64> %tmp, <8 x i64>* %arg
      ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  ; CHECK-LABEL: define ({{[^@]+}})@avx2_legal512_prefer256_call_avx2_legal256_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:.%*]])

; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
    ; CHECK-LABEL: define
    {([^@]+)}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
    ; CHECK-NEXT: [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
    ; CHECK-NEXT: ret i32 [[C]]
    ;
    %A = load i32, i32* %X
    %B = load i32, i32* %Y
    %C = add i32 %A, %B
    ret i32 %C
}
define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
    ; CHECK-LABEL: define
    {([^@]+)}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32* [[B:%.*]])
    ; CHECK-NEXT: [[A:%.*]] = alloca i32
    ; CHECK-NEXT: store i32 1, i32* [[A]]
    ; CHECK-NEXT: [[A_VAL:%.*]] = load i32, i32* [[A]]
    ; CHECK-NEXT: [[B_VAL:%.*]] = load i32, i32* [[B]]
    ; CHECK-NEXT: [[C:%.*]] = call i32
    @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %X, i32* %Y) #2 {
    %A = load i32, i32* %X
    %B = load i32, i32* %Y
    %C = add i32 %A, %B
    ret i32 %C
}

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
    %A = alloca i32
    store i32 1, i32* %A
    %C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
    ret i32 %C
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5
attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-
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; NOTE: Assertions have been autogenerate by utils/update_llc_test_checks.py
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s
; --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=prefer-256-bit,avx512vbmi | FileCheck %s
; --check-prefixes=CHECK,CHECK-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for
vpmaddwd+vpaddd
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s
; --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cascadelake | FileCheck %s
; --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cooperlake | FileCheck %s
; --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mcpu=cannonlake | FileCheck %s
; --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=celake-client | FileCheck %s
; --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=celake-server | FileCheck %s
; --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=tigerlake | FileCheck %s
; --check-prefixes=CHECK,CHECK-VBMI

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed to be made aware of the
legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.
define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: add256:
    ; CHECK:  # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpadd 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %d = load <16 x i32>, <16 x i32>* %a
    %e = load <16 x i32>, <16 x i32>* %b
    %f = add <16 x i32> %d, %e
    store <16 x i32> %f, <16 x i32>* %c
    ret void
}

define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: add512:
    ; CHECK:  # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpadd (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %d = load <16 x i32>, <16 x i32>* %a
    %e = load <16 x i32>, <16 x i32>* %b
    %f = add <16 x i32> %d, %e
    store <16 x i32> %f, <16 x i32>* %c
    ret void
}

define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: avg_v64i8_256:
    ; CHECK:  # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rsi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
    ; CHECK-NEXT: vpavgb (%rdi), %ymm0, %ymm0
    ; CHECK-NEXT: vmovdqu %ymm1, (%rax)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %1 = load <64 x i8>, <64 x i8>* %a
    %2 = load <64 x i8>, <64 x i8>* %b
    %3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}
define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vectorwidth"="256" {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1

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; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:  movl %edx, %eax
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:  xorl %ecx, %ecx
; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:  vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT:  .p2align 4, 0x90
; CHECK-NEXT:  .LBB8_1: # %vector.body
; CHECK-NEXT:       # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:  vpmovsxbw 16(%rdi,%rcx), %ymm3
; CHECK-NEXT:  vpmovsxbw (%rdi,%rcx), %ymm4

; CHECK-NEXT:  vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-NEXT:  vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT:  vpadd %ymm2, %ymm3, %ymm2
; CHECK-NEXT:  vpmovsxbw (%rsi,%rcx), %ymm3
; CHECK-NEXT:  vpmaddwd %ymm4, %ymm3, %ymm3
; CHECK-NEXT:  vpadd %ymm1, %ymm3, %ymm1
; CHECK-NEXT:  addq $32, %rcx
; CHECK-NEXT:  cmpq %rcx, %rax
; CHECK-NEXT:  jne .LBB8_1
; CHECK-NEXT:  %bb.2: # %middle.block
; CHECK-NEXT:  vpadd %ymm0, %ymm1, %ymm1
; CHECK-NEXT:  vpadd %ymm0, %ymm2, %ymm0
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:  vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:  vpaddd %ymm0, %ymm2, %ymm0
; CHECK-NEXT:  vpshufd {{.*#}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpshufd {{.*#}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq

call

entry:
%3 = zext i32 %2 to i64
br label %vector.body

to:

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mol nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

to:

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> unde, <32 x i32> <i32 2, i32 3, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> unde, <32 x i32> <i32 1, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: _Z9test_charPcS_i_512:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:   movl %edx, %eax
    ; CHECK-NEXT:   vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT:   xorl %ecx, %ecx
    ; CHECK-NEXT:   vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT:   .p2align 4, 0x90
    ; CHECK-NEXT:  .LBB9_1: # %vector.body
    ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
    ; CHECK-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
    ; CHECK-NEXT:    vpmaddwd %zmm2, %zmm2, %zmm2
    ; CHECK-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
    ; CHECK-NEXT:    addq $32, %rcx
    ; CHECK-NEXT:    cmpq %rcx, %rax
    ; CHECK-NEXT:    jne .LBB9_1
    ; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
    ; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
    ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %ymm1
    ; CHECK-NEXT:    jne .LBB9_1
    ; CHECK-NEXT:    # %bb.2: # %middle.block
    ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm1
    ; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
    ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %ymm1
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsubd {{.*#+}} %xmm1 = %xmm0[2,3,0,1]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpsubd {{.*#+}} %xmm1 = %xmm0[1,1,2,3]
; CHECK-NEXT: vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %31 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx15 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx15, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx17 = add <32 x i32> %bin.rdx15, %rdx.shuf17

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define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
; CHECK-LABEL: sad_16i8_256:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:    movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT:    .p2align 4, 0x90
; CHECK-NEXT:  .LBB10_1: # %vector.body
; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm2
; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm2, %xmm2
; CHECK-NEXT:    vpaddd %ymm1, %ymm2, %ymm1
; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:    vmovd %xmm0, %eax
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
; CHECK-NEXT:  .LBB10_2: # %bb.2: # %middle.block
; CHECK-NEXT:    vpadd %ymm0, %ymm1, %ymm0
; CHECK-NEXT:    vpxtractil28 %ymm0, %ymmx, %ymmx
; CHECK-NEXT:    vpadd %ymm0, %ymmx, %ymmx
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:    vpadd %ymm1, %ymmx, %ymmx
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT:    vpadd %ymm1, %ymmx, %ymmx
; CHECK-NEXT:    vmovd %ymm0, %eax
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
entry:
  br label %vector.body

vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi i8 %16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i32>*
%5 = wide.load1 to <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undefined, <16 x i32> undefined, <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undefined, <16 x i32> undefined, <i32 4, i32 5, i32 6, i32 7, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undefined, <16 x i32> undefined, <i32 2, i32 3, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undefined, <16 x i32> undefined, <i32 1, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined, i32 undefined>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
  ; CHECK-LABEL: sad_16i8_512:
  ; CHECK:       # %bb.0: # %entry
  ; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT:  movq $-1024, %rax # imm = 0xFC00
  ; CHECK-NEXT:  .p2align 4, 0x90
  ; CHECK-NEXT:  .LBB11_1: # %vector.body
  ; CHECK-NEXT:  # =>This Inner Loop Header: Depth=1
  ; CHECK-NEXT:  vmovdqu a+1024(%rax), %xmm1
  ; CHECK-NEXT:  vpsadbw b+1024(%rax), %xmm1, %xmm1
  ; CHECK-NEXT:  vpadd %zmm0, %zmm1, %zmm0
  ; CHECK-NEXT:  addq %rax, %zmm0
  ; CHECK-NEXT:  jne .LBB11_1
entry:
   br label %vector.body

vector.body:
   %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
   %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
   %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
   %1 = bitcast i8* %0 to <16 x i8>*
   %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
   %2 = zext <16 x i8> %wide.load to <16 x i32>
   %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
   %4 = bitcast i8* %3 to <16 x i8>*
   %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
   %5 = zext <16 x i8> %wide.load1 to <16 x i32>
   %6 = sub ns %10 i32 %2, %5
   %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
   %8 = sub ns %10 %6 to %16 x i32> %9 = select <16 x i1> %7, %10, %16 x i32> %8
   %10 = add ns <16 x i32> %9, %vec.phi
   %index.next = add i64 %index, 4
   %11 = icmp eq i64 %index.next, 1024
   br i1 %11, label %middle.block, label %vector.body

middle.block:
   %rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> %11, <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx = add <16 x i32> %10, %rdx.shuf
   %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> %bin.rdx %3 = add <16 x i32> %12, %rdx.shuf %bin.rdx2 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> %bin.rdx2 %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3

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decl %rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define void @sbtos16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbtos16f32_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:    vcvtdq2ps %ymm0, %ymm0
    ; CHECK-NEXT:    vpmovm2d %k0, %ymm1
    ; CHECK-NEXT:    vcvtdq2ps %ymm1, %ymm1
    ; CHECK-NEXT:    vmovaps %ymm1, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @sbtos16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbtos16f32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @sbtos16f64_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbtos16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:    vzeroupper
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}
define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res)  "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f64_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm1
    ; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, 64(%rdi)
    ; CHECK-NEXT:    vmovaps %zmm1, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res)  "min-legal-vector-width"="256" {
    ; CHECK-LABEL: ubto16f32_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    vpmovm2d %k1, %zmm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm1
    ; CHECK-NEXT:    vextracti64x4 %1, %zmm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, 64(%rdi)
    ; CHECK-NEXT:    vmovaps %zmm1, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: ubto16f32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vpsrld $31, %zmm0, %zmm0
    ; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: ubto16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    kshiftrw $8, %k1, %ymm0
    ; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm1
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm0
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0
    ; CHECK-NEXT:    vpmovm2d %k0, %ymm2
    ; CHECK-NEXT:    vpsrld $31, %ymm2, %ymm2
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm2
    ; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm3
    ; CHECK-NEXT:    vpmovm2d %k0, %ymm2
    ; CHECK-NEXT:    vpsrld $31, %ymm2, %ymm2
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm2
    ; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm2
    ; CHECK-NEXT:    vpmovaps %ymm2, 32(%rdi)
    ; CHECK-NEXT:    vpmovaps %ymm3, (%rdi)
    ; CHECK-NEXT:    vmovaps %ymm0, 96(%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer

%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {  
; CHECK-LABEL: ubto16f64_512:
; CHECK:  
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrl $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {  
; CHECK-LABEL: test_16f32toub_256:
; CHECK:  
; CHECK-NEXT: vcvtpps2dq (%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k0
; CHECK-NEXT: vcvtpps2dq 32(%rdi), %ymm1
; CHECK-NEXT: vpslld $31, %ymm1, %ymm1
; CHECK-NEXT: vpmovd2m %ymm1, %k1
; CHECK-NEXT: kunpckbw %k0, %k1, %k1
; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT: retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptoui <16 x float> %a to <16 x i1>
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
; CHECK-LABEL: test_16f32toub_512:
; CHECK:  
; CHECK-NEXT: vcvtpps2dq (%rdi), %zmm1
define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: test_16f32tosb_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vcvttps2dq (%rdi), %ymm1
    ; CHECK-NEXT:    vpmovd2m %ymm1, %k0
    ; CHECK-NEXT:    vcvttps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:    vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:    kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:    vmovdqu16 %ymm0, %ymm0 {%k1} {z}
    ; CHECK-NEXT:    retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptoui <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: test_16f32tosb_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vcvttps2dq (%rdi), %ymm1
    ; CHECK-NEXT:    vpmovd2m %ymm1, %k0
    ; CHECK-NEXT:    vcvttps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:    vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:    kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:    vmovdqu16 %ymm0, %ymm0 {%k1} {z}
    ; CHECK-NEXT:    retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: mul256:
    ; CHECK-AVX512:       # %bb.0:
    ; CHECK-AVX512-NEXT:    vmovdqa (%rdi), %ymm0
    ; CHECK-AVX512-NEXT:    vmovdqa 32(%rdi), %ymm1
    ; CHECK-AVX512-NEXT:    vmovdqa (%rsi), %ymm2
    ; CHECK-AVX512-NEXT:    vmovdqa 32(%rsi), %ymm3
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
    CHECK-AVX512-LABEL: mul512:
    CHECK-AVX512:       # %bb.0:
    CHECK-AVX512-NEXT:    vpmullw %zmm2, %zmm3, %zmm1
    CHECK-AVX512-NEXT:    vmovdqa64 (%rdi), %zmm0
    CHECK-AVX512-NEXT:    vmovdqa64 (%rsi), %zmm1
    CHECK-AVX512-NEXT:    vpmullw %zmm2, %zmm3, %zmm1
    CHECK-AVX512-NEXT:    vzeroupper
    CHECK-AVX512-NEXT:    retq
%
    %d = load <64 x i8>, <64 x i8>* %a
    %e = load <64 x i8>, <64 x i8>* %b
    %f = mul <64 x i8> %d, %e
    store <64 x i8> %f, <64 x i8>* %c
    ret void
}
; CHECK-AVX512-NEXT:   vpmullw %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:   vpandq %zmm3, %zmm0, %zmm0
; CHECK-AVX512-NEXT:   vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-AVX512-NEXT:   vmovdqa64 %zmm0, (%rdx)
; CHECK-AVX512-NEXT:   vzeroupper
; CHECK-AVX512-NEXT:   retq
;
; CHECK-VBMI-LABEL:   mul512:
; CHECK-VBMI:        # %bb.0:
; CHECK-VBMI-NEXT:   vmovdqa64 (%rdi), %zmm0
; CHECK-VBMI-NEXT:   vmovdqa64 (%rsi), %zmm1
; CHECK-VBMI-NEXT:   vpunpckhbw {{.*#+}} zmm2 = zmm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,36,6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT:   vpunpckhbw {{.*#+}} zmm3 = zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,36,6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT:   vpmullw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT:   vmovdqa64 {{.*#+}} zmm1 = [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72,74,76,78,80,82,84,86,88,90,92,94,96,98,100,102,104,106,108,110,112,114,116,118,120,122,124,126]
; CHECK-VBMI-NEXT:   vpmullw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT:   vmovdqa64 {{.*#+}} zmm1 = %mask = icmp eq <4 x i32> %trigger, %zeroinitializer

%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {
; CHECK-LABEL: mload_v4i32:
; CHECK:        # %bb.0:
; CHECK-NEXT:   vptestnmd %xmm0, %xmm0, %k1
; CHECK-NEXT:   vpblendmd (%rdi), %xmm1, %xmm0 {%k1}
; CHECK-NEXT:   retq
%mask = icmp eq <4 x i32> %trigger, %zeroinitializer

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%res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>* %addr, i32 4, <4 x i1> %mask, <4 x i32> %dst)
ret <4 x i32> %res
}
declare <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32, <4 x i1>, <4 x i32>)

define <16 x i32> @trunc_v16i64_v16i32(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i32:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT: vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT: vpmovqd %ymm0, %xmm0
; CHECK-NEXT: vpmovqd %ymm1, %xmm1
; CHECK-NEXT: vpmovqd %ymm2, %xmm2
; CHECK-NEXT: vpmovqd %ymm3, %xmm3
; CHECK-NEXT: vinserti128 $1, %xmm1, %ymm0, %ymm0
; CHECK-NEXT: vpmovqd %ymm2, %xmm1
; CHECK-NEXT: vpmovqd %ymm3, %xmm2
; CHECK-NEXT: vinserti128 $1, %xmm2, %ymm1, %ymm1
; CHECK-NEXT: retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i32>
ret <16 x i32> %b
}

define <16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i64_v16i8:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vmovdqa 64(%rdi), %ymm2
; CHECK-NEXT: vmovdqa 96(%rdi), %ymm3
; CHECK-NEXT: vpmovqb %ymm3, %xmm3
; CHECK-NEXT: vpmovqb %ymm2, %xmm2
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm2 = xmm2[0],xmm3[0],xmm2[1],xmm3[1]
; CHECK-NEXT: vpmovqb %ymm1, %xmm1
; CHECK-NEXT: vpmovqb %ymm0, %xmm0
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm2[0]
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i64>, <16 x i64>* %x
%b = trunc <16 x i64> %a to <16 x i8>
ret <16 x i8> %b
}

define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_v16i32_v16i8:
define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: trunc_v8i64_v8i8:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0  
    ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1  
    ; CHECK-NEXT:  vpmovdb %ymm1, %xmm1  
    ; CHECK-NEXT:  vpmovdb %ymm0, %xmm0  
    ; CHECK-NEXT:  vpmovdb {{.*#+}} %xmm0 = %xmm0[0],%xmm1[0]  
    ; CHECK-NEXT:  vzeroupper  
    ; CHECK-NEXT:  retq  
    %a = load <16 x i32>, <16 x i32>* %x  
    %b = trunc <16 x i32> %a to <16 x i8>  
    ret <16 x i8> %b  
}  
define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: trunc_v8i64_v8i16:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0  
    ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1  
    ; CHECK-NEXT:  vpmovqw %ymm1, %xmm1  
    ; CHECK-NEXT:  vpmovqw %ymm0, %xmm0  
    ; CHECK-NEXT:  vpmovqw {{.*#+}} %xmm0 = %xmm0[0],%xmm1[0],%xmm0[1],%xmm1[1]  
    ; CHECK-NEXT:  vzeroupper  
    ; CHECK-NEXT:  retq  
    %a = load <8 x i64>, <8 x i64>* %x  
    %b = trunc <8 x i64> %a to <8 x i16>  
    ret <8 x i16> %b  
}  
define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vpsrlq $48, 32(%rdi), %ymm1  
    ; CHECK-NEXT:  vpsrlq $48, (%rdi), %ymm2
define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" { ; CHECK-NEXT:    %a = load <8 x i64>, <8 x i64>* %x
%b = lshr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
%c = trunc <8 x i64> %b to <8 x i32>
ret <8 x i32> %c
}

define <32 x i8> @trunc_v32i16_v32i8_zeroes(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" { ; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-AVX512-NEXT:    vpsrlw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT:    vpackuswb %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-AVX512-NEXT:    retq

; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:    vpsrlw %8, (%rdi), %ymm0
; CHECK-VBMI-NEXT:    vpackuswb %ymm0, %ymm1, %ymm0
; CHECK-VBMI-NEXT:    vpermq {{.*##}} ymm0 = ymm0[0,2,1,3]
; CHECK-VBMI-NEXT:    retq

; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_zeroes:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:    vpsrlw %8, (%rdi), %ymm0
; CHECK-VBMI-NEXT:    vpackuswb %ymm0, %ymm1, %ymm0
; CHECK-VBMI-NEXT:    vpermq {{.*##}} ymm0 = ymm0[0,2,1,3]
; CHECK-VBMI-NEXT:    retq

define <8 x i32> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" { ; CHECK-LABEL:    %a = load <32 x i16>, <32 x i16>* %x
%b = lshr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
}

define <8 x i32> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" { ; CHECK-LABEL:    %a = load <32 x i16>, <32 x i16>* %x
%b = lshr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" { ; CHECK-NEXT:    %a = load <8 x i64>, <8 x i64>* %x
%b = lshr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
%c = trunc <8 x i64> %b to <8 x i32>
ret <8 x i32> %c
}
define <16 x i16> @trunc_v16i32_v16i16_sign(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_sign:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vpsraq $48, 32(%rdi), %ymm0
    ; CHECK-NEXT:  vpsraq $48, (%rdi), %ymm1
    ; CHECK-NEXT:  vpmovqd %ymm1, %xmm1
    ; CHECK-NEXT:  vpmovqd %ymm0, %xmm0
    ; CHECK-NEXT:  vinserti128 $1, %xmm0, %ymm1, %ymm0
    ; CHECK-NEXT:  retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = ashr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
    %c = trunc <8 x i64> %b to <8 x i32>
    ret <8 x i32> %c
}

define <32 x i8> @trunc_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_sign:
    ; CHECK-AVX512:       # %bb.0:
    ; CHECK-AVX512-NEXT:  vpsraw $8, 32(%rdi), %ymm0
    ; CHECK-AVX512-NEXT:  vpsraw $8, (%rdi), %ymm1
    ; CHECK-AVX512-NEXT:  vpacksswb %ymm0, %ymm1, %ymm0
    ; CHECK-AVX512-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-AVX512-NEXT:  retq
    %a = load <16 x i32>, <16 x i32>* %x
    %b = ashr <16 x i32> %a, <i16 16, i16 16, i16 16, i16 16, i16 16, i16 16, i16 16, i16 16>
    %c = trunc <16 x i32> %b to <16 x i16>
    ret <16 x i16> %c
}

define <32 x i8> @trunc_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_sign:
    ; CHECK-VBMI:       # %bb.0:
    ; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm1
    ; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
    ; CHECK-VBMI-NEXT:  vpermi2b 32(%rdi), %ymm1, %ymm0
    ; CHECK-VBMI-NEXT:  retq
    %a = load <32 x i16>, <32 x i16>* %x
    %b = ashr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
    %c = trunc <32 x i16> %b to <32 x i8>
    ret <32 x i8> %c
define void @zext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: zext_v16i8_v16i64:
; CHECK:   # %bb.0:
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm1 =
  xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,
  xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
; CHECK-NEXT:  vpshufd {{.*#+}} xmm2 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm2 =
  xmm2[0],zero,zero,xmm2[1],zero,zero,xmm2[2],zero,zero,xmm2[3],zero,zero,zero,zero
; CHECK-NEXT:  vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT:  vpshufd {{.*#+}} xmm3 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm3 =
  xmm3[0],zero,zero,xmm3[1],zero,zero,xmm3[2],zero,zero,xmm3[3],zero,zero,zero,zero
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm1 =
  xmm1[0],zero,zero,xmm1[1],zero,zero,xmm1[2],zero,zero,xmm1[3],zero,zero,zero,zero
; CHECK-NEXT:  vpmovzxbq {{.*#+}} ymm0 =
  xmm0[0],zero,zero,zero,zero,zero,zero,zero,xmm0[1],zero,zero,zero,zero,zero,xmm0[2],zero,zero,zero,zero,
  zero,zero,zero,zero,xmm0[3],zero,zero,zero,zero
; CHECK-NEXT:  vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:  vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT:  vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT:  vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT:  vzeroupper
%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:   # %bb.0:
; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm0 =
  xmm0[0],%ymm0
; CHECK-NEXT:  vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm1 =
  xmm1[0],%ymm1
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT:  vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm3 =
  xmm3[0],%ymm3
; CHECK-NEXT:  vpmovzxwq %ymm0, %xmm0
; CHECK-NEXT:  vpmovzxwq %ymm1, %xmm1
; CHECK-NEXT:  vpmovzxwq %xmm2, %ymm2
; CHECK-NEXT:  vmovdqa %ymm2, 64(%rdi)
; CHECK-NEXT:  vmovdqa %ymm0, (%rdi)
; CHECK-NEXT:  vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdi)
; CHECK-NEXT:  vzeroupper
%a = sext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}
def void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {
CHECK-LABEL: vselect_split_v8i16_setcc:
CHECK:       # %bb.0:
CHECK-NEXT: vmovdqa (%rsi), %ymm2
CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
CHECK-NEXT: vpcmpeqw %xmm1, %xmm0, %k1
CHECK-NEXT: kshiftrb $4, %k1, %k2
CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
CHECK-NEXT: vmovdqa %ymm2, (%rdx)
CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
CHECK-NEXT: vzeroupper
CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i16> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}
def void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {
CHECK-LABEL: vselect_split_v8i32_setcc:
CHECK:       # %bb.0:
CHECK-NEXT: vmovdqa (%rsi), %ymm2
CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
CHECK-NEXT: vpcmpeqd %ymm1, %ymm0, %k1
CHECK-NEXT: kshiftrb $4, %k1, %k2
CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
CHECK-NEXT: vmovdqa %ymm2, (%rdx)
CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
CHECK-NEXT: vzeroupper
CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i32> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void

define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: vselect_split_v16i8_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:  vpcmpeqb %xmm1, %xmm0, %k1
    ; CHECK-NEXT:  kshiftrw $8, %k1, %k2
    ; CHECK-NEXT:  vmovdqa32 32(%rdi), %ymm3 [%k2]
    ; CHECK-NEXT:  vmovdqa32 (%rdi), %ymm2 [%k1]
    ; CHECK-NEXT:  vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm3, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <16 x i32>, <16 x i32>* %p
    %y = load <16 x i32>, <16 x i32>* %q
    %a = icmp eq <16 x i8> %s, %t
    %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
    store <16 x i32> %b, <16 x i32>* %r
    ret void
}

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: vselect_split_v16i16_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:  vpcmpeqw %ymm1, %ymm0, %k1
    ; CHECK-NEXT:  kshiftrw $8, %k1, %k2
    ; CHECK-NEXT:  vmovdqa32 32(%rdi), %ymm3 [%k2]
    ; CHECK-NEXT:  vmovdqa32 (%rdi), %ymm2 [%k1]
    ; CHECK-NEXT:  vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm3, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <16 x i32>, <16 x i32>* %p
    %y = load <16 x i32>, <16 x i32>* %q
    %a = icmp eq <16 x i16> %s, %t
    %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
    store <16 x i32> %b, <16 x i32>* %r
    ret void
}

define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_packus_v16i32_v16i8:
    }

define void @trunc_packus_v16i32_v16i8_store(<16 x i32>* %p, <16 x i8>* %q) "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vmovdqa (%rdi), %ymm0
; CHECK-NEXT:   vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT:   vpermq {{.*#+}} ymm0 = ymm0[0,1,2,3]
; CHECK-NEXT:   vpmovuswb %ymm0, %xmm0
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
  ;%a = load <16 x i32>, <16 x i32>* %p
  ;%b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
  ;%c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
  ;%d = icmp sgt <16 x i32> %c, zeroinitializer
  ;%e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
  ;%f = trunc <16 x i32> %e to <16 x i8>
  ret <16 x i8> %f
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_argument_return:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vmovdqa (%rdi), %ymm0
; CHECK-NEXT:   vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT:   vpermq {{.*#+}} ymm0 = ymm0[0,1,2,3]
; CHECK-NEXT:   vpmovuswb %ymm0, %xmm0
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
  ;%a = load <64 x i1>, <64 x i1>* %p
  ;%b = icmp slt <64 x i1> %a, <i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255, i1 255>
  ;%c = select <64 x i1> %b, <64 x i32> %a, <64 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
  ;%d = icmp sgt <64 x i32> %c, zeroinitializer
  ;%e = select <64 x i1> %d, <64 x i32> %c, <64 x i32> zeroinitializer
  ;%f = trunc <64 x i32> %e to <64 x i8>
  store <64 x i8> %f, <64 x i8>* %q
  ret void
}

define void @v64i1_shuffle(<64 x i8>* %x, <64 x i8>* %y) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_shuffle:
; CHECK:       # %bb.0:
; CHECK-NEXT:   vmovdqa (%rdi), %ymm0
; CHECK-NEXT:   vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT:   vpermq {{.*#+}} ymm0 = ymm0[0,1,2,3]
; CHECK-NEXT:   vpmovuswb %ymm0, %xmm0
; CHECK-NEXT:   vzeroupper
; CHECK-NEXT:   retq
  ;%a = load <64 x i8>, <64 x i8>* %p
  ;%b = icmp slt <64 x i8> %a, <i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255, i8 255>
  ;%c = select <64 x i1> %b, <64 x i32> %a, <64 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
  ;%d = icmp sgt <64 x i32> %c, zeroinitializer
  ;%e = select <64 x i1> %d, <64 x i32> %c, <64 x i32> zeroinitializer
  ;%f = trunc <64 x i32> %e to <64 x i8>
  store <64 x i8> %f, <64 x i8>* %q
  ret void
}
; CHECK-LABEL: v64i1_shuffle:
; CHECK:     # %bb.0: # %entry
; CHECK-NEXT: vmovdqa (%rdi), %ymm1
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm0
; CHECK-NEXT: vptestnmb %ymm1, %ymm1, %k0
; CHECK-NEXT: kshiftrd $1, %k0, %k1
; CHECK-NEXT: movq $-3, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $63, %k0, %k2
; CHECK-NEXT: kshiftrq $62, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-5, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $3, %k0, %k2
; CHECK-NEXT: kshiftrq $61, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-9, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $2, %k0, %k2
; CHECK-NEXT: kshiftrq $60, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-17, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $5, %k0, %k2
; CHECK-NEXT: kshiftrq $59, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-33, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $4, %k0, %k2
; CHECK-NEXT: kshiftrq $58, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-65, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $7, %k0, %k2
; CHECK-NEXT: kshiftrq $57, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-129, %rax
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $6, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $56, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-257, %rax # imm = 0xFEFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $9, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $55, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-513, %rax # imm = 0xFDFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $8, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $54, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $11, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $53, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $10, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $52, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-4097, %rax # imm = 0xEFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $13, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $51, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-8193, %rax # imm = 0xDBFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $12, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $50, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-16385, %rax # imm = 0xBFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $15, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $49, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-32769, %rax # imm = 0xFFFF7FFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $14, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $48, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-65537, %rax # imm = 0xFFFEFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $17, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $47, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-131073, %rax # imm = 0xFFFFDFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $16, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $46, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-262145, %rax # imm = 0xFFFFBFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $19, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $45, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-524289, %rax # imm = 0xFFFF7FFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $18, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $44, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-1048577, %rax # imm = 0xFFEFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $21, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $43, %k2, %k2

; CHECK-NEXT:    kshiftr $8, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $22, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $4398046511105, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftr $11, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $21, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-8796093022209, %rax # imm = 0xFFFFFFFF7FFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftr $10, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $20, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-17592186044417, %rax # imm = 0xFFFFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftr $13, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $19, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-35184372088833, %rax # imm = 0xFFFFFFFFDFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftr $12, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $18, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-70368744177665, %rax # imm = 0xFFFFFFFFBFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftr $14, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $17, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-140737488355329, %rax # imm = 0xFFFFFFFF7FFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftr $15, %k1, %k2
; CHECK-NEXT:    kshiflq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $16, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-281474976710657, %rax # imm = 0xFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $22, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $8, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-72057594037927937, %rax # imm = 0xFEFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $25, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $7, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-144115188075855873, %rax # imm = 0xFDFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $24, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $6, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-288230376151711745, %rax # imm = 0xFBFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $27, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $5, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $26, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $4, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-1152921504606846977, %rax # imm = 0xEFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $29, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $3, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $-2305843009213693953, %rax # imm = 0xDFFFFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $28, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $2, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
entry:
  %a = load <64 x i8>, <64 x i8>* %x
  %b = icmp eq <64 x i8> %a, zeroInitializer
  %shuf = shufflevector <64 x i1> %b, <64 x i1> undef, <64 x i32> %i32 1, i32 2, i32 3, i32 4, i32 5, i32 6, i32 7, i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 32, i32 33, i32 34, i32 35, i32 36, i32 37, i32 38, i32 39, i32 40, i32 41, i32 42, i32 43, i32 44, i32 45, i32 46, i32 47, i32 48, i32 49, i32 50, i32 51, i32 52, i32 53, i32 54, i32 55, i32 56, i32 57, i32 58, i32 59, i32 60, i32 61, i32 62>
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, i32 %i32 1, <64 x i1> %shuf)
ret void
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>*, i32, <64 x i1>)

@mem64_dst = global i64 0, align 8
@mem64_src = global i64 0, align 8
define i32 @v64i1_inline_asm() "min-legal-vector-width"="256" {
  ; CHECK-LABEL: v64i1_inline_asm:
  ; CHECK: # %bb.0:
  ; CHECK-NEXT: kmovq {.*}%(%rip), %k0
  ; CHECK-NEXT: #APP
  ; CHECK-NEXT: #NO_APP
  ; CHECK-NEXT: kmovq %k0, {.*}%(%rip)
  ; CHECK-NEXT: movl-[0-9]+(%rsp), %eax
  ; CHECK-NEXT: retq
  %1 = alloca i32, align 4
  %2 = load i64, i64* @mem64_src, align 8
  %3 = call i64 asm "", "%.k.r{dirflag}.r{flags}+%i64 %2"
  store i64 %3, i64* @mem64_dst, align 8
  %4 = load i32, i32* %1, align 4
  ret i32 %4
}
define void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: cmp_v8i64_sext:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm1
    ; CHECK-NEXT:  vpcmpeqgtq 32(%rdi), %ymm1, %ymm1
    ; CHECK-NEXT:  vpcmpeqgtq (%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <8 x i64>, <8 x i64>* %xptr
    %y = load <8 x i64>, <8 x i64>* %yptr
    %cmp = icmp slt <8 x i64> %x, %y
    %ext = sext <8 x i1> %cmp to <8 x i64>
    store <8 x i64> %ext, <8 x i64>* %zptr
    ret void
}

define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: cmp_v8i64_zext:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm1
    ; CHECK-NEXT:  vpcmpeqgtq 32(%rdi), %ymm1, %ymm1
    ; CHECK-NEXT:  vpcmpeqgtq (%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <8 x i64>, <8 x i64>* %xptr
    %y = load <8 x i64>, <8 x i64>* %yptr
    %cmp = icmp slt <8 x i64> %x, %y
    %ext = zext <8 x i1> %cmp to <8 x i64>
    store <8 x i64> %ext, <8 x i64>* %zptr
    ret void
}

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: var_rotate_v16i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
    ; CHECK-NEXT:  vpsubb %xmm1, %xmm2, %xmm2
    ; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm1 =
xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zero,xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xmm1[15],zero;
CHECK-NEXT: vpmovzxbw {{.*#+}} ymm0 =
xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero;
CHECK-NEXT: vpsllvw %ymm1, %ymm0, %ymm1;
CHECK-NEXT: vpmovzxbw {{.*#+}} ymm2 =
xmm2[0],zero,xmm2[1],zero,xmm2[2],zero,xmm2[3],zero,xmm2[4],zero,xmm2[5],zero,xmm2[6],zero,xmm2[7],zero,xmm2[8],zero,xmm2[9],zero,xmm2[10],zero,xmm2[11],zero,xmm2[12],zero,xmm2[13],zero,xmm2[14],zero,xmm2[15],zero;
CHECK-NEXT: vpsrlvw %ymm2, %ymm0, %ymm0;
CHECK-NEXT: vpor %ymm0, %ymm1, %ymm0;
CHECK-NEXT: vpmovwb %ymm0, %xmm0;
CHECK-NEXT: vzeroupper;
CHECK-NEXT: retq;
%b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b;
%shl = shl <16 x i8> %a, %b;
%lshr = lshr <16 x i8> %a, %b8;
%or = or <16 x i8> %shl, %lshr;
ret <16 x i8> %or;
}
define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
CHECK-NEXT: # %bb.0:
CHECK-NEXT: vpsllw $4, %ymm0, %ymm2;
CHECK-NEXT: vpsrlw $4, %ymm0, %ymm3;
CHECK-NEXT: vpternlogq $216, [.*](%rip), %ymm2, %ymm3;
CHECK-NEXT: vpsllw $5, %ymm1, %ymm1;
CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0;
CHECK-NEXT: vpsllw $2, %ymm0, %ymm2;
CHECK-NEXT: vpsrlw $6, %ymm0, %ymm3;
CHECK-NEXT: vpternlogq $216, [.*](%rip), %ymm2, %ymm3;
CHECK-NEXT: vpaddb %ymm1, %ymm1, %ymm1;
CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0;
CHECK-NEXT: vpsrlw $7, %ymm0, %ymm2;
CHECK-NEXT: vpaddb %ymm0, %ymm0, %ymm3;
CHECK-NEXT: vpternlogq $248, [.*](%rip), %ymm2, %ymm3;
CHECK-NEXT: vpaddb %ymm1, %ymm1, %ymm1;
CHECK-NEXT: vpblendvb %ymm1, %ymm3, %ymm0, %ymm0;
CHECK-NEXT: retq;
%b8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b;
%shl = shl <32 x i8> %a, %b;
%lshr = lshr <32 x i8> %a, %b8;
%or = or <32 x i8> %shl, %lshr;
}
define <32 x i8> @splatvar_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: splatvar_rotate_v32i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpbroadcastb %xmm1, %xmm1
    ; CHECK-NEXT:    vpand {{.*}}(%rip), %xmm1, %xmm1
    ; CHECK-NEXT:    vpmovzxbq {{.*#+}} xmm2 = xmm1[0],zero,zero,zero,zero,zero,zero,xmm1[1],zero,zero,zero,zero,zero,zero,zero
    ; CHECK-NEXT:    vpsllw %xmm2, %ymm0, %ymm3
    ; CHECK-NEXT:    vmovdqa {{.*#+}} xmm4 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
    ; CHECK-NEXT:    vpsubb %xmm1, %xmm4, %xmm4
    ; CHECK-NEXT:    vpcmpeqd %xmm4, %xmm4, %xmm4
    ; CHECK-NEXT:    vpsllw %xmm2, %xmm2, %ymm2
    ; CHECK-NEXT:    vpbroadcastb %xmm2, %ymm2
    ; CHECK-NEXT:    vpmovzxbq {{.*#+}} xmm1 = xmm1[0],zero,zero,zero,zero,zero,zero,xmm1[1],zero,zero,zero,zero,zero,zero,zero
    ; CHECK-NEXT:    vpsrlw %xmm1, %ymm0, %ymm5
    ; CHECK-NEXT:    vpsrlw %ymm1, %ymm1, %ymm1
    ; CHECK-NEXT:    vpsrlw %ymm0, %ymm0
    ; CHECK-NEXT:    vpsrlw $8, %ymm0, %ymm0
    ; CHECK-NEXT:    vpbroadcastb %ymm0, %ymm0
    ; CHECK-NEXT:    vpternlogq $236, %ymm5, %ymm2, %ymm0
    ; CHECK-NEXT:    retq
    %splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
    %splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %splat
    %shl = shl <32 x i8> %a, %splat
    %lshr = lshr <32 x i8> %a, %splat8
    %or = or <32 x i8> %shl, %lshr
    ret <32 x i8> %or
}

define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: constant_rotate_v32i8:
    ; CHECK-AVX512:       # %bb.0:
    ; CHECK-AVX512-NEXT:    vpsllw $4, %ymm0, %ymm1
    ; CHECK-AVX512-NEXT:    vpand {{.*}}(%rip), %ymm1, %ymm1
    ; CHECK-AVX512-NEXT:    vbroadcasti128 {{.*#+}} ymm2 = [8192, 24640, 41088, 57536, 57344, 41152, 24704, 8256, 8192, 24640, 41088, 57536, 57344, 41152, 24704, 8256]
    ; CHECK-AVX512-NEXT:    # ymm2 = mem[0,1,0,1]
    ; CHECK-AVX512-NEXT:    vpblendvb %ymm2, %ymm1, %ymm0, %ymm1
    ; CHECK-AVX512-NEXT:    vpsllw $2, %ymm1, %ymm3
    ; CHECK-AVX512-NEXT:    vpand {{.*}}(%rip), %ymm3
    ; CHECK-AVX512-NEXT:    vpaddd %ymm2, %ymm2, %ymm2
    ; CHECK-AVX512-NEXT:    vpblendvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpaddb %ymm1, %ymm1, %ymm3
; CHECK-AVX512-NEXT:  vpaddb %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT:  vblendvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpxor %xmm2, %xmm2, %xmm2
; CHECK-AVX512-NEXT:  vpuncckhbw {{.*#+}} ymm3 =
ymm0[8],ymmm2[8],ymmm0[9],ymmm2[9],ymmm0[10],ymmm2[10],ymmm0[11],ymmm2[11],ymmm0[12],ymmm2[12],ymmm0[13]
,ymmm2[13],ymmm0[14],ymmm2[14],ymmm0[15],ymmm2[15],ymmm0[24],ymmm2[24],ymmm0[25],ymmm2[25],ymmm0[26],ymmm2[26],ymmm0[27],ymmm2[27],ymmm0[28],ymmm2[28],ymmm0[29],ymmm2[29],ymmm0[30],ymmm2[30],ymmm0[31],ymmm2[31]
; CHECK-AVX512-NEXT:  vpsllvw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vparsw S8, %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vpuncckhbw {{.*#+}} ymm0 =
ymm0[0],ymmm2[0],ymmm0[1],ymmm2[1],ymmm0[2],ymmm2[2],ymmm0[3],ymmm2[3],ymmm0[4],ymmm2[4],ymmm0[5],ymmm2[5],ymmm0[6],ymmm2[6],ymmm0[7],ymmm2[7],ymmm0[16],ymmm2[16],ymmm0[17],ymmm2[17],ymmm0[18],ymmm2[18],ymmm0[19],ymmm2[19],ymmm0[20],ymmm2[20],ymmm0[21],ymmm2[21],ymmm0[22],ymmm2[22],ymmm0[23],ymmm2[23]
; CHECK-AVX512-NEXT:  vpsllvw {{.*}}(%rip), %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vparsw S8, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpackuswb %ymm3, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpor %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:  retq

; CHECK-VBMI-LABEL:  constant_rotate_v32i8:
; CHECK-VBMI:     # %bb.0:
; CHECK-VBMI-NEXT:  vpsllw $4, %ymm0, %ymm1
; CHECK-VBMI-NEXT:  vpand {{.*}}(%rip), %ymm1, %ymm1
; CHECK-VBMI-NEXT:  vbroadcasti128 {{.*#+}} ymm2 =
[8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]
; CHECK-VBMI-NEXT:  # ymm2 = mem[0,1,0,1]
; CHECK-VBMI-NEXT:  vblendvb %ymm2, %ymm0, %ymm0, %ymm1
; CHECK-VBMI-NEXT:  vpsllw S2, %ymm1, %ymm3
; CHECK-VBMI-NEXT:  vpand {{.*}}(%rip), %ymm3, %ymm3
; CHECK-VBMI-NEXT:  vpaddb %ymm2, %ymm2, %ymm2
; CHECK-VBMI-NEXT:  vblendvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT:  vpaddb %ymm1, %ymm1, %ymm3
; CHECK-VBMI-NEXT:  vpsllw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-VBMI-NEXT:  vpunccklwb {{.*#+}} ymm0 =
ymm0[8],ymmm2[8],ymmm0[9],ymmm2[9],ymmm0[10],ymmm2[10],ymmm0[11],ymmm2[11],ymmm0[12],ymmm2[12],ymmm0[13]
,ymmm2[13],ymmm0[14],ymmm2[14],ymmm0[15],ymmm2[15],ymmm0[24],ymmm2[24],ymmm0[25],ymmm2[25],ymmm0[26],ymmm2[26],ymmm0[27],ymmm2[27],ymmm0[28],ymmm2[28],ymmm0[29],ymmm2[29],ymmm0[30],ymmm2[30],ymmm0[31],ymmm2[31]
; CHECK-VBMI-NEXT:  vpsllw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-VBMI-NEXT:  vpunccklwb {{.*#+}} ymm0 =
ymm0[0],ymmm2[0],ymmm0[1],ymmm2[1],ymmm0[2],ymmm2[2],ymmm0[3],ymmm2[3],ymmm0[4],ymmm2[4],ymmm0[5],ymmm2[5],ymmm0[6],ymmm2[6],ymmm0[7],ymmm2[7],ymmm0[16],ymmm2[16],ymmm0[17],ymmm2[17],ymmm0[18],ymmm2[18],ymmm0[19],ymmm2[19],ymmm0[20],ymmm2[20],ymmm0[21],ymmm2[21],ymmm0[22],ymmm2[22],ymmm0[23],ymmm2[23]
define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: splatconstant_rotate_v32i8:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:   vpsllw $4, %ymm0, %ymm1
  ; CHECK-NEXT:   vpsrlw $4, %ymm0, %ymm0
  ; CHECK-NEXT:   vpternlogq $216, {.*}(%rip), %ymm1, %ymm0
  ; CHECK-NEXT:   vpand {.*}(%rip), %ymm0, %ymm0
  ; CHECK-NEXT: retq
  %shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>  
  %lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>  
  %or = or <32 x i8> %shl, %lshr  
  ret <32 x i8> %or
}

define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: splatconstant_rotate_mask_v32i8:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:   vpsllw $4, %ymm0, %ymm1
  ; CHECK-NEXT:   vpsrlw $4, %ymm0, %ymm0
  ; CHECK-NEXT:   vpternlogq $216, {.*}(%rip), %ymm1, %ymm0
  ; CHECK-NEXT:   vpand {.*}(%rip), %ymm0, %ymm0
  ; CHECK-NEXT: retq
  %shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>  
  %lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>  
  %rmask = and <32 x i8> %lshr, <i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55>  
  %lmask = and <32 x i8> %shl, <i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33>  
  %or = or <32 x i8> %lmask, %rmask  
  ret <32 x i8> %or
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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN ObjectContainerBSDArchive.cpp

LINK_LIBS
   lldbCore
   lldbHost
   lldbSymbol
LINK_COMPONENTS
   Support
)

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Maxim Vafin <maxvafin@gmail.com>
Nick Hutchinson <nshutchinson@gmail.com>
Oleksandr Sochka <sasha.sochka@gmail.com>
target triple = "hexagon"

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
    br i1 undef, label %while.end, label %while.body.lr.ph

  while.body.lr.ph: ; preds = %entry
    br label %while.body

  while.body: ; preds = %exit.2, %while.body.lr.ph
    %lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
    switch i32 undef, label %exit [
      i32 1, label %sw.bb.i
      i32 2, label %sw.bb3.i
    ]

  sw.bb.i: ; preds = %while.body
    unreachable

  sw.bb3.i: ; preds = %while.body
    unreachable

  exit: ; preds = %while.body

switch i32 undef, label %exit.2 [  
   i32 1, label %sw.bb.i17  
   i32 2, label %sw.bb3.i20  
]

sw.bb.i17: ; preds = %exit  
%0 = bitcast %struct.0* %lsr.iv to i32*  
%1 = load i32, i32* %0, align 4  
unreachable

sw.bb3.i20: ; preds = %exit  
%2 = bitcast %struct.0* %lsr.iv to i8**  
%3 = load i8*, i8** %2, align 4  
unreachable

exit.2: ; preds = %exit  
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1  
br label %while.body

while.end: ; preds = %entry  
ret void
]

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }  
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes  
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=4 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS________OP M,IS__TUNIT_OPM  
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=3 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_OPM,NOT_CGSCC_NPM,NOT_TUNIT_OPM,IS__TUNIT____,IS________NP M,IS__TUNIT_NPM  
; RUN: opt -attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_NPM,IS__CGSCC____,IS________OP M,IS__CGSCC_OPM  
; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS________NP M,IS__CGSCC_NPM  
; Test that we only promote arguments when the caller/callee have compatible  
; function attrubtes.

 target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {

; NOT_TUNIT_NPM-LABEL: define ({{[^@]+}}) @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnullreadonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define ({{[^@]+}}) @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {

; IS__TUNIT_OPM-LABEL: define ({{[^@]+}}) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_OPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {

; IS__TUNIT_NPM-LABEL: define ({{[^@]+}}) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define ({{[^@]+}}) @avx512_legal512_prefer512_call_avx512_legal512_prefer512
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])

; IS__UNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)

; IS__UNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64

; IS__UNIT_NPM-NEXT: call fastcc void

@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])

; IS__UNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2

; IS__UNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512

; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])

; IS__CGSCC_OPM-NEXT: bb:

; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32

; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32

; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*

; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)

; IS__CGSCC_OPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64

; IS__CGSCC_OPM-NEXT: call fastcc void

@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])

; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2

; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer512

; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])

; IS__CGSCC_NPM-NEXT: bb:

; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32

; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32

; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*

; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)

; IS__CGSCC_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64

; IS__CGSCC_NPM-NEXT: call fastcc void

@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])

; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2

; IS__CGSCC_NPM-NEXT: ret void

; bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

;
; NOT_TUNIT_NPM-LABEL: define
{[[@*+]])}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{[[@*+]])}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:.*]], <8 x i64> [[TMP0:.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {

;
; IS__TUNIT_OPM-LABEL: define {[[@*+]})}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:.*]] = alloca <8 x i64>, align 32

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; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

}; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

};

; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[ARG1_PRIV]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void

; IS_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS_TUNIT_NPM-NEXT:  store <8 x i64> [[ARG1_PRIV]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT:  ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg 
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
    ; IS__TUNIT_OPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
    ; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
    ; IS__TUNIT_OPM-NEXT: bb:
    ; IS__TUNIT_OPM-NEXT: [[TMP:.%*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_OPM-NEXT: [[TMP2:.%*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_OPM-NEXT: [[TMP3:.%*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
    ; IS__TUNIT_OPM-NEXT: [[TMP4:.%*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
    ; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    ; IS__TUNIT_OPM-NEXT: ret void
    ;
    ; IS__TUNIT_NPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
    ; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
    ; IS__TUNIT_NPM-NEXT: bb:
    ; IS__TUNIT_NPM-NEXT: [[TMP:.%*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_NPM-NEXT: [[TMP2:.%*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_NPM-NEXT: [[TMP3:.%*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__TUNIT_NPM-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64> noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
    ; IS__TUNIT_NPM-NEXT: [[TMP4:.%*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
    ; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    ; IS__TUNIT_NPM-NEXT: ret void
    ;
    ; IS__CGSCC_OPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
    ; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
    ; IS__CGSCC_OPM-NEXT: bb:
    ; IS__CGSCC_OPM-NEXT: [[TMP:.%*]] = alloca <8 x i64>, align 32
    ; IS__CGSCC_OPM-NEXT: [[TMP2:.%*]] = alloca <8 x i64>, align 32
    ; IS__CGSCC_OPM-NEXT: [[TMP3:.%*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

; IS__CGSCC_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:  ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])

; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {

; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (8 x i64)* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], 8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void


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; IS__TUNIT_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT:    ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:    ret void
;
; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:    ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

}; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

; NOT_TUNIT_NPM-LABEL: define
{[@@*]+}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{[@@*]+}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {

; IS__TUNIT_OPM-LABEL: define {[@@*]+}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP2]] to i8*  
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {[@@*]+}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.mems.set.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal256_call_avx512_legal512_prefer256(callee_avx512_legal256_call_avx512_legal512_prefer256(<8 x i64>* noalias noclause nontree nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias noclause nontree nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}} @avx512_legal256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* noalias noclause nontree nofree nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.mems.set.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal256_call_avx512_legal512_prefer256(callee_avx512_legal256_call_avx512_legal512_prefer256(<8 x i64>* noalias noclause nontree nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias noclause nontree nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}} @avx512_legal256_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* noalias noclause nontree nofree nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.mems.set.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal256_call_avx512_legal512_prefer256(callee_avx512_legal256_call_avx512_legal512_prefer256(<8 x i64>* noalias noclause nontree nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias noclause nontree nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #2 {

; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofreenonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofreenonnull readonlyalign 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofreenonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* noalias nocapture nofreenonnull readonlyalign 64 dereferenceable(64) [[ARG1:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {

; IS_TUNIT_OPM-LABEL: define {[^@]+} @avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS_TUNIT_OPM-SAME: ( <8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS_TUNIT_OPM-NEXT: bb:
; IS_TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS_TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS_TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nofreenonnull writeonlyalign 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS_TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

; IS__UNIT_OPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__UNIT_OPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__UNIT_OPM-NEXT:    ret void

; IS__UNIT_NPM-LABEL: define ([[^@]+])@avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture writeonly [[ARG:%.*]])

; IS__UNIT_NPM-NEXT: bb:
; IS__UNIT_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__UNIT_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__UNIT_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__UNIT_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__UNIT_NPM-NEXT:    call fastcc void

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

; IS__UNIT_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__UNIT_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__UNIT_NPM-NEXT:    ret void

; IS__CGSCC_OPM-LABEL: define ([[^@]+])@avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])

; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:    call fastcc void

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

; IS__CGSCC_OPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:    ret void

; IS__CGSCC_NPM-LABEL: define ([[^@]+])@avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])

; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:    call fastcc void
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocation nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readable %arg1) #3 {

; NOT_TUNIT_NPM-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree norealid 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
;
; IS__UNIT_OPM-LABEL: define [([^@]+)] @avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__UNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__UNIT_OPM-NEXT: bb:
; IS__UNIT_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__UNIT_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__UNIT_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__UNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__UNIT_OPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__UNIT_OPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__UNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__UNIT_OPM-NEXT: ret void
;
; IS__UNIT_NPM-LABEL: define [([^@]+)] @avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__UNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__UNIT_NPM-NEXT: bb:
; IS__UNIT_NPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__UNIT_NPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__UNIT_NPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__UNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__UNIT_NPM-NEXT: [[TMP0:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__UNIT_NPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP0]])
; IS__UNIT_NPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__UNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__UNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define [([^@]+)] @avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define { [^@]+ } @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP2]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx2_legal256_prefer256_prefer256(8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP4]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
;
; NOT_TUNIT_NPM-LABEL: define
{[^@]+} @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {[^@]+} @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonnull readonly align 64 dereferenceable(64) [[TMP3]])
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonnull readonly align 64 dereferenceable(64) [[TMP3]])
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; RUN: llc -O3 -mtriple=powerpc-unknown-linux-gnu -mcpu=e500 -mattr=spes < %s | FileCheck %s

; PowerPC SPE is a rare in-tree target that has the FP_TO_SINT node marked as Legal.

; Verify that fptosi(42.1) isn't simplified when the rounding mode is unknown.
; Verify that no gross errors happen.
; CHECK-LABEL: @f20
; COMMON: cfdctsiz
define i32 @f20(double %a) strictfp {
  entry:
  %result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1, metadata !"fpexcept.strict")
  strictfp
  ret i32 %result
}

@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)

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==============================================================================
; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,AVX512VL256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,AVX512VL512
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,SKX512

define void @zext256() "min-legal-vector-width"="256" {
    ; VEC256-LABEL: 'zext256'
    ; VEC256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; VEC256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; VEC256-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; VEC256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; VEC256-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; VEC256-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; AVX512VL512-LABEL: 'zext256'
    ; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
define void @zext512() "min-legal-vector-width"="512" {
    ; AVX-LABEL: 'zext512'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    %A = zext <8 x i16> undef to <8 x i64>
    %B = zext <8 x i32> undef to <8 x i64>
    %C = zext <16 x i8> undef to <16 x i32>
    %D = zext <16 x i16> undef to <16 x i32>
    %E = zext <32 x i8> undef to <32 x i16>
    ret void
}

define void @zext512() "min-legal-vector-width"="512" {
    ; AVX512VL512-LABEL: ' AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    ; SKX512-LABEL: 'zext256'
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    %A = zext <8 x i16> undef to <8 x i64>
    %B = zext <8 x i32> undef to <8 x i64>
    %C = zext <16 x i8> undef to <16 x i32>
    %D = zext <16 x i16> undef to <16 x i32>
    %E = zext <32 x i8> undef to <32 x i16>
    ret void
}

define void @zext512() "min-legal-vector-width"="512" {
    ; AVX512VL256-LABEL: 'AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <16 x i32>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    ; %A = zext <8 x i16> undef to <8 x i64>
    %B = zext <8 x i32> undef to <8 x i64>
    %C = zext <16 x i8> undef to <16 x i32>
    %D = zext <16 x i16> undef to <16 x i32>
    %E = zext <32 x i8> undef to <32 x i16>
    ret void
}


%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext256() "min-legal-vector-width"="256" {
  ; VEC256-LABEL: 'sext256'
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; AVX512VL512-LABEL: 'sext256'
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; SKX512-LABEL: 'sext256'
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

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define void @sext() "min-legal-vector-width"="512" {
  ; AVX-LABEL: 'sext512'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ret void
}

define void @sext512() "min-legal-vector-width="512" {
  ; AVX512VL256-LABEL: 'sext512'
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ret void
}

define void @sext512() "min-legal-vector-width="512" {
  ; AVX512VL512-LABEL: 'sext512'
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ret void
}
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
define void @trunc256() "min-legal-vector-width"="256" {  
  ; AVX-LABEL: 'trunc256'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>

define void @trunc256() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'trunc256'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
define i32 @zext256_vXi1() "min-legal-vector-width"="256" {  
  ; AVX-LABEL: 'zext256_vXi1'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = zext <32 x i1> undef to <32 x

AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>

AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V64i16 = zext <64 x i1> undef to <64 x i16>

SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>

SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>

SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i64 = zext <16 x i1> undef to <16 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
define i32 @sext256_vXi1() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'sext256_vXi1'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

  ; AVX512VL256-LABEL: 'sext256_vXi1'
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V64i16 = sext <64 x i1> undef to <64 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V64i16 = sext <64 x i1> undef to <64 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
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i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 = sext <8 x i1> undef to <8 x i64>
%V16i64 = sext <16 x i1> undef to <16 x i64>
%V32i64 = sext <32 x i1> undef to <32 x i64>
%V64i64 = sext <64 x i1> undef to <64 x i64>
%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>
%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>
%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>
ret i32 undef
}

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
; AVX-LABEL: 'trunc_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'trunc_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'trunc_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>
%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>
%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to <16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>
%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>
ret i32 undef
}
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-- $Id: COPYING,v 1.6 2018/01/01 12:00:00 tom Exp $
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1.140 color-convert 1.9.3

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1.141 python 3.7.3 2+deb10u3

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This package was put together by Klee Dienes <klee@debian.org> from sources from ftp.python.org:/pub/python, based on the Debianization by the previous maintainers Bernd S. Brentrup <bsb@uni-muenster.de> and Bruce Perens.

Current maintainer is Matthias Klose <doko@debian.org> until the final 2.3 version is released.

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A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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[This is the first released version of the library GPL. It is
numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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## Commands to generate dependency files 
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)  
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS) 

## Flags for position independent code 
SHAREDLIBCFLAGS = -fPIC  
SHAREDLIBCXXFLAGS = -fPIC  
SHAREDLIBCPPFLAGS = -DPIC 

## Additional flags when building libraries and with threads 
THREADSCPPFLAGS = -D_REENTRANT 
LIBCPPFLAGS = 

## Compiler switch to embed a runtime search path 
LD_RPATH=  
LD_RPATH_PRE=-Wl,-rpath, 

## Compiler switch to embed a library name 
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET)) 

## Shared library options 
LD_SOOPTIONS= -Wl,-Bsymbolic 

## Shared object suffix 
SO = so  
## Non-shared intermediate object suffix 
STATIC_O = ao 

## Compilation rules 
%.S$(STATIC_O): $(srcdir)/%.c 
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $< 
%.o: $(srcdir)/%.c 
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $< 

%.S$(STATIC_O): $(srcdir)/%.cpp 
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $< 
%.o: $(srcdir)/%.cpp 
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\{(\"GEN_DEPS.c\"\) $(<) \| sed \'|s/(\$\*\$)\.o[ ]*:+/\1\.o $@ : /g\'| > $@; \\
\[ -s $@ \] || rm -f $@’

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\{(\"GEN_DEPS.cc\"\) $(<) \| sed \'|s/(\$\*\$)\.o[ ]*:+/\1\.o $@ : /g\'| > $@; \\
\[ -s $@ \] || rm -f $@’

## Versioned libraries rules
%.s(SO).$(SO).$(SO_TARGET_VERSION_MAJOR): %.s(SO).$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@

%.s(SO): %.s(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup
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This package was debianized by Akira TAGOH <tagoh@debian.org> on Thu, 7 Mar 2002 01:05:25 +0900.  

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI) in the Netherlands as a successor of a language called ABC. Guido is Python's principal author, although it includes many contributions from others. The last version released from CWI was Python 1.2. In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI) in Reston, Virginia where he released several versions of the software. Python 1.6 was the last of the versions released by CNRI. In 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. Python 2.0 was the first and only release from BeOpen.com.

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Here is a list of the main contributors to lm-sensors version 3.

* Frodo Looijaard
  Original author of libsensors, sensors-detect, sensors and isadump.
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  Original author of sensord.
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  Many improvements to the libsensors configuration file scanner.
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  Configuration file converter.
  Rewrite of sensors-detect.
  Support for multiple configuration files in libsensors.

1.183 python-setuptools 57.5.0
1.183.1 Available under license:
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1.188 libxslt 1.1.34 4

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1.190 openssh 7.9p1-10+deb10u1

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/* OPENBSD ORIGINAL: sys/sys/poll.h */

#if !defined(HAVE_POLL) && !defined(HAVE_POLL_H)
#ifndef _COMPAT_POLL_H_
#define _COMPAT_POLL_H_

typedef struct pollfd {
    int fd;
    short events;
    short revents;
} pollfd_t;

typedef unsigned int nfds_t;

#define POLLIN 0x0001
#define POLLOUT 0x0004
#define POLLERR 0x0008
#define POLLHUP 0x0100
#define POLLNVAL 0x0200

#if 0
/* the following are currently not implemented */
#define POLLPRI 0x0002
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLWRNORM POLLHUP POLLOUT
#define POLLRDDBAND 0x0080
#define POLLWRBAND 0x0100
#endif

#define INFTIM (-1) /* not standard */

int poll(struct pollfd *, nfds_t, int);
#endif /* !_COMPAT_POLL_H_ */
#endif /* !HAVE_POLL_H */

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* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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*/

#ifndef HAVE_SETRESGID
int	setresgid(gid_t, gid_t, gid_t);
#endif
#ifndef HAVE_SETRESUID
int	setresuid(uid_t, uid_t, uid_t);
#endif
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  *
  */

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

#define _W_INT(w)  (*(int*)&(w))  /* convert union wait to int */
#define WIFEXITED(w)  (!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w)  ((_W_INT(w)) & 0100)
#define WIFSIGNALED(w)  (!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w)  (int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1)
#define WTERMSIG(w)  (int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1)
#define WCOREFLAG 0x80
#define WCOREDUMP(w)  ((_W_INT(w)) & WCOREFLAG)

/* Prototype */
pid_t waitpid(int, int *, int);

#endif /* !_HAVE_WAITPID */
#endif /* _BSD_WAITPID_H */
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1.195 x-text 0.0.0-20170706130353-cfdf022e86b4

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1.196 eslint-plugin-import 2.20.2
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1.197 lib-pq 1.2.0
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1.198 beautiful-soup 4.7.1-1

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1.199 @reduxjs/toolkit 1.6.2

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1.200 immutable 4.0.0-rc.12

1.201 openssl 1.1.1l

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--------------------------------------------------------------------------------

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* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/tests/conftest.py
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest/universal_http/aiohttp.py
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest/universal_http/async_requests.py
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest/universal_http/requests.py
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest/polling/poller.py
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest/service_client.py
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest/polling/async_poller.py

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Metadata-Version: 2.1
Name: msrest
Version: 0.6.21
Summary: AutoRest swagger generator Python client runtime.
Home-page: https://github.com/Azure/msrest-for-python
Author: Microsoft Corporation
License: MIT License
Description: AutoRest: Python Client Runtime

.. image:: https://travis-ci.org/Azure/msrest-for-python.svg?branch=master
   :target: https://travis-ci.org/Azure/msrest-for-python

.. image:: https://codecov.io/gh/azure/msrest-for-python/branch/master/graph/badge.svg
   :target: https://codecov.io/gh/azure/msrest-for-python

Installation
-------------

To install:

.. code-block:: bash

   $ pip install msrest

Release History
----------------

2021-01-26 Version 0.6.21
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**Bug Fixes**

- Fixes `failsafe_deserialize` introduced in `0.6.20` #232

2021-01-25 Version 0.6.20
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**Features**

- Add `failsafe_deserialize` method to the `Deserializer` object. #232
- Serialize `datetime`, `date`, `time`, `timedelta` and `Decimal` correctly when serializing `object` . #224

2020-09-08 Version 0.6.19
+++++++++++++++++++++++++
**Bugfixes**

- Fix serialization of random Model object  #220
- Fix serialization of unicode string in Py2 and object mode  #221

2020-07-27 Version 0.6.18
+++++++++++++++++++++++++

**Features**

- Add support for attributes/text in the same XML node  #218

2020-06-25 Version 0.6.17
+++++++++++++++++++++++++

**Bugfixes**

- Fix XML and discriminator  #214

2020-06-09 Version 0.6.16
+++++++++++++++++++++++++

**Bugfixes**

- Fix XML parsing with namespaces and attributes  #209

**Features**

- Add py.typed for mypy support

2020-06-04 Version 0.6.15
+++++++++++++++++++++++++

**Bugfixes**

- Fix RFC regression introduced in 0.6.14 (RFC parse date are no longer pickable)  #208
- Fix XML parsing with namespaces  #206

Thanks to ivanst0 for the contribution

2020-05-18 Version 0.6.14
+++++++++++++++++++++++++
**Bugfixes**

- Fix "from_dict" in some complex flattening scenario  #204
- Fix RFC date parsing if machine locale is not English  #201

2020-04-07 Version 0.6.13
+++++++++++++++++++++++++

**Bugfixes**

- Fix deserializer and flattening if intermediate node is None  #198
- Fix validation exception message for minimum/maximum checks  #199

2020-04-06 Version 0.6.12
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**Features**

- Add "time" serializer/deserializer  #196

2020-01-30 Version 0.6.11
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**Features**

- XML mode can now be enabled even if the given Model has no XML metadata  #184
- Add Kerberos Authentication  #186
- Improve error message if expected type is dictionary and something else is provided  #188

**Bugfixes**

- Fix comma separated serialization of array in query  #186
- Fix validation of basic types in some complex scenario  #189

Thanks to catatonicprime for the contribution

2019-09-04 Version 0.6.10
+++++++++++++++++++++++++

**Features**

- XML mode now supports OpenAPI additional properties  #174

**Bugfixes**
- Accept "is_xml" kwargs to force XML serialization #178
- Disable XML deserialization if received element is not an ElementTree #178
- A "null" enum deserialize as None, and not "None" anymore #173
- Fix some UTF8 encoding issue in Python 2.7 and XML mode #172

2019-07-24 Version 0.6.9
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**Features**

- Accept extensions of JSON mimetype as valid JSON #167

2019-06-24 Version 0.6.8
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**BugFixes**

- Impossible to serialize XML if model contains UTF8 characters on Python 2.7 #165
- Impossible to deserialize a HTTP response as XML if body contains UTF8 characters on Python 2.7 #165
- Loading a serialized configuration fails with NameError on NoOptionError #162

Thanks to cclauss for the contribution

2019-06-12 Version 0.6.7
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**Features**

- Add DomainCredentials credentials for EventGrid

Thanks to kalyanaj for the contribution

2019-03-21 Version 0.6.6
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**Bugfixes**

- Make 0.6.x series compatible with pyinstaller again
- sdist now includes tests

Thanks to dotlambda for the contribution

2019-03-11 Version 0.6.5
++++++++++++++++++++++++

**Bugfixes**
- Fix list of integers serialization if div is provided #151
- Fix parsing of UTF8 with BOM #145

Thanks to eduardomourar for the contribution

2019-01-09 Version 0.6.4
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**Bugfixes**

- Fix regression on credentials configuration if used outside of Autorest scope #135

2019-01-08 Version 0.6.3
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**Features**

- Updated **experimental** async support. Requires Autorest.Python 4.0.64.

2018-11-19 Version 0.6.2
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**Bugfixes**

- Fix circular dependency in TYPE_CHECKING mode #128

2018-10-15 Version 0.6.1
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**Bugfixes**

- Remove unnecessary verbose "warnings" log #126

2018-10-02 Version 0.6.0
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**Features**

- The environment variable AZURE_HTTP_USER_AGENT, if present, is now injected part of the UserAgent
- New **preview** msrest.universal_http module. Provide tools to generic HTTP management (sync/async, requests/aiohttp, etc.)
  - New **preview** msrest.pipeline implementation:
    - A Pipeline is an ordered list of Policies than can process an HTTP request and response in a generic way.
    - More details in the wiki page about Pipeline: https://github.com/Azure/msrest-for-python/wiki/msrest-0.6.0--Pipeline
  - Adding new attributes to Configuration instance:
- http_logger_policy - Policy to handle HTTP logging
- user_agent_policy - Policy to handle UserAgent
- pipeline - The current pipeline used by the SDK client
- async_pipeline - The current async pipeline used by the async SDK client

- Installing "msrest[async]" now installs the **experimental** async support. Works ONLY for AutoRest.Python 4.0.63.

**Breaking changes**

- The HTTPDriver API introduced in 0.5.0 has been replaced by the Pipeline implementation.

- The following classes have been moved from "msrest.pipeline" to "msrest.universal_http":
  - ClientRedirectPolicy
  - ClientProxies
  - ClientConnection

- The following classes have been moved from "msrest.pipeline" to "msrest.universal_http.requests":
  - ClientRetryPolicy

**Bugfixes**

- Fix "long" on Python 2 if used with the "object" type #121

Thanks to robgolding for the contribution

2018-09-04 Version 0.5.5
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**Bugfixes**

- Fix a serialization issue if additional_properties is declared, and "automatic model" syntax is used ("automatic model" being the ability to pass a dict to command and have the model auto-created) #120

2018-07-12 Version 0.5.4
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**Features**

- Support additionalProperties and XML

**BugFixes**

- Better parse empty node and not string types
- Improve "object" XML parsing
**BugFixes**

- Fix some XML serialization subtle scenarios

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**Features**

- deserialize/from_dict now accepts a content-type parameter to parse XML strings

**Bugfixes**

- Fix some complex XML Swagger definitions.

This release likely breaks already generated XML SDKs, that needs to be regenerated with autorest.python 3.0.58

---

**Bugfixes**

- Lower Accept header overwrite logging message #110
- Fix 'object' type and XML format

Thanks to dharmab for the contribution

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**Disclaimer**

This released is designed to be backward compatible with 0.4.x, but there is too many internal refactoring and new features to continue with 0.4.x versionning

**Features**

- Add XML support
- Add many type hints, and MyPY testing on CI.
- HTTP calls are made through a HTTPDriver API. Only implementation is `requests` for now. This driver API is *not* considered stable
  and you should pin your msrest version if you want to provide a personal implementation.
**Bugfixes**

- Incorrect milliseconds serialization for some datetime object #94

**Deprecation**

That will trigger a DeprecationWarning if an old Autorest generated code is used.

- `_client.add_header` is deprecated, and `config.headers` should be used instead
- `_client.send_formdata` is deprecated, and `_client.put/get/delete/post + _client.send` should be used instead

2018-04-30 Version 0.4.29
+++++++++++++++++++++++++

**Bugfixes**

- Improve `SDKClient.__exit__` to take `exc_details` as optional parameters and not required #93
- `refresh_session` should also use the permanent HTTP session if available #91

2018-04-18 Version 0.4.28
+++++++++++++++++++++++++

**Features**

- `msrest` is now able to keep the "requests.Session" alive for performance. To activate this behavior:

  - Use the final `Client` as a context manager (requires generation with Autorest.Python 3.0.50 at least)
  - Use `client.config.keep_alive = True` and `client.close()` (requires generation with Autorest.Python 3.0.50 at least)
    - Use `client.config.keep_alive = True` and `client._client.close()` (not recommended, but available in old releases of SDK)

- All Authentication classes now define `signed_session` and `refresh_session` with an optional `session` parameter.
  To take benefits of the session improvement, a subclass of Authentication *MUST* add this optional parameter
  and use it if it's not `None`:

    def signed_session(self, session=None):
      session = session or requests.Session()

      # As usual from here.

2018-03-07 Version 0.4.27
+++++++++++++++++++++++++

**Features**
- Disable HTTP log by default (security), add 'enable_http_log' to restore it #86

**BugFixes**

- Fix incorrect date parsing if ms precision is over 6 digits #82

2018-01-30 Version 0.4.26
+++++++++++++++++++++++++

**Features**

- Add TopicCredentials for EventGrid client

2018-01-08 Version 0.4.25
+++++++++++++++++++++++++

**Bugfixes**

- Fix minimal dependency of isodate
- Fix serialisation from dict if datetime provided

2018-01-03 Version 0.4.24
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**Bugfixes**

- Date parsing is now compliant with AutoRest/Swagger 2.0 specification (less lenient)

**Internal optimisation**

- Call that does not return a streamable object are now executed in requests stream mode False (was True whatever the type of the call).
  This should reduce the number of leaked opened session and allow urllib3 to manage connection pooling more efficiently.
  Only clients generated with AutoRest.Python >= 2.1.31 (not impacted otherwise, fully backward compatible)

2017-12-21 Version 0.4.23
+++++++++++++++++++++++++

**Bugfixes**

- Accept to deserialize enum of different type if content string match #75
- Stop failing on deserialization if enum string is unknown. Return the string instead.

**Features**

- Model now accept kwargs in constructor for future kwargs models

2017-12-15 Version 0.4.22
+++++++++++++++++++++++++

**Bugfixes**

- Do not validate additional_properties #73
- Improve validation error if expected type is dict, but actual type is not #73

2017-12-14 Version 0.4.21
+++++++++++++++++++++++++

**Bugfixes**

- Fix additional_properties if Swagger was flatten #72

2017-12-13 Version 0.4.20
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**Features**

- Add support for additional_properties
  
  - By default, all additional_properties are kept.
  - Additional properties are sent to the server only if it was specified in the Swagger, or if "enable_additional_properties_sending" is called on the model we want it. This is a class method that enables it for all instance of this model.

2017-11-20 Version 0.4.19
+++++++++++++++++++++++++

**Features**

- The interpretation of Swagger 2.0 "discriminator" is now lenient. This means for these two scenarios:
  
  - Discriminator value is missing from the received payload
  - Discriminator value is not defined in the Swagger

  Instead of failing with an exception, this now returns the base type for this "discriminator".

  Note that this is not a contradiction of the Swagger 2.0 spec, that specifies "validation SHOULD fail [...] there may exist valid reasons in particular circumstances to ignore a particular item,"
but the full implications must be understood and carefully weighed before choosing a different course."

This cannot be configured for now and is the new default behavior, but can be in the future if needed.

**Bugfixes**

- Optional formdata parameters were raising an exception (#65)
- "application/x-www-form-urlencoded" form was sent using "multipart/form-data". This causes problems if the server does not support "multipart/form-data" (#66)

2017-10-26 Version 0.4.18
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**Features**

- Add ApiKeyCredentials class. This can be used to support OpenAPI ApiKey feature.
- Add CognitiveServicesAuthentication class. Pre-declared ApiKeyCredentials class for Cognitive Services.

2017-10-12 Version 0.4.17
+++++++++++++++++++++++++

This make Authentication classes more consistent:

- OAuthTokenAuthentication is now a subclass of BasicTokenAuthentication (was Authentication)
- BasicTokenAuthentication has now a "set_token" methods that does nothing.

This allows test like "isinstance(o, BasicTokenAuthentication)" to be guaranted that the following attributes exists:

- token
- set_token()
- signed_session()

This means for users of "msrestazure", that they are guaranted that all AD classes somehow inherits from "BasicTokenAuthentication"

2017-10-05 Version 0.4.16
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**Bugfixes**

- Fix regression: accept "set<str>" as a valid "[str]" (#60)

2017-09-28 Version 0.4.15
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**Bugfixes**

- Always log response body (#16)
- Improved exception message if error JSON is Odata v4 (#55)
- Refuse "str" as a valid "[str]" type (#41)
- Better exception handling if input from server is not JSON valid

**Features**

- Add Configuration.session_configuration_callback to customize the requests.Session if necessary (#52)
- Add a flag to Serializer to disable client-side-validation (#51)
- Remove "import requests" from "exceptions.py" for apps that require fast loading time (#23)

Thank you to jayden-at-arista for the contribution

2017-08-23 Version 0.4.14
+++++++++++++++++++++++++

**Bugfixes**

- Fix regression introduced in msrest 0.4.12 - dict syntax with enum modeled as string and enum used

2017-08-22 Version 0.4.13
+++++++++++++++++++++++++

**Bugfixes**

- Fix regression introduced in msrest 0.4.12 - dict syntax using isodate.Duration (#42)

2017-08-21 Version 0.4.12
+++++++++++++++++++++++++

**Features**

- Input is now more lenient
- Model have a "validate" method to check content constraints
- Model have now 4 new methods:
  - "serialize" that gives the RestAPI that will be sent
  - "as_dict" that returns a dict version of the Model. Callbacks are available.
  - "deserialize" the parses the RestAPI JSON into a Model
  - "from_dict" that parses several dict syntax into a Model. Callbacks are available.

More details and examples in the Wiki article on Github:
https://github.com/Azure/msrest-for-python/wiki/msrest-0.4.12---Serialization-change

**Bugfixes**
- Better Enum checking (#38)

2017-06-21 Version 0.4.11
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**Bugfixes**

- Fix incorrect dependency to "requests" 2.14.x, instead of 2.x meant in 0.4.8

2017-06-15 Version 0.4.10
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**Features**

- Add requests hooks to configuration

2017-06-08 Version 0.4.9
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**Bugfixes**

- Accept "null" value for paging array as an empty list and do not raise (#30)

2017-05-22 Version 0.4.8
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**Bugfixes**

- Fix random "pool is closed" error (#29)
- Fix requests dependency to version 2.x, since version 3.x is announced to be breaking.

2017-04-04 Version 0.4.7
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**BugFixes**

- Refactor paging #22:
  - "next" is renamed "advance_page" and "next" returns only 1 element (Python 2 expected behavior)
  - paging objects are now real generator and support the "next()" built-in function without need for "iter()"
  - Raise accurate DeserialisationError on incorrect RestAPI discriminator usage #27
  - Fix discriminator usage of the base class name #27
  - Remove default mutable arguments in Clients #20
  - Fix object comparison in some scenarios #24

2017-03-06 Version 0.4.6
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Open Source Used In Cisco Telemetry Broker On Prem 1.2 1845
**Bugfixes**

- Allow Model sub-classes to be serialized if type is "object"

2017-02-13 Version 0.4.5
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**Bugfixes**

- Fix polymorphic deserialization #11
- Fix regexp validation if \w is used in Python 2.7 #13
- Fix dict deserialization if keys are unicode in Python 2.7

**Improvements**

- Add polymorphic serialisation from dict objects
- Remove chardet and use HTTP charset declaration (fallback to utf8)

2016-09-14 Version 0.4.4
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**Bugfixes**

- Remove paging URL validation, part of fix https://github.com/Azure/autorest/pull/1420

**Disclaimer**

In order to get paging fixes for impacted clients, you need this package and Autorest > 0.17.0 Nightly 20160913

2016-09-01 Version 0.4.3
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**Bugfixes**

- Better exception message (https://github.com/Azure/autorest/pull/1300)

2016-08-15 Version 0.4.2
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**Bugfixes**

- Fix serialization if "object" type contains None (https://github.com/Azure/autorest/issues/1353)

2016-08-08 Version 0.4.1
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**Bugfixes**
- Fix compatibility issues with requests 2.11.0 (https://github.com/Azure/autorest/issues/1337)
- Allow url of ClientRequest to have parameters (https://github.com/Azure/autorest/issues/1217)

2016-05-25 Version 0.4.0
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This version has no bug fixes, but implements new features of Autorest:
- Base64 url type
- unixtime type
- x-ms-enum modelAsString flag

**Behaviour changes**

- Add Platform information in UserAgent
- Needs Autorest > 0.17.0 Nightly 20160525

2016-04-26 Version 0.3.0
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**Bugfixes**

- Read only values are no longer in __init__ or sent to the server (https://github.com/Azure/autorest/pull/959)
- Useless kwarg removed

**Behaviour changes**

- Needs Autorest > 0.16.0 Nightly 20160426

2016-03-25 Version 0.2.0
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**Bugfixes**

- Manage integer enum values (https://github.com/Azure/autorest/pull/879)
- Add missing application/json Accept HTTP header (https://github.com/Azure/azure-sdk-for-python/issues/553)

**Behaviour changes**

- Needs Autorest > 0.16.0 Nightly 20160324

2016-03-21 Version 0.1.3
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**Bugfixes**
- Deserialisation of generic resource if null in JSON (https://github.com/Azure/azure-sdk-for-python/issues/544)

2016-03-14 Version 0.1.2
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**Bugfixes**

- urllib3 side effect (https://github.com/Azure/autorest/issues/824)

2016-03-04 Version 0.1.1
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**Bugfixes**

- Source package corrupted in Pypi (https://github.com/Azure/autorest/issues/799)

2016-03-04 Version 0.1.0
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**Behavioural Changes**

- Removed custom logging set up and configuration. All loggers are now children of the root logger 'msrest' with no pre-defined configurations.
- Replaced _required attribute in Model class with more extensive _validation dict.

**Improvement**

- Removed hierarchy scanning for attribute maps from base Model class - relies on generator to populate attribute maps according to hierarchy.
- Base class Paged now inherits from collections.Iterable.
- Data validation during serialization using custom parameters (e.g. max, min etc).
- Added ValidationError to be raised if invalid data encountered during serialization.

2016-02-29 Version 0.0.3
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**Bugfixes**

- Source package corrupted in Pypi (https://github.com/Azure/autorest/issues/718)

2016-02-19 Version 0.0.2
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**Bugfixes**
- Fixed bug in exception logging before logger configured.

2016-02-19 Version 0.0.1
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- Initial release.

Platform: UNKNOWN
Classifier: Development Status :: 4 - Beta
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.5
Classifier: Programming Language :: Python :: 3.6
Classifier: Programming Language :: Python :: 3.7
Classifier: Programming Language :: Python :: 3.8
Classifier: Programming Language :: Python :: 3.9
Classifier: License :: OSI Approved :: MIT License
Classifier: Topic :: Software Development
Provides-Extra: async

Found in path(s):
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest.egg-info/PKG-INFO
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/PKG-INFO

1.209 libksba 1.5.0-3
1.209.1 Available under license:

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1.214 require-main-filename 2.0.0
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1.215 goprotobuf 1.5.1
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1.216 click 8.0.3

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[metadata]
name = click
version = attr: click.__version__
url = https://palletsprojects.com/p/click/
project_urls =
  Donate = https://palletsprojects.com/donate
  Documentation = https://click.palletsprojects.com/
  Changes = https://click.palletsprojects.com/changes/
  Source Code = https://github.com/pallets/click/
  Issue Tracker = https://github.com/pallets/click/issues/
Twitter = https://twitter.com/PalletsTeam
Chat = https://discord.gg/pallets
license = BSD-3-Clause
license_files = LICENSE.rst
author = Armin Ronacher
author_email = armin.ronacher@active-4.com
maintainer = Pallets
maintainer_email = contact@palletsprojects.com
description = Composable command line interface toolkit
long_description = file: README.rst
long_description_content_type = text/x-rst
classifiers =
Development Status :: 5 - Production/Stable
Intended Audience :: Developers
License :: OSI Approved :: BSD License
Operating System :: OS Independent
Programming Language :: Python

[options]
packages = find:
    package_dir = = src
    include_package_data = true
    python_requires = >= 3.6

[options.packages.find]
    where = src

[tool:pytest]
testpaths = tests
filterwarnings =
    error

[coverage:run]
branch = true
    source =
    click
    tests

[coverage:paths]
    source =
    click
    */site-packages

[flake8]
    select = B, E, F, W, B9, ISC
    ignore =
    E203
    E501
max-line-length = 80
per-file-ignores =
src/click/__init__.py: F401

[mypy]
files = src/click
python_version = 3.6
disallow_subclassing_any = True
disallow_untyped_calls = True
disallow_untyped_defs = True
disallow_incomplete_defs = True
check_untyped_defs = True
no_implicit_optional = True
local_partial_types = True
no_implicit_reexport = True
strict_equality = True
warn_redundant_casts = True
warn_unused_configs = True
warn_unused_ignores = True
warn_return_any = True
warn_unreachable = True

[mypy-colorama.*]
ignore_missing_imports = True

[mypy-importlib_metadata.*]
ignore_missing_imports = True

[egg_info]
tag_build =
tag_date = 0

Found in path(s):
* /opt/cola/permits/1212549943_1634637984.77/0/click-8-0-3-tar-gz/click-8.0.3/setup.cfg
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Metadata-Version: 2.1
Name: click
Version: 8.0.3
Summary: Composable command line interface toolkit
Home-page: https://palletsprojects.com/p/click/
Author: Armin Ronacher
Author-email: armin.ronacher@active-4.com
Maintainer: Pallets
Maintainer-email: contact@palletsprojects.com
License: BSD-3-Clause
Project-URL: Documentation, https://click.palletsprojects.com/
Project-URL: Changes, https://click.palletsprojects.com/changes/
Project-URL: Twitter, https://twitter.com/PalletsTeam
Project-URL: Chat, https://discord.gg/pallets
Platform: UNKNOWN
Click is a Python package for creating beautiful command line interfaces in a composable way with as little code as necessary. It's the "Command Line Interface Creation Kit". It's highly configurable but comes with sensible defaults out of the box.

It aims to make the process of writing command line tools quick and fun while also preventing any frustration caused by the inability to implement an intended CLI API.

Click in three points:

- Arbitrary nesting of commands
- Automatic help page generation
- Supports lazy loading of subcommands at runtime

Installing
----------

Install and update using `pip`:

```
$ pip install -U click
```


A Simple Example
-----------------

```python
import click

@click.command()
```
@click.option("--count", default=1, help="Number of greetings.")
@click.option("--name", prompt="Your name", help="The person to greet.")
def hello(count, name):
    """Simple program that greets NAME for a total of COUNT times."""
    for _ in range(count):
        click.echo(f"Hello, {name}!")

if __name__ == '__main__':
    hello()

.. code-block:: text

    $ python hello.py --count=3
    Your name: Click
    Hello, Click!
    Hello, Click!
    Hello, Click!

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------

The Pallets organization develops and supports Click and other popular packages. In order to grow the community of contributors and users, and allow the maintainers to devote more time to the projects, `please donate today`_.

.. _please donate today: https://palletsprojects.com/donate

Links
-----

- Documentation: https://click.palletsprojects.com/
- Changes: https://click.palletsprojects.com/changes/
- PyPI Releases: https://pypi.org/project/click/
- Source Code: https://github.com/pallets/click
- Issue Tracker: https://github.com/pallets/click/issues
- Website: https://palletsprojects.com/p/click
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- Chat: https://discord.gg/pallets

Found in path(s):
* /opt/cola/permits/1212549943_1634637984.77/0/click-8-0-3-tar-gz/click-8.0.3/src/click.egg-info/PKG-INFO
* /opt/cola/permits/1212549943_1634637984.77/0/click-8-0-3-tar-gz/click-8.0.3/PKG-INFO

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As the userbase of Click grows, more and more major feature requests pop up in Click's bugtracker. As reasonable as it may be for those features to be bundled with Click instead of being a standalone project, many of those requested features are either highly experimental or have unproven practical use, while potentially being a burden to maintain.

This is why click-contrib exists. The GitHub organization is a collection of possibly experimental third-party packages whose featureset does not belong into Click, but also a playground for major features that may be added to Click in the future. It is also meant to coordinate and concentrate effort on writing third-party extensions for Click, and to ease the effort of searching for such extensions. In that sense it could be described as a low-maintenance alternative to extension repositories of other frameworks.

Please note that the quality and stability of those packages may be different than what you expect from Click itself. While published under a common organization, they are still projects separate from Click.

.. _click-contrib: https://github.com/click-contrib/

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1.217 libjson-perl 4.03000-1

1.217.1 Available under license:

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NAME
JSON - JSON (JavaScript Object Notation) encoder/decoder

SYNOPSIS
use JSON; # imports encode_json, decode_json, to_json and from_json.

# simple and fast interfaces (expect/generate UTF-8)
$utf8_encoded_json_text = encode_json $perl_hash_or_arrayref;
$perl_hash_or_arrayref = decode_json $utf8_encoded_json_text;

# OO-interface

$json = JSON->new->allow_nonref;

$json_text   = $json->encode( $perl_scalar );
$perl_scalar = $json->decode( $json_text );

$pretty_printed = $json->pretty->encode( $perl_scalar ); # pretty-printing

VERSION
2.93

DESCRIPTION
This module is a thin wrapper for JSON::XS-compatible modules with a few additional features. All the backend modules convert a Perl data structure to a JSON text as of RFC4627 (which we know is obsolete but we still stick to; see below for an option to support part of RFC7159) and vice versa. This module uses JSON::XS by default, and when JSON::XS is not available, this module falls back on JSON::PP, which is in the Perl core since 5.14. If JSON::PP is not available either, this module then falls back on JSON::backportPP (which is actually JSON::PP in a different .pm file) bundled in the same distribution as this module. You can also explicitly specify to use Cpanel::JSON::XS, a fork of JSON::XS by Reini Urban.

All these backend modules have slight incompatibilities between them, including extra features that other modules don't support, but as long as you use only common features (most important ones are described below), migration from backend to backend should be reasonably easy. For details, see each backend module you use.

CHOOSING BACKEND
This module respects an environmental variable called "PERL_JSON_BACKEND" when it decides a backend module to use. If this environmental variable is not set, it tries to load JSON::XS, and if JSON::XS is not available, it falls back on JSON::PP, and then JSON::backportPP if JSON::PP is not available either.

If you always don't want it to fall back on pure perl modules, set the variable like this ("export" may be "setenv", "set" and the likes, depending on your environment):

> export PERL_JSON_BACKEND=JSON::XS
If you prefer Cpanel::JSON::XS to JSON::XS, then:

```
> export PERL_JSON_BACKEND=Cpanel::JSON::XS,JSON::XS,JSON::PP
```

You may also want to set this variable at the top of your test files, in order not to be bothered with incompatibilities between backends (you need to wrap this in "BEGIN", and set before actually "use"-ing JSON module, as it decides its backend as soon as it's loaded):

```
BEGIN { $ENV{PERL_JSON_BACKEND}='JSON::backportPP'; }
use JSON;
```

**USING OPTIONAL FEATURES**

There are a few options you can set when you "use" this module:

- **-support_by_pp**
  ```
  BEGIN { $ENV{PERL_JSON_BACKEND} = 'JSON::XS' }
  use JSON -support_by_pp;
  ```

  With this option, this module loads its pure perl backend along with its XS backend (if available), and lets the XS backend to watch if you set a flag only JSON::PP supports. When you do, the internal JSON::XS object is replaced with a newly created JSON::PP object with the setting copied from the XS object, so that you can use JSON::PP flags (and its slower "decode"/"encode" methods) from then on. In other words, this is not something that allows you to hook JSON::XS to change its behavior while keeping its speed. JSON::XS and JSON::PP objects are quite different (JSON::XS object is a blessed scalar reference, while JSON::PP object is a blessed hash reference), and can't share their internals.

  To avoid needless overhead (by copying settings), you are advised not to use this option and just to use JSON::PP explicitly when you need JSON::PP features.

- **-convert_blessed_universally**
  ```
  use JSON -convert_blessed_universally;
  ```

  ```
  my $json = JSON->new->allow_nonref->convert_blessed;
  my $object = bless {foo => 'bar'}, 'Foo';
  $json->encode($object); # => {"foo":"bar"}
  ```
JSON::XS-compatible backend modules don't encode blessed objects by default (except for their boolean values, which are typically blessed JSON::PP::Boolean objects). If you need to encode a data structure that may contain objects, you usually need to look into the structure and replace objects with alternative non-blessed values, or enable "convert_blessed" and provide a "TO_JSON" method for each object's (base) class that may be found in the structure, in order to let the methods replace the objects with whatever scalar values the methods return.

If you need to serialise data structures that may contain arbitrary objects, it's probably better to use other serialisers (such as Sereal or Storable for example), but if you do want to use this module for that purpose, "-convert_blessed_universally" option may help, which tweaks "encode" method of the backend to install "UNIVERSAL::TO_JSON" method (locally) before encoding, so that all the objects that don't have their own "TO_JSON" method can fall back on the method in the "UNIVERSAL" namespace. Note that you still need to enable "convert_blessed" flag to actually encode objects in a data structure, and "UNIVERSAL::TO_JSON" method installed by this option only converts blessed hash/array references into their unblessed clone (including private keys/values that are not supposed to be exposed). Other blessed references will be converted into null.

This feature is experimental and may be removed in the future.

-no_export

When you don't want to import functional interfaces from a module, you usually supply "()" to its "use" statement.

use JSON (); # no functional interfaces

If you don't want to import functional interfaces, but you also want to use any of the above options, add "-no_export" to the option list.

    # no functional interfaces, while JSON::PP support is enabled.
    use JSON -support_by_pp, -no_export;

FUNCTIONAL INTERFACE

This section is taken from JSON::XS. "encode_json" and "decode_json" are exported by default.

This module also exports "to_json" and "from_json" for backward
compatibility. These are slower, and may expect/generate different stuff from what "encode_json" and "decode_json" do, depending on their options. It's better just to use Object-Oriented interfaces than using these two functions.

```plaintext
encode_json
   $json_text = encode_json $perl_scalar

Converts the given Perl data structure to a UTF-8 encoded, binary string (that is, the string contains octets only). Croaks on error.

This function call is functionally identical to:

   $json_text = JSON->new->utf8->encode($perl_scalar)

Except being faster.

decode_json
   $perl_scalar = decode_json $json_text

The opposite of "encode_json": expects an UTF-8 (binary) string and tries to parse that as an UTF-8 encoded JSON text, returning the resulting reference. Croaks on error.

This function call is functionally identical to:

   $perl_scalar = JSON->new->utf8->decode($json_text)

Except being faster.

to_json
   $json_text = to_json($perl_scalar[, $optional_hashref])

Converts the given Perl data structure to a Unicode string by default. Croaks on error.

Basically, this function call is functionally identical to:

   $json_text = JSON->new->encode($perl_scalar)

Except being slower.

You can pass an optional hash reference to modify its behavior, but that may change what "to_json" expects/generates (see "ENCODING/CODESET FLAG NOTES" for details).

   $json_text = to_json($perl_scalar, {utf8 => 1, pretty => 1})
from_json
$
perl\_scalar = from\_json(json\_text[, Optional\_hashref])
$

The opposite of "to_json": expects a Unicode string and tries to parse it, returning the resulting reference. Croaks on error.

Basically, this function call is functionally identical to:

$perl\_scalar = JSON->new->decode(json\_text)
$

You can pass an optional hash reference to modify its behavior, but that may change what "from_json" expects/generates (see "ENCODING/CODESET FLAG NOTES" for details).

$perl\_scalar = from\_json(json\_text, {utf8 => 1})
# => JSON->new->utf8(1)->decode(json\_text)

JSON::is_bool
$\is\_boolean = JSON::is\_bool($scalar)
$

Returns true if the passed scalar represents either JSON::true or JSON::false, two constants that act like 1 and 0 respectively and are also used to represent JSON "true" and "false" in Perl strings.

See MAPPING, below, for more information on how JSON values are mapped to Perl.

COMMON OBJECT-ORIENTED INTERFACE
This section is also taken from JSON::XS.

The object oriented interface lets you configure your own encoding or decoding style, within the limits of supported formats.

new
$\json = JSON->new
$

Creates a new JSON::XS-compatible backend object that can be used to de/encode JSON strings. All boolean flags described below are by default *disabled*.

The mutators for flags all return the backend object again and thus calls can be chained:

my $\json = JSON->new->utf8->space\_after->encode([a => 1,2])
=> {"a": [1, 2]}
ascii
   $json = $json->ascii([$enable])

   $enabled = $json->get_ascii

If $enable is true (or missing), then the "encode" method will not
generate characters outside the code range 0..127 (which is
ASCII). Any Unicode characters outside that range will be escaped
using either a single \uXXXX (BMP characters) or a double
\uHHHH\uLLLLL escape sequence, as per RFC4627. The resulting
encoded JSON text can be treated as a native Unicode string, an
ascii-encoded, latin1-encoded or UTF-8 encoded string, or any
other superset of ASCII.

If $enable is false, then the "encode" method will not escape
Unicode characters unless required by the JSON syntax or other
flags. This results in a faster and more compact format.

See also the section *ENCODING/CODESET FLAG NOTES* later in this
document.

The main use for this flag is to produce JSON texts that can be
transmitted over a 7-bit channel, as the encoded JSON texts will
not contain any 8 bit characters.

   JSON->new->ascii(1)->encode([chr 0x10401])
   => ["\ud801\udc01"]

latin1
   $json = $json->latin1([$enable])

   $enabled = $json->get_latin1

If $enable is true (or missing), then the "encode" method will
encode the resulting JSON text as latin1 (or iso-8859-1), escaping
any characters outside the code range 0..255. The resulting string
can be treated as a latin1-encoded JSON text or a native Unicode
string. The "decode" method will not be affected in any way by
this flag, as "decode" by default expects Unicode, which is a
strict superset of latin1.

If $enable is false, then the "encode" method will not escape
Unicode characters unless required by the JSON syntax or other
flags.

See also the section *ENCODING/CODESET FLAG NOTES* later in this
document.
The main use for this flag is efficiently encoding binary data as JSON text, as most octets will not be escaped, resulting in a smaller encoded size. The disadvantage is that the resulting JSON text is encoded in latin1 (and must correctly be treated as such when storing and transferring), a rare encoding for JSON. It is therefore most useful when you want to store data structures known to contain binary data efficiently in files or databases, not when talking to other JSON encoders/decoders.

```
JSON->new->latin1->encode (\"\x{89}\x{abc}\")
=> [\"\x{89}\u0abc\"]  # (perl syntax, U+abc escaped, U+89 not)
```

utf8
```
$json = $json->utf8([$enable])

$enabled = $json->get_utf8
```

If $enable is true (or missing), then the "encode" method will encode the JSON result into UTF-8, as required by many protocols, while the "decode" method expects to be handled an UTF-8-encoded string. Please note that UTF-8-encoded strings do not contain any characters outside the range 0..255, they are thus useful for byte-wise/binary I/O. In future versions, enabling this option might enable autodetection of the UTF-16 and UTF-32 encoding families, as described in RFC4627.

If $enable is false, then the "encode" method will return the JSON string as a (non-encoded) Unicode string, while "decode" expects thus a Unicode string. Any decoding or encoding (e.g. to UTF-8 or UTF-16) needs to be done yourself, e.g. using the Encode module.

See also the section *ENCODING/CODESET FLAG NOTES* later in this document.

Example, output UTF-16BE-encoded JSON:
```
use Encode;
$jsontext = encode "UTF-16BE", JSON->new->encode ($object);
```

Example, decode UTF-32LE-encoded JSON:
```
use Encode;
$object = JSON->new->decode (decode "UTF-32LE", $jsontext);
```

pretty
```
$json = $json->pretty([$enable])
```
This enables (or disables) all of the "indent", "space_before" and "space_after" (and in the future possibly more) flags in one call to generate the most readable (or most compact) form possible.

**indent**

```php
$json = $json->indent([$enable])

$enabled = $json->get_indent
```

If $enable is true (or missing), then the "encode" method will use a multiline format as output, putting every array member or object/hash key-value pair into its own line, indenting them properly.

If $enable is false, no newlines or indenting will be produced, and the resulting JSON text is guaranteed not to contain any "newlines".

This setting has no effect when decoding JSON texts.

**space_before**

```php
$json = $json->space_before([$enable])

$enabled = $json->get_space_before
```

If $enable is true (or missing), then the "encode" method will add an extra optional space before the ":" separating keys from values in JSON objects.

If $enable is false, then the "encode" method will not add any extra space at those places.

This setting has no effect when decoding JSON texts. You will also most likely combine this setting with "space_after".

Example, space_before enabled, space_after and indent disabled:

```
{"key":"value"}
```

**space_after**

```php
$json = $json->space_after([$enable])

$enabled = $json->get_space_after
```

If $enable is true (or missing), then the "encode" method will add an extra optional space after the ":" separating keys from values in JSON objects and extra whitespace after the "," separating key-value pairs and array members.
If $enable is false, then the "encode" method will not add any extra space at those places.

This setting has no effect when decoding JSON texts.

Example, space_before and indent disabled, space_after enabled:

{"key": "value"}

relaxed
$json = $json->relaxed([$enable])

$enabled = $json->get_relaxed

If $enable is true (or missing), then "decode" will accept some extensions to normal JSON syntax (see below). "encode" will not be affected in anyway. *Be aware that this option makes you accept invalid JSON texts as if they were valid!* I suggest only to use this option to parse application-specific files written by humans (configuration files, resource files etc.)

If $enable is false (the default), then "decode" will only accept valid JSON texts.

Currently accepted extensions are:

* list items can have an end-comma

JSON *separates* array elements and key-value pairs with commas. This can be annoying if you write JSON texts manually and want to be able to quickly append elements, so this extension accepts comma at the end of such items not just between them:

[ 1,
  2, \- this comma not normally allowed ]

{
  "k1": "v1",
  "k2": "v2", \- this comma not normally allowed
}

* shell-style '#'-comments

Whenever JSON allows whitespace, shell-style comments are additionally allowed. They are terminated by the first
carriage-return or line-feed character, after which more white-space and comments are allowed.

[ 1, # this comment not allowed in JSON  
   # neither this one...  
]

canonical

$json = $json->canonical([$enable])

$enabled = $json->get_canonical

If $enable is true (or missing), then the "encode" method will output JSON objects by sorting their keys. This is adding a comparatively high overhead.

If $enable is false, then the "encode" method will output key-value pairs in the order Perl stores them (which will likely change between runs of the same script, and can change even within the same run from 5.18 onwards).

This option is useful if you want the same data structure to be encoded as the same JSON text (given the same overall settings). If it is disabled, the same hash might be encoded differently even if contains the same data, as key-value pairs have no inherent ordering in Perl.

This setting has no effect when decoding JSON texts.

This setting has currently no effect on tied hashes.

allow_nonref

$json = $json->allow_nonref([$enable])

$enabled = $json->get_allow_nonref

If $enable is true (or missing), then the "encode" method can convert a non-reference into its corresponding string, number or null JSON value, which is an extension to RFC4627. Likewise, "decode" will accept those JSON values instead of croaking.

If $enable is false, then the "encode" method will croak if it isn't passed an arrayref or hashref, as JSON texts must either be an object or array. Likewise, "decode" will croak if given something that is not a JSON object or array.

Example, encode a Perl scalar as JSON value with enabled
"allow_nonref", resulting in an invalid JSON text:

```perl
JSON->new->allow_nonref->encode ("Hello, World!") => "Hello, World!"
```

allow_unknown
```
$json = $json->allow_unknown ([$enable])

$enabled = $json->get_allow_unknown
```

If $enable is true (or missing), then "encode" will *not* throw an exception when it encounters values it cannot represent in JSON (for example, filehandles) but instead will encode a JSON "null" value. Note that blessed objects are not included here and are handled separately by `allow_nonref`.

If $enable is false (the default), then "encode" will throw an exception when it encounters anything it cannot encode as JSON.

This option does not affect "decode" in any way, and it is recommended to leave it off unless you know your communications partner.

allow_blessed
```
$json = $json->allow_blessed([$enable])

$enabled = $json->get_allow_blessed
```

See "OBJECT SERIALISATION" for details.

If $enable is true (or missing), then the "encode" method will not barf when it encounters a blessed reference that it cannot convert otherwise. Instead, a JSON "null" value is encoded instead of the object.

If $enable is false (the default), then "encode" will throw an exception when it encounters a blessed object that it cannot convert otherwise.

This setting has no effect on "decode".

convert_blessed
```
$json = $json->convert_blessed([$enable])

$enabled = $json->get_convert_blessed
```

See "OBJECT SERIALISATION" for details.
If $enable is true (or missing), then "encode", upon encountering a blessed object, will check for the availability of the "TO_JSON" method on the object's class. If found, it will be called in scalar context and the resulting scalar will be encoded instead of the object.

The "TO_JSON" method may safely call die if it wants. If "TO_JSON" returns other blessed objects, those will be handled in the same way. "TO_JSON" must take care of not causing an endless recursion cycle (== crash) in this case. The name of "TO_JSON" was chosen because other methods called by the Perl core (== not by the user of the object) are usually in upper case letters and to avoid collisions with any "to_json" function or method.

If $enable is false (the default), then "encode" will not consider this type of conversion.

This setting has no effect on "decode".

filter_json_object

$json = $json->filter_json_object([$coderef])

When $coderef is specified, it will be called from "decode" each time it decodes a JSON object. The only argument is a reference to the newly-created hash. If the code references returns a single scalar (which need not be a reference), this value (i.e. a copy of that scalar to avoid aliasing) is inserted into the deserialised data structure. If it returns an empty list (NOTE: *not* "undef", which is a valid scalar), the original deserialised hash will be inserted. This setting can slow down decoding considerably.

When $coderef is omitted or undefined, any existing callback will be removed and "decode" will not change the deserialised hash in any way.

Example, convert all JSON objects into the integer 5:

my $js = JSON->new->filter_json_object(sub { 5 });
# returns [5]
$js->decode ('[{}]'); # the given subroutine takes a hash reference.
# throw an exception because allow_nonref is not enabled
# so a lone 5 is not allowed.
$js->decode ('{"a":1, "b":2}');

filter_json_single_key_object

$json = $json->filter_json_single_key_object($key => $coderef)

Works remotely similar to "filter_json_object", but is only called
for JSON objects having a single key named $key.

This $coderef is called before the one specified via
"filter_json_object", if any. It gets passed the single value in
the JSON object. If it returns a single value, it will be inserted
into the data structure. If it returns nothing (not even "undef"
but the empty list), the callback from "filter_json_object" will
be called next, as if no single-key callback were specified.

If $coderef is omitted or undefined, the corresponding callback
will be disabled. There can only ever be one callback for a given
key.

As this callback gets called less often then the
"filter_json_object" one, decoding speed will not usually suffer
as much. Therefore, single-key objects make excellent targets to
serialise Perl objects into, especially as single-key JSON objects
are as close to the type-tagged value concept as JSON gets (it's
basically an ID/VALUE tuple). Of course, JSON does not support
this in any way, so you need to make sure your data never looks
like a serialised Perl hash.

Typical names for the single object key are "__class_whatever__",
or "$__dollars_are_rarely_used__$" or "ugly_brace_placement",
or even things like "__class_md5sum(classname)___", to reduce the risk
of clashing with real hashes.

Example, decode JSON objects of the form "{ "__widget__" => <id> }
" into the corresponding $WIDGET{<id>} object:

```perl
# return whatever is in $WIDGET[5]:
JSON
  ->new
  ->filter_json_single_key_object (__widget__ => sub {
    $WIDGET{ $_[0] }
  })
  ->decode ('{"__widget__": 5')
```

```
# this can be used with a TO_JSON method in some "widget" class
# for serialisation to json:
sub WidgetBase::TO_JSON {
  my ($self) = @_;

  unless ($self->{id}) {
    $self->{id} = ..get..some..id..
    $WIDGET{$self->{id}} = $self;
  }
```

```
max_depth
$json = $json->max_depth([$maximum_nesting_depth])

$max_depth = $json->get_max_depth

Sets the maximum nesting level (default 512) accepted while encoding or decoding. If a higher nesting level is detected in JSON text or a Perl data structure, then the encoder and decoder will stop and croak at that point.

Nesting level is defined by number of hash- or arrayrefs that the encoder needs to traverse to reach a given point or the number of "{" or "[" characters without their matching closing parenthesis crossed to reach a given character in a string.

Setting the maximum depth to one disallows any nesting, so that ensures that the object is only a single hash/object or array.

If no argument is given, the highest possible setting will be used, which is rarely useful.

max_size
$json = $json->max_size([$maximum_string_size])

$max_size = $json->get_max_size

Set the maximum length a JSON text may have (in bytes) where decoding is being attempted. The default is 0, meaning no limit. When "decode" is called on a string that is longer then this many bytes, it will not attempt to decode the string but throw an exception. This setting has no effect on "encode" (yet).

If no argument is given, the limit check will be deactivated (same as when 0 is specified).

encode
$json_text = $json->encode($perl_scalar)

Converts the given Perl value or data structure to its JSON representation. Croaks on error.

decode
$perl_scalar = $json->decode($json_text)

The opposite of "encode": expects a JSON text and tries to parse
it, returning the resulting simple scalar or reference. Croaks on error.

decode_prefix

($perl_scalar, $characters) = $json->decode_prefix($json_text)

This works like the "decode" method, but instead of raising an exception when there is trailing garbage after the first JSON object, it will silently stop parsing there and return the number of characters consumed so far.

This is useful if your JSON texts are not delimited by an outer protocol and you need to know where the JSON text ends.

    JSON->new->decode_prefix ('[1] the tail')
=> ([1], 3)

ADDITIONAL METHODS

The following methods are for this module only.

backend

$backend = $json->backend

Since 2.92, "backend" method returns an abstract backend module used currently, which should be JSON::Backend::XS (which inherits JSON::XS or Cpanel::JSON::XS), or JSON::Backend::PP (which inherits JSON::PP), not to monkey-patch the actual backend module globally.

If you need to know what is used actually, use "isa", instead of string comparison.

is_xs

$boolean = $json->is_xs

Returns true if the backend inherits JSON::XS or Cpanel::JSON::XS.

is_pp

$boolean = $json->is_pp

Returns true if the backend inherits JSON::PP.

property

$settings = $json->property()

Returns a reference to a hash that holds all the common flag settings.
$json = $json->property('utf8' => 1)
$value = $json->property('utf8') # 1

You can use this to get/set a value of a particular flag.

INCREMENTAL PARSING
This section is also taken from JSON::XS.

In some cases, there is the need for incremental parsing of JSON
texts. While this module always has to keep both JSON text and
resulting Perl data structure in memory at one time, it does allow
you to parse a JSON stream incrementally. It does so by
accumulating text until it has a full JSON object, which it then
can decode. This process is similar to using "decode_prefix" to
see if a full JSON object is available, but is much more efficient
(and can be implemented with a minimum of method calls).

This module will only attempt to parse the JSON text once it is
sure it has enough text to get a decisive result, using a very
simple but truly incremental parser. This means that it sometimes
won't stop as early as the full parser, for example, it doesn't
detect mismatched parentheses. The only thing it guarantees is
that it starts decoding as soon as a syntactically valid JSON text
has been seen. This means you need to set resource limits (e.g.
"max_size") to ensure the parser will stop parsing in the presence
if syntax errors.

The following methods implement this incremental parser.

incr_parse
$json->incr_parse( [$string] ) # void context

$obj_or_undef = $json->incr_parse( [$string] ) # scalar context

@obj_or_empty = $json->incr_parse( [$string] ) # list context

This is the central parsing function. It can both append new text
and extract objects from the stream accumulated so far (both of
these functions are optional).

If $string is given, then this string is appended to the already
existing JSON fragment stored in the $json object.

After that, if the function is called in void context, it will
simply return without doing anything further. This can be used to
add more text in as many chunks as you want.

If the method is called in scalar context, then it will try to
extract exactly *one* JSON object. If that is successful, it will return this object, otherwise it will return "undef". If there is a parse error, this method will croak just as "decode" would do (one can then use "incr_skip" to skip the erroneous part). This is the most common way of using the method.

And finally, in list context, it will try to extract as many objects from the stream as it can find and return them, or the empty list otherwise. For this to work, there must be no separators (other than whitespace) between the JSON objects or arrays, instead they must be concatenated back-to-back. If an error occurs, an exception will be raised as in the scalar context case. Note that in this case, any previously-parsed JSON texts will be lost.

Example: Parse some JSON arrays/objects in a given string and return them.

```perl
my @objs = JSON->new->incr_parse ("[5][7][1,2]");
```

**incr_text**

```perl
$lvalue_string = $json->incr_text
```

This method returns the currently stored JSON fragment as an lvalue, that is, you can manipulate it. This *only* works when a preceding call to "incr_parse" in *scalar context* successfully returned an object. Under all other circumstances you must not call this function (I mean it. although in simple tests it might actually work, it *will* fail under real world conditions). As a special exception, you can also call this method before having parsed anything.

That means you can only use this function to look at or manipulate text before or after complete JSON objects, not while the parser is in the middle of parsing a JSON object.

This function is useful in two cases: a) finding the trailing text after a JSON object or b) parsing multiple JSON objects separated by non-JSON text (such as commas).

**incr_skip**

```perl
$json->incr_skip
```

This will reset the state of the incremental parser and will remove the parsed text from the input buffer so far. This is useful after "incr_parse" died, in which case the input buffer and incremental parser state is left unchanged, to skip the text parsed so far and to reset the parse state.
The difference to "incr_reset" is that only text until the parse error occurred is removed.

incr_reset
   $json->incr_reset

This completely resets the incremental parser, that is, after this call, it will be as if the parser had never parsed anything.

This is useful if you want to repeatedly parse JSON objects and want to ignore any trailing data, which means you have to reset the parser after each successful decode.

MAPPING
Most of this section is also taken from JSON::XS.

This section describes how the backend modules map Perl values to JSON values and vice versa. These mappings are designed to “do the right thing” in most circumstances automatically, preserving round-tripping characteristics (what you put in comes out as something equivalent).

For the more enlightened: note that in the following descriptions, lowercase *perl* refers to the Perl interpreter, while uppercase *Perl* refers to the abstract Perl language itself.

JSON -> PERL
object
   A JSON object becomes a reference to a hash in Perl. No ordering of object keys is preserved (JSON does not preserve object key ordering itself).

array
   A JSON array becomes a reference to an array in Perl.

string
   A JSON string becomes a string scalar in Perl - Unicode codepoints in JSON are represented by the same codepoints in the Perl string, so no manual decoding is necessary.

number
   A JSON number becomes either an integer, numeric (floating point) or string scalar in perl, depending on its range and any fractional parts. On the Perl level, there is no difference between those as Perl handles all the conversion details, but an integer may take slightly less memory and might represent more values exactly than floating point
numbers.

If the number consists of digits only, this module will try to represent it as an integer value. If that fails, it will try to represent it as a numeric (floating point) value if that is possible without loss of precision. Otherwise it will preserve the number as a string value (in which case you lose roundtripping ability, as the JSON number will be re-encoded to a JSON string).

Numbers containing a fractional or exponential part will always be represented as numeric (floating point) values, possibly at a loss of precision (in which case you might lose perfect roundtripping ability, but the JSON number will still be re-encoded as a JSON number).

Note that precision is not accuracy - binary floating point values cannot represent most decimal fractions exactly, and when converting from and to floating point, this module only guarantees precision up to but not including the least significant bit.

true, false
These JSON atoms become "JSON::true" and "JSON::false", respectively. They are overloaded to act almost exactly like the numbers 1 and 0. You can check whether a scalar is a JSON boolean by using the "JSON::is_bool" function.

null
A JSON null atom becomes "undef" in Perl.

shell-style comments ("# *text*")
As a nonstandard extension to the JSON syntax that is enabled by the "relaxed" setting, shell-style comments are allowed. They can start anywhere outside strings and go till the end of the line.

PERL -> JSON
The mapping from Perl to JSON is slightly more difficult, as Perl is a truly typeless language, so we can only guess which JSON type is meant by a Perl value.

hash references
Perl hash references become JSON objects. As there is no inherent ordering in hash keys (or JSON objects), they will usually be encoded in a pseudo-random order. This module can optionally sort the hash keys (determined by the *canonical* flag), so the same data structure will serialise to the same
JSON text (given same settings and version of the same backend), but this incurs a runtime overhead and is only rarely useful, e.g. when you want to compare some JSON text against another for equality.

array references
Perl array references become JSON arrays.

other references
Other unblessed references are generally not allowed and will cause an exception to be thrown, except for references to the integers 0 and 1, which get turned into "false" and "true" atoms in JSON. You can also use "JSON::false" and "JSON::true" to improve readability.

```
encode_json [0,JSON::true]     # yields [false,true]
```

JSON::true, JSON::false, JSON::null
These special values become JSON true and JSON false values, respectively. You can also use "1" and "0" directly if you want.

blessed objects
Blessed objects are not directly representable in JSON, but "JSON::XS" allows various ways of handling objects. See "OBJECT SERIALISATION", below, for details.

simple scalars
Simple Perl scalars (any scalar that is not a reference) are the most difficult objects to encode: this module will encode undefined scalars as JSON "null" values, scalars that have last been used in a string context before encoding as JSON strings, and anything else as number value:

```
# dump as number
encode_json [-3.0e17]      # yields [-3e+17]
my $value = 5; encode_json [$value]  # yields [5]

# used as string, so dump as string
print $value;
encode_json [$value]      # yields ["5"]

# undef becomes null
encode_json [undef]        # yields [null]
```

You can force the type to be a string by stringifying it:
my $x = 3.1;  # some variable containing a number
"$x";         # stringified
$x .= '';     # another, more awkward way to stringify
print $x;     # perl does it for you, too, quite often

You can force the type to be a number by numifying it:

my $x = "3";  # some variable containing a string
$x += 0;     # numify it, ensuring it will be dumped as a number
$x *= 1;     # same thing, the choice is yours.

You can not currently force the type in other, less obscure, ways. Tell me if you need this capability (but don't forget to explain why it's needed :).

Note that numerical precision has the same meaning as under Perl (so binary to decimal conversion follows the same rules as in Perl, which can differ to other languages). Also, your perl interpreter might expose extensions to the floating point numbers of your platform, such as infinities or NaN's - these cannot be represented in JSON, and it is an error to pass those in.

OBJECT SERIALISATION
As for Perl objects, this module only supports a pure JSON representation (without the ability to deserialise the object automatically again).

SERIALISATION
What happens when this module encounters a Perl object depends on the "allow_blessed" and "convert_blessed" settings, which are used in this order:

1. "convert_blessed" is enabled and the object has a "TO_JSON" method.
   In this case, the "TO_JSON" method of the object is invoked in scalar context. It must return a single scalar that can be directly encoded into JSON. This scalar replaces the object in the JSON text.

For example, the following "TO_JSON" method will convert all URI objects to JSON strings when serialised. The fact that these values originally were URI objects is lost.

    sub URI::TO_JSON {
        my ($uri) = @_;
        $uri->as_string
    }
2. "allow_blessed" is enabled.
   The object will be serialised as a JSON null value.

3. none of the above
   If none of the settings are enabled or the respective methods
   are missing, this module throws an exception.

ENCODING/ CODESET FLAG NOTES
This section is taken from JSON::XS.

The interested reader might have seen a number of flags that
signify encodings or codesets - "utf8", "latin1" and "ascii".
There seems to be some confusion on what these do, so here is a
short comparison:

"utf8" controls whether the JSON text created by "encode" (and
expected by "decode") is UTF-8 encoded or not, while "latin1" and
"ascii" only control whether "encode" escapes character values
outside their respective codeset range. Neither of these flags
conflict with each other, although some combinations make less
sense than others.

Care has been taken to make all flags symmetrical with respect to
"encode" and "decode", that is, texts encoded with any combination
of these flag values will be correctly decoded when the same flags
are used - in general, if you use different flag settings while
encoding vs. when decoding you likely have a bug somewhere.

Below comes a verbose discussion of these flags. Note that a
"codeset" is simply an abstract set of character-codepoint pairs,
while an encoding takes those codepoint numbers and *encodes* them, in our case into octets. Unicode is (among other things) a
codeset, UTF-8 is an encoding, and ISO-8859-1 (= latin 1) and
ASCII are both codesets *and* encodings at the same time, which
can be confusing.

"utf8" flag disabled
   When "utf8" is disabled (the default), then "encode"/"decode"
generate and expect Unicode strings, that is, characters with
high ordinal Unicode values (> 255) will be encoded as such
characters, and likewise such characters are decoded as-is, no
changes to them will be done, except "(re-)interpreting" them
as Unicode codepoints or Unicode characters, respectively (to
Perl, these are the same thing in strings unless you do
funny/weird/dumb stuff).

   This is useful when you want to do the encoding yourself (e.g.
when you want to have UTF-16 encoded JSON texts) or when some other layer does the encoding for you (for example, when printing to a terminal using a filehandle that transparently encodes to UTF-8 you certainly do NOT want to UTF-8 encode your data first and have Perl encode it another time).

"utf8" flag enabled
If the "utf8"-flag is enabled, "encode"/"decode" will encode all characters using the corresponding UTF-8 multi-byte sequence, and will expect your input strings to be encoded as UTF-8, that is, no "character" of the input string must have any value > 255, as UTF-8 does not allow that.

The "utf8" flag therefore switches between two modes: disabled means you will get a Unicode string in Perl, enabled means you get an UTF-8 encoded octet/binary string in Perl.

"latin1" or "ascii" flags enabled
With "latin1" (or "ascii") enabled, "encode" will escape characters with ordinal values > 255 (> 127 with "ascii") and encode the remaining characters as specified by the "utf8" flag.

If "utf8" is disabled, then the result is also correctly encoded in those character sets (as both are proper subsets of Unicode, meaning that a Unicode string with all character values < 256 is the same thing as a ISO-8859-1 string, and a Unicode string with all character values < 128 is the same thing as an ASCII string in Perl).

If "utf8" is enabled, you still get a correct UTF-8-encoded string, regardless of these flags, just some more characters will be escaped using "uXXXXX" then before.

Note that ISO-8859-1-*encoded* strings are not compatible with UTF-8 encoding, while ASCII-encoded strings are. That is because the ISO-8859-1 encoding is NOT a subset of UTF-8 (despite the ISO-8859-1 *codeset* being a subset of Unicode), while ASCII is.

Surprisingly, "decode" will ignore these flags and so treat all input values as governed by the "utf8" flag. If it is disabled, this allows you to decode ISO-8859-1- and ASCII-encoded strings, as both strict subsets of Unicode. If it is enabled, you can correctly decode UTF-8 encoded strings.

So neither "latin1" nor "ascii" are incompatible with the "utf8" flag - they only govern when the JSON output engine
escapes a character or not.

The main use for "latin1" is to relatively efficiently store binary data as JSON, at the expense of breaking compatibility with most JSON decoders.

The main use for "ascii" is to force the output to not contain characters with values > 127, which means you can interpret the resulting string as UTF-8, ISO-8859-1, ASCII, KOI8-R or most about any character set and 8-bit-encoding, and still get the same data structure back. This is useful when your channel for JSON transfer is not 8-bit clean or the encoding might be mangled in between (e.g. in mail), and works because ASCII is a proper subset of most 8-bit and multibyte encodings in use in the world.

BACKWARD INCOMPATIBILITY
Since version 2.90, stringification (and string comparison) for "JSON::true" and "JSON::false" has not been overloaded. It shouldn't matter as long as you treat them as boolean values, but a code that expects they are stringified as "true" or "false" doesn't work as you have expected any more.

if (JSON::true eq 'true') {  # now fails
    print "The result is $JSON::true now."; # => The result is 1 now.
}

And now these boolean values don't inherit JSON::Boolean, either. When you need to test a value is a JSON boolean value or not, use "JSON::is_bool" function, instead of testing the value inherits a particular boolean class or not.

BUGS
Please report bugs on backend selection and additional features this module provides to RT or GitHub issues for this module:

https://rt.cpan.org/Public/Dist/Display.html?Queue=JSON
https://github.com/makamaka/JSON/issues

Please report bugs and feature requests on decoding/encoding and boolean behaviors to the author of the backend module you are using.

SEE ALSO
JSON::XS, Cpanel::JSON::XS, JSON::PP for backends.

JSON::MaybeXS, an alternative that prefers Cpanel::JSON::XS.
"RFC4627"(<http://www.ietf.org/rfc/rfc4627.txt>)

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JSON::XS was written by Marc Lehmann <schmorp[at]schmorp.de>

The release of this new version owes to the courtesy of Marc Lehmann.

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Found in path(s):
* /opt/cola/permits/1228286751_1637032133.21/0/libjson-perl-4-03000-org-1-tar-gz/JSON-4.03/README
No license file was found, but licenses were detected in source scan.

=head1 NAME
JSON - JSON (JavaScript Object Notation) encoder/decoder

=head1 SYNOPSIS
use JSON; # imports encode_json, decode_json, to_json and from_json.

# simple and fast interfaces (expect/generate UTF-8)
$utf8_encoded_json_text = encode_json $perl_hash_or_arrayref;
$perl_hash_or_arrayref = decode_json $utf8_encoded_json_text;

# OO-interface
$json = JSON->new->allow_nonref;

$json_text   = $json->encode( $perl_scalar );
$perl_scalar = $json->decode( $json_text );

$pretty_printed = $json->pretty->encode( $perl_scalar ); # pretty-printing

=head1 VERSION
4.02

=head1 DESCRIPTION
This module is a thin wrapper for L<JSON::XS>-compatible modules with a few additional features. All the backend modules convert a Perl data structure to a JSON text and vice versa. This module uses L<JSON::XS> by default, and when JSON::XS is not available, falls back on L<JSON::PP>, which is in the Perl core since 5.14. If JSON::PP is not available either, this module then falls back on JSON::backportPP (which is actually JSON::PP in a different .pm file) bundled in the same distribution as this module. You can also explicitly specify to use L<Cpanel::JSON::XS>, a fork of JSON::XS by Reini Urban.

All these backend modules have slight incompatibilities between them, including extra features that other modules don't support, but as long as you use only common features (most important ones are described below), migration from backend to backend should be reasonably easy. For details, see each backend module you use.

=head1 CHOOSING BACKEND

This module respects an environmental variable called C<PERL_JSON_BACKEND> when it decides a backend module to use. If this environmental variable is not set, it tries to load JSON::XS, and if JSON::XS is not available, it falls back on JSON::PP, and then JSON::backportPP if JSON::PP is not available either.

If you always don't want it to fall back on pure perl modules, set the variable like this (C<export> may be C<setenv>, C<set> and the likes, depending on your environment):

> export PERL_JSON_BACKEND=JSON::XS

If you prefer Cpanel::JSON::XS to JSON::XS, then:

> export PERL_JSON_BACKEND=Cpanel::JSON::XS,JSON::XS,JSON::PP

You may also want to set this variable at the top of your test files, in order not to be bothered with incompatibilities between backends (you need to wrap this in C<BEGIN>, and set before actually C<use>-ing JSON module, as it decides its backend as soon as it's loaded):

BEGIN { $ENV{PERL_JSON_BACKEND}='JSON::backportPP'; }
use JSON;

=head1 USING OPTIONAL FEATURES

There are a few options you can set when you C<use> this module. These historical options are only kept for backward compatibility, and should not be used in a new application.
with this option, this module loads its pure perl backend along with its XS backend (if available), and lets the XS backend to watch if you set a flag only JSON::PP supports. When you do, the internal JSON::XS object is replaced with a newly created JSON::PP object with the setting copied from the XS object, so that you can use JSON::PP flags (and its slower C<decode>/C<encode> methods) from then on. In other words, this is not something that allows you to hook JSON::XS to change its behavior while keeping its speed. JSON::XS and JSON::PP objects are quite different (JSON::XS object is a blessed scalar reference, while JSON::PP object is a blessed hash reference), and can't share their internals.

To avoid needless overhead (by copying settings), you are advised not to use this option and just to use JSON::PP explicitly when you need JSON::PP features.

If you need to serialise data structures that may contain arbitrary objects, it's probably better to use other serialisers (such as L<Sereal> or L<Storable> for example), but if you do want to use
this module for that purpose, C<-convert_blessed_universally> option may help, which tweaks C<encode> method of the backend to install C<UNIVERSAL::TO_JSON> method (locally) before encoding, so that all the objects that don't have their own C<TO_JSON> method can fall back on the method in the C<UNIVERSAL> namespace. Note that you still need to enable C<convert_blessed> flag to actually encode objects in a data structure, and C<UNIVERSAL::TO_JSON> method installed by this option only converts blessed hash/array references into their unblessed clone (including private keys/values that are not supposed to be exposed). Other blessed references will be converted into null.

This feature is experimental and may be removed in the future.

=item -no_export

When you don't want to import functional interfaces from a module, you usually supply C<()> to its C<use> statement.

    use JSON (); # no functional interfaces

If you don't want to import functional interfaces, but you also want to use any of the above options, add C<-no_export> to the option list.

    # no functional interfaces, while JSON::PP support is enabled.
    use JSON -support_by_pp, -no_export;

=back

=head1 FUNCTIONAL INTERFACE

This section is taken from JSON::XS. C<encode_json> and C<decode_json> are exported by default.

This module also exports C<to_json> and C<from_json> for backward compatibility. These are slower, and may expect/generate different stuff from what C<encode_json> and C<decode_json> do, depending on their options. It's better just to use Object-Oriented interfaces than using these two functions.

=head2 encode_json

    $json_text = encode_json $perl_scalar

Converts the given Perl data structure to a UTF-8 encoded, binary string (that is, the string contains octets only). Croaks on error.

This function call is functionally identical to:
\$json_text = JSON->new->utf8->encode($perl_scalar)

Except being faster.

=head2 decode_json

\$perl_scalar = decode_json \$json_text

The opposite of C<encode_json>: expects an UTF-8 (binary) string and tries to parse that as an UTF-8 encoded JSON text, returning the resulting reference. Croaks on error.

This function call is functionally identical to:

\$perl_scalar = JSON->new->utf8->decode($json_text)

Except being faster.

=head2 to_json

\$json_text = to_json($perl_scalar[, $optional_hashref])

Converts the given Perl data structure to a Unicode string by default. Croaks on error.

Basically, this function call is functionally identical to:

\$json_text = JSON->new->encode($perl_scalar)

Except being slower.

You can pass an optional hash reference to modify its behavior, but that may change what C<to_json> expects/generates (see C<ENCODING/CODESET FLAG NOTES> for details).

\$json_text = to_json($perl_scalar, \{utf8 => 1, pretty => 1\})
# => JSON->new->utf8(1)->pretty(1)->encode($perl_scalar)

=head2 from_json

\$perl_scalar = from_json($json_text[, $optional_hashref])

The opposite of C<to_json>: expects a Unicode string and tries to parse it, returning the resulting reference. Croaks on error.

Basically, this function call is functionally identical to:
$perl_scalar = JSON->new->decode($json_text)

You can pass an optional hash reference to modify its behavior, but that may change what C<from_json> expects/generates (see C<ENCODING/CODESET FLAG NOTES> for details).

$perl_scalar = from_json($json_text, {utf8 => 1})
# => JSON->new->utf8(1)->decode($json_text)

=head2 JSON::is_bool

$is_boolean = JSON::is_bool($scalar)

Returns true if the passed scalar represents either JSON::true or JSON::false, two constants that act like C<1> and C<0> respectively and are also used to represent JSON C<true> and C<false> in Perl strings.

See L<MAPPING>, below, for more information on how JSON values are mapped to Perl.

=head1 COMMON OBJECT-ORIENTED INTERFACE

This section is also taken from JSON::XS.

The object oriented interface lets you configure your own encoding or decoding style, within the limits of supported formats.

=head2 new

$json = JSON->new

Creates a new JSON::XS-compatible backend object that can be used to de/encode JSON strings. All boolean flags described below are by default I<disabled> (with the exception of C<allow_nonref>, which defaults to I<enabled> since version C<4.0>).

The mutators for flags all return the backend object again and thus calls can be chained:

my $json = JSON->new->utf8->space_after->encode({a => [1,2]})
=> {'a': [1, 2]}

=head2 ascii

$json = $json->ascii([$enable])
$enabled = $json->get_ascii

$enabled = $json->get_ascii

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If \texttt{\$enable} is true (or missing), then the \texttt{\$encode} method will not generate characters outside the code range \texttt{C<0..127>} (which is ASCII). Any Unicode characters outside that range will be escaped using either a single \texttt{\uXXXX} (BMP characters) or a double \texttt{\uHHHH\uLLLLL} escape sequence, as per RFC4627. The resulting encoded JSON text can be treated as a native Unicode string, an ascii-encoded, latin1-encoded or UTF-8 encoded string, or any other superset of ASCII.

If \texttt{\$enable} is false, then the \texttt{\$encode} method will not escape Unicode characters unless required by the JSON syntax or other flags. This results in a faster and more compact format.

See also the section \texttt{ENCODING/CODESET FLAG NOTES} later in this document.

The main use for this flag is to produce JSON texts that can be transmitted over a 7-bit channel, as the encoded JSON texts will not contain any 8 bit characters.

\begin{verbatim}
JSON->new->ascii(1)->encode([chr 0x10401])
=> ["\ud801\udc01"]
=head2 latin1

$json = $json->latin1([$enable])

$enabled = $json->get_latin1
\end{verbatim}

If \texttt{\$enable} is true (or missing), then the \texttt{\$encode} method will encode the resulting JSON text as latin1 (or iso-8859-1), escaping any characters outside the code range \texttt{C<0..255>}. The resulting string can be treated as a latin1-encoded JSON text or a native Unicode string. The \texttt{\$decode} method will not be affected in any way by this flag, as \texttt{\$decode} by default expects Unicode, which is a strict superset of latin1.

If \texttt{\$enable} is false, then the \texttt{\$encode} method will not escape Unicode characters unless required by the JSON syntax or other flags.

See also the section \texttt{ENCODING/CODESET FLAG NOTES} later in this document.

The main use for this flag is efficiently encoding binary data as JSON text, as most octets will not be escaped, resulting in a smaller encoded size. The disadvantage is that the resulting JSON text is encoded in latin1 (and must correctly be treated as such when storing and transferring), a rare encoding for JSON. It is therefore most useful when you want to store data structures known to contain binary data efficiently in files or databases, not when talking to other JSON encoders/decoders.

\begin{verbatim}
JSON->new->latin1->encode ("\x{89}\x{abc}")
\end{verbatim}
If `$enable` is true (or missing), then the `encode` method will encode the JSON result into UTF-8, as required by many protocols, while the `decode` method expects to be handled an UTF-8-encoded string. Please note that UTF-8-encoded strings do not contain any characters outside the range C<0..255>, they are thus useful for bytewise/binary I/O. In future versions, enabling this option might enable autodetection of the UTF-16 and UTF-32 encoding families, as described in RFC4627.

If `$enable` is false, then the `encode` method will return the JSON string as a (non-encoded) Unicode string, while `decode` expects thus a Unicode string. Any decoding or encoding (e.g. to UTF-8 or UTF-16) needs to be done yourself, e.g. using the Encode module.

See also the section I<ENCODING/CODESET FLAG NOTES> later in this document.

Example, output UTF-16BE-encoded JSON:

```perl
use Encode;
$jsoncontext = encode "UTF-16BE", JSON->new->encode ($object);
```

Example, decode UTF-32LE-encoded JSON:

```perl
use Encode;
$object = JSON->new->decode (decode "UTF-32LE", $jsoncontext);
```

This enables (or disables) all of the `indent`, `space_before` and `space_after` (and in the future possibly more) flags in one call to generate the most readable (or most compact) form possible.

If `$enable` is true (or missing), then the `encode` method will use a multiline
format as output, putting every array member or object/hash key-value pair into its own line, indenting them properly.

If C<$enable> is false, no newlines or indenting will be produced, and the resulting JSON text is guaranteed not to contain any C<newlines>.

This setting has no effect when decoding JSON texts.

=head2 space_before

$json = $json->space_before([$enable])

$enabled = $json->get_space_before

If C<$enable> is true (or missing), then the C<encode> method will add an extra optional space before the C<:> separating keys from values in JSON objects.

If C<$enable> is false, then the C<encode> method will not add any extra space at those places.

This setting has no effect when decoding JSON texts. You will also most likely combine this setting with C<space_after>.

Example, space_before enabled, space_after and indent disabled:

{"key": "value"}

=head2 space_after

$json = $json->space_after([$enable])

$enabled = $json->get_space_after

If C<$enable> is true (or missing), then the C<encode> method will add an extra optional space after the C<:> separating keys from values in JSON objects and extra whitespace after the C<,> separating key-value pairs and array members.

If C<$enable> is false, then the C<encode> method will not add any extra space at those places.

This setting has no effect when decoding JSON texts.

Example, space_before and indent disabled, space_after enabled:

{"key": "value"}

=head2 relaxed
$json = $json->relaxed([$enable])

$enabled = $json->get_relaxed

If C<$enable> is true (or missing), then C<decode> will accept some extensions to normal JSON syntax (see below). C<encode> will not be affected in any way. I<Be aware that this option makes you accept invalid JSON texts as if they were valid!>. I suggest only to use this option to parse application-specific files written by humans (configuration files, resource files etc.)

If C<$enable> is false (the default), then C<decode> will only accept valid JSON texts.

Currently accepted extensions are:

=over 4

=item * list items can have an end-comma

JSON I<separates> array elements and key-value pairs with commas. This can be annoying if you write JSON texts manually and want to be able to quickly append elements, so this extension accepts comma at the end of such items not just between them:

[
  1,
  2, <- this comma not normally allowed
]

{
  "k1": "v1",
  "k2": "v2", <- this comma not normally allowed
}

=item * shell-style '#'-comments

Whenever JSON allows whitespace, shell-style comments are additionally allowed. They are terminated by the first carriage-return or line-feed character, after which more white-space and comments are allowed.

[
  1, # this comment not allowed in JSON
    # neither this one...
]

=back
=head2 canonical

$json = $json->canonical([$enable])

$enabled = $json->getcanonical

If C<$enable> is true (or missing), then the C<encode> method will output JSON objects by sorting their keys. This is adding a comparatively high overhead.

If C<$enable> is false, then the C<encode> method will output key-value pairs in the order Perl stores them (which will likely change between runs of the same script, and can change even within the same run from 5.18 onwards).

This option is useful if you want the same data structure to be encoded as the same JSON text (given the same overall settings). If it is disabled, the same hash might be encoded differently even if contains the same data, as key-value pairs have no inherent ordering in Perl.

This setting has no effect when decoding JSON texts.

This setting has currently no effect on tied hashes.

=head2 allow_nonref

$json = $json->allow_nonref([$enable])

$enabled = $json->get_allow_nonref

Unlike other boolean options, this option is enabled by default beginning with version C<4.0>.

If C<$enable> is true (or missing), then the C<encode> method can convert a non-reference into its corresponding string, number or null JSON value, which is an extension to RFC4627. Likewise, C<decode> will accept those JSON values instead of croaking.

If C<$enable> is false, then the C<encode> method will croak if it isn't passed an arrayref or hashref, as JSON texts must either be an object or array. Likewise, C<decode> will croak if given something that is not a JSON object or array.

Example, encode a Perl scalar as JSON value with enabled C<allow_nonref>, resulting in an invalid JSON text:

JSON->new->allow_nonref->encode ("Hello, World!")
=> "Hello, World!"
=head2 allow_unknown

$json = $json->allow_unknown([$enable])

$enabled = $json->get_allow_unknown

If C<$enable> is true (or missing), then C<encode> will not throw an exception when it encounters values it cannot represent in JSON (for example, filehandles) but instead will encode a JSON C/null> value. Note that blessed objects are not included here and are handled separately by c<allow_blessed>.

If C<$enable> is false (the default), then C<encode> will throw an exception when it encounters anything it cannot encode as JSON.

This option does not affect C<decode> in any way, and it is recommended to leave it off unless you know your communications partner.

=head2 allow_blessed

$json = $json->allow_blessed([$enable])

$enabled = $json->get_allow_blessed

See L<OBJECT SERIALISATION> for details.

If C<$enable> is true (or missing), then the C<encode> method will not barf when it encounters a blessed reference that it cannot convert otherwise. Instead, a JSON C/null> value is encoded instead of the object.

If C<$enable> is false (the default), then C<encode> will throw an exception when it encounters a blessed object that it cannot convert otherwise.

This setting has no effect on C<decode>.

=head2 convert_blessed

$json = $json->convert_blessed([$enable])

$enabled = $json->get_convert_blessed

See L<OBJECT SERIALISATION> for details.

If C<$enable> is true (or missing), then C<encode>, upon encountering a blessed object, will check for the availability of the C<TO_JSON> method on the object's class. If found, it will be called in scalar context and the resulting scalar will be encoded instead of the object.
The C<TO_JSON> method may safely call die if it wants. If C<TO_JSON> returns other blessed objects, those will be handled in the same way. C<TO_JSON> must take care of not causing an endless recursion cycle (== crash) in this case. The name of C<TO_JSON> was chosen because other methods called by the Perl core (== not by the user of the object) are usually in upper case letters and to avoid collisions with any C<to_json> function or method.

If C<$enable> is false (the default), then C<encode> will not consider this type of conversion.

This setting has no effect on C<decode>.

=head2 allow_tags (since version 3.0)

$json = $json->allow_tags([$enable])

$enabled = $json->get_allow_tags

See L<Object Serialisation> for details.

If C<$enable> is true (or missing), then C<encode>, upon encountering a blessed object, will check for the availability of the C<FREEZE> method on the object's class. If found, it will be used to serialise the object into a nonstandard tagged JSON value (that JSON decoders cannot decode).

It also causes C<decode> to parse such tagged JSON values and deserialise them via a call to the C<THAW> method.

If C<$enable> is false (the default), then C<encode> will not consider this type of conversion, and tagged JSON values will cause a parse error in C<decode>, as if tags were not part of the grammar.

=head2 boolean_values (since version 4.0)

$json->boolean_values([$false, $true])

($false, $true) = $json->get_boolean_values

By default, JSON booleans will be decoded as overloaded C<$JSON::false> and C<$JSON::true> objects.

With this method you can specify your own boolean values for decoding - on decode, JSON C<$false> will be decoded as a copy of C<$false>, and JSON C<$true> will be decoded as C<$true> ("copy" here is the same thing as assigning a value to another variable, i.e. C<$copy = $false>).
This is useful when you want to pass a decoded data structure directly to other serialisers like YAML, Data::MessagePack and so on.

Note that this works only when you \<decode\>. You can set incompatible boolean objects (like L\<boolean\>), but when you \<encode\> a data structure with such boolean objects, you still need to enable \<convert_blessed\> (and add a C\<TO_JSON\> method if necessary).

Calling this method without any arguments will reset the booleans to their default values.

\<get_boolean_values\> will return both \<$false\> and \<$true\> values, or the empty list when they are set to the default.

=head2 filter_json_object

\$
json = \$json->filter_json_object([\$coderef])

When \<$coderef\> is specified, it will be called from \<decode\> each time it decodes a JSON object. The only argument is a reference to the newly-created hash. If the code references returns a single scalar (which need not be a reference), this value (or rather a copy of it) is inserted into the deserialised data structure. If it returns an empty list (NOTE: I<not> \<$undef\>, which is a valid scalar), the original deserialised hash will be inserted. This setting can slow down decoding considerably.

When \<$coderef\> is omitted or undefined, any existing callback will be removed and \<decode\> will not change the deserialised hash in any way.

Example, convert all JSON objects into the integer 5:

my \$js = JSON->new->filter_json_object(sub { 5 });
# returns [5]
\$js->decode('[{}]');
# returns 5
\$js->decode('{"a":1, "b":2}');

=head2 filter_json_single_key_object

\$
json = \$json->filter_json_single_key_object($key => \$coderef)

Works remotely similar to \<filter_json_object\>, but is only called for JSON objects having a single key named \<$key\>.

This \<$coderef\> is called before the one specified via \<filter_json_object\>, if any. It gets passed the single value in the JSON
object. If it returns a single value, it will be inserted into the data structure. If it returns nothing (not even C<undef> but the empty list), the callback from C<filter_json_object> will be called next, as if no single-key callback were specified.

If C<$coderef> is omitted or undefined, the corresponding callback will be disabled. There can only ever be one callback for a given key.

As this callback gets called less often then the C<filter_json_object> one, decoding speed will not usually suffer as much. Therefore, single-key objects make excellent targets to serialise Perl objects into, especially as single-key JSON objects are as close to the type-tagged value concept as JSON gets (it's basically an ID/VALUE tuple). Of course, JSON does not support this in any way, so you need to make sure your data never looks like a serialised Perl hash.

Typical names for the single object key are C<__class_whatever__>, or C<$__dollars_are_rarely_used__$> or C<}ugly_brace_placement>, or even things like C<__class_md5sum(classname)__>, to reduce the risk of clashing with real hashes.

Example, decode JSON objects of the form C<< { "__widget__" => <id> } >> into the corresponding C<< $WIDGET{<id>} >> object:

# return whatever is in $WIDGET{5}:

JSON
 ->new
 ->filter_json_single_key_object (__widget__ => sub {
     $WIDGET{ $_[0] }
 })
 -decode ('"__widget__": 5')

# this can be used with a TO_JSON method in some "widget" class
# for serialisation to json:

sub WidgetBase::TO_JSON {
    my ($self) = @_;

    unless ($self->{id}) {
        $self->{id} = ..get..some..id.;
        $WIDGET[$self->{id}] = $self;
    }

    { __widget__ => $self->{id} }
}

=head2 max_depth

$json = $json->max_depth([$maximum_nesting_depth])
$max_depth = $json->get_max_depth

Sets the maximum nesting level (default C<512>) accepted while encoding or decoding. If a higher nesting level is detected in JSON text or a Perl data structure, then the encoder and decoder will stop and croak at that point.

Nesting level is defined by number of hash- or arrayrefs that the encoder needs to traverse to reach a given point or the number of C<{> or C<]> characters without their matching closing parenthesis crossed to reach a given character in a string.

Setting the maximum depth to one disallows any nesting, so that ensures that the object is only a single hash/object or array.

If no argument is given, the highest possible setting will be used, which is rarely useful.

See L<JSON::XS/SECURITY CONSIDERATIONS> for more info on why this is useful.

=head2 max_size

$json = $json->max_size([$maximum_string_size])

$max_size = $json->get_max_size

Set the maximum length a JSON text may have (in bytes) where decoding is being attempted. The default is C<0>, meaning no limit. When C<decode> is called on a string that is longer then this many bytes, it will not attempt to decode the string but throw an exception. This setting has no effect on C<encode> (yet).

If no argument is given, the limit check will be deactivated (same as when C<0> is specified).

See L<JSON::XS/SECURITY CONSIDERATIONS> for more info on why this is useful.

=head2 encode

$json_text = $json->encode($perl_scalar)

Converts the given Perl value or data structure to its JSON representation. Croaks on error.

=head2 decode

$perl_scalar = $json->decode($json_text)
The opposite of C<encode>: expects a JSON text and tries to parse it, returning the resulting simple scalar or reference. Croaks on error.

=head2 decode_prefix

($perl_scalar, $characters) = $json->decode_prefix($json_text)

This works like the C<decode> method, but instead of raising an exception when there is trailing garbage after the first JSON object, it will silently stop parsing there and return the number of characters consumed so far.

This is useful if your JSON texts are not delimited by an outer protocol and you need to know where the JSON text ends.

   JSON->new->decode_prefix ("[1] the tail")
   => ([1], 3)

=head1 ADDITIONAL METHODS

The following methods are for this module only.

=head2 backend

   $backend = $json->backend

Since 2.92, C<backend> method returns an abstract backend module used currently, which should be JSON::Backend::XS (which inherits JSON::XS or Cpanel::JSON::XS), or JSON::Backend::PP (which inherits JSON::PP), not to monkey-patch the actual backend module globally.

If you need to know what is used actually, use C<isa>, instead of string comparison.

=head2 is_xs

   $boolean = $json->is_xs

Returns true if the backend inherits JSON::XS or Cpanel::JSON::XS.

=head2 is_pp

   $boolean = $json->is_pp

Returns true if the backend inherits JSON::PP.
$settings = $json->property()

Returns a reference to a hash that holds all the common flag settings.

$json = $json->property('utf8' => 1)
$value = $json->property('utf8') # 1

You can use this to get/set a value of a particular flag.

=head2 boolean

$boolean_object = JSON->boolean($scalar)

Returns $JSON::true if $scalar contains a true value, $JSON::false otherwise. You can use this as a full-qualified function (C<JSON::boolean($scalar)>).

=head1 INCREMENTAL PARSING

This section is also taken from JSON::XS.

In some cases, there is the need for incremental parsing of JSON texts. While this module always has to keep both JSON text and resulting Perl data structure in memory at one time, it does allow you to parse a JSON stream incrementally. It does so by accumulating text until it has a full JSON object, which it then can decode. This process is similar to using C<decode_prefix> to see if a full JSON object is available, but is much more efficient (and can be implemented with a minimum of method calls).

This module will only attempt to parse the JSON text once it is sure it has enough text to get a decisive result, using a very simple but truly incremental parser. This means that it sometimes won't stop as early as the full parser, for example, it doesn't detect mismatched parentheses. The only thing it guarantees is that it starts decoding as soon as a syntactically valid JSON text has been seen. This means you need to set resource limits (e.g. C<max_size>) to ensure the parser will stop parsing in the presence if syntax errors.

The following methods implement this incremental parser.

=head2 incr_parse

$json->incr_parse( [$string] ) # void context

$obj_or_undef = $json->incr_parse( [$string] ) # scalar context

@obj_or_empty = $json->incr_parse( [$string] ) # list context
This is the central parsing function. It can both append new text and extract objects from the stream accumulated so far (both of these functions are optional).

If C<$string> is given, then this string is appended to the already existing JSON fragment stored in the C<$json> object.

After that, if the function is called in void context, it will simply return without doing anything further. This can be used to add more text in as many chunks as you want.

If the method is called in scalar context, then it will try to extract exactly I<one> JSON object. If that is successful, it will return this object, otherwise it will return C<undef>. If there is a parse error, this method will croak just as C<decode> would do (one can then use C<incr_skip> to skip the erroneous part). This is the most common way of using the method.

And finally, in list context, it will try to extract as many objects from the stream as it can find and return them, or the empty list otherwise. For this to work, there must be no separators (other than whitespace) between the JSON objects or arrays, instead they must be concatenated back-to-back. If an error occurs, an exception will be raised as in the scalar context case. Note that in this case, any previously-parsed JSON texts will be lost.

Example: Parse some JSON arrays/objects in a given string and return them.

    my @objs = JSON->new->incr_parse ("[5][7][1,2]");

=head2 incr_text

This method returns the currently stored JSON fragment as an lvalue, that is, you can manipulate it. This I<only> works when a preceding call to C<incr_parse> in I<scalar context> successfully returned an object. Under all other circumstances you must not call this function (I mean it. although in simple tests it might actually work, it I<will> fail under real world conditions). As a special exception, you can also call this method before having parsed anything.

That means you can only use this function to look at or manipulate text before or after complete JSON objects, not while the parser is in the middle of parsing a JSON object.

This function is useful in two cases: a) finding the trailing text after a
JSON object or b) parsing multiple JSON objects separated by non-JSON text (such as commas).

=head2 incr_skip

$json->incr_skip

This will reset the state of the incremental parser and will remove the parsed text from the input buffer so far. This is useful after C<incr_parse> died, in which case the input buffer and incremental parser state is left unchanged, to skip the text parsed so far and to reset the parse state.

The difference to C<incr_reset> is that only text until the parse error occurred is removed.

=head2 incr_reset

$json->incr_reset

This completely resets the incremental parser, that is, after this call, it will be as if the parser had never parsed anything.

This is useful if you want to repeatedly parse JSON objects and want to ignore any trailing data, which means you have to reset the parser after each successful decode.

=head1 MAPPING

Most of this section is also taken from JSON::XS.

This section describes how the backend modules map Perl values to JSON values and vice versa. These mappings are designed to "do the right thing" in most circumstances automatically, preserving round-tripping characteristics (what you put in comes out as something equivalent).

For the more enlightened: note that in the following descriptions, lowercase I<perl> refers to the Perl interpreter, while uppercase I<Perl> refers to the abstract Perl language itself.

=head2 JSON -> PERL

=over 4

=item object

A JSON object becomes a reference to a hash in Perl. No ordering of object keys is preserved (JSON does not preserve object key ordering itself).
A JSON array becomes a reference to an array in Perl.

A JSON string becomes a string scalar in Perl - Unicode codepoints in JSON are represented by the same codepoints in the Perl string, so no manual decoding is necessary.

A JSON number becomes either an integer, numeric (floating point) or string scalar in Perl, depending on its range and any fractional parts. On the Perl level, there is no difference between those as Perl handles all the conversion details, but an integer may take slightly less memory and might represent more values exactly than floating point numbers.

If the number consists of digits only, this module will try to represent it as an integer value. If that fails, it will try to represent it as a numeric (floating point) value if that is possible without loss of precision. Otherwise it will preserve the number as a string value (in which case you lose roundtripping ability, as the JSON number will be re-encoded to a JSON string).

Numbers containing a fractional or exponential part will always be represented as numeric (floating point) values, possibly at a loss of precision (in which case you might lose perfect roundtripping ability, but the JSON number will still be re-encoded as a JSON number).

Note that precision is not accuracy - binary floating point values cannot represent most decimal fractions exactly, and when converting from and to floating point, this module only guarantees precision up to but not including the least significant bit.

These JSON atoms become C<JSON::true> and C<JSON::false>, respectively. They are overloaded to act almost exactly like the numbers C<1> and C<0>. You can check whether a scalar is a JSON boolean by using the C<JSON::is_bool> function.

A JSON null atom becomes C<undef> in Perl.

A JSON null atom becomes C<undef> in Perl.
As a nonstandard extension to the JSON syntax that is enabled by the C<relaxed> setting, shell-style comments are allowed. They can start anywhere outside strings and go till the end of the line.

=item tagged values (C<< (I<tag>)I<value> >>).

Another nonstandard extension to the JSON syntax, enabled with the C<allow_tags> setting, are tagged values. In this implementation, the I<tag> must be a perl package/class name encoded as a JSON string, and the I<value> must be a JSON array encoding optional constructor arguments.

See L<Object Serialisation>, below, for details.

=back

=head2 PERL -> JSON

The mapping from Perl to JSON is slightly more difficult, as Perl is a truly typeless language, so we can only guess which JSON type is meant by a Perl value.

=over 4

=item hash references

Perl hash references become JSON objects. As there is no inherent ordering in hash keys (or JSON objects), they will usually be encoded in a pseudo-random order. This module can optionally sort the hash keys (determined by the I<canonical> flag), so the same data structure will serialise to the same JSON text (given same settings and version of the same backend), but this incurs a runtime overhead and is only rarely useful, e.g. when you want to compare some JSON text against another for equality.

=item array references

Perl array references become JSON arrays.

=item other references

Other unblessed references are generally not allowed and will cause an exception to be thrown, except for references to the integers C<0> and C<1>, which get turned into C<false> and C<true> atoms in JSON. You can also use C<JSON::false> and C<JSON::true> to improve readability.

    encode_json [0,JSON::true]  # yields [false,true]
=item JSON::true, JSON::false, JSON::null

These special values become JSON true and JSON false values, respectively. You can also use \C<\1> and \C<\0> directly if you want.

=item blessed objects

Blessed objects are not directly representable in JSON, but \C<JSON::XS> allows various ways of handling objects. See L<Object Serialisation>, below, for details.

=item simple scalars

Simple Perl scalars (any scalar that is not a reference) are the most difficult objects to encode: this module will encode undefined scalars as JSON C<null> values, scalars that have last been used in a string context before encoding as JSON strings, and anything else as number value:

```
# dump as number
encode_json [-3.0e17] # yields [-3e+17]
my $value = 5; encode_json [$value]  # yields [5]

# used as string, so dump as string
print $value;
encode_json [$value] # yields ['5']

# undef becomes null
encode_json [undef]   # yields [null]
```

You can force the type to be a string by stringifying it:

```
my $x = 3.1; # some variable containing a number
"$x";       # stringified
$x := "";   # another, more awkward way to stringify
print $x;   # perl does it for you, too, quite often
```

You can force the type to be a number by numifying it:

```
my $x = "3";  # some variable containing a string
$x += 0;     # numify it, ensuring it will be dumped as a number
$x *= 1;     # same thing, the choice is yours.
```

You can not currently force the type in other, less obscure, ways. Tell me if you need this capability (but don't forget to explain why it's needed :).

Since version 2.91_01, JSON::PP uses a different number detection logic
that converts a scalar that is possible to turn into a number safely. The new logic is slightly faster, and tends to help people who use older perl or who want to encode complicated data structure. However, this may results in a different JSON text from the one JSON::XS encodes (and thus may break tests that compare entire JSON texts). If you do need the previous behavior for better compatibility or for finer control, set PERL_JSON_PP_USE_B environmental variable to true before you use JSON.

Note that numerical precision has the same meaning as under Perl (so binary to decimal conversion follows the same rules as in Perl, which can differ to other languages). Also, your perl interpreter might expose extensions to the floating point numbers of your platform, such as infinities or NaN's - these cannot be represented in JSON, and it is an error to pass those in.

JSON.pm backend modules trust what you pass to encode method (or encode_json function) is a clean, validated data structure with values that can be represented as valid JSON values only, because it's not from an external data source (as opposed to JSON texts you pass to decode or decode_json, which JSON backends consider tainted and don't trust). As JSON backends don't know exactly what you and consumers of your JSON texts want the unexpected values to be (you may want to convert them into null, or to stringify them with or without normalisation (string representation of infinities/NaN may vary depending on platforms), or to croak without conversion), you're advised to do what you and your consumers need before you encode, and also not to numify values that may start with values that look like a number (including infinities/NaN), without validating.

=head2 OBJECT SERIALISATION

As JSON cannot directly represent Perl objects, you have to choose between a pure JSON representation (without the ability to deserialise the object automatically again), and a nonstandard extension to the JSON syntax, tagged values.

=head3 SERIALISATION

What happens when this module encounters a Perl object depends on the allow_blessed, convert_blessed and allow_tags settings, which are used in this order:

=item 1. C<allow_tags> is enabled and the object has a C<FREEZE> method.
In this case, C<JSON> creates a tagged JSON value, using a nonstandard extension to the JSON syntax.

This works by invoking the C<FREEZE> method on the object, with the first argument being the object to serialise, and the second argument being the constant string C<JSON> to distinguish it from other serialisers.

The C<FREEZE> method can return any number of values (i.e. zero or more). These values and the package/classname of the object will then be encoded as a tagged JSON value in the following format:

("classname")[FREEZE return values...]

e.g.:

("URI")["http://www.google.com"]
("MyDate")[2013,10,29]
("ImageData::JPEG")["Z3...ViCg=="]

For example, the hypothetical C<My::Object> C<FREEZE> method might use the objects C<type> and C<id> members to encode the object:

sub My::Object::FREEZE {
    my ($self, $serialiser) = @_;

    ($self->[type], $self->[id])
}

=item 2. C<convert_blessed> is enabled and the object has a C<TO_JSON> method.

In this case, the C<TO_JSON> method of the object is invoked in scalar context. It must return a single scalar that can be directly encoded into JSON. This scalar replaces the object in the JSON text.

For example, the following C<TO_JSON> method will convert all L<URI> objects to JSON strings when serialised. The fact that these values originally were L<URI> objects is lost.

sub URI::TO_JSON {
    my ($uri) = @_;
    $uri->as_string
}

=item 3. C<allow_blessed> is enabled.

The object will be serialised as a JSON null value.
If none of the settings are enabled or the respective methods are missing, this module throws an exception.

For deserialisation there are only two cases to consider: either nonstandard tagging was used, in which case C<allow_tags> decides, or objects cannot be automatically be deserialised, in which case you can use postprocessing or the C<filter_json_object> or C<filter_json_single_key_object> callbacks to get some real objects our of your JSON.

This section only considers the tagged value case: a tagged JSON object is encountered during decoding and C<allow_tags> is disabled, a parse error will result (as if tagged values were not part of the grammar).

If C<allow_tags> is enabled, this module will look up the C<THAW> method of the package/classname used during serialisation (it will not attempt to load the package as a Perl module). If there is no such method, the decoding will fail with an error.

Otherwise, the C<THAW> method is invoked with the classname as first argument, the constant string C<JSON> as second argument, and all the values from the JSON array (the values originally returned by the C<FREEZE> method) as remaining arguments.

The method must then return the object. While technically you can return any Perl scalar, you might have to enable the C<allow_nonref> setting to make that work in all cases, so better return an actual blessed reference.

As an example, let's implement a C<THAW> function that regenerates the C<My::Object> from the C<FREEZE> example earlier:

```perl
sub My::Object::THAW {
    my ($class, $serialiser, $type, $id) = @_;

    $class->new (type => $type, id => $id)
}
```

This section is taken from JSON::XS.
The interested reader might have seen a number of flags that signify encodings or codesets - C<utf8>, C<latin1> and C<ascii>. There seems to be some confusion on what these do, so here is a short comparison:

C<utf8> controls whether the JSON text created by C<encode> (and expected by C<decode>) is UTF-8 encoded or not, while C<latin1> and C<ascii> only control whether C<encode> escapes character values outside their respective codeset range. Neither of these flags conflict with each other, although some combinations make less sense than others.

Care has been taken to make all flags symmetrical with respect to C<encode> and C<decode>, that is, texts encoded with any combination of these flag values will be correctly decoded when the same flags are used - in general, if you use different flag settings while encoding vs. when decoding you likely have a bug somewhere.

Below comes a verbose discussion of these flags. Note that a "codeset" is simply an abstract set of character-codepoint pairs, while an encoding takes those codepoint numbers and I<encodes> them, in our case into octets. Unicode is (among other things) a codeset, UTF-8 is an encoding, and ISO-8859-1 (= latin 1) and ASCII are both codesets I<and> encodings at the same time, which can be confusing.

=item C<utf8> flag disabled

When C<utf8> is disabled (the default), then C<encode>/C<decode> generate and expect Unicode strings, that is, characters with high ordinal Unicode values (> 255) will be encoded as such characters, and likewise such characters are decoded as-is, no changes to them will be done, except "(re-)interpreting" them as Unicode codepoints or Unicode characters, respectively (to Perl, these are the same thing in strings unless you do funny/weird/dumb stuff).

This is useful when you want to do the encoding yourself (e.g. when you want to have UTF-16 encoded JSON texts) or when some other layer does the encoding for you (for example, when printing to a terminal using a filehandle that transparently encodes to UTF-8 you certainly do NOT want to UTF-8 encode your data first and have Perl encode it another time).

=item C<utf8> flag enabled

If the C<utf8>-flag is enabled, C<encode>/C<decode> will encode all characters using the corresponding UTF-8 multi-byte sequence, and will expect your input strings to be encoded as UTF-8, that is, no "character" of the input string must have any value > 255, as UTF-8 does not allow that.
The C<utf8> flag therefore switches between two modes: disabled means you will get a Unicode string in Perl, enabled means you get an UTF-8 encoded octet/binary string in Perl.

=item C<latin1> or C<ascii> flags enabled

With C<latin1> (or C<ascii>) enabled, C<encode> will escape characters with ordinal values > 255 (> 127 with C<ascii>) and encode the remaining characters as specified by the C<utf8> flag.

If C<utf8> is disabled, then the result is also correctly encoded in those character sets (as both are proper subsets of Unicode, meaning that a Unicode string with all character values < 256 is the same thing as a ISO-8859-1 string, and a Unicode string with all character values < 128 is the same thing as an ASCII string in Perl).

If C<utf8> is enabled, you still get a correct UTF-8-encoded string, regardless of these flags, just some more characters will be escaped using C<uXXXX> then before.

Note that ISO-8859-1-1-encoded strings are not compatible with UTF-8 encoding, while ASCII-encoded strings are. That is because the ISO-8859-1 encoding is NOT a subset of UTF-8 (despite the ISO-8859-1 I<codeset> being a subset of Unicode), while ASCII is.

Surprisingly, C<decode> will ignore these flags and so treat all input values as governed by the C<utf8> flag. If it is disabled, this allows you to decode ISO-8859-1- and ASCII-encoded strings, as both strict subsets of Unicode. If it is enabled, you can correctly decode UTF-8 encoded strings.

So neither C<latin1> nor C<ascii> are incompatible with the C<utf8> flag - they only govern when the JSON output engine escapes a character or not.

The main use for C<latin1> is to relatively efficiently store binary data as JSON, at the expense of breaking compatibility with most JSON decoders.

The main use for C<ascii> is to force the output to not contain characters with values > 127, which means you can interpret the resulting string as UTF-8, ISO-8859-1, ASCII, KOI8-R or most about any character set and 8-bit-encoding, and still get the same data structure back. This is useful when your channel for JSON transfer is not 8-bit clean or the encoding might be mangled in between (e.g. in mail), and works because ASCII is a proper subset of most 8-bit and multibyte encodings in use in the world.

=back

=head1 BACKWARD INCOMPATIBILITY
Since version 2.90, stringification (and string comparison) for
C<JSON::true> and C<JSON::false> has not been overloaded. It shouldn't
matter as long as you treat them as boolean values, but a code that
expects they are stringified as "true" or "false" doesn't work as
you have expected any more.

    if (JSON::true eq 'true') {  # now fails
        print "The result is $JSON::true now."; # => The result is 1 now.
    }

And now these boolean values don't inherit JSON::Boolean, either.
When you need to test a value is a JSON boolean value or not, use
C<JSON::is_bool> function, instead of testing the value inherits
a particular boolean class or not.

=head1 BUGS

Please report bugs on backend selection and additional features
this module provides to RT or GitHub issues for this module:

L<https://github.com/makamaka/JSON/issues>

As for bugs on a specific behavior, please report to the author
of the backend module you are using.

As for new features and requests to change common behaviors, please
ask the author of JSON::XS (Marc Lehmann, E<lt>schmorp[at]schmorp.deE<gt>)
first, by email (important!), to keep compatibility among JSON.pm
backends.

=head1 SEE ALSO

L<JSON::XS>, L<Cpanel::JSON::XS>, L<JSON::PP> for backends.
L<JSON::MaybeXS>, an alternative that prefers Cpanel::JSON::XS.
C<RFC4627>(L<http://www.ietf.org/rfc/rfc4627.txt>)
RFC7159 (L<http://www.ietf.org/rfc/rfc7159.txt>)
RFC8259 (L<http://www.ietf.org/rfc/rfc8259.txt>)

=head1 AUTHOR

Makamaka Hannyaramitu, E<lt>makamaka[at]cpans.orgE<gt>
JSON::XS was written by Marc Lehmann <schmorpschmorp.de>

The release of this new version owes to the courtesy of Marc Lehmann.

=head1 CURRENT MAINTAINER

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=head1 COPYRIGHT AND LICENSE

Copyright 2005-2013 by Makamaka Hannyaharamitu

Most of the documentation is taken from JSON::XS by Marc Lehmann

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it under the same terms as Perl itself.

=cut

Pod

=head1 NAME

JSON::PP5005 - Helper module in using JSON::PP in Perl 5.005

=head1 DESCRIPTION

JSON::PP calls internally.

=head1 AUTHOR

Makamaka Hannyaharamitu, <makamaka@cpan.org>

=head1 COPYRIGHT AND LICENSE

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it under the same terms as Perl itself.

=cut
use strict;
use warnings;
use Benchmark qw( cmpthese timethese );

our $VERSION = '1.00';

my $wanttime = $ARGV[1] || 5;

use JSON qw( -support_by_pp -no_export ); # for JSON::PP::Boolean inheritance
use JSON::PP ();
use JSON::XS ();
use utf8;

my $pp = JSON::PP->new->utf8;
my $xs = JSON::XS->new->utf8;

local $/;

my $json = <>;
my $perl = JSON::XS::decode_json $json;
my $result;

printf( "JSON::PP %s\n", JSON::PP->VERSION );
printf( "JSON::XS %s\n", JSON::XS->VERSION );

print "------------------------------\n";
print "->decode()\n";
print "------------------------------\n";

$result = timethese( -$wanttime,
    #
    'JSON::PP' => sub { $pp->decode( $json ) },
    'JSON::XS' => sub { $xs->decode( $json ) },
    #
    'none'
);
cmpthese( $result );

print "------------------------------\n";
SYNOPSIS

bench_decode.pl json-file
# or
bench_decode.pl json-file minimum-time

DESCRIPTION

L<JSON::PP> and L<JSON::XS> decoding benchmark.

AUTHOR

makamaka

LICENSE

This library is free software; you can redistribute it and/or modify it under the same terms as Perl itself.

Found in path(s):
* /opt/cola/permits/1228286751_1637032133.21/0/libjson-perl-4.03000-orig-1.tar-gz/JSON-4.03/eg/bench_decode.pl
No license file was found, but licenses were detected in source scan.

---
abstract: 'JSON (JavaScript Object Notation) encoder/decoder'
author:
- 'Makamaka Hannyaharamitu, E<lt>makamaka[at]cpan.orgE<gt>'
build_requires:
ExtUtils::MakeMaker: '0'
configure_requires:
ExtUtils::MakeMaker: '0'
dynamic_config: 1
generated_by: 'ExtUtils::MakeMaker version 7.34, CPAN::Meta::Converter version 2.150010'
license: perl
meta-spec:
url: http://module-build.sourceforge.net/META-spec-v1.4.html
version: '1.4'
name: JSON
no_index:
directory:
- t
- inc
  recommends:
  JSON::XS: '2.34'
  requires:
  Test::More: '0'
  resources:
  bugtracker: https://github.com/makamaka/JSON/issues
  repository: https://github.com/makamaka/JSON
  version: '4.03'
  x_serialization_backend: 'CPAN::Meta::YAML version 0.018'

Found in path(s):
  * /opt/cola/permits/1228286751_1637032133.21/0/libjson-perl-4-03000-orig-1-tar-gz/JSON-4.03/META.yml

No license file was found, but licenses were detected in source scan.

=pod

=head1 NAME

JSON::PP - JSON::XS compatible pure-Perl module.

=head1 SYNOPSIS

use JSON::PP;

# exported functions, they croak on error
# and expect/generate UTF-8

$utf8_encoded_json_text = encode_json $perl_hash_or_arrayref;
$perl_hash_or_arrayref  = decode_json $utf8_encoded_json_text;

# OO-interface

$json = JSON::PP->new->ascii->pretty->allow_nonref;

$pretty_printed_json_text = $json->encode( $perl_scalar );
$perl_scalar = $json->decode( $json_text );

# Note that JSON version 2.0 and above will automatically use
# JSON::XS or JSON::PP, so you should be able to just:

use JSON;

=head1 VERSION

4.05
=head1 DESCRIPTION

JSON::PP is a pure perl JSON decoder/encoder, and (almost) compatible to much faster L<JSON::XS> written by Marc Lehmann in C. JSON::PP works as a fallback module when you use L<JSON> module without having installed JSON::XS.

Because of this fallback feature of JSON.pm, JSON::PP tries not to be more JavaScript-friendly than JSON::XS (i.e. not to escape extra characters such as U+2028 and U+2029, etc), in order for you not to lose such JavaScript-friendliness silently when you use JSON.pm and install JSON::XS for speed or by accident. If you need JavaScript-friendly RFC7159-compliant pure perl module, try L<JSON::Tiny>, which is derived from L<Mojolicious> web framework and is also smaller and faster than JSON::PP.

JSON::PP has been in the Perl core since Perl 5.14, mainly for CPAN toolchain modules to parse META.json.

=head1 FUNCTIONAL INTERFACE

This section is taken from JSON::XS almost verbatim. C<encode_json> and C<decode_json> are exported by default.

=head2 encode_json

$json_text = encode_json $perl_scalar

Converts the given Perl data structure to a UTF-8 encoded, binary string (that is, the string contains octets only). Croaks on error.

This function call is functionally identical to:

$json_text = JSON::PP->new->utf8->encode($perl_scalar)

Except being faster.

=head2 decode_json

$perl_scalar = decode_json $json_text

The opposite of C<encode_json>: expects an UTF-8 (binary) string and tries to parse that as an UTF-8 encoded JSON text, returning the resulting reference. Croaks on error.

This function call is functionally identical to:

$perl_scalar = JSON::PP->new->utf8->decode($json_text)
Except being faster.

=head2 JSON::PP::is_bool

```
$boolean = JSON::PP::is_bool($scalar)
```

Returns true if the passed scalar represents either JSON::PP::true or JSON::PP::false, two constants that act like C<1> and C<0> respectively and are also used to represent JSON C<true> and C<false> in Perl strings.

See L<MAPPING>, below, for more information on how JSON values are mapped to Perl.

=head1 OBJECT-ORIENTED INTERFACE

This section is also taken from JSON::XS.

The object oriented interface lets you configure your own encoding or decoding style, within the limits of supported formats.

=head2 new

```
$json = JSON::PP->new
```

Creates a new JSON::PP object that can be used to de/encode JSON strings. All boolean flags described below are by default I<disabled> (with the exception of C<allow_nonref>, which defaults to I<enabled> since version C<4.0>).

The mutators for flags all return the JSON::PP object again and thus calls can be chained:

```
my $json = JSON::PP->new->utf8->space_after->encode({a => [1,2]})
=> {"a": [1, 2]}
```

=head2 ascii

```
$json = $json->ascii([$enable])
$enabled = $json->get_ascii
```

If C<$enable> is true (or missing), then the C<encode> method will not generate characters outside the code range C<0..127> (which is ASCII). Any Unicode characters outside that range will be escaped using either a single \uXXXX (BMP characters) or a double \uHHHH\uLLLLL escape sequence, as per RFC4627. The resulting encoded JSON text can be treated as a native Unicode string, an ascii-encoded, latin1-encoded or UTF-8 encoded string,
If $<enable>$ is false, then the $<encode>$ method will not escape Unicode characters unless required by the JSON syntax or other flags. This results in a faster and more compact format.

See also the section I<ENCODING/CODESET FLAG NOTES> later in this document.

The main use for this flag is to produce JSON texts that can be transmitted over a 7-bit channel, as the encoded JSON texts will not contain any 8 bit characters.

$ json = $json->latin1($<enable>)

$ enabled = $json->get_latin1

If $<enable>$ is true (or missing), then the $<encode>$ method will encode the resulting JSON text as latin1 (or iso-8859-1), escaping any characters outside the code range C<0..255>. The resulting string can be treated as a latin1-encoded JSON text or a native Unicode string. The $<decode>$ method will not be affected in any way by this flag, as $<decode>$ by default expects Unicode, which is a strict superset of latin1.

If $<enable>$ is false, then the $<encode>$ method will not escape Unicode characters unless required by the JSON syntax or other flags.

See also the section I<ENCODING/CodeSET FLAG NOTES> later in this document.

The main use for this flag is efficiently encoding binary data as JSON text, as most octets will not be escaped, resulting in a smaller encoded size. The disadvantage is that the resulting JSON text is encoded in latin1 (and must correctly be treated as such when storing and transferring), a rare encoding for JSON. It is therefore most useful when you want to store data structures known to contain binary data efficiently in files or databases, not when talking to other JSON encoders/decoders.

$ json = $json->utf8($<enable>)

or any other superset of ASCII.

If $<enable>$ is false, then the $<encode>$ method will not escape Unicode characters unless required by the JSON syntax or other flags. This results in a faster and more compact format.

See also the section I<ENCODING/CodeSET FLAG NOTES> later in this document.
$enabled = $json->get_utf8

If C<$enable> is true (or missing), then the C<encoded> method will encode the JSON result into UTF-8, as required by many protocols, while the C<decoded> method expects to be handled an UTF-8-encoded string. Please note that UTF-8-encoded strings do not contain any characters outside the range C<0..255>, they are thus useful for bytewise/binary I/O. In future versions, enabling this option might enable autodetection of the UTF-16 and UTF-32 encoding families, as described in RFC4627.

If C<$enable> is false, then the C<encoded> method will return the JSON string as a (non-encoded) Unicode string, while C<decoded> expects thus a Unicode string. Any decoding or encoding (e.g. to UTF-8 or UTF-16) needs to be done yourself, e.g. using the Encode module.

See also the section I<ENCODING/CODESET FLAG NOTES> later in this document.

Example, output UTF-16BE-encoded JSON:

```perl
use Encode;
$jsontext = encode "UTF-16BE", JSON::PP->new->encode ($object);
```

Example, decode UTF-32LE-encoded JSON:

```perl
use Encode;
$object = JSON::PP->new->decode (decode "UTF-32LE", $jsontext);
```

=head2 pretty

$json = $json->pretty([$enable])

This enables (or disables) all of the C<indent>, C<space_before> and C<space_after> (and in the future possibly more) flags in one call to generate the most readable (or most compact) form possible.

=head2 indent

$json = $json->indent([$enable])

$enabled = $json->get_indent

If C<$enable> is true (or missing), then the C<encoded> method will use a multiline format as output, putting every array member or object/hash key-value pair into its own line, indenting them properly.

If C<$enable> is false, no newlines or indenting will be produced, and the resulting JSON text is guaranteed not to contain any C<newlines>. 
This setting has no effect when decoding JSON texts.

The default indent space length is three.
You can use $indent_length to change the length.

=head2 space_before

$json = $json->space_before([$enable])

$enabled = $json->get_space_before

If C<$enable> is true (or missing), then the C<encode> method will add an extra optional space before the C<:> separating keys from values in JSON objects.

If C<$enable> is false, then the C<encode> method will not add any extra space at those places.

This setting has no effect when decoding JSON texts. You will also most likely combine this setting with C<space_after>.

Example, space_before enabled, space_after and indent disabled:

{"key": "value"}

=head2 space_after

$json = $json->space_after([$enable])

$enabled = $json->get_space_after

If C<$enable> is true (or missing), then the C<encode> method will add an extra optional space after the C<:> separating keys from values in JSON objects and extra whitespace after the C<,> separating key-value pairs and array members.

If C<$enable> is false, then the C<encode> method will not add any extra space at those places.

This setting has no effect when decoding JSON texts.

Example, space_before and indent disabled, space_after enabled:

{"key": "value"}

=head2 relaxed

$json = $json->relaxed([$enable])
$enabled = $json->get_relaxed

If C<$enable> is true (or missing), then C<decode> will accept some extensions to normal JSON syntax (see below). C<encode> will not be affected in anyway. I<Be aware that this option makes you accept invalid JSON texts as if they were valid!>. I suggest only to use this option to parse application-specific files written by humans (configuration files, resource files etc.)

If C<$enable> is false (the default), then C<decode> will only accept valid JSON texts.

Currently accepted extensions are:

=over 4
=item * list items can have an end-comma

JSON I<separates> array elements and key-value pairs with commas. This can be annoying if you write JSON texts manually and want to be able to quickly append elements, so this extension accepts comma at the end of such items not just between them:

[ 1, 2, <- this comma not normally allowed ]

{  "k1": "v1",  "k2": "v2", <- this comma not normally allowed }

=item * shell-style '#'-comments

Whenever JSON allows whitespace, shell-style comments are additionally allowed. They are terminated by the first carriage-return or line-feed character, after which more white-space and comments are allowed.

[ 1, # this comment not allowed in JSON    # neither this one... ]

=item * C-style multiple-line '/ * * '/-comments (JSON::PP only)

Whenever JSON allows whitespace, C-style multiple-line comments are additionally allowed. Everything between C</*> and C<*/> is a comment, after which more white-space and comments are allowed.
Whenever JSON allows whitespace, C++-style one-line comments are additionally allowed. They are terminated by the first carriage-return or line-feed character, after which more white-space and comments are allowed.

Literal ASCII TAB characters are now allowed in strings (and treated as C<TAB>).

If C<$enable$> is true (or missing), then the C<encode> method will output JSON objects by sorting their keys. This is adding a comparatively high overhead.

If C<$enable$> is false, then the C<encode> method will output key-value pairs in the order Perl stores them (which will likely change between runs of the same script, and can change even within the same run from 5.18 onwards).

This option is useful if you want the same data structure to be encoded as the same JSON text (given the same overall settings). If it is disabled, the same hash might be encoded differently even if contains the same data, as key-value pairs have no inherent ordering in Perl.
This setting has no effect when decoding JSON texts.

This setting has currently no effect on tied hashes.

=head2 allow_nonref

`$json = $json->allow_nonref([$enable])`

`$enabled = $json->get_allow_nonref`

Unlike other boolean options, this option is enabled by default beginning with version C<4.0>.

If C<$enable> is true (or missing), then the C<encode> method can convert a non-reference into its corresponding string, number or null JSON value, which is an extension to RFC4627. Likewise, C<decode> will accept those JSON values instead of croaking.

If C<$enable> is false, then the C<encode> method will croak if it isn't passed an arrayref or hashref, as JSON texts must either be an object or array. Likewise, C<decode> will croak if given something that is not a JSON object or array.

Example, encode a Perl scalar as JSON value without enabled C<allow_nonref>, resulting in an error:

```
JSON::PP->new->allow_nonref(0)->encode("Hello, World!")
=> hash- or arrayref expected...
```

=head2 allow_unknown

`$json = $json->allow_unknown([$enable])`

`$enabled = $json->get_allow_unknown`

If C<$enable> is true (or missing), then C<encode> will not throw an exception when it encounters values it cannot represent in JSON (for example, filehandles) but instead will encode a JSON C<null> value. Note that blessed objects are not included here and are handled separately by C<allow_blessed>.

If C<$enable> is false (the default), then C<encode> will throw an exception when it encounters anything it cannot encode as JSON.

This option does not affect C<decode> in any way, and it is recommended to leave it off unless you know your communications partner.

=head2 allow_blessed
$json = $json->allow_blessed([$enable])

$enabled = $json->get_allow_blessed

See L<OBJECT SERIALISATION> for details.

If C<$enable> is true (or missing), then the C<encode> method will not barf when it encounters a blessed reference that it cannot convert otherwise. Instead, a JSON C/null> value is encoded instead of the object.

If C<$enable> is false (the default), then C<encode> will throw an exception when it encounters a blessed object that it cannot convert otherwise.

This setting has no effect on C<decode>.

=head2 convert_blessed

$json = $json->convert_blessed([$enable])

$enabled = $json->get_convert_blessed

See L<OBJECT SERIALISATION> for details.

If C<$enable> is true (or missing), then C<encode>, upon encountering a blessed object, will check for the availability of the C<TO_JSON> method on the object's class. If found, it will be called in scalar context and the resulting scalar will be encoded instead of the object.

The C<TO_JSON> method may safely call die if it wants. If C<TO_JSON> returns other blessed objects, those will be handled in the same way. C<TO_JSON> must take care of not causing an endless recursion cycle (== crash) in this case. The name of C<TO_JSON> was chosen because other methods called by the Perl core (== not by the user of the object) are usually in upper case letters and to avoid collisions with any C<to_json> function or method.

If C<$enable> is false (the default), then C<encode> will not consider this type of conversion.

This setting has no effect on C<decode>.

=head2 allow_tags

$json = $json->allow_tags([$enable])

$enabled = $json->get_allow_tags
See L&lt;OBJECT SERIALISATION&gt; for details.

If C&lt;$enable&gt; is true (or missing), then C&lt;encode&gt;, upon encountering a blessed object, will check for the availability of the C&lt;FREEZE&gt; method on the object's class. If found, it will be used to serialise the object into a nonstandard tagged JSON value (that JSON decoders cannot decode).

It also causes C&lt;decode&gt; to parse such tagged JSON values and deserialise them via a call to the C&lt;THAW&gt; method.

If C&lt;$enable&gt; is false (the default), then C&lt;encode&gt; will not consider this type of conversion, and tagged JSON values will cause a parse error in C&lt;decode&gt;, as if tags were not part of the grammar.

=head2 boolean_values

```
$json->boolean_values([$false, $true])

($false, $true) = $json->get_boolean_values
```

By default, JSON booleans will be decoded as overloaded C&lt;$JSON::PP::false&gt; and C&lt;$JSON::PP::true&gt; objects.

With this method you can specify your own boolean values for decoding - on decode, JSON C&lt;false&gt; will be decoded as a copy of C&lt;$false&gt;, and JSON C&lt;true&gt; will be decoded as C&lt;$true&gt; ("copy" here is the same thing as assigning a value to another variable, i.e. C&lt;$copy = $false&gt;).

This is useful when you want to pass a decoded data structure directly to other serialisers like YAML, Data::MessagePack and so on.

Note that this works only when you C&lt;decode&gt;. You can set incompatible boolean objects (like L&lt;boolean&gt;), but when you C&lt;encode&gt; a data structure with such boolean objects, you still need to enable C&lt;convert_blessed&gt; (and add a C&lt;TO_JSON&gt; method if necessary).

Calling this method without any arguments will reset the booleans to their default values.

C&lt;get_boolean_values&gt; will return both C&lt;$false&gt; and C&lt;$true&gt; values, or the empty list when they are set to the default.

=head2 filter_json_object

```
$json = $json->filter_json_object([$coderef])
```

When C&lt;$coderef&gt; is specified, it will be called from C&lt;decode&gt; each
time it decodes a JSON object. The only argument is a reference to the newly-created hash. If the code references returns a single scalar (which need not be a reference), this value (or rather a copy of it) is inserted into the deserialised data structure. If it returns an empty list (NOTE: I<not> C<undef>, which is a valid scalar), the original deserialised hash will be inserted. This setting can slow down decoding considerably.

When C<$coderef> is omitted or undefined, any existing callback will be removed and C<decode> will not change the deserialised hash in any way.

Example, convert all JSON objects into the integer 5:

```perl
my $js = JSON::PP->new->filter_json_object(sub { 5 });
# returns [5]
$js->decode('[{}]');
# returns 5
$js->decode('{"a":1, "b":2}');
```

=head2 filter_json_single_key_object

$js = $js->filter_json_single_key_object($key => $coderef)

Works remotely similar to C<filter_json_object>, but is only called for JSON objects having a single key named C<$key>.

This C<$coderef> is called before the one specified via C<filter_json_object>, if any. It gets passed the single value in the JSON object. If it returns a single value, it will be inserted into the data structure. If it returns nothing (not even C<undef> but the empty list), the callback from C<filter_json_object> will be called next, as if no single-key callback were specified.

If C<$coderef> is omitted or undefined, the corresponding callback will be disabled. There can only ever be one callback for a given key.

As this callback gets called less often then the C<filter_json_object> one, decoding speed will not usually suffer as much. Therefore, single-key objects make excellent targets to serialise Perl objects into, especially as single-key JSON objects are as close to the type-tagged value concept as JSON gets (it's basically an ID/VALUE tuple). Of course, JSON does not support this in any way, so you need to make sure your data never looks like a serialised Perl hash.

Typical names for the single object key are C<__class_whatever__>, or C<$__dollars_are_rarely_used__$> or C<}ugly_brace_placement>, or even things like C<__class_md5sum(classname)__>, to reduce the risk of clashing
with real hashes.

Example, decode JSON objects of the form C<< { "__widget__" => <id> } >> into the corresponding C<< $WIDGET{<id>} >> object:

    # return whatever is in $WIDGET{5}:
    JSON::PP
       ->new
       ->filter_json_single_key_object (__widget__ => sub {
            $WIDGET{ $_[0] }
       })
       ->decode ('{"__widget__": 5'}

    # this can be used with a TO_JSON method in some "widget" class
    # for serialisation to json:
    sub WidgetBase::TO_JSON {
       my ($self) = @_;

       unless ($self->{id}) {
           $self->{id} = ..get..some..id.;
           $WIDGET{ $self->{id} } = $self;
       }

       { __widget__ => $self->{id} }
    }

=head2 shrink

    $json = $json->shrink([$enable])

    $enabled = $json->get_shrink

If C<$enable> is true (or missing), the string returned by C<encode> will be shrunk (i.e. downgraded if possible).

The actual definition of what shrink does might change in future versions, but it will always try to save space at the expense of time.

If C<$enable> is false, then JSON::PP does nothing.

=head2 max_depth

    $json = $json->max_depth([$maximum_nesting_depth])

    $max_depth = $json->get_max_depth

Sets the maximum nesting level (default C<512>) accepted while encoding or decoding. If a higher nesting level is detected in JSON text or a Perl
data structure, then the encoder and decoder will stop and croak at that point.

Nesting level is defined by number of hash- or arrayrefs that the encoder needs to traverse to reach a given point or the number of `C<{{>` or `C<[>` characters without their matching closing parenthesis crossed to reach a given character in a string.

Setting the maximum depth to one disallows any nesting, so that ensures that the object is only a single hash/object or array.

If no argument is given, the highest possible setting will be used, which is rarely useful.

See `L<JSON::XS/SECURITY CONSIDERATIONS>` for more info on why this is useful.

=head2 max_size

```perl
$json = $json->max_size([$maximum_string_size])
$max_size = $json->get_max_size
```

Set the maximum length a JSON text may have (in bytes) where decoding is being attempted. The default is `C<0>`, meaning no limit. When `C<decode>` is called on a string that is longer then this many bytes, it will not attempt to decode the string but throw an exception. This setting has no effect on `C<encode>` (yet).

If no argument is given, the limit check will be deactivated (same as when `C<0>` is specified).

See `L<JSON::XS/SECURITY CONSIDERATIONS>` for more info on why this is useful.

=head2 encode

```perl
$json_text = $json->encode($perl_scalar)
```

Converts the given Perl value or data structure to its JSON representation. Croaks on error.

=head2 decode

```perl
$perl_scalar = $json->decode($json_text)
```

The opposite of `C<encode>`: expects a JSON text and tries to parse it, returning the resulting simple scalar or reference. Croaks on error.

=head2 decode_prefix
($perl_scalar, $characters) = $json->decode_prefix($json_text)

This works like the C<decode> method, but instead of raising an exception when there is trailing garbage after the first JSON object, it will silently stop parsing there and return the number of characters consumed so far.

This is useful if your JSON texts are not delimited by an outer protocol and you need to know where the JSON text ends.

    JSON::PP->new->decode_prefix ('[1] the tail')
    => ([1], 3)

=head1 FLAGS FOR JSON::PP ONLY

The following flags and properties are for JSON::PP only. If you use any of these, you can't make your application run faster by replacing JSON::PP with JSON::XS. If you need these and also speed boost, you might want to try L<Cpanel::JSON::XS>, a fork of JSON::XS by Reini Urban, which supports some of these (with a different set of incompatibilities). Most of these historical flags are only kept for backward compatibility, and should not be used in a new application.

=head2 allow_singlequote

    $json = $json->allow_singlequote([$enable])
    $enabled = $json->get_allow_singlequote

If C<$enable> is true (or missing), then C<decode> will accept invalid JSON texts that contain strings that begin and end with single quotation marks. C<encode> will not be affected in any way. I<Be aware that this option makes you accept invalid JSON texts as if they were valid!>. I suggest only to use this option to parse application-specific files written by humans (configuration files, resource files etc.)

If C<$enable> is false (the default), then C<decode> will only accept valid JSON texts.

    $json->allow_singlequote->decode(qq|"foo":"bar"|);
    $json->allow_singlequote->decode(qq|"foo":'bar'|);
    $json->allow_singlequote->decode(qq|'foo':'bar'|);

=head2 allow_barekey

    $json = $json->allow_barekey([$enable])
    $enabled = $json->get_allow_barekey
If $enable$ is true (or missing), then C<decode> will accept invalid JSON texts that contain JSON objects whose names don't begin and end with quotation marks. C<encode> will not be affected in any way. I<Be aware that this option makes you accept invalid JSON texts as if they were valid!>. I suggest only to use this option to parse application-specific files written by humans (configuration files, resource files etc.)

If $enable$ is false (the default), then C<decode> will only accept valid JSON texts.

```perl
$json->allow_barekey->decode(qq|{foo:"bar"}|);
```

=head2 allow_bignum

```perl
$json = $json->allow_bignum([$enable])
$enabled = $json->get_allow_bignum
```

If $enable$ is true (or missing), then C<decode> will convert big integers Perl cannot handle as integer into L<Math::BigInt> objects and convert floating numbers into L<Math::BigFloat> objects. C<encode> will convert C<Math::BigInt> and C<Math::BigFloat> objects into JSON numbers.

```perl
$json->allow_nonref->allow_bignum;
$bigfloat = $json->decode('2.000000000000000000000000001');
print $json->encode($bigfloat);
# => 2.0000000000000000000000000000001
```

See also L<MAPPING>.

=head2 loose

```perl
$json = $json->loose([$enable])
$enabled = $json->get_loose
```

If $enable$ is true (or missing), then C<decode> will accept invalid JSON texts that contain unescaped [\x00-\x1f\x22\x5c] characters. C<encode> will not be affected in any way. I<Be aware that this option makes you accept invalid JSON texts as if they were valid!>. I suggest only to use this option to parse application-specific files written by humans (configuration files, resource files etc.)

If $enable$ is false (the default), then C<decode> will only accept valid JSON texts.
$json->loose->decode(qq|"abc
def"|);

=head2 escape_slash

$json = $json->escape_slash($enable)
$enabled = $json->get_escape_slash

If C<$enable> is true (or missing), then C<encode> will explicitly escape I<slash> (solidus; C<\u002F>) characters to reduce the risk of XSS (cross site scripting) that may be caused by C<<</script>> in a JSON text, with the cost of bloating the size of JSON texts.

This option may be useful when you embed JSON in HTML, but embedding arbitrary JSON in HTML (by some HTML template toolkit or by string interpolation) is risky in general. You must escape necessary characters in correct order, depending on the context.

C<decode> will not be affected in any way.

=head2 indent_length

$json = $json->indent_length($number_of_spaces)
$length = $json->get_indent_length

This option is only useful when you also enable C<indent> or C<prettify>.

JSON::XS indents with three spaces when you C<encode> (if requested by C<indent> or C<prettify>), and the number cannot be changed.
JSON::PP allows you to change/get the number of indent spaces with these mutator/accessor. The default number of spaces is three (the same as JSON::XS), and the acceptable range is from C<0> (no indentation; it'd be better to disable indentation by C<indent(0)>) to C<15>.

=head2 sort_by

$json = $json->sort_by($code_ref)
$json = $json->sort_by($subroutine_name)

If you just want to sort keys (names) in JSON objects when you C<encode>, enable C<canonical> option (see above) that allows you to sort object keys alphabetically.

If you do need to sort non-alphabetically for whatever reasons, you can give a code reference (or a subroutine name) to C<sort_by>, then the argument will be passed to Perl's C<sort> built-in function.

As the sorting is done in the JSON::PP scope, you usually need to
prepend C<JSON::PP::> to the subroutine name, and the special variables C<$a> and C<$b> used in the subroutine used by C<sort> function.

Example:

```
my %ORDER = (id => 1, class => 2, name => 3);
$json->sort_by(sub {
    ($ORDER{$JSON::PP::a} // 999) <=> ($ORDER{$JSON::PP::b} // 999)
    or $JSON::PP::a cmp $JSON::PP::b
});
print $json->encode([{
    name => 'CPAN', id => 1, href => 'http://cpan.org'
}]);
# ["id":1,"name":"CPAN","href":"http://cpan.org"]
```

Note that C<sort_by> affects all the plain hashes in the data structure. If you need finer control, C<tie> necessary hashes with a module that implements ordered hash (such as L<Hash::Ordered> and L<Tie::IxHash>). C<canonical> and C<sort_by> don't affect the key order in C<tie>d hashes.

```
use Hash::Ordered;
tie my %hash, 'Hash::Ordered',
    (name => 'CPAN', id => 1, href => 'http://cpan.org');
print $json->encode(%hash);
# ["id":1,"name":"CPAN","href":"http://cpan.org"] # order is kept
```

=head1 INCREMENTAL PARSING

This section is also taken from JSON::XS.

In some cases, there is the need for incremental parsing of JSON texts. While this module always has to keep both JSON text and resulting Perl data structure in memory at one time, it does allow you to parse a JSON stream incrementally. It does so by accumulating text until it has a full JSON object, which it then can decode. This process is similar to using C<decode_prefix> to see if a full JSON object is available, but is much more efficient (and can be implemented with a minimum of method calls).

JSON::PP will only attempt to parse the JSON text once it is sure it has enough text to get a decisive result, using a very simple but truly incremental parser. This means that it sometimes won't stop as early as the full parser, for example, it doesn't detect mismatched parentheses. The only thing it guarantees is that it starts decoding as soon as a syntactically valid JSON text has been seen. This means you need to set resource limits (e.g. C<max_size>) to ensure the parser will stop parsing in the presence if syntax errors.
The following methods implement this incremental parser.

=head2 incr_parse

$json->incr_parse( [$string] ) # void context

$obj_or_undef = $json->incr_parse( [$string] ) # scalar context

@obj_or_empty = $json->incr_parse( [$string] ) # list context

This is the central parsing function. It can both append new text and extract objects from the stream accumulated so far (both of these functions are optional).

If C<$string> is given, then this string is appended to the already existing JSON fragment stored in the C<$json> object.

After that, if the function is called in void context, it will simply return without doing anything further. This can be used to add more text in as many chunks as you want.

If the method is called in scalar context, then it will try to extract exactly I<one> JSON object. If that is successful, it will return this object, otherwise it will return C<undef>. If there is a parse error, this method will croak just as C<decode> would do (one can then use C<incr_skip> to skip the erroneous part). This is the most common way of using the method.

And finally, in list context, it will try to extract as many objects from the stream as it can find and return them, or the empty list otherwise. For this to work, there must be no separators (other than whitespace) between the JSON objects or arrays, instead they must be concatenated back-to-back. If an error occurs, an exception will be raised as in the scalar context case. Note that in this case, any previously-parsed JSON texts will be lost.

Example: Parse some JSON arrays/objects in a given string and return them.

    my @objs = JSON::PP->new->incr_parse ("[5][7][1,2]"):

=head2 incr_text

$lvalue_string = $json->incr_text

This method returns the currently stored JSON fragment as an lvalue, that is, you can manipulate it. This I<only> works when a preceding call to
C<incr_parse> in I<scalar context> successfully returned an object. Under all other circumstances you must not call this function (I mean it. although in simple tests it might actually work, it I<will> fail under real world conditions). As a special exception, you can also call this method before having parsed anything.

That means you can only use this function to look at or manipulate text before or after complete JSON objects, not while the parser is in the middle of parsing a JSON object.

This function is useful in two cases: a) finding the trailing text after a JSON object or b) parsing multiple JSON objects separated by non-JSON text (such as commas).

=head2 incr_skip

$json->incr_skip

This will reset the state of the incremental parser and will remove the parsed text from the input buffer so far. This is useful after C<incr_parse> died, in which case the input buffer and incremental parser state is left unchanged, to skip the text parsed so far and to reset the parse state.

The difference to C<incr_reset> is that only text until the parse error occurred is removed.

=head2 incr_reset

$json->incr_reset

This completely resets the incremental parser, that is, after this call, it will be as if the parser had never parsed anything.

This is useful if you want to repeatedly parse JSON objects and want to ignore any trailing data, which means you have to reset the parser after each successful decode.

=head1 MAPPING

Most of this section is also taken from JSON::XS.

This section describes how JSON::PP maps Perl values to JSON values and vice versa. These mappings are designed to "do the right thing" in most circumstances automatically, preserving round-tripping characteristics (what you put in comes out as something equivalent).

For the more enlightened: note that in the following descriptions,
lowercase I<perl> refers to the Perl interpreter, while uppercase I<Perl> refers to the abstract Perl language itself.

=head2 JSON -> PERL

=over 4

=item object

A JSON object becomes a reference to a hash in Perl. No ordering of object keys is preserved (JSON does not preserve object key ordering itself).

=item array

A JSON array becomes a reference to an array in Perl.

=item string

A JSON string becomes a string scalar in Perl - Unicode codepoints in JSON are represented by the same codepoints in the Perl string, so no manual decoding is necessary.

=item number

A JSON number becomes either an integer, numeric (floating point) or string scalar in perl, depending on its range and any fractional parts. On the Perl level, there is no difference between those as Perl handles all the conversion details, but an integer may take slightly less memory and might represent more values exactly than floating point numbers.

If the number consists of digits only, JSON::PP will try to represent it as an integer value. If that fails, it will try to represent it as a numeric (floating point) value if that is possible without loss of precision. Otherwise it will preserve the number as a string value (in which case you lose roundtripping ability, as the JSON number will be re-encoded to a JSON string).

Numbers containing a fractional or exponential part will always be represented as numeric (floating point) values, possibly at a loss of precision (in which case you might lose perfect roundtripping ability, but the JSON number will still be re-encoded as a JSON number).

Note that precision is not accuracy - binary floating point values cannot represent most decimal fractions exactly, and when converting from and to floating point, JSON::PP only guarantees precision up to but not including the least significant bit.

When C<allow_bignum> is enabled, big integer values and any numeric
values will be converted into L<Math::BigInt> and L<Math::BigFloat> objects respectively, without becoming string scalars or losing precision.

=item true, false

These JSON atoms become C<JSON::PP::true> and C<JSON::PP::false>, respectively. They are overloaded to act almost exactly like the numbers C<1> and C<0>. You can check whether a scalar is a JSON boolean by using the C<JSON::PP::is_bool> function.

=item null

A JSON null atom becomes C<undef> in Perl.

=item shell-style comments (C<< # I<text> >>)

As a nonstandard extension to the JSON syntax that is enabled by the C<relaxed> setting, shell-style comments are allowed. They can start anywhere outside strings and go till the end of the line.

=item tagged values (C<< (I<tag>)I<value> >>).

Another nonstandard extension to the JSON syntax, enabled with the C<allow_tags> setting, are tagged values. In this implementation, the I<tag> must be a perl package/class name encoded as a JSON string, and the I<value> must be a JSON array encoding optional constructor arguments.

See L<OBJECT SERIALISATION>, below, for details.

=back

=head2 PERL -> JSON

The mapping from Perl to JSON is slightly more difficult, as Perl is a truly typeless language, so we can only guess which JSON type is meant by a Perl value.

=over 4

=item hash references

Perl hash references become JSON objects. As there is no inherent ordering in hash keys (or JSON objects), they will usually be encoded in a pseudo-random order. JSON::PP can optionally sort the hash keys (determined by the I<canonical> flag and/or I<sort_by> property), so the same data structure will serialise to the same JSON text (given
same settings and version of JSON::PP), but this incurs a runtime
overhead and is only rarely useful, e.g. when you want to compare some
JSON text against another for equality.

=item array references

Perl array references become JSON arrays.

=item other references

Other unblessed references are generally not allowed and will cause an
exception to be thrown, except for references to the integers C<0> and
C<1>, which get turned into C<false> and C<true> atoms in JSON. You can
also use C<JSON::PP::false> and C<JSON::PP::true> to improve
readability.

    to_json [\0, JSON::PP::true]   # yields [false,true]

=item JSON::PP::true, JSON::PP::false

These special values become JSON true and JSON false values,
respectively. You can also use C<\1> and C<\0> directly if you want.

=item JSON::PP::null

This special value becomes JSON null.

=item blessed objects

Blessed objects are not directly representable in JSON, but C<JSON::PP>
allows various ways of handling objects. See L<OBJECT SERIALISATION>,
below, for details.

=item simple scalars

Simple Perl scalars (any scalar that is not a reference) are the most
difficult objects to encode: JSON::PP will encode undefined scalars as
JSON C<null> values, scalars that have last been used in a string context
before encoding as JSON strings, and anything else as number value:

    # dump as number
    encode_json [-3.0e17]       # yields [-3e+17]
    my $value = 5; encode_json [$value] # yields [5]

    # used as string, so dump as string
    print $value;
    encode_json [$value]       # yields ['5']
# undef becomes null
encode_json [undef] # yields [null]

You can force the type to be a JSON string by stringifying it:

my $x = 3.1; # some variable containing a number
"$x"; # stringified
$x = ''; # another, more awkward way to stringify
print $x; # perl does it for you, too, quite often
    # (but for older perls)

You can force the type to be a JSON number by numifying it:

my $x = "3"; # some variable containing a string
$x += 0; # numify it, ensuring it will be dumped as a number
$x *= 1; # same thing, the choice is yours.

You can not currently force the type in other, less obscure, ways.

Since version 2.91_01, JSON::PP uses a different number detection logic
that converts a scalar that is possible to turn into a number safely.
The new logic is slightly faster, and tends to help people who use older
perl or who want to encode complicated data structure. However, this may
results in a different JSON text from the one JSON::XS encodes (and
thus may break tests that compare entire JSON texts). If you do
need the previous behavior for compatibility or for finer control,
set PERL_JSON_PP_USE_B environmental variable to true before you
C<use> JSON::PP (or JSON.pm).

Note that numerical precision has the same meaning as under Perl (so
binary to decimal conversion follows the same rules as in Perl, which
can differ to other languages). Also, your perl interpreter might expose
extensions to the floating point numbers of your platform, such as
infinities or NaN's - these cannot be represented in JSON, and it is an
error to pass those in.

JSON::PP (and JSON::XS) trusts what you pass to C<encode> method
(or C<encode_json> function) is a clean, validated data structure with
values that can be represented as valid JSON values only, because it's
not from an external data source (as opposed to JSON texts you pass to
C<decode> or C<decode_json>, which JSON::PP considers tainted and
doesn't trust). As JSON::PP doesn't know exactly what you and consumers
of your JSON texts want the unexpected values to be (you may want to
convert them into null, or to stringify them with or without
normalisation (string representation of infinities/NaN may vary
depending on platforms), or to croak without conversion), you're advised
to do what you and your consumers need before you encode, and also not
to numify values that may start with values that look like a number (including infinities/NaN), without validating.

=head2 OBJECT SERIALISATION

As JSON cannot directly represent Perl objects, you have to choose between a pure JSON representation (without the ability to deserialise the object automatically again), and a nonstandard extension to the JSON syntax, tagged values.

=head3 SERIALISATION

What happens when C<JSON::PP> encounters a Perl object depends on the C<allow_blessed>, C<convert_blessed>, C<allow_tags> and C<allow_bignum> settings, which are used in this order:

=item 1. C<allow_tags> is enabled and the object has a C<FREEZE> method.

In this case, C<JSON::PP> creates a tagged JSON value, using a nonstandard extension to the JSON syntax.

This works by invoking the C<FREEZE> method on the object, with the first argument being the object to serialise, and the second argument being the constant string C<JSON> to distinguish it from other serialisers.

The C<FREEZE> method can return any number of values (i.e. zero or more). These values and the package/classname of the object will then be encoded as a tagged JSON value in the following format:

("classname")[FREEZE return values...]

e.g.:

("URI")["http://www.google.com/"]
("MyDate")[2013,10,29]
("ImageData::JPEG")["Z3...ViCg=="]

For example, the hypothetical C<My::Object> C<FREEZE> method might use the objects C<type> and C<id> members to encode the object:

sub My::Object::FREEZE {
    my ($self, $serialiser) = @_;

    ($self->[type], $self->[id])

=item 2. C<convert_blessed> is enabled and the object has a C<TO_JSON> method.

In this case, the C<TO_JSON> method of the object is invoked in scalar context. It must return a single scalar that can be directly encoded into JSON. This scalar replaces the object in the JSON text.

For example, the following C<TO_JSON> method will convert all L<URI> objects to JSON strings when serialised. The fact that these values originally were L<URI> objects is lost.

```perl
sub URI::TO_JSON {    
    my ($uri) = @_;    
    $uri->as_string
}
```

=item 3. C<allow_bignum> is enabled and the object is a C<Math::BigInt> or C<Math::BigFloat>.

The object will be serialised as a JSON number value.

=item 4. C<allow_blessed> is enabled.

The object will be serialised as a JSON null value.

=item 5. none of the above

If none of the settings are enabled or the respective methods are missing, C<JSON::PP> throws an exception.

=back

=head3 DESERIALISATION

For deserialisation there are only two cases to consider: either nonstandard tagging was used, in which case C<allow_tags> decides, or objects cannot be automatically be deserialised, in which case you can use postprocessing or the C<filter_json_object> or C<filter_json_single_key_object> callbacks to get some real objects our of your JSON.

This section only considers the tagged value case: a tagged JSON object is encountered during decoding and C<allow_tags> is disabled, a parse error will result (as if tagged values were not part of the grammar).

If C<allow_tags> is enabled, C<JSON::PP> will look up the C<THAW> method of the package/classname used during serialisation (it will not attempt to load the package as a Perl module). If there is no such method, the
decoding will fail with an error.

Otherwise, the \texttt{THAW} method is invoked with the classname as first argument, the constant string \texttt{JSON} as second argument, and all the values from the JSON array (the values originally returned by the \texttt{FREEZE} method) as remaining arguments.

The method must then return the object. While technically you can return any Perl scalar, you might have to enable the \texttt{allow_nonref} setting to make that work in all cases, so better return an actual blessed reference.

As an example, let's implement a \texttt{THAW} function that regenerates the \texttt{My::Object} from the \texttt{FREEZE} example earlier:

```perl
sub My::Object::THAW {
    my ($class, $serialiser, $type, $id) = @_;

    $class->new (type => $type, id => $id)
}
```

=head1 ENCODING/CODESET FLAG NOTES

This section is taken from JSON::XS.

The interested reader might have seen a number of flags that signify encodings or codesets - \texttt{utf8}, \texttt{latin1} and \texttt{ascii}. There seems to be some confusion on what these do, so here is a short comparison:

\texttt{utf8} controls whether the JSON text created by \texttt{encode} (and expected by \texttt{decode}) is UTF-8 encoded or not, while \texttt{latin1} and \texttt{ascii} only control whether \texttt{encode} escapes character values outside their respective codeset range. Neither of these flags conflict with each other, although some combinations make less sense than others.

Care has been taken to make all flags symmetrical with respect to \texttt{encode} and \texttt{decode}, that is, texts encoded with any combination of these flag values will be correctly decoded when the same flags are used - in general, if you use different flag settings while encoding vs. when decoding you likely have a bug somewhere.

Below comes a verbose discussion of these flags. Note that a "codeset" is simply an abstract set of character-codepoint pairs, while an encoding takes those codepoint numbers and \texttt{encodes} them, in our case into octets. Unicode is (among other things) a codeset, UTF-8 is an encoding, and ISO-8859-1 (= latin 1) and ASCII are both codesets \texttt{and} encodings at the same time, which can be confusing.
=item C<utf8> flag disabled

When C<utf8> is disabled (the default), then C<encode>/C<decode> generate and expect Unicode strings, that is, characters with high ordinal Unicode values (> 255) will be encoded as such characters, and likewise such characters are decoded as-is, no changes to them will be done, except "(re-)interpreting" them as Unicode codepoints or Unicode characters, respectively (to Perl, these are the same thing in strings unless you do funny/weird/dumb stuff).

This is useful when you want to do the encoding yourself (e.g. when you want to have UTF-16 encoded JSON texts) or when some other layer does the encoding for you (for example, when printing to a terminal using a filehandle that transparently encodes to UTF-8 you certainly do NOT want to UTF-8 encode your data first and have Perl encode it another time).

=item C<utf8> flag enabled

If the C<utf8>-flag is enabled, C<encode>/C<decode> will encode all characters using the corresponding UTF-8 multi-byte sequence, and will expect your input strings to be encoded as UTF-8, that is, no "character" of the input string must have any value > 255, as UTF-8 does not allow that.

The C<utf8> flag therefore switches between two modes: disabled means you will get a Unicode string in Perl, enabled means you get an UTF-8 encoded octet/binary string in Perl.

=item C<latin1> or C<ascii> flags enabled

With C<latin1> (or C<ascii>) enabled, C<encode> will escape characters with ordinal values > 255 (> 127 with C<ascii>) and encode the remaining characters as specified by the C<utf8> flag.

If C<utf8> is disabled, then the result is also correctly encoded in those character sets (as both are proper subsets of Unicode, meaning that a Unicode string with all character values < 256 is the same thing as a ISO-8859-1 string, and a Unicode string with all character values < 128 is the same thing as an ASCII string in Perl).

If C<utf8> is enabled, you still get a correct UTF-8-encoded string, regardless of these flags, just some more characters will be escaped using C<\uXXXX> then before.

Note that ISO-8859-1-I<encoded> strings are not compatible with UTF-8 encoding, while ASCII-encoded strings are. That is because the ISO-8859-1
encoding is NOT a subset of UTF-8 (despite the ISO-8859-1 \codeset{} being a subset of Unicode), while ASCII is.

Surprisingly, C<decode> will ignore these flags and so treat all input values as governed by the C<utf8> flag. If it is disabled, this allows you to decode ISO-8859-1- and ASCII-encoded strings, as both strict subsets of Unicode. If it is enabled, you can correctly decode UTF-8 encoded strings.

So neither C<latin1> nor C<ascii> are incompatible with the C<utf8> flag - they only govern when the JSON output engine escapes a character or not.

The main use for C<latin1> is to relatively efficiently store binary data as JSON, at the expense of breaking compatibility with most JSON decoders.

The main use for C<ascii> is to force the output to not contain characters with values > 127, which means you can interpret the resulting string as UTF-8, ISO-8859-1, ASCII, KOI8-R or most about any character set and 8-bit-encoding, and still get the same data structure back. This is useful when your channel for JSON transfer is not 8-bit clean or the encoding might be mangled in between (e.g. in mail), and works because ASCII is a proper subset of most 8-bit and multibyte encodings in use in the world.

=head1 BUGS

Please report bugs on a specific behavior of this module to RT or GitHub issues (preferred):

L<https://github.com/makamaka/JSON-PP/issues>


As for new features and requests to change common behaviors, please ask the author of JSON::XS (Marc Lehmann, E<lt>schmorp[at]schmorp.deE<gt>) first, by email (important!), to keep compatibility among JSON.pm backends.

Generally speaking, if you need something special for you, you are advised to create a new module, maybe based on L<JSON::Tiny>, which is smaller and written in a much cleaner way than this module.

=head1 SEE ALSO

The F<json_pp> command line utility for quick experiments.

L<JSON::XS>, L<Cpanel::JSON::XS>, and L<JSON::Tiny> for faster alternatives. L<JSON> and L<JSON::MaybeXS> for easy migration.
RFC4627 (L<http://www.ietf.org/rfc/rfc4627.txt>)

RFC7159 (L<http://www.ietf.org/rfc/rfc7159.txt>)

RFC8259 (L<http://www.ietf.org/rfc/rfc8259.txt>)

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=cut

Found in path(s):
* /opt/cola/permits/1228286751_1637032133.21/0/libjson-perl-4-03000-orig-1-tar-gz/JSON-4.03/lib/JSON/backportPP.pm
No license file was found, but licenses were detected in source scan.

use strict;
use warnings;
use Benchmark qw( cmpthese timethese );

our $VERSION = '1.00';

my $wanttime = $ARGV[1] || 5;

use JSON qw( -support_by_pp -no_export ); # for JSON::PP::Boolean inheritance
use JSON::PP ();
use JSON::XS ();
use utf8;

my $pp = JSON::PP->new->utf8;
my $xs = JSON::XS->new->utf8;
local $/;

my $json = <>;
my $perl = JSON::XS::decode_json $json;
my $result;

printf( "JSON::PP %s\n", JSON::PP->VERSION );
printf( "JSON::XS %s\n", JSON::XS->VERSION );

print "-----------------------------\n";
print "->encode()\n";
print "-----------------------------\n";

$result = timethese( -$wanttime,
    {   'JSON::PP' => sub { $pp->encode( $perl ) },
        'JSON::XS' => sub { $xs->encode( $perl ) },
    },
    'none'
);
cmpthese( $result );

print "-----------------------------\n";
print "->pretty->canonical->encode()\n";
print "-----------------------------\n";

$pp->pretty->canonical;
$xs->pretty->canonical;

$result = timethese( -$wanttime,
    {   'JSON::PP' => sub { $pp->encode( $perl ) },
        'JSON::XS' => sub { $xs->encode( $perl ) },
    },
    'none'
);
cmpthese( $result );

print "-----------------------------\n";

__END__

=pod
=head1 SYNOPSIS
bench_encode.pl json-file
    # or
bench_encode.pl json-file minimum-time

=head1 DESCRIPTION

L<JSON::PP> and L<JSON::XS> encoding benchmark.

=head1 AUTHOR

makamaka

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Found in path(s):
* /opt/cola/permits/1228286751_1637032133.21/0/libjson-perl-4-03000-orig-1-tar-gz/JSON-4.03/eg/bench_encode.pl
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=head1 NAME

JSON::PP::Boolean - dummy module providing JSON::PP::Boolean

=head1 SYNOPSIS

# do not "use" yourself

=head1 DESCRIPTION

This module exists only to provide overload resolution for Storable and similar modules. See L<JSON::PP> for more info about this class.

=head1 AUTHOR

This idea is from L<JSON::XS::Boolean> written by Marc Lehmann <schmor[at]schmor.de>

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=pod

=head1 NAME

JSON::PP56 - Helper module in using JSON::PP in Perl 5.6

=head1 DESCRIPTION

JSON::PP calls internally.

=head1 AUTHOR

Makamaka Hannyaharamitu, E<lt>makamaka[at]cpan.orgE<gt>

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Found in path(s):
* /opt/cola/permits/1228286751_1637032133.21/0/libjson-perl-4-03000-orig-1-tar-gz/JSON-4.03/lib/JSON/backportPP/Compat5006.pm

1.218 python-setuptools 59.6.0

1.218.1 Available under license :

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1.219 patch 2.7.6 3+deb10u1

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1.220 perl 5.28.1-6+deb10u1

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cpan/CPAN-Meta/lib/CPAN/Meta/History/Meta_1_2.pod
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ext/GDBM_File/*
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ext/PerlIO-scalar/*
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1.223 go-runtime 1.14

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1.226 adduser 3.118

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Found in path(s):
* `/opt/ws_local/PERMITS_SQL/1047774514_1591431535.84/0/isarray-1-0-0-4-tgz/package/README.md`

### 1.228 @emotion/babel-plugin 11.1.2

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1.232 gopkg.in/fsnotify 1.4.7

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1.236 lines-and-columns 1.1.6

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1.237 influxdata-wlog 0.0.0-20160411224016-7c63b0a71ef8

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1.238 azure-pipeline-go 0.2.3

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1.239 lzo 2.10-2

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1.242 dictionaries-common 1.28.1

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1.243 icu 67.1-r0

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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)
## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<
%
%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
%

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\(GEN\_DEPS.c\) $< \n| sed \"s/\(\$\*\)\.o[:]+\!\!A\!\!L\!\!o\!\! o\!\! @ : /g\" \> $@; \n\[ -s $@ ] || rm -f $@’
%
%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\(GEN\_DEPS.cc\) $< \n| sed \"s/\(\$\*\)\.o[:]+\!\!A\!\!L\!\!o\!\! o\!\! @ : /g\" \> $@; \n\[ -s $@ ] || rm -f $@’
## Versioned libraries rules

%.$(SO).$(SO_TARGET_VERSION_MAJOR); %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s $<F> $@
%.$(SO); %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s $*F.$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbiotic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup

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1.248 emacsen-common 3.0.4

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1.251 selinux 2.8-1+b1

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* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1-1-tar-gz/webencodings-0.5.1/webencodings/x_user_defined.py
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1-1-tar-gz/webencodings-
from setuptools import setup, find_packages
import io
import os
import re

VERSION = re.search("VERSION = '([^']+)'", io.open("path.join(path.dirname(__file__), 'webencodings', '__init__.py'), encoding='utf-8').read().strip()).group(1)

LONG_DESCRIPTION = io.open("path.join(path.dirname(__file__), 'README.rst'), encoding='utf-8').read()

setup(
    name='webencodings',
    version=VERSION,
    url='https://github.com/SimonSapin/python-webencodings',
    license='BSD',
    author='Simon Sapin',
    author_email='simon.sapin@exyr.org',
    maintainer='Geoffrey Sneddon',
    maintainer_email='me@gstudier.com',
    description='Character encoding aliases for legacy web content',
    long_description=LONG_DESCRIPTION,
    classifiers=[
        'Development Status :: 4 - Beta',
        'Intended Audience :: Developers',
        'License :: OSI Approved :: BSD License',
        'Programming Language :: Python',
        'Programming Language :: Python :: 2',
        'Programming Language :: Python :: 2.6',
        'Programming Language :: Python :: 2.7',
        'Programming Language :: Python :: 3',
        'Programming Language :: Python :: 3.3',
        'Programming Language :: Python :: 3.4',
        'Programming Language :: Python :: 3.5',
        'Programming Language :: Python :: 3.6',
    ]
)
Regenerate the webencodings.labels module.

.. code-block::

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.. code-block::

   import json
   try:
       from urllib import urlopen
   except ImportError:
       from urllib.request import urlopen

   def assert_lower(string):
       assert string == string.lower()
       return string

   def generate(url):
       parts = '{"\n
Map encoding labels to their name.

.. code-block::

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.. code-block::
# XXX Do not edit!
# This file is automatically generated by mklabels.py

LABELS = {
    ''
}
labels = [
    (repr(assert_lower(label)).lstrip('u'),
     repr(encoding['name']).lstrip('u'))
    for category in json.loads(urlopen(url).read().decode('ascii'))
    for encoding in category['encodings']
    for label in encoding['labels']]
max_len = max(len(label) for label, name in labels)
parts.extend(
    '    %s:%s %s,
' % (label, ' ' * (max_len - len(label)), name)
    for label, name in labels)
parts.append('}
return ''.join(parts)

if __name__ == '__main__':
    print(generate('http://encoding.spec.whatwg.org/encodings.json'))

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* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1/webencodings/mklabels.py
No license file was found, but licenses were detected in source scan.

Metadata-Version: 1.1
Name: webencodings
Version: 0.5.1
Summary: Character encoding aliases for legacy web content
Home-page: https://github.com/SimonSapin/python-webencodings
Author: Geoffrey Sneddon
Author-email: me@gsnedders.com
License: BSD
Description: python-webencodings

This is a Python implementation of the `WHATWG Encoding standard
<http://encoding.spec.whatwg.org/>`_.

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker:
  https://github.com/gsnedders/python-webencodings
* PyPI releases: http://pypi.python.org/pypi/webencodings
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* Python 2.6+ and 3.3+
In order to be compatible with legacy web content when interpreting something like `Content-Type: text/html; charset=latin1`,
tools need to use a particular set of aliases for encoding labels as well as some overriding rules.
For example, `US-ASCII` and `iso-8859-1` on the web are actually aliases for `windows-1252`, and an UTF-8 or UTF-16 BOM takes precedence over any other encoding declaration.
The Encoding standard defines all such details so that implementations do not have to reverse-engineer each other.

This module has encoding labels and BOM detection, but the actual implementation for encoders and decoders is Python.

Platform: UNKNOWN
Classifier: Development Status :: 4 - Beta
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Classifier: Programming Language :: Python :: 3.6
Classifier: Programming Language :: Python :: Implementation :: CPython
Classifier: Programming Language :: Python :: Implementation :: PyPy
Classifier: Topic :: Internet :: WWW/HTTP

Found in path(s):
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1.tar.gz/webencodings-0.5.1/PKG-INFO
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1/webencodings.egg-info/PKG-INFO
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python-webencodings

This is a Python implementation of the `WHATWG Encoding standard <http://encoding.spec.whatwg.org/>`

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
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In order to be compatible with legacy web content when interpreting something like "Content-Type: text/html; charset=latin1", tools need to use a particular set of aliases for encoding labels as well as some overriding rules.

For example, "US-ASCII" and "iso-8859-1" on the web are actually aliases for "windows-1252", and an UTF-8 or UTF-16 BOM takes precedence over any other encoding declaration.

The Encoding standard defines all such details so that implementations do not have to reverse-engineer each other.

This module has encoding labels and BOM detection, but the actual implementation for encoders and decoders is Pythons.

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1.259 bind 9.11.5.P4+dfsg-5.1+deb10u6

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* configure.ac, Makefile.am: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

Author: Julio Merino <jmmv@users.sourceforge.net>

* atf-c/ui.c: The format_paragraph and format_text functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5ccf2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp: These files were derived from the file_handle, systembuf, pipe and pistream classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* admin/check-style.sh, admin/check-style-common.awk, admin/check-style-cpp.awk, admin/check-style-shell.awk: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

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1.265 libcap-ng 0.7.7 3.1

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1.269 mawk 1.3.3 17ubuntu3

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1.270 rsyslog 8.1901.01

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A. HISTORY OF THE SOFTWARE
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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Thanks to the many outside volunteers who have worked under Guido's direction to make these releases possible.

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1.286 util-linux 2.37-r0

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When a "work that uses the Library" uses material from a header file
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derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
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If such an object file uses only numerical parameters, data
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functions (ten lines or less in length), then the use of the object
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Otherwise, if the work is a derivative of the Library, you may
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1.289 musl 1.2.2-r3
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1.290 redux-persist 5.10.0

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However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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The End

1.299 string-prototype-trimend 1.0.1

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1.306 insomniacslk-dhcp 0.0.0-20210120172423-cc9239ac6294

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** uuid.c: PostgreSQL Binding (C part)
*/

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###
### uuid.pod: Perl Binding (Perl/POD part)
###
### =pod
OSSP::uuid - B<OSSP uuid> Perl Binding

DESCRIPTION

B<OSSP uuid> is a ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and RFC 4122 compliant I<Universally Unique Identifier> (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

B<OSSP:uuid> is the Perl binding to the B<OSSP uuid> API. Three variants are provided:

TIE-STYLE API

The TIE-style API is a functionality-reduced wrapper around the OO-style API and intended for very high-level convenience programming:

```
use OSSP::uuid;

tie $uuid, 'OSSP::uuid::tie', $mode, ...

$uuid = [ $mode, ... ];

print "UUID=$uuid\n";

untie $uuid;
```

OO-STYLE API

The OO-style API is a wrapper around the C-style API and intended for high-level regular programming.

```
use OSSP::uuid;
```
Additionally, the strings \texttt{"v1"}, \texttt{"v3"}, \texttt{"v4"}, \texttt{"v5"} and \texttt{"mc"} can be used in \texttt{$mode$} and the strings \texttt{"bin"}, \texttt{"str"}, and \texttt{"txt"} can be used for \texttt{$fmt$}.

\section*{C-STYLE API}

The C-style API is a direct mapping of the B\texttt{<OSSP uuid>} ISO-C API to Perl and is intended for low-level programming. See \texttt{uuid(3)} for a description of the functions and their expected arguments.
Additionally, the following constants are exported for use in `$rc`, `$mode`, `$fmt` and `$ver`:

- `UUID_VERSION`
- `UUID_LENGTH_BIN`
- `UUID_LENGTH_STR`
- `UUID_OK`
- `UUID_ARG`
- `UUID_MEM`
- `UUID_SYS`
- `UUID_INT`
- `UUID_IMP`
- `UUID_MAKE_V1`
- `UUID_MAKE_V3`
- `UUID_MAKE_V4`
- `UUID_MAKE_V5`
- `UUID_MAKE_MC`
- `UUID_FMT_BIN`
- `UUID_FMT_STR`
- `UUID_FMT_SIV`
- `UUID_FMT_TXT`

=head1 EXAMPLES

The following two examples create the version 3 UUID
C<02d9e6d5-9467-382e-8f9b-9300a64ac3cd>, both via the OO-style and the
C-style API. Error handling is omitted here for easier reading, but has
to be added for production-quality code.

# TIE-style API (very high-level)
use OSSP::uuid;
tie my $uuid, 'OSSP::uuid::tie';
$uuid = [ "v1" ];
print "UUIDs: $uuid, $uuid, $uuid\n";
$uuid = [ "v3", "ns:URL", "http://www.ossp.org/" ];
print "UUIDs: $uuid, $uuid, $uuid\n";
untie $uuid;
use OSSP::uuid;
my $uuid    = new OSSP::uuid;
my $uuid_ns = new OSSP::uuid;
$uuid_ns->load("ns:URL");
$uuid->make("v3", $uuid_ns, "http://www.ossp.org/");
undef $uuid_ns;
my $str = $uuid->export("str");
undef $uuid;
print "$str\n";

use OSSP::uuid qw(:all);
my $uuid;    uuid_create($uuid);
my $uuid_ns; uuid_create($uuid_ns);
uuid_load($uuid_ns, "ns:URL");
uuid_make($uuid, UUID_MAKE_V3, $uuid_ns, "http://www.ossp.org/");
uuid_destroy($uuid_ns);
my $str; uuid_export($uuid, UUID_FMT_STR, $str, undef);
uuid_destroy($uuid);
print "$str\n";

=head1 SEE ALSO

uuid(1), uuid-config(1), uuid(3).

=head1 HISTORY

The Perl binding B<OSSP::uuid> to B<OSSP uuid> was implemented in
November 2004 by Ralf S. Engelschall E<lt>rse@engelschall.comE<gt>.

=cut

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**
** uuid_ac.c: auto-configuration
*/

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## NAME

B OSSP uuid - B Universally Unique Identifier (C++ API)

## VERSION

OSSP uuid UUID_VERSION_STR

## DESCRIPTION

B uuid++ is the ISO-C++ language binding of the B OSSP uuid C API. It provides a thin ISO-C++ class B uuid wrapping the ISO-C API type B uuid_t.

## APPLICATION PROGRAMMING INTERFACE

The ISO-C++ Application Programming Interface (API) of B OSSP uuid consists of the following components:

## CONSTANTS

The constants are the same to those provided by the ISO-C API. See uuid(3) for details.

## CLASSES

The following classes are provided:

- **B uuid**
  
  This is the class corresponding to the C API type B uuid_t. It is the main object.

- **B uuid_error_t**
  
  This is the class corresponding to the C API function B uuid_error. It is the object thrown as an exception in case of any errors.
The following methods are provided:

\texttt{=over 4}

\texttt{=item B\textless uuid\textgreater ()};

The standard constructor.

\texttt{=item B\textless uuid\textgreater (const uuid &\_obj)};

The copy constructor for \texttt{B\textless uuid\textgreater} class.

\texttt{=item B\textless uuid\textgreater (const uuid\_t *\_obj)};

The import constructor for C API objects.

\texttt{=item B\textless uuid\textgreater (const void *\_bin)};

The import constructor for binary representation.

\texttt{=item B\textless uuid\textgreater (const char *\_str)};

The import constructor for string representation.

\texttt{=item ~B\textless uuid\textgreater ();}

The standard destructor for \texttt{B\textless uuid\textgreater} class.

\texttt{=item uuid &B\textless uuid\textgreater ::operator\=>(const uuid &\_obj)};

The assignment operator corresponding to the copy constructor.

\texttt{=item uuid &B\textless uuid\textgreater ::operator\=>(const uuid\_t *\_obj)};

The assignment operator corresponding to the import constructor for C API objects.

\texttt{=item uuid &B\textless uuid\textgreater ::operator\=>(const void *\_bin)};

The assignment operator corresponding to the import constructor for binary representation.

\texttt{=item uuid &B\textless uuid\textgreater ::operator\=>(const char *\_str)};

The assignment operator corresponding to the import constructor for string and single integer value representation.

\texttt{=item uuid B\textless uuid\textgreater ::clone\>(void)};

Regular method corresponding to the C API function \texttt{B\textless uuid\_clone\>}. 

---

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=item void B<uuid::load>(const char * _name);

Regular method corresponding to the C API function B<uuid_load>.

=item void B<uuid::make>(unsigned int _mode, ...);

Regular method corresponding to the C API function B<uuid_make>.

=item int B<uuid::isnil>(void);

Regular method corresponding to the C API function B<uuid_isnil>.

=item int B<uuid::compare>(const uuid & _obj);

Regular method corresponding to the C API function B<uuid_compare>.

=item int B<uuid::operator==>(const uuid & _obj);

The comparison operator corresponding to B<uuid_compare> usage for equality.

=item int B<uuid::operator!=(const uuid & _obj);

The comparison operator corresponding to B<uuid_compare> usage for inequality.

=item int B<uuid::operatorE<lt>>(const uuid & _obj);

The comparison operator corresponding to B<uuid_compare> usage for less-than.

=item int B<uuid::operatorE<lt>=>(const uuid & _obj);

The comparison operator corresponding to B<uuid_compare> usage for less-than-or-equal.

=item int B<uuid::operatorE<gt>>(const uuid & _obj);

The comparison operator corresponding to B<uuid_compare> usage for greater-than.

=item int B<uuid::operatorE<gt>=>(const uuid & _obj);

The comparison operator corresponding to B<uuid_compare> usage for greater-than-or-equal.

=item void B<uuid::import>(const void * _bin);

Regular method corresponding to the C API function B<uuid_import> for binary representation usage.

=item void B<uuid::import>(const char * _str);

Regular method corresponding to the C API function B<uuid_import> for string representation usage.
Regular method corresponding to the C API function `uuid::binary` for binary representation usage.

Regular method corresponding to the C API function `uuid::string` for string representation usage.

Regular method corresponding to the C API function `uuid::integer` for single integer value representation usage.

Regular method corresponding to the C API function `uuid::summary` for textual summary representation usage.

Regular method corresponding to the C API function `uuid::version`.

The standard constructor for `uuid_error_t` class.

The standard constructor for `uuid_error_t` class with return code initialization.

The standard destructor for `uuid_error_t` class.

Regular method for setting the return code.

Regular method for fetching the return code.

Regular method for fetching the string corresponding to the current return code.

=back

=head1 EXAMPLE
The following shows an example usage of the C++ API. Exception handling is omitted for code simplification and has to be re-added for production code.

/* generate a DCE 1.1 v1 UUID from system environment */
char *uuid_v1(void)
{
    uuid id;
    char *str;

    id.make(UUID_MAKE_V1);
    str = id.string();
    return str;
}

/* generate a DCE 1.1 v3 UUID from an URL */
char *uuid_v3(const char *url)
{
    uuid id;
    uuid id_ns;
    char *str;

    id_ns.load("ns:URL");
    id.make(UUID_MAKE_V3, &id_ns, url);
    str = id.string();
    return str;
}

=head1 SEE ALSO

uuid(3).

=cut

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**
** uuid.h: library API definition
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** uuid_mac.h: Media Access Control (MAC) resolver API definition
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**
** ui64.c: implementation of 64-bit unsigned integer arithmetic
*/

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** uuid_time.h: Time Management API
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** uuid_str.c: string formatting functions
*/

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**
** uuid_dce.c: DCE 1.1 compatibility API implementation

*/

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** uuid_str.h: string formatting functions
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--
-- uuid.sql: PostgreSQL Binding (SQL part)
--
--
-- prolog
DROP FUNCTION uuid_in(CSTRING) CASCADE;
DROP FUNCTION uuid_out(uuid) CASCADE;
DROP FUNCTION uuid_recv(INTERNAL) CASCADE;
DROP FUNCTION uuid_send(uuid) CASCADE;
DROP TYPE uuid CASCADE;
DROP CAST (CSTRING AS uuid) CASCADE;
DROP CAST (uuid AS CSTRING) CASCADE;

DROP FUNCTION uuid(CSTRING) CASCADE;
DROP FUNCTION uuid(INTEGER) CASCADE;
DROP FUNCTION uuid(INTEGER, CSTRING, CSTRING) CASCADE;

DROP FUNCTION uuid_eq(uuid) CASCADE;
DROP FUNCTION uuid_ne(uuid) CASCADE;
DROP FUNCTION uuid_lt(uuid, uuid) CASCADE;
DROP FUNCTION uuid_gt(uuid, uuid) CASCADE;
DROP FUNCTION uuid_le(uuid, uuid) CASCADE;
DROP FUNCTION uuid_ge(uuid, uuid) CASCADE;
DROP OPERATOR =(uuid,uuid) CASCADE;
DROP OPERATOR <>(uuid,uuid) CASCADE;
DROP OPERATOR <(uuid,uuid) CASCADE;
DROP OPERATOR >(uuid,uuid) CASCADE;
DROP OPERATOR <=(uuid,uuid) CASCADE;
DROP OPERATOR >=(uuid,uuid) CASCADE;

DROP FUNCTION uuid_hash(uuid) CASCADE;
DROP FUNCTION uuid_cmp(uuid, uuid) CASCADE;
DROP OPERATOR CLASS uuid_ops USING hash CASCADE;
DROP OPERATOR CLASS uuid_ops USING btree CASCADE;

BEGIN;

--
-- the UUID data type
--

CREATE FUNCTION uuid_in(CSTRING) RETURNS uuid
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_in';

CREATE FUNCTION uuid_out(uuid) RETURNS CSTRING
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_out';
CREATE FUNCTION
    uuid_recv(INternal) RETURNS uuid
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_recv';

CREATE FUNCTION
    uuid_send(uuid) RETURNS BYTEA
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_send';

CREATE TYPE uuid (
    INPUT   = uuid_in,   -- for SQL input
    OUTPUT  = uuid_out,  -- for SQL output
    RECEIVE = uuid_recv, -- for DB input
    SEND    = uuid_send, -- for DB output
    DEFAULT = 'uuid(1)',
    INTERNALLENGTH = 16,
    ALIGNMENT = char
);

COMMENT ON TYPE uuid
    IS 'UUID type';

-- CREATE CAST (CSTRING AS uuid)
--     WITH FUNCTION uuid_in(CSTRING) AS ASSIGNMENT;
--
-- CREATE CAST (uuid AS CSTRING)
--     WITH FUNCTION uuid_out(uuid) AS ASSIGNMENT;
--
-- the UUID constructor function
--

CREATE FUNCTION
    uuid(CSTRING) RETURNS uuid
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_in';

CREATE FUNCTION
    uuid(INTEGER) RETURNS uuid
VOLATILE CALLED ON NULL INPUT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_make';

CREATE FUNCTION
    uuid(INTEGER, CSTRING, CSTRING) RETURNS uuid
VOLATILE CALLED ON NULL INPUT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_make';
--
-- the UUID operators
--

CREATE FUNCTION
    uuid_eq(uuid, uuid) RETURNS BOOL
  IMMUtable STRICT
  LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_eq';

CREATE FUNCTION
    uuid_ne(uuid, uuid) RETURNS BOOL
  IMMUtable STRICT
  LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_ne';

CREATE FUNCTION
    uuid_lt(uuid, uuid) RETURNS BOOL
  IMMUtable STRICT
  LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_lt';

CREATE FUNCTION
    uuid_gt(uuid, uuid) RETURNS BOOL
  IMMUtable STRICT
  LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_gt';

CREATE FUNCTION
    uuid_le(uuid, uuid) RETURNS BOOL
  IMMUtable STRICT
  LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_le';

CREATE FUNCTION
    uuid_ge(uuid, uuid) RETURNS BOOL
  IMMUtable STRICT
  LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_ge';

CREATE OPERATOR = (  
    lefTarg = uuid,  
    rightarg = uuid,  
    negator = <>,  
    procedure = uuid_eq
);

CREATE OPERATOR <> (  
    lefTarg = uuid,  
    rightarg = uuid,  
    negator = =,  
    procedure = uuid_ne
);
CREATE OPERATOR < (  
    leftarg  = uuid,  
    rightarg = uuid,  
    commutator = >,  
    negator   = >=,  
    procedure = uuid_lt
);

CREATE OPERATOR > (  
    leftarg  = uuid,  
    rightarg = uuid,  
    commutator = <,  
    negator   = <=,  
    procedure = uuid_gt
);

CREATE OPERATOR <= (  
    leftarg  = uuid,  
    rightarg = uuid,  
    commutator = >=,  
    negator   = >,  
    procedure = uuid_le
);

CREATE OPERATOR >= (  
    leftarg  = uuid,  
    rightarg = uuid,  
    commutator = <=,  
    negator   = <,  
    procedure = uuid_ge
);

--
-- the UUID support for indexing
--

CREATE FUNCTION uuid_hash(uuid) RETURNS INTEGER  
IMMUTABLE STRICT  
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_hash';

CREATE FUNCTION uuid_cmp(uuid, uuid) RETURNS INTEGER  
IMMUTABLE STRICT  
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_cmp';

CREATE OPERATOR CLASS uuid_ops
DEFAULT FOR TYPE uuid USING hash AS
OPERATOR 1 =, -- 1: equal
FUNCTION 1 uuid_hash(uuid);

CREATE OPERATOR CLASS uuid_ops
DEFAULT FOR TYPE uuid USING btree AS
OPERATOR 1 <, -- 1: less than
OPERATOR 2 <=, -- 2: less than or equal
OPERATOR 3 =, -- 3: equal
OPERATOR 4 >=, -- 4: greater than or equal
OPERATOR 5 >, -- 5: greater than
FUNCTION 1 uuid_cmp(uuid, uuid);

--
--  epilog
--

COMMIT;

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** ui128.h: API declaration
*/

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** uuid_mac.c: Media Access Control (MAC) resolver implementation
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uuid.c: library API implementation

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** uuid_sha1.c: SHA-1 API implementation
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** uuid_time.c: Time Management
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uuid_sha1.h: SHA-1 API definition
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** uuid++: library C++ API implementation
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** uuid.c: PHP/Zend API (language: C)
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## uuid.tm: Perl XS typemap for xsubpp(1)

##
## TYPEMAP
## uuid_t * T_PTRREF
## uuid_t ** T_PTRREF
## uuid_rc_t T_IV
## uuid_fmt_t T_IV
## int * T_PV
## size_t * T_PV
## const void * T_PV
## void ** T_PV

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/perl/uuid.tm

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##
## OSSP uuid - Universally Unique Identifier
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## Copyright (c) 2004-2008 The OSSP Project <http://www.ossp.org/>
##
## This file is part of OSSP uuid, a library for the generation
## of UUIDs which can found at http://www.ossp.org/pkg/lib/uuid/
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BOSSP uuid - Universally Unique Identifier

OSSP uuid UUID_VERSION_STR

This is the ISO-C application programming interface (API) of BOSSP uuid.

According to the DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 standards, a DCE 1.1 variant UUID is a 128 bit number defined out of 7 fields, each field a multiple of an octet in size and stored in network byte order:
An example of a UUID binary representation is the octet stream C<0xF8 0x1D 0x4F 0xAE 0x7D 0xEC 0x11 0xD0 0xA7 0x65 0x00 0xA0 0xC9 0x1E 0x6B 0xF6>. The binary representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_BIN>.

=head2 UUID ASCII String Representation

According to the DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 standards, a DCE 1.1 variant UUID is represented as an ASCII string consisting of 8 hexadecimal digits followed by a hyphen, then three groups of 4 hexadecimal digits each followed by a hyphen, then 12 hexadecimal digits. Formally, the string representation is defined by
the following grammar:

```
uuid                        = <time_low> "-" <time_mid> "-" <time_high_and_version> "-" <clock_seq_high_and_reserved> <clock_seq_low> "-" <node>
  time_low                    = 4*<hex_octet>
  time_mid                    = 2*<hex_octet>
  time_high_and_version       = 2*<hex_octet>
  clock_seq_high_and_reserved = <hex_octet>
  clock_seq_low               = <hex_octet>
  node                        = 6*<hex_octet>
  hex_octet                   = <hex_digit> <hex_digit>
  hex_digit                   = "0"|"1"|"2"|"3"|"4"|"5"|"6"|"7"|"8"|"9"
                                |"a"|"b"|"c"|"d"|"e"|"f"
                                |"A"|"B"|"C"|"D"|"E"|"F"
```

An example of a UUID string representation is the ASCII string "C<f81d4fae-7dec-11d0-a765-00a0c91e6bf6>". The string representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_STR>.

Notice: a corresponding URL can be generated out of a ASCII string representation of an UUID by prefixing with "C<urn:uuid:>". An example of a UUID single integer value representation is the decimal number "C<329800735698586629295641978511506172918>". The string representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_SIV>.

Notice: a corresponding ISO OID can be generated under the "{joint-iso-itu-t(2) uuid(25)}" arc out of a single integer value representation of a UUID by prefixing with "C<2.25.". An example OID is "C<2.25.329800735698586629295641978511506172918>". Additionally, an URL can be generated by further prefixing with "C<urn:oid:>", as in "C<urn:oid:2.25.329800735698586629295641978511506172918>".

=head2 UUID Single Integer Value Representation

According to the ISO/IEC 11578:1996 and ITU-T Rec. X.667 standards, a DCE 1.1 variant UUID can be also represented as a single integer value consisting of a decimal number with up to 39 digits.

An example of a UUID single integer value representation is the decimal number "C<329800735698586629295641978511506172918>". The string representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_SIV>.

Notice: a corresponding ISO OID can be generated under the "{joint-iso-itu-t(2) uuid(25)}" arc out of a single integer value representation of a UUID by prefixing with "C<2.25.". An example OID is "C<2.25.329800735698586629295641978511506172918>". Additionally, an URL can be generated by further prefixing with "C<urn:oid:>", as in "C<urn:oid:2.25.329800735698586629295641978511506172918>".

=head2 UUID Variants and Versions
A UUID has a variant and version. The variant defines the layout of the UUID. The version defines the content of the UUID. The UUID variant supported in BOSSP uuid is the DCE 1.1 variant only. The DCE 1.1 UUID variant versions supported in BOSSP uuid are:

=over 4

=item B<Version 1> (time and node based)

These are the classical UUIDs, created out of a 60-bit system time, a 14-bit local clock sequence and 48-bit system MAC address. The MAC address can be either the real one of a physical network interface card (NIC) or a random multi-cast MAC address. Version 1 UUIDs are usually used as one-time global unique identifiers.

=item B<Version 3> (name based, MD5)

These are UUIDs which are based on the 128-bit MD5 message digest of the concatenation of a 128-bit namespace UUID and a name string of arbitrary length. Version 3 UUIDs are usually used for non-unique but repeatable message digest identifiers.

=item B<Version 4> (random data based)

These are UUIDs which are based on just 128-bit of random data. Version 4 UUIDs are usually used as one-time local unique identifiers.

=item B<Version 5> (name based, SHA-1)

These are UUIDs which are based on the 160-bit SHA-1 message digest of the concatenation of a 128-bit namespace UUID and a name string of arbitrary length. Version 5 UUIDs are usually used for non-unique but repeatable message digest identifiers.

=back

=head2 UUID Uniqueness

Version 1 UUIDs are guaranteed to be unique through combinations of hardware addresses, time stamps and random seeds. There is a reference in the UUID to the hardware (MAC) address of the first network interface card (NIC) on the host which generated the UUID -- this reference is intended to ensure the UUID will be unique in space as the MAC address of every network card is assigned by a single global authority (IEEE) and is guaranteed to be unique. The next component in a UUID is a timestamp which, as clock always (should) move forward, will be unique in time. Just in case some part of the above goes wrong (the hardware address cannot be determined or the clock moved steps
backward), there is a random clock sequence component placed into the UUID as a "catch-all" for uniqueness.

Version 3 and version 5 UUIDs are guaranteed to be inherently globally unique if the combination of namespace and name used to generate them is unique.

Version 4 UUIDs are not guaranteed to be globally unique, because they are generated out of locally gathered pseudo-random numbers only. Nevertheless there is still a high likelihood of uniqueness over space and time and that they are computationally difficult to guess.

=head2 Nil UUID

There is a special I<Nil> UUID consisting of all octets set to zero in the binary representation. It can be used as a special UUID value which does not conflict with real UUIDs.

=head1 APPLICATION PROGRAMMING INTERFACE

The ISO-C Application Programming Interface (API) of B<OSSP uuid> consists of the following components.

=head2 CONSTANTS

The following constants are provided:

=over 4

=item B<UUID_VERSION>

The hexadecimal encoded B<OSSP uuid> version. This allows compile-time checking of the B<OSSP uuid> version. For run-time checking use B<uuid_version>() instead.

The hexadecimal encoding for a version "$I<v>.$I<r>.$I<t>.$I<l>" is calculated with the B<GNU shtool> B<version> command and is (in Perl-style for concise description) "sprintf('0x%x%2x%d%02x', $I<v>, $I<r>, {qw(s 9 . 2 b 1 a 0})->[[$I<t>], ($I<t> eq 's' ? 99 : $I<l>))", i.e., the version 0.9.6 is encoded as "0x009206".

=item B<UUID_LEN_BIN>, B<UUID_LEN_STR>, B<UUID_LEN_SIV>

The number of octets of the UUID binary and string representations. Notice that the lengths of the string representation (B<UUID_LEN_STR>) and the lengths of the single integer value representation (B<UUID_LEN_SIV>) does I<not> include the necessary C<NUL> termination character.
The I<mode> bits for use with B<uuid_make>(). The B<UUID_MAKE_V>V>N specify which UUID version to generate. The B<UUID_MAKE_MC> forces the use of a random multi-cast MAC address instead of the real physical MAC address in version 1 UUIDs.

The possible numerical return-codes of API functions. The C<UUID_RC_OK> indicates success, the others indicate errors. Use B<uuid_error>() to translate them into string versions.

The I<fmt> formats for use with B<uuid_import>() and B<uuid_export>(). The B<UUID_FMT_BIN> indicates the UUID binary representation (of length B<UUID_LEN_BIN>), the B<UUID_FMT_STR> indicates the UUID string representation (of length B<UUID_LEN_STR>), the B<UUID_FMT_SIV> indicates the UUID single integer value representation (of maximum length B<UUID_LEN_SIV>) and the B<UUID_FMT_TXT> indicates the textual description (of arbitrary length) of a UUID.

The following functions are provided:

Create a new UUID object and store a pointer to it in C<*I<uuid>. A UUID object consists of an internal representation of a UUID, the internal PRNG and MD5 generator contexts, and cached MAC address and timestamp information. The initial UUID is the I<Nil> UUID.

Destroy UUID object I<uuid>. 

Clone UUID object I<uuid> and store new UUID object in I<uuid_clone>. 

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=item uuid_rc_t B<uuid_isnil>(const uuid_t *I<uuid>, int *I<result>);

Checks whether the UUID in I<uuid> is the I<Nil> UUID. If this is the case, it returns I<true> in C<*I<result>>. Else it returns I<false> in C<*I<result>>.

=item uuid_rc_t B<uuid_compare>(const uuid_t *I<uuid>, const uuid_t *I<uuid2>, int *I<result>);

Compares the order of the two UUIDs in I<uuid1> and I<uuid2> and returns the result in C<*I<result>>: C<-1> if I<uuid1> is smaller than I<uuid2>, C<0> if I<uuid1> is equal to I<uuid2> and C<+1> if I<uuid1> is greater than I<uuid2>.

=item uuid_rc_t B<uuid_import>(uuid_t *I<uuid>, uuid_fmt_t I<fmt>, const void *I<data_ptr>, size_t I<data_len>);

Imports a UUID I<uuid> from an external representation of format I<fmt>. The data is read from the buffer at I<data_ptr> which contains at least I<data_len> bytes.

The format of the external representation is specified by I<fmt> and the minimum expected length in I<data_len> depends on it. Valid values for I<fmt> are B<UUID_FMT_BIN>, B<UUID_FMT_STR> and B<UUID_FMT_SIV>.

=item uuid_rc_t B<uuid_export>(const uuid_t *I<uuid>, uuid_fmt_t I<fmt>, void *I<data_ptr>, size_t *I<data_len>);

Exports a UUID I<uuid> into an external representation of format I<fmt>. Valid values for I<fmt> are B<UUID_FMT_BIN>, B<UUID_FMT_STR>, B<UUID_FMT_SIV> and B<UUID_FMT_TXT>.

The data is written to the buffer whose location is obtained by dereferencing I<data_ptr> after a "cast" to the appropriate pointer-to-pointer type. Hence the generic pointer argument I<data_ptr> is expected to be a pointer to a "pointer of a particular type", i.e., it has to be of type "C<unsigned char **>" for B<UUID_FMT_BIN> and "C<char **>" for B<UUID_FMT_STR>, B<UUID_FMT_SIV> and B<UUID_FMT_TXT>.

The buffer has to be room for at least C<*I<data_len>> bytes. If the value of the pointer after "casting" and dereferencing I<data_ptr> is C<NULL>, I<data_len> is ignored as input and a new buffer is allocated and returned in the pointer after "casting" and dereferencing I<data_ptr> (the caller has to free(3) it later on).

If I<data_len> is not C<NULL>, the number of available bytes in the buffer has to be provided in C<*I<data_len>> and the number of actually written bytes are returned in C<*I<data_len>> again. The minimum
required buffer length depends on the external representation as specified by I<fmt> and is at least B<UUID_LEN_BIN> for B<UUID_FMT_BIN>, B<UUID_LEN_STR> for B<UUID_FMT_STR> and B<UUID_LEN_SIV> for B<UUID_FMT_SIV>. For B<UUID_FMT_TXT> a buffer of unspecified length is required and hence it is recommended to allow B<OSSP uuid> to allocate the buffer as necessary.

=item uuid_rc_t B<uuid_load>(uuid_t *I<uuid>, const char *I<name>);

Loads a pre-defined UUID value into the UUID object I<uuid>. The following I<name> arguments are currently known:

=over 4

=item I<name> I<UUID>

=item nil 00000000-0000-0000-0000-000000000000

=item ns:DNS 6ba7b810-9dad-11d1-80b4-00c04fd430c8

=item ns:URL 6ba7b811-9dad-11d1-80b4-00c04fd430c8

=item ns:OID 6ba7b812-9dad-11d1-80b4-00c04fd430c8

=item ns:X500 6ba7b814-9dad-11d1-80b4-00c04fd430c8

=back

The "C<ns:>I<XXX>" are names of pre-defined name-space UUIDs for use in the generation of DCE 1.1 version 3 and version 5 UUIDs.

=item uuid_rc_t B<uuid_make>(uuid_t *I<uuid>, unsigned int I<mode>, ...);

Generates a new UUID in I<uuid> according to I<mode> and optional arguments (dependent on I<mode>).

If I<mode> contains the C<UUID_MAKE_V1> bit, a DCE 1.1 variant UUID of version 1 is generated. Then optionally the bit C<UUID_MAKE_MC> forces the use of random multi-cast MAC address instead of the real physical MAC address (the default). The UUID is generated out of the 60-bit current system time, a 12-bit clock sequence and the 48-bit MAC address.

If I<mode> contains the C<UUID_MAKE_V3> or C<UUID_MAKE_V5> bit, a DCE 1.1 variant UUID of version 3 or 5 is generated and two additional arguments are expected: first, a namespace UUID object (C<uuid_t *>), Second, a name string of arbitrary length (C<const char *>). The UUID is generated out of the 128-bit MD5 or 160-bit SHA-1 from the concatenated octet stream of namespace UUID and name string.
If I<mode> contains the C<UUID_MAKE_V4> bit, a DCE 1.1 variant UUID of version 4 is generated. The UUID is generated out of 128-bit random data.

=item char *B<uuid_error>(uuid_rc_t I<rc>);

Returns a constant string representation corresponding to the return-code I<rc> for use in displaying B<OSSP uuid> errors.

=item unsigned long B<uuid_version>(void);

Returns the hexadecimal encoded B<OSSP uuid> version as compiled into the library object files. This allows run-time checking of the B<OSSP uuid> version. For compile-time checking use C<UUID_VERSION> instead.

=back

=head1 EXAMPLE

The following shows an example usage of the API. Error handling is omitted for code simplification and has to be re-added for production code.

/* generate a DCE 1.1 v1 UUID from system environment */
char *uuid_v1(void)
{
    uuid_t *uuid;
    char *str;

    uuid_create(&uuid);
    uuid_make(uuid, UUID_MAKE_V1);
    str = NULL;
    uuid_export(uuid, UUID_FMT_STR, &str, NULL);
    uuid_destroy(uuid);
    return str;
}

/* generate a DCE 1.1 v3 UUID from an URL */
char *uuid_v3(const char *url)
{
    uuid_t *uuid;
    uuid_t *uuid_ns;
    char *str;

    uuid_create(&uuid);
    uuid_create(&uuid_ns);
    uuid_load(uuid_ns, "ns:URL");
uuid_make(uuid, UUID_MAKE_V3, uuid_ns, url);
str = NULL;
uuid_export(uuid, UUID_FMT_STR, &str, NULL);
uuid_destroy(uuid_ns);
uuid_destroy(uuid);
return str;
}

SEE ALSO

The following are references to B<UUID> documentation and specifications:

=over 4

=item

B<A Universally Unique IDentifier (UUID) URN Namespace>,
P. Leach, M. Mealling, R. Salz,
IETF RFC-4122,
July 2005, 32 pages,
http://www.ietf.org/rfc/rfc4122.txt

=item

Information Technology -- Open Systems Interconnection (OSI),
B<Procedures for the operation of OSI Registration Authorities:
Generation and Registration of Universally Unique Identifiers (UUIDs)
and their Use as ASN.1 Object Identifier Components>,
December 2004, 25 pages,

=item

B<DCE 1.1: Remote Procedure Call>,
appendix B<Universally Unique Identifier>,
Open Group Technical Standard
Document Number C706, August 1997, 737 pages,
(supersedes C309 DCE: Remote Procedure Call 8/1994,
which was basis for ISO/IEC 11578:1996 specification),
http://www.opengroup.org/publications/catalog/c706.htm

=item

Information technology -- Open Systems Interconnection (OSI),
B<Remote Procedure Call (RPC)>,
ISO/IEC 11578:1996,
August 2001, 570 pages, (CHF 340.00),
B<HTTP Extensions for Distributed Authoring (WebDAV)>,
section B<6.4.1 Node Field Generation Without the IEEE 802 Address>,
IETF RFC-2518,
February 1999, 94 pages,
http://www.ietf.org/rfc/rfc2518.txt

=B<DCE 1.1 compliant UUID functions>,
FreeBSD manual pages uuid(3) and uuidgen(2),
http://www.freebsd.org/cgi/man.cgi?query=uuid&manpath=FreeBSD+6.0-RELEASE

=head1 HISTORY

B<OSSP uuid> was implemented in January 2004 by Ralf S. Engelschall
E<lt>rse@engelschall.comE<gt>. It was prompted by the use of UUIDs
in the B<OSSP as> and B<OpenPKG> projects. It is a clean room
implementation intended to be strictly standards compliant and maximum
portable.

=head1 SEE ALSO

uuid(1), uuid-config(1), OSSP::uuid(3).

=cut

Found in path(s):
  */opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid.pod
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 **
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uuid++.hh: library C++ API definition
*/

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid++.hh
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**
** uuid_prng.h: PRNG API definition
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*/

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//
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**
** This file is part of OSSP ui64, a 64-bit arithmetic library
** which can be found at http://www.ossp.org/pkg/lib/ui64/.
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**
** ui64.h: API declaration
*/

Found in path(s):
  */opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid_ui64.h
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##
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## of UUIDs which can found at http://www.ossp.org/pkg/lib/uuid/
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##
## uuid_cli.pod: manual page
##
#### NAME

B<OSSP uuid> - B<Universally Unique Identifier Command-Line Tool>

#### VERSION

OSSP uuid UUID_VERSION_STR

#### SYNOPSIS

B<uuid>
[B<-v> I<version>]
[B<-m>]
[B<-n> I<count>]
[B<-l>]
[B<-F> I<format>]
[B<-o> I<filename>]
[I<namespace> I<name>]

B<uuid>
B<-d>
[B<-r>]
[B<-o> I<filename>]
I<uuid>

#### DESCRIPTION

---
B OSSP uuid is a ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant Universally Unique Identifier (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood of uniqueness over space and time and are computationally difficult to guess. They are globally unique identifiers which can be locally generated without contacting a global registration authority. UUIDs are intended as unique identifiers for both mass tagging objects with an extremely short lifetime and to reliably identifying very persistent objects across a network.

This is the command line interface (CLI) of B OSSP uuid. For a detailed description of UUIDs see the documentation of the application programming interface (API) in uuid(3).

=head1 OPTIONS

=over 3

=item B <-v> I <version>

Sets the version of the generated DCE 1.1 variant UUID. Supported are I <version> "C<1>", "C<3>", "C<4>" and "C<5>". The default is "C<1>".

For version 3 and version 5 UUIDs the additional command line arguments I <namespace> and I <name> have to be given. The I <namespace> is either a UUID in string representation or an identifier for internally pre-defined namespace UUIDs (currently known are "C<ns:DNS>", "C<ns:URL>", "C<ns:OID>", and "C<ns:X500>"). The I <name> is a string of arbitrary length.

=item B <-m>

Forces the use of a random multi-cast MAC address when generating version 1 UUIDs. By default the real physical MAC address of the system is used.

=item B <-n> I <count>

Generate I <count> UUIDs instead of just a single one (the default).
If option B<-n> is used with a I<count> greater than C<1>, then this option can enforce the reset the UUID context for each generated UUID. This makes no difference for I<version> C<3>, C<4> and C<5> UUIDs. But version C<1> UUIDs are based on the previously generated UUID which is remembered in the UUID context of the API. Option B<-1> deletes the remembered UUID on each iteration.

=over 4
=item B<-F> I<format>

Representation format for importing or exporting an UUID. The following (case insensitive) format identifiers are currently recognized:

=back
=over 4
=item C<BIN> (binary representation)

This is the raw 128 bit network byte order binary representation of a UUID. Example is the octet stream C<0xF8 0x1D 0x4F 0xAE 0x7D 0xEC 0x11 0xD0 0xA7 0x65 0x00 0xA0 0xC9 0x1E 0x6B 0xF6>.

=back
=over 4
=item C<STR> (string representation)

This is the 36 character hexadecimal ASCII string representation of a UUID. Example is the string "C<f81d4fae-7dec-11d0-a765-00a0c91e6bf6>".

=back
=over 4
=item C<SIV> (single integer value representation)

This is the maximum 39 character long single integer value representation of a UUID. Example is the string "C<3298007356985866629295641978511506172918>".

=back
=over 4
=item B<-o> I<filename>

Write output to I<filename> instead of to F<stdout>.

=back

=head1 EXAMPLES

# generate DCE 1.1 v1 UUID (time and node based)
$ uuid -v1
01c47915-4777-11d8-bc70-0090272ff725

# decode and dump DCE 1.1 v1 UUID (time and node based)
$ uuid -d 01c47915-4777-11d8-bc70-0090272ff725
encode: STR: 01c47915-4777-11d8-bc70-0090272ff725
   SIV: 23493740375285788879230943747721111141
   version: 1 (time and node based)
   clock: 15472 (usually random)
   node: 00:90:27:2f:f7:25 (global unicast)

# generate DCE 1.1 v3 UUID (name based)
$ uuid -v3 ns:URL http://www.ossp.org/
02d9e6d5-9467-382e-8f9b-9300a64ac3cd

# decode and dump DCE 1.1 v3 UUID (name based)
$ uuid -d 02d9e6d5-9467-382e-8f9b-9300a64ac3cd
encode: STR: 02d9e6d5-9467-382e-8f9b-9300a64ac3cd
   SIV: 3789866285607910888100818383505376205
   version: 3 (name based, MD5)
   (not decipherable: MD5 message digest only)

# generate DCE 1.1 v4 UUID 4 (random data based)
$ uuid -v4
eb424026-6f54-4ef8-a4d0-bb658a1fc6cf

# decode and dump DCE 1.1 v4 UUID 4 (random data based)
$ uuid -d eb424026-6f54-4ef8-a4d0-bb658a1fc6cf
encode: STR: eb424026-6f54-4ef8-a4d0-bb658a1fc6cf
   SIV: 312712571721458096795100956955942831823
   version: 4 (random data based)
   (no semantics: random data only)

=head1 SEE ALSO

uuid(3), OSSP::uuid(3).
=cut
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**
** uuid_cli.c: command line tool
*/

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** uuid_md5.h: MD5 API definition
*/

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uuid_compat.pod: Data::UUID Backward Compatibility Perl API (Perl/POD part)

=head1 NAME

Data::UUID - B<OSSP uuid> Backward Compatibility Perl Binding

=head1 DESCRIPTION

B<Data::UUID> is the B<OSSP uuid> backward compatibility Perl binding to the API of the original B<Data::UUID> module. It allows other B<Data::UUID> based Perl modules to run with B<OSSP::uuid> without changes.

=head1 SEE ALSO

B<OSSP::uuid>.

=head1 HISTORY

The backward compatibility Perl binding B<Data::UUID> for B<OSSP uuid> was originally implemented in 2004 by Piotr Roszatycki E<lt>dexter@debian.orgE<gt>. It was later cleaned up and speed optimized in December 2005 by David Wheeler E<lt>david@justatheory.comE<gt>.
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 **
 ** uuid_prng.c: PRNG API implementation
 */

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A
dnl config.m4: PHP/Zend API build-time configuration (language: m4)
dnl

PHP_ARG_ENABLE(uuid, OSSP uuid module,  
[ --enable-uuid     Enable OSSP uuid extension module.])

if test "$PHP_UUID" != "no"; then
    PHP_NEW_EXTENSION(uuid, uuid.c, $ext_shared)
    AC_DEFINE(HAVE_UUID, 1, [Have OSSP uuid library])
    PHP_ADD_LIBPATH([..], )
    PHP_ADD_LIBRARY([uuid], , UUID_SHARED_LIBADD)
    PHP_ADD_INCLUDE([..])
    PHP_SUBST(UUID_SHARED_LIBADD)

dnl avoid linking conflict with a potentially existing uuid_create(3) in libc
AC_CHECK_FUNC(uuid_create,[
    SAVE_LDFLAGS="$LDFLAGS"
    LDFLAGS="$LDFLAGS -Wl,-Bsymbolic"
    AC_TRY_LINK([], [], [EXTRA_LDFLAGS="$EXTRA_LDFLAGS -Wl,-Bsymbolic"])
    LDFLAGS="$SAVE_LDFLAGS"])
fi

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/php/config.m4
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/**
 * OSSP ui128 - 128-Bit Arithmetic
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 */

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ui128.c: implementation of 128-bit unsigned integer arithmetic

*/

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OSSP uuid - Universally Unique Identifier
Version 1.6.2 (04-Jul-2008)

ABSTRACT

OSSP uuid is a ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant Universally Unique Identifier (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood
of uniqueness over space and time and are computationally difficult to guess. They are globally unique identifiers which can be locally generated without contacting a global registration authority. UUIDs are intended as unique identifiers for both mass tagging objects with an extremely short lifetime and to reliably identifying very persistent objects across a network.

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HOME AND DOCUMENTATION

The documentation and latest release can be found on

- http://www.ossp.org/pkg/lib/uuid/

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## uuid.pc.in: pkg-config(1) specification

```
prefix=@prefix@
exec_prefix=${prefix}/
includedir=${prefix}/include
libdir=${exec_prefix}/lib

Name: OSSP uuid
Description: Universally Unique Identifier (UUID) Library
Version: @UUID_VERSION_RAW@
URL: http://www.ossp.org/pkg/lib/uuid/
Cflags: -I${includedir}
Libs: -L${libdir} -luuid
Libs.private: @LIBS@

Found in path(s):
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1.309 libssh2 1.8.0 2.1

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Upstream-Contact: Daniel Stenberg <daniel@haxx.se>
Source: http://libssh2.org/

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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1.312 debianutils 4.8.4

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```

Upstream source https://invisible-island.net/ncurses/ncurses.html
This package is used for testing builds of ncurses.

Current ncurses maintainer: Thomas Dickey <dickey@invisible-island.net>

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1.322 audit 2.8.2-1ubuntu1.1

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1.325 html5lib 1.1

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1.335 man-pages 4.16 2
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import re
import sys
import textwrap
from pathlib import Path

# The standard short names in Debian are defined here:
# https://www.debian.org/doc/packaging-manuals/copyright-format/1.0/
license_information = [
    {
        'shortname': 'BSD-3-clause',
        'filename': 'BSD-3-clause-UCB',
        'upstream_names': ['BSD_3_CLAUSE_UCB'],
    },
    {
        'shortname': 'BSD-4-clause',
        'filename': 'BSD-4-clause-UCB',
        'upstream_names': ['BSD_4_CLAUSE_UCB', 'BSD_ONELINE_CDROM'],
    },
    {
        'shortname': 'Expat',
        'filename': 'Expat',
        'upstream_names': ['PERMISSIVE_MISC', 'MIT'],
    },
    {
        # This shortname is not defined by the standard.
        'shortname': 'freely-redistributable',
        'filename': 'freely-redistributable',
        'upstream_names': ['FREELY_REDISTRIBUTABLE'],
    },
    {
        'shortname': 'GPL-2',
        'filename': 'GPL-2',
        'upstream_names': ['GPLv2_MISC', 'GPLv2_ONELINE'],
    },
    {
        'shortname': 'GPL-2+',
        'filename': 'GPL-2+',
        'upstream_names': ['GPL_NOVERSION_ONELINE', 'GPLv2+',
                           'GPLv2+_DOC_FULL', 'GPLv2+_DOC_MISC',
                           'GPLv2+_DOC_ONEPARA', 'GPLv2+_SW_3_PARA',
                           'GPLv2+_SW_ONEPARA'],
    },
    {
        # This shortname is not defined by the standard.
        'shortname': 'henry-spencer-regex',
        'filename': 'henry-spencer-regex',
        'upstream_names': ['MISC'],
    },
    {
        # This shortname is not defined by the standard.
        'shortname': 'LDPv1',
        'filename': 'LDPv1',
        'upstream_names': ['LDPv1'],
    },
]
'shortname': 'public-domain',
'filename': 'public-domain',
'upstream_names': ['PUBLIC_DOMAIN'],
},

# This shortname is not defined by the standard.
'shortname': 'verbatim',
'filename': 'verbatim',
'upstream_names': ['VERBATIM', 'VERBATIM_ONE_PARA',
                   'VERBATIM_TWO_PARA', 'VERBATIM_PROF'],
},
]

licenses_with_manpages = {}
symlinks = {}

def get_license_shortname(name):
    """Gets the Debian shortname for the name supplied by upstream.
    Includes a check that upstream's names are only assigned
to one shortname.
    """
    shortname = ""
    already_found = False
    for info in license_information:
        if name in info['upstream_names']:
            if not already_found:
                shortname = info['shortname']
                already_found = True
            else:
                sys.exit("Fatal error: Upstream license name defined multiple times: " + name)
    return shortname

def add_manpage_to_shortname(manpage, copyright_holders, licenses):
    # Ensure a string for the filename
    filename = str(manpage)
    # Strip the leading "./"
    filename = filename[3:len(filename)]
    # Common case: only one license for the manpage, so
    # the shortname is just e.g. "GPL-2+
    shortname = " " and ",join(sorted(licenses))
    if shortname not in licenses_with_manpages:
        licenses_with_manpages[shortname] = {'files': [filename], 'copyright': copyright_holders}
    else:
        licenses_with_manpages[shortname]['files'].append(filename)
    # Do not add same lines twice
    joined_copyright_holders = list(set(existing_copyright_holders + copyright_holders))
licenses_with_manpages[shortname][\'copyright\'] = joined_copyright_holders

def get_copyright_stanza(shortname, file_info):
    stanza = ""
    # Collect files and symlinks into a common list
    all_files = file_info[\'files\'
    for file in file_info[\'files\']:
        if file in symlinks:
            all_files = all_files + symlinks[file]
    # Join the files into a whitespace separated list,
    # at most 76 characters long
    files = " ".join(sorted(all_files))
    # The wrap is 69 + 7 (length of \"Files: \") = 76
    files = textwrap.wrap(files, width=69, break_long_words=False, break_on_hyphens=False)
    files = "\n   ".join(files)
    # Now format the copyright holders
    copyright = "\n         ".join(sorted(file_info[\'copyright\'])))
    # An empty field is an error, so ensure a value
    if len(copyright) == 0:
        copyright = "(could not be detected automatically)"
    # Finally, create the stanza
    stanza += "Files: " + files
    stanza += "\n\nCopyright: " + copyright
    stanza += "\nLicense: " + shortname + "\n\n"
    return stanza

def get_license_text(shortname):
    """Gets the text for the Debian license shortname."""
    text = ""
    for info in license_information:
        if info[\'shortname\'] == shortname:
            text += "License: " + shortname + "\n"
    with open("licenses/" + info[\'filename\']) as licensefile:
        for line in licensefile:
            text += " " + line
    return text

p = Path("..")
for manpage in p.glob("man?/*"):
    with manpage.open() as file:
        licenses = []
        copyright_holders = []
        manpage_is_symlink = False
        for line in file:
            # Do not create copyright stanzas for symlink files
            # but add them to a symlink list
            symlink = re.search(r"^-\so (.*)", line)
            if symlink:
                symmetric_links = file_info[\'files\']
                symmetric_links = symmetric_links + symlink
                file_info[\'files\'] = symmetric_links
                # Collect files and symlinks into a common list
                all_files = file_info[\'files\'
                for file in file_info[\'files\']:
                    if file in symlinks:
                        all_files = all_files + symlinks[file]
                # Join the files into a whitespace separated list,
                # at most 76 characters long
                files = " ".join(sorted(all_files))
                # The wrap is 69 + 7 (length of \"Files: \") = 76
                files = textwrap.wrap(files, width=69, break_long_words=False, break_on_hyphens=False)
                files = "\n   ".join(files)
                # Now format the copyright holders
                copyright = "\n         ".join(sorted(file_info[\'copyright\'])))
                # An empty field is an error, so ensure a value
                if len(copyright) == 0:
                    copyright = "(could not be detected automatically)"
                # Finally, create the stanza
                stanza += "Files: " + files
                stanza += "\nCopyright: " + copyright
                stanza += "\nLicense: " + shortname + "\n\n"
                return stanza

    def get_license_text(shortname):
        """Gets the text for the Debian license shortname."""
        text = ""
        for info in license_information:
            if info[\'shortname\'] == shortname:
                text += "License: " + shortname + "\n"
        with open("licenses/" + info[\'filename\']) as licensefile:
            for line in licensefile:
                text += " " + line
        return text

p = Path("..")
for manpage in p.glob("man?/*"):
    with manpage.open() as file:
        licenses = []
        copyright_holders = []
        manpage_is_symlink = False
        for line in file:
            # Do not create copyright stanzas for symlink files
            # but add them to a symlink list
            symlink = re.search(r"^-\so (.*)", line)
            if symlink:
manpage_is_symlink = True
# Ensure a string for the filename
linkname = str(manpage)
# Strip the leading "./"
linkname = linkname[3:len(linkname)]
filename = symlink.group(1)
if filename in symlinks:
symlinks[filename].append(linkname)
else:
symlinks[filename] = [linkname]
break
# Only parse the header, so stop after seeing ".TH"
if re.search(r"\^\TH", line):
    break
# Extract all copyright holders
copyright = re.search(r"\^\\\\w*"\w\\\w*?Copyright (.*)", line)
if copyright:
copyright_holders.append(copyright.group(1))
# Match the beginning of the license
license_start = re.search(r"\^\\\\ "%\%LICENSE_START\((.*)\)\", line)
if license_start:
    license_name = license_start.group(1)
license_short_name = get_license_shortname(license_name)
if not license_short_name:
sys.exit("Fatal error: Upstream license name not known: " + license_name)
licenses.append(license_short_name)
if not manpage_is_symlink:
    add_manpage_to_shortname(manpage, copyright_holders, licenses)

# Flatten the symlinks by detecting and removing
# a symlink which points to another symlink.
for link_to_test in symlinks:
    # Now cycle through all symlink entries
    for link in symlinks:
        if link_to_test in symlinks[link]:
symlinks[link] = symlinks[link] + symlinks[link_to_test]
# Make sorting of licenses deterministic
stanzas = ""
license_texts = []
for shortname in sorted(licenses_with_manpages):
stanzas += get_copyright_stanza(shortname, licenses_with_manpages[shortname])
text = get_license_text(shortname)
if text:
    license_texts.append(text)

# Read in the first lines of copyright, without
# the automatically generated parts. Stop after
# Open the file for output
with open("copyright", "w") as copyright_file:
copyright_file.write(manual_lines)
copyright_file.write(stanzas)
copyright_file.write("\n".join(license_texts))

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man3/pthread_cleanup_pop_restore_np.3 man3/pthread_cleanup_push.3
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man3/pthread_getaffinity_np.3 man3/pthread_getattr_default_np.3
man3/pthread_getattr_np.3 man3/pthread_getconcurrency.3
man3/pthread_getcpuclockid.3 man3/pthread_getname_np.3
man3/pthread_getschedparam.3 man3/pthread_join.3 man3/pthread_kill.3
man3/pthread_mprotect_np.3 man3/pthread_mutex_consistent.3
man3/pthread_mutex_consistent_np.3 man3/pthread_mutexattr_destroy.3
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man3/pthread_setconcurrency.3 man3/pthread_setname_np.3
man3/pthread_setschedparam.3 man3/pthread_setschedprio.3
man3/pthread_sigtimedwait.3
man3/pthread_spin_destroy.3 man3/pthread_spin_init.3
man3/pthread_spin_lock.3 man3/pthread_spin_trylock.3
man3/pthread_spin_unlock.3 man3/pthread_testcancel.3
man3/pthread_timestate.3 man3/pthread_tryjoin.3
man3/pthread_yield.3 man3/putc.3 man3/putc_unlocked.3 man3/putchar.3
man3/putchar_unlocked.3 man3/putenv.3 man3/putenv_unlocked.3
man3/putw.3 man3/putwc_unlocked.3 man3/putwchar_unlocked.3
man3/pvalloc.3 man3/qecvt.3 man3/qecvt_r.3 man3/qfcvt.3
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Found in path(s):
* /opt/cola/permits/1212207596_1633726302.78/0/helidon-config-yaml-1-2-0-sources-1-
  jar/io/helidon/config/yaml/internal/YamlConfigParser.java

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io.helidon.config.yaml.internal.YamlConfigParser

Found in path(s):
* /opt/cola/permits/1212207596_1633726302.78/0/helidon-config-yaml-1-2-0-sources-1-jar/META-
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* /opt/cola/permits/1212207596_1633726302.78/0/helidon-config-yaml-1-2-0-sources-1-jar/META-INF/services/java.nio.file.spi.FileTypeDetector

1.340 google-api-go-client 0.29.0

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package api

import {
    "bytes"
    "io/ioutil"
    "os"
    "path/filepath"
    "regexp"
    "testing"
}

// Files in this package use a BSD-style license.
var sentinel = regexp.MustCompile(`(//|#) Copyright \d\d\d\d (Google LLC|The Go Authors)(\.|\s)* (All rights reserved.)(//|#) Use of this source code is governed by a BSD-style (//|#) license that can be found in the LICENSE file.
```

const prefix = "// Copyright"

// A few files have to be skipped.
var skip = map[string]bool{
    "tools.go": true, // This file requires another comment above the license.
    "internal/third_party/uritemplates/uritemplates.go": true, // This file is licensed to an individual.
    "internal/third_party/uritemplates/uritemplates_test.go": true, // This file is licensed to an individual.
}

// This test validates that all go files in the repo start with an appropriate license.
func TestLicense(t *testing.T) {
    err := filepath.Walk(".", func(path string, fi os.FileInfo, err error) error {
        if skip[path] {
            return nil
        }
        if err != nil {
            return err
        }
        if filepath.Ext(path) != ".go" && filepath.Ext(path) != ".sh" {
            return nil
        }
    })
}

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src, err := ioutil.ReadFile(path)
if err != nil {
    return nil
}

// Verify that the license is matched.
if !sentinel.Match(src) {
    t.Errorf("%v: license header not present", path)
    return nil
}

// Also check it is at the top of .go files (but not .sh files, because they must have a shebang first).
if filepath.Ext(path) == ".go" && !bytes.HasPrefix(src, []byte(prefix))) {
    t.Errorf("%v: license header not at the top", path)
}
return nil
}

1.341 autogen 5.18.12-4
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The following people have worked to translate sudo into other languages as part of the Translation Project, see https://translationproject.org for more details.

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A. HISTORY OF THE SOFTWARE
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization.
created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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1.360 libunistring 0.9.10 1

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This package was originally put together by Austin Donnelly
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Then the package has been maintained by Charles Briscoe-Smith
<cpbs@debian.org>. I gathered data for the 1999-2001 calendar files
from various sources on the Internet, and I'd also like to thank Oliver
Elphick, Julian Gilbey, Daniel Martin and Jaldhar H. Vyas for providing
much useful data on the various religious calendars. I have edited the
files they provided to fit calendar's requirements, so any errors should
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After cpbs@debian.org, Marco d'Itri <md@linux.it> maintained it for
almost two years, before Tollef Fog Heen <tfheen@debian.org> took over.
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1.362 pyyaml 3.13-2
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1.363 containerd-continuity 0.1.0

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1.364 linux-libc-dev 4.19.208-1

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include/dt-bindings/clock/sun*.h
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1.366 sqlx 1.2.0

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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,- Bsymb...
## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<
%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

## Versioned libraries rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ & & ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ & & ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

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1.376 structured-merge-diff 4.1.0

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Gocheck - A rich testing framework for Go

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# Contributions to the urllib3 project

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  * HTTPS patch (which inspired HTTPSConnectionPool)

* erikcederstrand <http://code.google.com/u/erikcederstrand/>
  * NTLM-authenticated HTTPSConnectionPool
  * Basic-authenticated HTTPSConnectionPool (merged into make_headers)

* niphlod <niphlod@gmail.com>
  * Client-verified SSL certificates for HTTPSConnectionPool
  * Response gzip and deflate encoding support
  * Better unicode support for filepost using StringIO buffers

* btoconnor <brian@btoconnor.net>
  * Non-multipart encoding for POST requests

* p.dobrogost <http://code.google.com/u/@WBRSRIBZhBFXQB6/>
  * Code review, PEP8 compliance, benchmark fix

* kennethreitz <me@kennethreitz.com>
  * Bugfixes, suggestions, Requests integration

* georgemarshall <https://github.com/georgemarshall>
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* brandon-rhodes <http://rhodesmill.org/brandon>
  * Design review, bugfixes, test coverage.

* studer <theo.studer@gmail.com>
  * IPv6 url support and test coverage

* Shivaram Lingamneni <slingamn@cs.stanford.edu>
  * Support for explicitly closing pooled connections

* hartator <hartator@gmail.com>
  * Corrected multipart behavior for params

* Thomas Weischuh <thomas@t-8ch.de>
  * Support for TLS SNI
  * API unification of ssl_version/cert_reqs
  * SSL fingerprint and alternative hostname verification
  * Bugfixes in testsuite

* Sune Kirkeby <mig@ibofobi.dk>
  * Optional SNI-support for Python 2 via PyOpenSSL.
* Marc Schlaich <marc.schlaich@gmail.com>
  * Various bugfixes and test improvements.

* Bryce Boe <bbzbryce@gmail.com>
  * Correct six.moves conflict
  * Fixed pickle support of some exceptions

* Boris Figovsky <boris.figovsky@ravellosystems.com>
  * Allowed to skip SSL hostname verification

* Cory Benfield <https://lukasa.co.uk/about/>
  * Stream method for Response objects.
  * Return native strings in header values.
  * Generate 'Host' header when using proxies.

* Jason Robinson <jaywink@basshero.org>
  * Add missing WrappedSocket.fileno method in PyOpenSSL

* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
  * Fixed a race condition

* Stanislaw Vitkovskiy <stas.vitkovsky@gmail.com>
  * Added HTTPS (CONNECT) proxy support

* Stephen Holsapple <sholsapp@gmail.com>
  * Added abstraction for granular control of request fields

* Martin von Gagern <Martin.vGagern@gmx.net>
  * Support for non-ASCII header parameters

* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
  * Support for separate connect and request timeouts

* Peter Waller <p@pwaller.net>
  * HTTPResponse.tell() for determining amount received over the wire

* Nipunn Koorapati <nipunn1313@gmail.com>
  * Ignore default ports when comparing hosts for equality

* Danilo @dbrgn <https://dbrgn.ch/>
  * Disabled TLS compression by default on Python 3.2+
  * Disabled TLS compression in pyopenssl contrib module
  * Configurable cipher suites in pyopenssl contrib module

* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
  * Account retries on proxy errors
* Nicolas Delaby <nicolas.delaby@ezeep.com>
  * Use the platform-specific CA certificate locations

* Josh Schneier <https://github.com/jschneier>
  * HTTPHeaderDict and associated tests and docs
  * Bugfixes, docs, test coverage

* Tahia Khan <http://tahia.tk/>
  * Added Timeout examples in docs

* Arthur Grunseid <https://grunseid.com>
  * source_address support and tests (with https://github.com/bui)

* Ian Cordasco <graffatcolmingov@gmail.com>
  * PEP8 Compliance and Linting
  * Add ability to pass socket options to an HTTP Connection

* Erik Tollerud <erik.tollerud@gmail.com>
  * Support for standard library io module.

* Krishna Prasad <kprasad.iitd@gmail.com>
  * Google App Engine documentation

* Aaron Meurer <asmeurer@gmail.com>
  * Added Url.url, which unparses a Url

* Evgeny Kapun <abacabadabacaba@gmail.com>
  * Bugfixes

* Benjamen Meyer <bm_witness@yahoo.com>
  * Security Warning Documentation update for proper capture

* Shivan Sornarajah <github@sornars.com>
  * Support for using ConnectionPool and PoolManager as context managers.

* Alex Gaynor <alex.gaynor@gmail.com>
  * Updates to the default SSL configuration

* Tomas Tomecek <ttomecek@redhat.com>
  * Implemented generator for getting chunks from chunked responses.

* tlynn <https://github.com/tlynn>
  * Respect the warning preferences at import.

* David D. Riddle <ddriddle@illinois.edu>
  * IPv6 bugfixes in testsuite

* Thea Flowers <magicalgirl@google.com>
* App Engine environment tests.
* Documentation re-write.

* John Krauss <https://github.com/talos>
  * Clues to debugging problems with `cryptography` dependency in docs

* Disassem <https://github.com/Disassem>
  * Fix pool-default headers not applying for url-encoded requests like GET.

* James Atherfold <jlatherfold@hotmail.com>
  * Bugfixes relating to cleanup of connections during errors.

* Christian Pedersen <https://github.com/chripede>
  * IPv6 HTTPS proxy bugfix

* Jordan Moldow <https://github.com/jmoldow>
  * Fix low-level exceptions leaking from `HTTPResponse.stream()`.
  * Bugfix for `ConnectionPool.urlopen(release_conn=False)`.
  * Creation of `HTTPConnectionPool.ResponseCls`.

* Predrag Gruevski <https://github.com/obi1kenobi>
  * Made cert digest comparison use a constant-time algorithm.

* Adam Talsma <https://github.com/a-tal>
  * Bugfix to `ca_cert` file paths.

* Evan Meagher <https://evanmeagher.net>
  * Bugfix related to `memoryview` usage in PyOpenSSL adapter

* John Vandenberg <jayvdb@gmail.com>
  * Python 2.6 fixes; pyflakes and pep8 compliance

* Andy Caldwell <andy.m.caldwell@googlemail.com>
  * Bugfix related to reusing connections in indeterminate states.

* Ville Skytt <ville.skytta@iki.fi>
  * Logging efficiency improvements, spelling fixes, Travis config.

* Shige Takeda <smtakeda@gmail.com>
  * Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response

* Jess Shapiro <jesse@jesseshapiro.net>
  * Various character-encoding fixes/tweaks
  * Disabling IPv6 DNS when IPv6 connections not supported

* David Foster <http://dafoster.net/>
  * Ensure order of request and response headers are preserved.
* Jeremy Cline <jeremy@jcline.org>
  * Added connection pool keys by scheme

* Aviv Palivoda <palaviv@gmail.com>
  * History list to Retry object.
  * HTTPResponse contains the last Retry object.

* Nate Prewitt <nate.prewitt@gmail.com>
  * Ensure timeouts are not booleans and greater than zero.
  * Fixed infinite loop in ``stream`` when amt=None.
  * Added length_remaining to determine remaining data to be read.
  * Added enforce_content_length to raise exception when incorrect content-length received.

* Seth Michael Larson <sethmichaellarson@protonmail.com>
  * Created selectors backport that supports PEP 475.

* Alexandre Dias <alex.dias@smarkets.com>
  * Don't retry on timeout if method not in whitelist

* Moinuddin Quadri <moin18@gmail.com>
  * Lazily load idna package

* Tom White <s6yg1ez3@mail2tor.com>
  * Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

* Tim Burke <tim.burke@gmail.com>
  * Stop buffering entire deflate-encoded responses.

* Tuukka Mustonen <tuukka.mustonen@gmail.com>
  * Add counter for status_forcelist retries.

* Erik Rose <erik@mozilla.com>
  * Bugfix to pyopenssl vendoring

* Wolfgang Richter <wolfgang.richter@gmail.com>
  * Bugfix related to loading full certificate chains with PyOpenSSL backend.

* Mike Miller <github@mikeage.net>
  * Logging improvements to include the HTTP(S) port when opening a new connection

* Ioannis Tziakos <mail@itziakos.gr>
  * Fix `util.selectors._fileobj_to_fd` to accept `''long``.
  * Update appveyor tox setup to use the 64bit python.

* Akamai (through Jess Shapiro) <jshapiro@akamai.com>
  * Ongoing maintenance; 2017-2018

* Dominique Leuenberger <dimstar@opensuse.org>
* Minor fixes in the test suite

* Will Bond <will@wbond.net>
  * Add Python 2.6 support to `contrib.securetransport`

* Aleksei Alekseev <alekseev.yeskela@gmail.com>
  * using auth info for socks proxy

* Chris Wilcox <git@crwilcox.com>
  * Improve contribution guide
  * Add ```HTTPResponse.geturl``` method to provide ```urlib2.urlopen().geturl()``` behavior

* Bruce Merry <https://www.brucemerry.org.za>
  * Fix leaking exceptions when system calls are interrupted with zero timeout

* Hugo van Kemenade <https://github.com/hugovk>
  * Drop support for EOL Python 2.6

* Tim Bell <https://github.com/timb07>
  * Bugfix for responses with Content-Type: message/* logging warnings

* Justin Bramley <https://github.com/jbramleycl>
  * Add ability to handle multiple Content-Encodings

* Katsuhiko YOSHIDA <https://github.com/kyoshidajp>
  * Remove Authorization header regardless of case when redirecting to cross-site

* James Meickle <https://permadeath.com/>
  * Improve handling of Retry-After header

* Chris Jerdonek <chris.jerdonek@gmail.com>
  * Remove a spurious TypeError from the exception chain inside
    HTTPConnectionPool._make_request(), also for BaseExceptions.

* Jorge Lopez Silva <https://github.com/jalopezsilva>
  * Added support for forwarding requests through HTTPS proxies.

* Benno Rice <benno@jeamland.net>
  * Allow cadata parameter to be passed to underlying ```SSLContext.load_verify_locations()```.

* Keiichi Kobayashi <abok.1k@gmail.com>
  * Rename VerifiedHTTPSConnection to HTTPSConnection

* Himanshu Garg <garg_himanshu@outlook.com>
  * DOC & LICENSE Update

* Hod Bin Noon <bin.noon.hod@gmail.com>
  * Test improvements
* Chris Olufson <tycarac@gmail.com>
  * Fix for connection not being released on HTTP redirect and response not preloaded

* [Bastiaan Bakker] <https://github.com/bastiaanb>
  * Support for logging session keys via environment variable `SSLKEYLOGFILE` (Python 3.8+)

* [Your name or handle] <[email or website]>
  * [Brief summary of your changes]

1.396 ramda 0.25.0

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1.397 azure-storage-blob 12.8.1

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1.398 function-bind 1.1.1

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1.399 libtool 2.4.6-9

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1.405 libcap-ng 0.7.9-2.2+b1
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1.408 urllib3 1.24.1

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# Contributions to the urllib3 project

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# Contributors

In chronological order:
* victor.vde <http://code.google.com/u/victor.vde/>
  * HTTPS patch (which inspired HTTPSConnectionPool)

* erikcederstrand <http://code.google.com/u/erikcederstrand/>
  * NTLM-authenticated HTTPSConnectionPool
  * Basic-authenticated HTTPSConnectionPool (merged into make_headers)

* niphlod <niphlod@gmail.com>
  * Client-verified SSL certificates for HTTPSConnectionPool
  * Response gzip and deflate encoding support
  * Better unicode support for filepost using StringIO buffers

* btoconnor <brian@btconner.net>
  * Non-multipart encoding for POST requests

* p.dobrogost <http://code.google.com/u/@WBRSRIBZDhBFXQB6/>
  * Code review, PEP8 compliance, benchmark fix

* kennethreitz <me@kennethreitz.com>
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* Thomas Kluyver <thomas@kluyver.me.uk>
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* studer <theo.studer@gmail.com>
  * IPv6 url support and test coverage

* Shivaram Lingamneni <slingamn@cs.stanford.edu>
  * Support for explicitly closing pooled connections

* hartator <hartator@gmail.com>
  * Corrected multipart behavior for params

* Thomas Weischuh <thomas@t-8ch.de>
  * Support for TLS SNI
  * API unification of ssl_version/cert_reqs
  * SSL fingerprint and alternative hostname verification
  * Bugfixes in testsuite

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  * Fixed pickle support of some exceptions

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  * Allowed to skip SSL hostname verification

* Cory Benfield <https://lukasa.co.uk/about/>
  * Stream method for Response objects.
  * Return native strings in header values.
  * Generate 'Host' header when using proxies.

* Jason Robinson <jaywink@basshero.org>
  * Add missing WrappedSocket.fileno method in PyOpenSSL

* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
  * Fixed a race condition

* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
  * Added HTTPS (CONNECT) proxy support

* Stephen Holsapple <sholsapp@gmail.com>
  * Added abstraction for granular control of request fields

* Martin von Gagern <Martin.vGagern@gmx.net>
  * Support for non-ASCII header parameters

* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
  * Support for separate connect and request timeouts

* Peter Waller <p@pwaller.net>
  * HTTPResponse.tell() for determining amount received over the wire

* Nipunn Koorapati <nipunn1313@gmail.com>
  * Ignore default ports when comparing hosts for equality

* Danilo @dbrgn <https://dbrgn.ch/>
  * Disabled TLS compression by default on Python 3.2+
  * Disabled TLS compression in pyopenssl contrib module
  * Configurable cipher suites in pyopenssl contrib module

* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
  * Account retries on proxy errors

* Nicolas Delaby <nicolas.delaby@ezeep.com>
* Use the platform-specific CA certificate locations

* Josh Schneier <https://github.com/jschneier>
* HTTPHeaderDict and associated tests and docs
* Bugfixes, docs, test coverage

* Tahia Khan <http://tahia.tk/>
* Added Timeout examples in docs

* Arthur Grunseid <https://grunseid.com>
* source_address support and tests (with https://github.com/bui)

* Ian Cordasco <graffatcolmingov@gmail.com>
* PEP8 Compliance and Linting
* Add ability to pass socket options to an HTTP Connection

* Erik Tollerud <erik.tollerud@gmail.com>
* Support for standard library io module.

* Krishna Prasad <kprasad.iitd@gmail.com>
* Google App Engine documentation

* Aaron Meurer <asmeurer@gmail.com>
* Added Url.url, which unparses a Url

* Evgeny Kapun <abacabadabacaba@gmail.com>
* Bugfixes

* Benjamen Meyer <bm_witness@yahoo.com>
* Security Warning Documentation update for proper capture

* Shivan Sornarajah <github@sornars.com>
* Support for using ConnectionPool and PoolManager as context managers.

* Alex Gaynor <alex.gaynor@gmail.com>
* Updates to the default SSL configuration

* Tomas Tomecek <ttomecek@redhat.com>
* Implemented generator for getting chunks from chunked responses.

* tlynn <https://github.com/tlynn>
* Respect the warning preferences at import.

* David D. Riddle <ddriddle@illinois.edu>
* IPv6 bugfixes in testsuite

* Thea Flowers <magicalgirl@google.com>
* App Engine environment tests.
* Documentation re-write.

* John Krauss <https://github.com/talos>
  * Clues to debugging problems with `cryptography` dependency in docs

* Disassem <https://github.com/Disassem>
  * Fix pool-default headers not applying for url-encoded requests like GET.

* James Atherfold <jlatherfold@hotmail.com>
  * Bugfixes relating to cleanup of connections during errors.

* Christian Pedersen <https://github.com/chripede>
  * IPv6 HTTPS proxy bugfix

* Jordan Moldow <https://github.com/jmoldow>
  * Fix low-level exceptions leaking from ``HTTPResponse.stream()``.
  * Bugfix for `ConnectionPool.urlopen(release_conn=False)`.
  * Creation of `HTTPConnectionPool.ResponseCls`.

* Predrag Gruevski <https://github.com/obi1kenobi>
  * Made cert digest comparison use a constant-time algorithm.

* Adam Talsma <https://github.com/a-tal>
  * Bugfix to ca_cert file paths.

* Evan Meagher <https://evanmeagher.net>
  * Bugfix related to `memoryview` usage in PyOpenSSL adapter

* John Vandenberg <jayvdb@gmail.com>
  * Python 2.6 fixes; pyflakes and pep8 compliance

* Andy Caldwell <andy.m.caldwell@googlemail.com>
  * Bugfix related to reusing connections in indeterminate states.

* Ville Skytt <ville.skytta@iki.fi>
  * Logging efficiency improvements, spelling fixes, Travis config.

* Shige Takeda <smtakeda@gmail.com>
  * Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response

* Jesse Shapiro <jesse@jesseshapiro.net>
  * Various character-encoding fixes/tweaks
  * Disabling IPv6 DNS when IPv6 connections not supported

* David Foster <http://dafoster.net/>
  * Ensure order of request and response headers are preserved.

* Jeremy Cline <jeremy@jcline.org>
* Added connection pool keys by scheme

* Aviv Palivoda <palaviv@gmail.com>
  * History list to Retry object.
  * HTTPResponse contains the last Retry object.

* Nate Prewitt <nate.prewitt@gmail.com>
  * Ensure timeouts are not booleans and greater than zero.
  * Fixed infinite loop in `stream` when amt=None.
  * Added length_remaining to determine remaining data to be read.
  * Added enforce_content_length to raise exception when incorrect content-length received.

* Seth Michael Larson <sethmichaellarson@protonmail.com>
  * Created selectors backport that supports PEP 475.

* Alexandre Dias <alex.dias@smarkets.com>
  * Don't retry on timeout if method not in whitelist

* Moinuddin Quadri <moin18@gmail.com>
  * Lazily load idna package

* Tom White <s6yg1ez3@mail2tor.com>
  * Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

* Tim Burke <tim.burke@gmail.com>
  * Stop buffering entire deflate-encoded responses.

* Tuukka Mustonen <tuukka.mustonen@gmail.com>
  * Add counter for status_forcelist retries.

* Erik Rose <erik@mozilla.com>
  * Bugfix to pyopenssl vending

* Wolfgang Richter <wolfgang.richter@gmail.com>
  * Bugfix related to loading full certificate chains with PyOpenSSL backend.

* Mike Miller <github@mikeage.net>
  * Logging improvements to include the HTTP(S) port when opening a new connection

* Ioannis Tziakos <mail@itziakos.gr>
  * Fix `util.selectors._fileobj_to_fd` to accept `long`.
  * Update appveyor tox setup to use the 64bit python.

* Akamai (through Jesse Shapiro) <jshapiro@akamai.com>
  * Ongoing maintenance

* Dominique Leuenberger <dimstar@opensuse.org>
  * Minor fixes in the test suite
* Will Bond <will@wbond.net>
  * Add Python 2.6 support to `contrib.securetransport`

* Aleksei Alekseev <alekseev.yeskela@gmail.com>
  * using auth info for socks proxy

* Chris Wilcox <git@crwilcox.com>
  * Improve contribution guide
  * Add `HTTPResponse.geturl` method to provide `urlib2.urlopen().geturl()` behavior

* Bruce Merry <https://www.brucemerry.org.za>
  * Fix leaking exceptions when system calls are interrupted with zero timeout

* Hugo van Kemenade <https://github.com/hugovk>
  * Drop support for EOL Python 2.6

* Tim Bell <https://github.com/timb07>
  * Bugfix for responses with Content-Type: message/* logging warnings

* Justin Bramley <https://github.com/jbramleycl>
  * Add ability to handle multiple Content-Encodings

* [Your name or handle] <[email or website]>
  * [Brief summary of your changes]

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1.428 error-ex 1.3.2

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1.429 pflag 1.0.0

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1.431 csstype 3.0.6

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1.446 asyncio 3.4.3

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1.449 libxau 1.0.8-1+b2

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1.451 libcap 2.25-2

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Version 2, June 1991

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1.457 ispell 3.4.00-6
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Ispell has a long and convoluted history. I have tried to track down as much as possible about it and condense it below.

THE DEVELOPMENT OF SPELL-CHECKING AND THE FIRST ISPELL

The following background information on spelling checkers in general, and ispell in particular, was provided to me by Les Earnest (les@dec-lite.stanford.edu):

> The earliest spelling checker (of sorts) of which I am aware was in a program that attempted to automatically receive human-keyed Morse code, which can be ambiguous because of the variable timing between dots, dashes, intercharacter pauses, and interword pauses. This program didn't use a full dictionary; instead, used a table of digraphs (two-letter sequences) that occur in English and barred improper letter sequences. This program was written by someone at MIT
> Lincoln Lab around 1959 and, I think, ran on the TX-2 computer there.
> Unfortunately, I don't remember his name. I might still have the
> paper he wrote in my files but it would take a major search to find it
> and I might not succeed.
>
> A program that I wrote in 1961 to read cursive writing contained a
> real spelling checker, using the 10,000 most common English words.
> It is reported in:
> L. Earnest, "Machine Recognition of Cursive Writing," Information
> Processing 62, (Proc. IFIP Congress 1962, Munich), North-Holland,
> Amsterdam, 1963.
>
> N. Lindgren, ``Machine Recognition of Human Language, Part III -
>
> I brought that dictionary to Stanford and got a PhD student to write
> a spelling checker for text in Lisp running on our PDP-6 computer at
> the Stanford Artificial Intelligence Lab around 1967.
> Unfortunately, I do not remember which student it was; it could have
> been Gil Falk. It was a rather simple program (certainly much
> simpler than the earlier cursive writing program) and I didn't think
> of it as a significant development at the time.
>
> [Later], I got another PhD student, Ralph Gorin, to do a better and
> faster spelling checker sometime in the early '70s, still using my
> old dictionary. Ralph later wrote an article about it in CACM. I
> believe that he later augmented the dictionary.
>
> [note: Ralph has since informed me that he wrote no such article. The
> program was called SPELL and was written in 1971. Ralph provided me
> with a reference to "Computer Programs for Spelling Correction", by
> James L. Peterson, Springer-Verlag, Berlin, 1980, No. 96 in the series
> "Lecture Notes in Computer Science." This book states that Ralph's
> SPELL program, which was the direct ancestor of ispell, was the first
> computer program written for checking the spelling of text documents.
> The book is also a good source of references on spelling programs.]

> ...  
>
> [Ispell] was originally written in PDP-10 assembly language and ran
> under the WAITS operating system, which is similar to TOPS-10 but existed
> only on SAIL (a dual processor KA10/PDP-6 system). It was and is called
> SPELL on that machine. It later was modified to run under Tenex and
> TOPS-20.

> [Ralph mentions that SPELL was also ported to MIT's ITS and TOPS-10.]

The Tenex version of ispell was later revised by W. E. Matson (1974),
and Bill Ackerman (1978). Bill has provided the following information:

> I came across the SPELL program in 1978 on ITS. It was a port from
> Stanford, and had the names Ralph Gorin (approximately 1971) and
> Wayne Matson (1974) associated with it. I did 3 things to it:
> > Rewrote it as a native program for ITS, and, shortly thereafter,
> > TOPS-20. (I never did anything for TOPS-10, and am not aware
> > that it ever ran on TOPS-10, though it may have.)
> > Replaced the heuristics for suffix removal, which I found unreliable
> > and unsatisfactory, with an algorithm that was driven by specific
> > suffix flags in the dictionary. This way, the dictionary would have
> > complete control over what words were legal, and there would be no
> > spurious hits.
> > Apparently most importantly, though I had no idea at time, gave it
> > the name "ISPELL", for "ITS version of spell", since I didn't
> > consider myself authorized to throw away an existing program
> > and overwrite it with a new one under the same name.
> > I have not followed the history of the program since then, and do not know
> > if it still uses the "suffix flags" in its dictionary. But if it does,
> > I introduced them. The Ispell algorithm that uses those flags to make
> > accurate decisions about the legality of words was documented in great
> > detail in James Peterson's Springer-Verlag book. (He spent a semester
> > at MIT while working on the book, and I provided him with a lot of
> > information and documentation at that time.)
> > Bill Ackerman
> > wba@apollo.hp.com
>
Michael Adler adds:

> I did work on ispell in 1982. Actually, I stole the ispell
> dictionary and suffix compression algorithm and wrote a spelling
> checker for CP/M in 8080 assembler that I very creatively called "SPELL."
> By sorting the dictionary alphabetically and using a difference encoding
> I managed to pack the entire dictionary that Bill was using in about
> 56Kb. The CP/M program read a document, sorted all the words alphabetically
> and then checked them. It then reread the document and compared words as
> it found them against the in memory, sorted and checked words. SPELL was
> around in the public domain on CP/M.
> I was in high school at the time and talked to Bill only over email.
> We wound up in the same compiler group at Apollo in the late 80's by
> coincidence.
DEVELOPMENT OF THE C/UNIX VERSION OF ISPELL

In 1983, Pace Willisson (pace@prep.ai.mit.edu) wrote a C/Unix version from scratch, based on the ispell documentation.

In 1987, Walt Buehring revised and enhanced ispell, and posted it to the Usenet along with a dictionary. In addition, Walt wrote the first version of "ispell.el", the emacs interface.

Geoff Kuenning (geoff@ITcorp.com, that's me, and by the way I pronounce it "Kenning"; the "u" is silent) picked up this version, fixed some bugs, and added further enhancements, all of which made me the de-facto ispell maintainer for the net. I also put quite a bit of work into improving the quality of the dictionaries. In 1987 I began work on the "munchlist" script, which I originally intended to be used to add flags to personal dictionary entries. At the same time I was studying German, and wanted to use ispell to check the papers I was writing for that class. After thinking about it for some time, I realized that the suffix flags could be table-driven, which would both add flexibility and would get rid of certain difficult-to-find bugs. In 1988 I rewrote major portions of the code to do this, resulting in the first multi-lingual version. Ole Bjoern Hessen (obh@ifi.uio.no) in Norway alpha-tested this version and provided several important enhancements.

Bob Devine (vianet!devine) provided two larger dictionaries (which became the basis for english.1 and english.2) to me for inclusion with subsequent releases.

Ashwin Ram (ram@cs.yale.edu) made substantial enhancements to Walt Buehring's emacs interface, and provided them to me for inclusion with an earlier release.

The emacs interface was then completely overhauled by Ken Stevens (stevens@hplabs.hp.com), who also beta-tested the software and without whom this posting would not have been possible. If there's a feature in the emacs interface that you like, you probably have Ken to thank for it. His efforts have been tireless for many years.

Martin Boyer made major contributions to the munchlist script, including producing a version that runs under perl (see languages/Where for instructions on how to get that version). Philippe-André Prindeville provided xspell (a Motif-based X interface), and Moritz Willers provided a NeXTStep interface.

DEVELOPMENT AND DEATH OF ISPELL 4.0

Meanwhile, and unbeknownst to me, Pace Willisson was working on his
own improvements to ispell. He focused primarily on dictionary size and startup time. His solution was a dictionary compression algorithm that detected and encoded frequent letter pairs. This also reduced the time needed to read it in. Pace also changed some internal data structures to improve startup time. Pace and I eventually discovered each other's efforts, and discussed re-merging our changes, but we decided that there would be too much work involved. This was partly because I was close to a release and didn't want to delay it with an extensive and error-prone merge.

In late 1992 (if my memory serves correctly), Richard Stallman contacted me, asking for permission to distribute ispell as part of the GNU suite. I responded that he was welcome to distribute it, but that I was not willing to place my software under the Gnu Public License. Through a misunderstanding, neither of us considered the possibility of finding a compromise license that both could live with. So Richard started a search for an alternate version, and found Pace working right in his back yard.

I have been told that when FSF first learned of Pace's version, they again considered using International Ispell instead because it was both more popular and more capable, but this idea was rejected due to the license misunderstanding. Instead, FSF enhanced Pace's version somewhat and called it ispell 4.0, apparently in the hopes that by numbering the version higher, it would become the standard.

When ispell 4.0 was released, much confusion ensued. Many ispell users innocently "upgraded" to 4.0 and then screamed when they could not find features to which they had grown accustomed. Europeans in general were angered by the apparent provincialism shown by the "dropping" of international support. I found myself inundated with questions about a version I had never heard of or seen.

One of the earliest and most common suggestions was that FSF should rename their version "gispell”. This had a lot of precedent, both in the naming of other FSF utilities and in the then-recent change of the suffix used by gzip from ".z" to ".gz". Unfortunately, the FSF refused to do this. I may have inadvertently contributed to this refusal with a Usenet posting in which I tried to clarify what had happened, pointing out that the FSF version was more recently related to Pace's than my own. This may have been seen as an acknowledgment that FSF should have the rights to the name "ispell," and that I should rename my version.

A flame war arose, and I decided that the only way to solve the problem was to rename my version to eliminate the confusion. However, at about the same time Richard Stallman and I began negotiating via e-mail. We itemized and clarified his objections to my license, and I
learned from a third party that FSF is willing to distribute software that falls under the University of California license (also known as the Berkeley license). Richard and I agreed that if I changed my license to be a paraphrase of the UC license, FSF would be willing to distribute my version with no changes. Since then, ispell 4.0 has been dropped by FSF and has pretty well disappeared from the net, leaving International Ispell as the version of choice for nearly everyone.

OTHER CONTRIBUTORS

Numerous other people have contributed enhancements, suggestions, and bug fixes. I used to attempt to keep track of all of them, and list their names here. However, I found that keeping the list of names correct was as time-consuming as fixing bugs, so I finally decided that the list had grown too long, and stopped keeping it current. This is unfortunate, because many people have made significant contributions to ispell and I would like to have a way to acknowledge them. So please remember that hundreds of people have helped in this effort, and that I appreciate each and every one of them.

#ifndef LOCAL_H_INCLUDED
#define LOCAL_H_INCLUDED

/*
 * $Id: local.h.bsd,v 1.1 2005/04/27 00:17:46 geoff Exp $
 */

/*
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 *    and/or other materials provided with the distribution.
 * 4. The code that causes the ’ispell -v’ command to display a prominent
 *    link to the official ispell Web site may not be removed.
 * 5. The name of Geoff Kuenning may not be used to endorse or promote
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*/

/*
* This file is a sample local.h file. It shows what I believe nearly every
* site will want to include in their local.h. You will probably want to
* expand this file; see "config.X" to learn of #defines that you might
* like to add to.
*/

/*
* WARNING WARNING WARNING
* This file is *NOT* a normal C header file! Although it uses C
* syntax and is included in C programs, it is also processed by shell
* scripts that are very stupid about format.
* Do not try to use #if constructs to configure this file for more
* than one configuration. Do not place whitespace after the "#" in
* "#define". Do not attempt to disable lines by commenting them out.
* Do not use backslashes to reduce the length of long lines.
* None of these things will work the way you expect them to.
* WARNING WARNING WARNING
*/

#define MINIMENU /* Display a mini-menu at the bottom of the screen */
#define GENERATE_LIBRARY_PROTOS
#define EGREPCMD "egrep -i"
#define HAS_RENAME

/*
* Important directory paths. If you change MAN45DIR from man5 to
* something else, you probably also want to set MAN45SECT and
* MAN45EXT (but not if you keep the man pages in section 5 and just
* store them in a different place).
*/
#define BINDIR "/usr/local/bin"
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1.460 python-monotonic 1.1-2

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1.462 libgcrypt 1.8.1 4ubuntu1.3

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1.466 tdb 1.44.5

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#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image::$(BSD_LIB)

$(BSD_LIB): $(OBJS)
(cd pic; $ (LDLIBS) $(BSDLIB_FLAGS) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../../$(BSD_LIB)
(cd ..; $ (LN) $ (LINK_FLAGS) \`
 `echo $(my_dir) | sed -e 's;lib/;';"/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)
install-strip: install

install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
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+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
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+ trivial database library - standalone version

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-
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Theodore Ts'o
23-June-2007

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That's all there is to it!
This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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Upstream Author: Theodore Ts'o <tytso@mit.edu>

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This package was put together by Yann Dirson <dirson@debian.org>,
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1.469 postcss-value-parser 4.1.0

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1.470 libdnet 1.12-8

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1.471 idna 6.3.0

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A library to support the Internationalised Domain Names in Applications (IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

```python
def main():
    python_version = sys.version_info[:2]
    if python_version < (2, 7):
        raise SystemExit("Sorry, Python 2.7 or newer required")

    package_data = {}
    exec(open('idna/package_data.py').read(), package_data)

    arguments = {
        'name': 'idna',
        'packages': ['idna'],
        'version': package_data['__version__'],
    }
```
'description': 'Internationalized Domain Names in Applications (IDNA)',
'long_description': io.open("README.rst", encoding="UTF-8").read(),
'author': 'Kim Davies',
'author_email': 'kim@cynosure.com.au',
'license': 'BSD-like',
'url': 'https://github.com/kjd/idna',
'classifiers': [
    'Development Status :: 5 - Production/Stable',
    'Intended Audience :: Developers',
    'Intended Audience :: System Administrators',
    'License :: OSI Approved :: BSD License',
    'Operating System :: OS Independent',
    'Programming Language :: Python',
    'Programming Language :: Python :: 2',
    'Programming Language :: Python :: 2.7',
    'Programming Language :: Python :: 3',
    'Programming Language :: Python :: 3.4',
    'Programming Language :: Python :: 3.5',
    'Programming Language :: Python :: 3.6',
    'Programming Language :: Python :: 3.7',
    'Programming Language :: Python :: 3.8',
    'Programming Language :: Python :: Implementation :: CPython',
    'Programming Language :: Python :: Implementation :: PyPy',
    'Topic :: Internet :: Name Service (DNS)',
    'Topic :: Software Development :: Libraries :: Python Modules',
    'Topic :: Utilities',
],
'python_requires': '>=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*',
}

setup(**arguments)

if __name__ == '__main__':
    main()

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License: BSD and Python and Unicode

Found in path(s):
* /opt/cola/permits/1184181836_1628533468.66/0/python3-idna-2.10-1.el7.src.rpm-cosi-expand-archive-LIRZpo9/python3-idna.spec
No license file was found, but licenses were detected in source scan.

Metadata-Version: 1.2
Name: idna
Version: 2.10
Summary: Internationalized Domain Names in Applications (IDNA)
Home-page: https://github.com/kjd/idna
Author: Kim Davies
Author-email: kim@cynosure.com.au
License: BSD-like
Description: Internationalized Domain Names in Applications (IDNA)

Support for the Internationalised Domain Names in Applications (IDNA) protocol as specified in `RFC 5891 <http://tools.ietf.org/html/rfc5891>`_. This is the latest version of the protocol and is sometimes referred to as “IDNA 2008”.

This library also provides support for Unicode Technical Standard 46, `Unicode IDNA Compatibility Processing <http://unicode.org/reports/tr46/>`_.

This acts as a suitable replacement for the “encodings.idna” module that comes with the Python standard library, but only supports the old, deprecated IDNA specification (`RFC 3490 <http://tools.ietf.org/html/rfc3490>`_).

Basic functions are simply executed:

.. code-block:: pycon

    # Python 3
    >>> import idna
    >>> idna.encode('.')
    b'xn--eckwd4c7c.xn--zckzah'
    >>> print(idna.decode('xn--eckwd4c7c.xn--zckzah'))
    

    # Python 2
    >>> import idna
    >>> idna.encode(u'.')
    'xn--eckwd4c7c.xn--zckzah'
    >>> print(idna.decode('xn--eckwd4c7c.xn--zckzah'))
    

Packages
========

The latest tagged release version is published in the PyPI repository:

.. image:: https://badge.fury.io/py/idna.svg
   :target: http://badge.fury.io/py/idna
Installation
------------

To install this library, you can use pip:

.. code-block:: bash

   $ pip install idna

Alternatively, you can install the package using the bundled setup script:

.. code-block:: bash

   $ python setup.py install

This library works with Python 2.7 and Python 3.4 or later.

Usage
-----

For typical usage, the `encode` and `decode` functions will take a domain name argument and perform a conversion to A-labels or U-labels respectively.

.. code-block:: pycon

    # Python 3
    >>> import idna
    >>> idna.encode('.')
    b'xn--eckwd4c7c.xn--zckzah'
    >>> print(idna.decode('xn--eckwd4c7c.xn--zckzah'))
    .

You may use the codec encoding and decoding methods using the `idna.codec` module:

.. code-block:: pycon

    # Python 2
    >>> import idna.codec
    >>> print u'.'.encode('idna')
    xn--80ahd1agd.xn--80akhbyknj4f
    >>> print 'xn--80ahd1agd.xn--80akhbyknj4f'.decode('idna')
    .

Conversions can be applied at a per-label basis using the `ulabel` or `alabel` functions if necessary:
Compatibility Mapping (UTS #46)

As described in `RFC 5895 <http://tools.ietf.org/html/rfc5895>`_, the IDNA specification no longer normalizes input from different potential ways a user may input a domain name. This functionality, known as a “mapping”, is now considered by the specification to be a local user-interface issue distinct from IDNA conversion functionality.

This library provides one such mapping, that was developed by the Unicode Consortium. Known as `Unicode IDNA Compatibility Processing <http://unicode.org/reports/tr46/>`_, it provides for both a regular mapping for typical applications, as well as a transitional mapping to help migrate from older IDNA 2003 applications.

For example, “Königsgäßchen” is not a permissible label as *LATIN CAPITAL LETTER K* is not allowed (nor are capital letters in general). UTS 46 will convert this into lower case prior to applying the IDNA conversion.

Transitional processing provides conversions to help transition from the older 2003 standard to the current standard. For example, in the original IDNA specification, the *LATIN SMALL LETTER SHARP S* (ß) was converted into two *LATIN SMALL LETTER S* (ss), whereas in the current IDNA specification this conversion is not performed.
Implementors should use transitional processing with caution, only in rare cases where conversion from legacy labels to current labels must be performed (i.e. IDNA implementations that pre-date 2008). For typical applications that just need to convert labels, transitional processing is unlikely to be beneficial and could produce unexpected incompatible results.

``encodings.idna`` Compatibility
+++++++++++++++++++++++++++++++++++++

Function calls from the Python built-in ``encodings.idna`` module are mapped to their IDNA 2008 equivalents using the ``idna.compat`` module. Simply substitute the ``import`` clause in your code to refer to the new module name.

Exceptions
----------

All errors raised during the conversion following the specification should raise an exception derived from the ``idna.IDNAError`` base class.

More specific exceptions that may be generated as ``idna.IDNABidiError`` when the error reflects an illegal combination of left-to-right and right-to-left characters in a label; ``idna.InvalidCodepoint`` when a specific codepoint is an illegal character in an IDN label (i.e. INVALID); and ``idna.InvalidCodepointContext`` when the codepoint is illegal based on its positional context (i.e. it is CONTEXTO or CONTEXTJ but the contextual requirements are not satisfied.)

Building and Diagnostics
------------------------

The IDNA and UTS 46 functionality relies upon pre-calculated lookup tables for performance. These tables are derived from computing against eligibility criteria in the respective standards. These tables are computed using the command-line script ``tools/idna-data``.

This tool will fetch relevant tables from the Unicode Consortium and perform the required calculations to identify eligibility. It has three main modes:

* ``idna-data make-libdata``. Generates ``idnadata.py`` and ``uts46data.py``, the pre-calculated lookup tables using for IDNA and UTS 46 conversions. Implementors who wish to track this library against a different Unicode version may use this tool to manually generate a different version of the ``idnadata.py`` and ``uts46data.py`` files.

* ``idna-data make-table``. Generate a table of the IDNA disposition (e.g. PVALID, CONTEXTJ, CONTEXTO) in the format found in Appendix B.1 of RFC 5892 and the pre-computed tables published by IANA <http://iana.org/>_.

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*``idna-data U+0061``. Prints debugging output on the various properties associated with an individual Unicode codepoint (in this case, U+0061), that are used to assess the IDNA and UTS 46 status of a codepoint. This is helpful in debugging or analysis.

The tool accepts a number of arguments, described using ````idna-data -h```. Most notably, the ````--version``` argument allows the specification of the version of Unicode to use in computing the table data. For example, ````idna-data --version 9.0.0 make-libdata``` will generate library data against Unicode 9.0.0.

Note that this script requires Python 3, but all generated library data will work in Python 2.7.

Testing
------

The library has a test suite based on each rule of the IDNA specification, as well as tests that are provided as part of the Unicode Technical Standard 46, `Unicode IDNA Compatibility Processing <http://unicode.org/reports/tr46/>`_.

The tests are run automatically on each commit at Travis CI:

.. image:: https://travis-ci.org/kjd/idna.svg?branch=master
   :target: https://travis-ci.org/kjd/idna

Platform: UNKNOWN
Classifier: Development Status :: 5 - Production/Stable
Classifier: Intended Audience :: Developers
Classifier: Intended Audience :: System Administrators
Classifier: License :: OSI Approved :: BSD License
Classifier: Operating System :: OS Independent
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.4
Classifier: Programming Language :: Python :: 3.5
Classifier: Programming Language :: Python :: 3.6
Classifier: Programming Language :: Python :: 3.7
Classifier: Programming Language :: Python :: 3.8
Classifier: Programming Language :: Python :: Implementation :: CPython
Classifier: Programming Language :: Python :: Implementation :: PyPy
Classifier: Topic :: Internet :: Name Service (DNS)
Classifier: Topic :: Software Development :: Libraries :: Python Modules
Classifier: Topic :: Utilities
Requires-Python: >=2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*
1.472 inherits 2.0.4

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1.473 ispell 3.4.00-6+b1

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1.474 types-hoist-non-react-statics 3.3.1

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1.475 fsnotify 1.4.3-0.20170329110642-4da3e2cfbabc

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1.476 libxml 2.9.4+dfsg1 7+deb10u2

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1.477 net-tools 1.60+git20180626.aebd88e-1
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1.478 is-cidr 4.0.2
1.478.1 Available under license:

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1.479 containerd-console 1.0.2

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1.480 spdx-license-ids 3.0.5

1.480.1 Available under license:

```yaml
root = true

[*]
charset = utf-8
end_of_line = lf
indent_style = tab
tab_width = 2
insert_final_newline = true
trim_trailing_whitespace = true

[*.md,yml]]
indent_style = space

[*] md
trim_trailing_whitespace = false
workflow "Test" {
  on = "push"
  resolves = ["npm"]
}

action "npm" {
  uses = "shinnn/actions-npm-alpine@1.0.0"
  args = "install-ci-test"
}
* text=auto

# spdx-license-ids


A list of [SPDX license](https://spdx.org/licenses/) identifiers

## Installation

[Download JSON directly](https://raw.githubusercontent.com/shinnn/spdx-license-ids/master/index.json), or
npm install spdx-license-ids

## Node.js API

### require('spdx-license-ids')

Type: `string[]`

All license IDs except for the currently deprecated ones.

```javascript
const ids = require('spdx-license-ids');
//=> ['0BSD', 'AAL', 'ADSL', 'AFL-1.1', 'AFL-1.2', 'AFL-2.0', 'AFL-2.1', 'AFL-3.0', 'AGPL-1.0-only', ...]
```

```javascript
ids.includes('BSD-3-Clause'); //=> true
ids.includes('CC-BY-1.0'); //=> true
ids.includes('GPL-3.0'); //=> false
```

### require('spdx-license-ids/deprecated')

Type: `string[]`

Deprecated license IDs.

```javascript
const deprecatedIds = require('spdx-license-ids/deprecated');
//=> ['AGPL-1.0', 'AGPL-3.0', 'GFDL-1.1', 'GFDL-1.2', 'GFDL-1.3', 'GPL-1.0', 'GPL-2.0', ...]
```

```javascript
deprecatedIds.includes('BSD-3-Clause'); //=> false
deprecatedIds.includes('CC-BY-1.0'); //=> false
deprecatedIds.includes('GPL-3.0'); //=> true
```

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[*.workflow]

indent_style = space
1.481 liblognorm 2.0.5-1
1.481.1 Available under license :

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Version 2.1, February 1999

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1.501 debianutils 4.8.6.1

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Go support for Protocol Buffers - Google's data interchange format

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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1.507 libusb 1.0.22-2

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package cloud

import (
    "bytes"
    "io/ioutil"
    "os"
    "path/filepath"
    "strings"
    "testing"
)

var sentinels = []string{
    "Copyright",
    "Google",
    "Licensed under the Apache License, Version 2.0 (the "License");",
}

func TestLicense(t *testing.T) {
    t.Parallel()
    err := filepath.Walk(".", func(path string, fi os.FileInfo, err error) error {
        if err != nil {
            return err
        }
        if ext := filepath.Ext(path); ext != ".go" && ext != ".proto" {
            if err != nil {
                return err
            }
        }
        if strings.HasSuffix(path, ".pb.go") {
            // .pb.go files are generated from the proto files.
        }
    })
}
// .proto files must have license headers.
return nil
}
if path == "bigtable/cmd/cbt/cbtdoc.go" {
// Automatically generated.
return nil
}
if path == "cmd/go-cloud-debug-agent/internal/debug/elf/elf.go" {
// BSD license, which is compatible, is embedded in the file.
return nil
}
src, err := ioutil.ReadFile(path)
if err != nil {
return nil
}
src = src[:300] // Ensure all of the sentinel values are at the top of the file.

// Find license
for _, sentinel := range sentinels {
if !bytes.Contains(src, []byte(sentinel)) {
t.Errorf("%v: license header not present. want %q", path, sentinel)
return nil
}
}

return nil
}
if err != nil {
t.Fatal(err)
}

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1.518 python-uritemplate 0.6-3
1.518.1 Available under license :
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uritemplate
===========
.. image:: https://secure.travis-ci.org/uri-templates/uritemplate-py.png?branch=master
   :alt: build status
   :target: http://travis-ci.org/uri-templates/uritemplate-py

This is a Python implementation of `RFC6570`_, URI Template, and can expand templates up to and including Level 4 in that specification.

It exposes a method, *expand*. For example:
It also exposes a method `variables` that returns all variables used in a uritemplate. For example:

```python
>>> from uritemplate import variables
>>> variables('http://www.{domain}[/top,next]{?q:20}')
set(['domain', 'next', 'q', 'top'])
```

This function can be useful to determine what keywords are available to be expanded.

Requirements
-------------
uritemplate works with Python 2.5+.

.. note:: You need to install `simplejson` module for Python 2.5.

.. _simplejson: https://pypi.python.org/pypi/simplejson/

Install
-------
The easiest way to install uritemplate is with pip:

```
$ pip install uritemplate
```

See its `Python Package Index entry` for more.

.. _Python Package Index entry: http://pypi.python.org/pypi/uritemplate

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1.519 pcre2 10.36-2

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PCRE2 is a library of functions to support regular expressions whose syntax
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Email local part: Philip.Hazel  
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1.520 brace-expansion 1.1.11

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1.524 loose-envify 1.4.0

1.525 visual-studio-runtime 14.00.24210.0

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import (
    "bytes"
    "io/ioutil"
    "os"
    "path/filepath"
    "strings"
    "testing"
var sentinels = []string{
"Copyright",
"Google",
`Licensed under the Apache License, Version 2.0 (the "License");
}    

func TestLicense(t *testing.T) {    
t.Parallel()    
err := filepath.Walk(".", func(path string, fi os.FileInfo, err error) error {
    if err != nil {
        return err
    }
    if ext := filepath.Ext(path); ext != ".go" && ext != ".proto" {
        return nil
    }
    if strings.HasSuffix(path, ".pb.go") {
        // .pb.go files are generated from the proto files.
        // .proto files must have license headers.
        return nil
    }
    if path == "bigtable/cmd/cbt/cbtdoc.go" {
        // Automatically generated.
        return nil
    }
    if path == "cmd/go-cloud-debug-agent/internal/debug/elf/elf.go" {
        // BSD license, which is compatible, is embedded in the file.
        return nil
    }
    src, err := ioutil.ReadFile(path)
    if err != nil {
        return nil
    }
    src = src[:300] // Ensure all of the sentinel values are at the top of the file.

    // Find license
    for _, sentinel := range sentinels {
        if !bytes.Contains(src, []byte(sentinel)) {
            t.Errorf("%v: license header not present. want %q", path, sentinel)
            return nil
        }
    }
    return nil
})
if err != nil {
    return nil
}
t.Fatal(err)
}
}

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   */

For lib/py/compat/win32/stdint.h

// ISO C9x compliant stdint.h for Microsoft Visual Studio
// Based on ISO/IEC 9899:TC2 Committee draft (May 6, 2005) WG14/N1124
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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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https://github.com/golang/protobuf

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1.533 typescript-compare 0.0.2

1.534 kerberos 1.18.3-6+deb11u1
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"src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_credential.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_credential.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_credential.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_credential.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mgglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including
the following new or changed files:
include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
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1.547 is-date-object 1.0.2

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1.578 base-passwd 3.5.46

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1.580 base-files 11.1+deb11u1

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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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tomb - support for clean goroutine termination in Go.

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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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https://github.com/golang/protobuf

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1.595 pyasn1 0.4.2

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ASN.1 library for Python

-----------------------
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This is a free and open source implementation of ASN.1 types and codecs as a Python package. It has been first written to support particular protocol (SNMP) but then generalized to be suitable for a wide range of protocols based on [ASN.1 specification](https://www.itu.int/rec/dologin_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items).

Features
--------

* Generic implementation of ASN.1 types (X.208)
* Standards compliant BER/CER/DER codecs
* Dumps/loads ASN.1 structures from Python types
* 100% Python, works with Python 2.4 up to Python 3.6
* MT-safe
* Contributed ASN.1 compiler [Asn1ate](https://github.com/kimgr/asn1ate)

Why using pyasn1

-------------

ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1.
The `pyasn1` package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

How to use `pyasn1`

With `pyasn1` you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```bash
Record ::= SEQUENCE {
    id        INTEGER,
    room  [0] INTEGER OPTIONAL,
    house [1] INTEGER DEFAULT 0
}
```  

Could be expressed in `pyasn1` like this:

```python
class Record(Sequence):
    componentType = NamedTypes(
        NamedType('id', Integer()),
        OptionalNamedType(
            'room', Integer().subtype(
                implicitTag=Tag(tagClassContext, tagFormatSimple, 0)
            )
        ),
        DefaultedNamedType(
            'house', Integer(0).subtype(
                implicitTag=Tag(tagClassContext, tagFormatSimple, 1)
            )
        )
    )
```  

It is in the spirit of ASN.1 to take abstract data description and turn it into a programming language specific form. Once you have your ASN.1 data structure expressed in Python, you can use it along the lines of similar Python type (e.g. ASN.1 `SET` is similar to Python `dict`, `SET OF` to `list`):
```python
>>> record = Record()
>>> record['id'] = 123
>>> record['room'] = 321
>>> str(record)
Record:
id=123
room=321
>>>
```

Part of the power of ASN.1 comes from its serialisation features. You can serialise your data structure and send it over the network.

```python
>>> from pyasn1.codec.der.encoder import encode
>>> substrate = encode(record)
>>> hexdump(substrate)
00000: 30 07 02 01 7B 80 02 01 41
```

Conversely, you can turn serialised ASN.1 content, as received from network or read from a file, into a Python object which you can introspect, modify, encode and send back.

```python
>>> from pyasn1.codec.der.decoder import decode
>>> received_record, rest_of_substrate = decode(substrate, asn1Spec=Record())

>>> for field in received_record:
...    print('{} is {}'.format(field, received_record[field]))
id is 123
room is 321
house is 0

>>> record == received_record
True

>>> received_record.update(room=123)
>>> substrate = encode(received_record)
>>> hexdump(substrate)
00000: 30 06 02 01 7B 80 02 01 7B
```

The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, list, dict etc.). But ASN.1 types exhibit more complicated behaviour. To make life easier for a Pythonista, they can turn their pyasn1 classes into Python built-ins:
```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)
{'id': 123, 'room': 321, 'house': 0}
```

Or vice-versa -- you can initialize an ASN.1 structure from a tree of Python objects:

```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id': 123, 'room': 321, 'house': 0}, asn1Spec=Record())
>>> str(record)
Record:
id=123
room=321
```

With ASN.1 design, serialisation codecs are decoupled from data objects, so you could turn every single ASN.1 object into many different serialised forms. As of this moment, pyasn1 supports BER, DER, CER and Python built-ins codecs. The extremely compact PER encoding is expected to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the [documentation](http://snmplabs.com/pyasn1/), compiled ASN.1 modules for different protocols and file formats could be found in the pyasn1-modules [repo](https://github.com/etingof/pyasn1-modules).

How to get pyasn1

-----------------

The pyasn1 package is distributed under terms and conditions of 2-clause BSD license[http://snmplabs.com/pyasn1/license.html]. Source code is freely available as a GitHub [repo](https://github.com/ETINGOF/pyasn1).

You could `pip install pyasn1` or download it from [PyPI](https://pypi.python.org/pypi/pyasn1).

If something does not work as expected, [open an issue](https://github.com/ETINGOF/pyasn1/issues) at GitHub or post your question on Stack Overflow[http://stackoverflow.com/questions/ask] or try browsing pyasn1 [mailing list archives](https://sourceforge.net/p/pyasn1/mailman/pyasn1-users/).

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Abstract Syntax Notation One (\`ASN.1\`) is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.

The \`pyasn1 <https://pypi.python.org/pypi/pyasn1/>\`_ library implements ASN.1 support in pure-Python.

What is ASN.1
-------------

ASN.1 is a large, arguably over-engineered and extremely old data modelling and serialisation tool. It is probably among the first serialisation protocols in the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first electronic mail (known as X.400). Later on it was split off the e-mail application and become a stand-alone tech still being actively supported by its designers and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the first, unsuccessful, version of the Internet. You can read many interesting discussions <https://news.ycombinator.com/item?id=8871453>_ on that topic.
In the following years, generations of software engineers tackled the serialisation problem many times. We can see that in Google's `ProtoBuffers <https://developers.google.com/protocol-buffers/>`_ or `FlatBuffers <https://google.github.io/flatbuffers/>`_, for example. Interestingly, many new takes on binary protocol design do not depart far from ASN.1 from technical perspective. It's more of a matter of striking a balance between processing overhead, wire format overhead and human readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

* Data types: the standard introduces a collection of basic data types (integers, bits, strings, arrays and records) that can be used for describing arbitrarily complex, nested data structures.

* Serialisation protocols: the above data structures could be converted into a series of octets for storage or transmission over the wire as well as recovered back into their structured form. The system is fully agnostic to hardware architectures differences.

* Schema language: ASN.1 data structures could be described in terms of a schema language for ASN.1 compiler to turn it into platform-specific implementation.

ASN.1 applications
-------------------

Being an old and generally successful standard, ASN.1 is widely adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:

* Signaling standards for the public switched telephone network (SS7 family)
* Network management standards (SNMP, CMIP)
* Directory standards (X.500 family, LDAP)
* Public Key Infrastructure standards (X.509, etc.)
* PBX control (CSTA)
* IP-based Videoconferencing (H.323 family)
* Biometrics (BIP, CBEFF, ACBio)
* Intelligent transportation (SAE J2735)
* Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

ASN.1 gotchas
-------------

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the `statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>`_ on discovered
security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

Documentation
-------------

.. toctree::
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/pyasn1/contents

Use case
--------

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/example-use-case

Download & Install
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Changes
-------

All changes and release history is maintained in changelog. There you could also download the latest unreleased pyasn1 tarball containing the latest fixes and improvements.

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/changelog

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Getting help
------------


Books on ASN.1
--------------

The pyasn1 implementation is largely based on reading up the following awesome books:

* `ASN.1 - Communication between heterogeneous systems <http://www.oss.com/asn1/dubuisson.html>`_ by Olivier Dubuisson

Here you can get the official standards which is hard to read:


On the other end of the readability spectrum, here is a quick and sweet write up:


If you are working with ASN.1, we'd highly recommend reading a proper book on the subject.

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Found in path(s):
* /opt/cola/permits/1114240522_1607457079.84/0/etingof-pyasn1-v0-4-2-0-gdf3a2b7-tar-gz/etingof-pyasn1-df3a2b7/LICENSE.rst
1.596 iputils 20180629-2+deb10u2

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1.597 e2fsprogs 1.46.2-2
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This is the Debian GNU/Linux prepackaged version of the static EXT2
file system consistency checker (e2fsck.static). The EXT2 utilities
were written by Theodore Ts'o <tytso@mit.edu> and Remy Card
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#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image::$(BSD_LIB)

$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../.$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) \`
echo $(my_dir) | sed -e 's;lib/;;'"/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$((INSTALL_PROGRAM) $(BSD_LIB) \$(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$((LDCONFIG))

install-strip: install

install-shlibs-strip:: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../.$(BSD_LIB)
This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

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Index: tdbsa/tdb.c

--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
 */
 /*
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes

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Theodore Ts'o
23-June-2007

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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base64.hpp is a repackaging of the base64.cpp and base64.h files into a
single header suitable for use as a header only library. This conversion was
done by Peter Thorson (webmaster@zaphoyd.com) in 2012. All modifications to
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base64.cpp and base64.h

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L. Peter Deutsch
ghost@aladdin.com

******* UTF8 Validation logic (utf8_validation.hpp) *******
utf8_validation.hpp is adapted from code originally written by Bjoern Hoehrmann
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1.603 netbase 6.3
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1.604 golang-protobuf-extensions 1.0.2-0.20181231171920-c182affec369

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1.606 lodash-es 4.17.20

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1.608 hosted-git-info 2.8.8

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1.610 escape-string-regexp 1.0.5

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1.611 axios 0.21.1

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1.612 mitchellh-mapstructure 0.0.0-20170523030023-d0303fe80992

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1.613 form3tech-oss-jwt-go

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1.614 go-runtime 1.13.1

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1.623 lolex 4.2.0

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1.624 containerd-typeurl 1.0.2

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1.625 perl 5.26.1 6ubuntu0.5
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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

------------------------------------------------------------------------------------------------------------------
---
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author:
- 'Ken Williams <kwilliams@cpan.org>'
- 'Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-
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use strict;
use Config;
BEGIN { require './test.pl' }

if ( $Config{usecrosscompile} ) {
    skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so
# that tests still pass for people intentionally working on older versions:
if ($opt eq '--now')
{
    my $current_year = (gmtime)[5] + 1900;
    is $v_year, $current_year, 'perl -v copyright includes current year';
    is $readme_year, $current_year, 'README copyright includes current year';
# Otherwise simply check that the two copyright dates match each other:

```perl
else

is $readme_year, $v_year, 'README and perl -v copyright dates match';

```

done_testing;

```perl
sub readme_year
# returns the latest copyright year from the top-level README file
{

open my $readme, '<', '../README' or die "Opening README failed: $!";

# The copyright message is the first paragraph:
local $/ = "
;
my $copyright_msg = <$readme>;

my ( $year ) = $copyright_msg =~ /.*\b(\d{4,})/s
    or die "Year not found in README copyright message '$copyright_msg'";

$year;
}

```

```perl
sub v_year
# returns the latest copyright year shown in perl -v
{

my $output = runperl switches => ['-v'];
my ( $year ) = $output =~ /copyright 1987.*\b(\d{4,})/i
    or die "Copyright statement not found in perl -v output '$output'";

$year;
}
```

---

**1.626 googleapis-gnostic 0.4.1**

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Gocheck - A rich testing framework for Go

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1.629 gzip 1.10-4

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* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-zip/azure-core-1.20.0/tests/testserver_tests/coretestserver/coretestserver/test_routes/basic.py
* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-zip/azure-core-1.20.0/tests/async_tests/test_rest_stream_responses_async.py
* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-zip/azure-core-1.20.0/tests/async_tests/test_rest_response_backcompat_async.py
* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-zip/azure-core-1.20.0/tests/async_tests/test_content_length_checking_async.py
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* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-zip/azure-core-1.20.0/tests/async_tests/test_base_polling_async.py
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* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-0-azure-core-1.20.0/tests/test_settings.py
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* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-0-azure-core-1.20.0/tests/async_tests/test_stream_generator_async.py
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* /opt/cola/permits/1226026786_1636488714.39/0/azure-core-1-20-0-0-azure-core-1.20.0/tests/async_tests/test_http_logging_policy_async.py
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Lot of things were rewritten and updated by Bartosz Fenski <fenio@debian.org>, which is now official maintainer.

It was downloaded from http://www.i-scream.org/libstatgrab/

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Upstream-Name: vflow
Source: https://github.com/VerizonDigital/vflow

Files: *
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1.640 openipmi 2.0.25-2.1

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1.646 react-lifecycles-compat 3.0.4

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1.650 diffutils 3.6-1

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GNU `diff' was written by Mike Haertel, David Hayes, Richard Stallman,
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1.651 isodate 0.6.1

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Metadata-Version: 2.1
Name: isodate
Version: 0.6.1
Summary: An ISO 8601 date/time/duration parser and formatter
Home-page: https://github.com/gweis/isodate/
Author: Gerhard Weis
Author-email: gerhard.weis@proclos.com
License: BSD
Platform: UNKNOWN
Classifier: Development Status :: 4 - Beta
Classifier: Intended Audience :: Developers
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ISO 8601 date/time parser

--------------------

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This module implements ISO 8601 date, time and duration parsing. The implementation follows ISO8601:2004 standard, and implements only date/time representations mentioned in the standard. If something is not mentioned there, then it is treated as non existent, and not as an allowed option.

For instance, ISO8601:2004 never mentions 2 digit years. So, it is not intended by this module to support 2 digit years. (while it may still be valid as ISO date, because it is not explicitly forbidden.) Another example is, when no time zone information is given for a time, then it should be interpreted as local time, and not UTC.

As this module maps ISO 8601 dates/times to standard Python data types, like *date*, *time*, *datetime* and *timedelta*, it is not possible to convert all possible ISO 8601 dates/times. For instance, dates before 0001-01-01 are not allowed by the Python *date* and *datetime* classes. Additionally fractional seconds are limited to microseconds. That means if the parser finds for instance nanoseconds it will round it to microseconds.

Documentation
-------------

Currently there are four parsing methods available.
* parse_time:
  parses an ISO 8601 time string into a *time* object
* parse_date:
  parses an ISO 8601 date string into a *date* object
* parse_datetime:
  parses an ISO 8601 date-time string into a *datetime* object
* parse_duration:
  parses an ISO 8601 duration string into a *timedelta* or *Duration* object.
* parse_tzinfo:
  parses the time zone info part of an ISO 8601 string into a *tzinfo* object.

As ISO 8601 allows to define durations in years and months, and *timedelta* does not handle years and months, this module provides a *Duration* class, which can be used almost like a *timedelta* object (with some limitations). However, a *Duration* object can be converted into a *timedelta* object.

There are also ISO formatting methods for all supported data types. Each *xxx_isoformat* method accepts a format parameter. The default format is always the ISO 8601 expanded format. This is the same format used by
*datetime.isoformat*:

* time.isoformat:
  Intended to create ISO time strings with default format
  *hh:mm:ssZ*.

* date.isoformat:
  Intended to create ISO date strings with default format
  *yyyy-mm-dd*.

* datetime.isoformat:
  Intended to create ISO date-time strings with default format
  *yyyy-mm-ddThh:mm:ssZ*.

* duration.isoformat:
  Intended to create ISO duration strings with default format
  *PnnYnnMnnDTnnHnnMnnS*.

* tz.isoformat:
  Intended to create ISO time zone strings with default format
  *hh:mm*.

* strftime:
  A re-implementation mostly compatible with Python's *strftime*, but
  supports only those format strings, which can also be used for dates
  prior 1900. This method also understands how to format *datetime* and
  *Duration* instances.

Installation:
-------------

This module can easily be installed with Python standard installation methods.

Either use *python setup.py install* or in case you have *setuptools* or
*distribute* available, you can also use *easy_install*.

Limitations:
------------

* The parser accepts several date/time representation which should be invalid
  according to ISO 8601 standard.

  1. for date and time together, this parser accepts a mixture of basic and extended format.
     e.g. the date could be in basic format, while the time is accepted in extended format.
     It also allows short dates and times in date-time strings.
  2. For incomplete dates, the first day is chosen. e.g. 19th century results in a date of
     1901-01-01.
  3. negative *Duration* and *timedelta* value are not fully supported yet.

Further information:
---------------------

The doc strings and unit tests should provide rather detailed information about
the methods and their limitations.

The source release provides a *setup.py* script, which can be used to run the unit tests included.

Source code is available at `<http://github.com/gweis/isodate>`.

**CHANGES**

0.6.1 (2021-12-13)

- support python 3.10 ()
- last version to support py 2.7

0.6.0 (2017-10-13)

- support incomplete month date (Fabien Loffredo)
- rely on duck typing when doing duration maths
- support ‘:’ as separator in fractional time zones (usrenmae)

0.5.4 (2015-08-06)

- Fix parsing of Periods (Fabien Bochu)
- Make Duration objects hashable (Geoffrey Fairchild)
- Add multiplication to duration (Reinoud Elhorst)

0.5.1 (2014-11-07)

- fixed pickling of Duration objects
- raise ISO8601Error when there is no 'T' separator in datetime strings (Adrian Coveney)

0.5.0 (2014-02-23)

- ISO8601Error are subclasses of ValueError now (Michael Hrivnak)
- improve compatibility across various python variants and versions
- raise exceptions when using fractional years and months in date maths with durations
- renamed method todatetime on Duration objects to totimedelta
0.4.9 (2012-10-30)
--------------
- support pickling FixedOffset instances
- make sure parsed fractional seconds are in microseconds
- add leading zeros when formatting microseconds (Jarom Loveridge)

0.4.8 (2012-05-04)
--------------
- fixed incompatibility of unit tests with python 2.5 and 2.6 (runs fine on 2.7 and 3.2)

0.4.7 (2012-01-26)
--------------
- fixed tzinfo formatting (never pass None into tzinfo.utcoffset())

0.4.6 (2012-01-06)
--------------
- added Python 3 compatibility via 2to3

0.4.5 (2012-01-06)
--------------
- made setuptools dependency optional

0.4.4 (2011-04-16)
--------------
- Fixed formatting of microseconds for datetime objects

0.4.3 (2010-10-29)
--------------
- Fixed problem with %P formatting and fractions (supplied by David Brooks)

0.4.2 (2010-10-28)
--------------
- Implemented unary - for Duration (supplied by David Brooks)
- Output fractional seconds with '%P' format. (partly supplied by David Brooks)
0.4.1 (2010-10-13)
------------------
- fixed bug in comparison between timedelta and Duration.
- fixed precision problem with microseconds (reported by Tommi Virtanen)

0.4.0 (2009-02-09)
------------------
- added method to parse ISO 8601 time zone strings
- added methods to create ISO 8601 conforming strings

0.3.0 (2009-1-05)
------------------
- Initial release

TODOs
=====

This to do list contains some thoughts and ideas about missing features, and parts to think about, whether to implement them or not. This list is probably not complete.

Missing features:
-----------------
* time formating does not allow to create fractional representations.
* parser for ISO intervals.
* currently microseconds are always padded to a length of 6 characters.
  trailing 0s should be optional

Documentation:
--------------
* parse_datetime:
  - complete documentation to show what this function allows, but ISO forbids.
    and vice verse.
  - support other separators between date and time than 'T'

* parse_date:
  - yeardigits should be always greater than 4
  - dates before 0001-01-01 are not supported

* parse_duration:
  - alternative formats are not fully supported due to parse_date restrictions
  - standard duration format is fully supported but not very restrictive.
**Duration:**
- support fractional years and month in calculations
- implement w3c order relation? (`<http://www.w3.org/TR/xmlschema-2/#duration-order>`_)
- refactor to have duration mathematics only at one place.
- localize __str__ method (does timedelta do this?)
- when is a Duration negative?
- normalize Durations. months [00-12] and years [-inf,+inf]

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- Arphic
- Artistic 2.0
- Artistic clarified
- ASL 1.0
- ASL 1.1
- ASL 2.0
- Baekmuk
- Barr
- Beerware
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- Boost
- Borceux
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- BSD Protection
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Open-vm-tools v10.3.0

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```text
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base64.c -- routines to encode/decode base64 data
$OpenLDAP: pkg/ldap/libraries/liblutil/base64.c,v 1.15 2006/01/03 22:12:11 kurt Exp $ /
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Name: custompalette
Columns: 4
#
  0 0 0  Index 3
  65 38 30  Index 4
103 62 49 Index 6
79 73 72 Index 7
114 101 97 Index 8
208 127 100 Index 9
151 144 142 Index 10
221 207 199 Index 11
NotoNastaliqUrdu-Regular.ttf:
(from https://github.com/googlei18n/noto-fonts)
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10x20-ISO8859-1.pcf
(from https://packages.ubuntu.com/xenial/xfonts-base)
"Public domain font. Share and enjoy."
GIMP Palette
Name: badpalettefile
Columns: 4
#
0 0 0 Index 3
012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789012
34567890123456789
103 62 49 Index 6
79 73 72 Index 7
114 101 97 Index 8
208 127 100 Index 9
151 144 142 Index 10
221 207 199 Index 11
GIMP Palette
Name: Test
Columns: 0
#
GIMP Palette
Name: badpaletteentry
Columns: 4
#
0 0 0 Index 3
65 38
103 62 49 Index 6
79 73 72 Index 7
114 101 97 Index 8
208 127 100 Index 9

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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Most GNU software, including some libraries, is covered by the ordinary GNU General Public License. This license, the GNU Lesser General Public License, applies to certain designated libraries, and is quite different from the ordinary General Public License. We use this license for certain libraries in order to permit linking those libraries into non-free programs.

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writing it). Whether that is true depends on what the Library does
and what the program that uses the Library does.

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and you may at your option offer warranty protection in exchange for a
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   a) The modified work must itself be a software library.

   b) You must cause the files modified to carry prominent notices
      stating that you changed the files and the date of any change.

   c) You must cause the whole of the work to be licensed at no
      charge to all third parties under the terms of this License.

   d) If a facility in the modified Library refers to a function or a
      table of data to be supplied by an application program that uses
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      is invoked, then you must make a good faith effort to ensure that,
      in the event an application does not supply such function or
      table, the facility still operates, and performs whatever part of
      its purpose remains meaningful.

      (For example, a function in a library to compute square roots has
      a purpose that is entirely well-defined independent of the
      application. Therefore, Subsection 2d requires that any
      application-supplied function or table used by this function must
      be optional: if the application does not supply it, the square
      root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.
5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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facilities. This must be distributed under the terms of the
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b) Give prominent notice with the combined library of the fact
that part of it is a work based on the Library, and explaining
where to find the accompanying uncombined form of the same work.

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>
Copyright (C) <year> <name of author>
This library is free software; you can redistribute it and/or modify it under the terms of the GNU Lesser General Public License as published by the Free Software Foundation; either version 2 of the License, or (at your option) any later version.

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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

-----------
EFFECTIVE OCT 2008, LICENSE IS BEING CHANGED TO LGPL-2.1 (though not reflected in released code until Nov 2009 - slow release cycle...)
-----------

Discussion thread from mailing list archive, with approval from everyone actively involved or holding original licensing rights included.

[Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:16

Attachments: Message as HTML
looks like 2.8.11 is out and marked as "GPL-2" ... releasing libraries unde="r=20
GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists
=2Dmike
Re: [Cracklib-devel] cracklib license
From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:18
I understand that, and you're welcome to bring it up with Alec directly and see if he wants to relicense his code as LGPL... but at this point, it was enough to just get it consistent and documented as to what it was released under. This wasn't actually a license change, just a clarification of the licensing that was already in place.

-- Nathan

Nathan Neulinger EMail: nneul@um...
University of Missouri - Rolla Phone: (573) 341-6679
UMR Information Technology Fax: (573) 341-4216

On Monday 01 October 2007, Neulinger, Nathan wrote:
> I understand that, and you're welcome to bring it up with Alec directly
> and see if he wants to relicense his code as LGPL... but at this point,
> it was enough to just get it consistent and documented as to what it was released under. This wasn't actually a license change, just a clarification of the licensing that was already in place.

the original license (before moving to sourceforge -- aka, 2.7) was not GPL-2 ... it was a modified artistic license ... i didnt notice the license change until it was mentioned in the latest notes.

unlike the old license, GPL-2 prevents people from using cracklib unless th=
eir=20
applications are also GPL-2 which imo is just wrong. it isn't the place of =
a=20
library to dictact to application writes what license they should be using.=
=20
thus LGPL-2.1 enters to fill this void.
=2Dmike

Re: [Cracklib-devel] cracklib license
From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:46
Seems like the ideal thing here would be for you and the other distro
maintainers to get together with Alec in a conversation and come to a
decision as to what licensing scheme y'all want. I haven't really done
much other than cleaning up the packaging and patches and a small bit of
additional code, so whatever licensing y'all come up with is fine by me.

-- Nathan
=20
---------------------------------------------
Nathan Neulinger EMail: nneul@um...
University of Missouri - Rolla Phone: (573) 341-6679
UMR Information Technology Fax: (573) 341-4216

> -----Original Message-----
> From: cracklib-devel-bounces@li...
> [mailto:cracklib-devel-bounces@li...] On Behalf Of
> Mike Frysinger
> Sent: Monday, October 01, 2007 8:33 PM
> To: Neulinger, Nathan
> Cc: cracklib-devel@li...; Alec Muffett
> Subject: Re: [Cracklib-devel] cracklib license
> >=20
> On Monday 01 October 2007, Neulinger, Nathan wrote:
> > I understand that, and you're welcome to bring it up with Alec
> directly
> > and see if he wants to relicense his code as LGPL... but at this
> point,
> > it was enough to just get it consistent and documented as to what
> it was
> > released under. This wasn't actually a license change, just a
> > clarification of the licensing that was already in place.
> >=20
> the original license (before moving to sourceforge -- aka, 2.7) was
> not
> GPL-2 ... it was a modified artistic license ... i didn't notice the
> license
> change until it was mentioned in the latest notes.
Open Source Used In Cisco Telemetry Broker On Prem 1.2

> =20
> unlike the old license, GPL-2 prevents people from using cracklib
> unless their
> applications are also GPL-2 which imo is just wrong, it isn't the
> place of a
> library to dictact to application writes what license they should
> be using.
> thus LGPL-2.1 enters to fill this void.
> -mike

Re: [Cracklib-devel] cracklib license
From: Alec Muffett <alecm@cr...> - 2007-10-02 08:57
> Seems like the ideal thing here would be for you and the other distro
> maintainers to get together with Alec in a conversation and come to a
> decision as to what licensing scheme y'all want. I haven't really done
> much other than cleaning up the packaging and patches and a small
> bit of
> additional code, so whatever licensing y'all come up with is fine
> by me.

I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be
linked with any code, not just GPL...

-a

Re: [Cracklib-devel] cracklib license
From: Devin Reade <gdr@gn...> - 2007-10-02 15:04
I would like to see it under LGPL as well. I think it is in everyone's
best interests to have as secure systems as possible, and I think tainting
it via GPL will just make it less likely that the library gets used, and
will not usually cause companies/developers to GPL the dependent code
(where it is not already GPL).

I like GPL, I use it when I can, but I don't think that it's the correct
license in this situation.

Devin
--
If it's sinful, it's more fun.

Re: [Cracklib-devel] cracklib license
From: Nalin Dahyabhai <nalin@re...> - 2008-01-28 16:32
On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:

---

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> Seems like the ideal thing here would be for you and the other distro
> maintainers to get together with Alec in a conversation and come to a
> decision as to what licensing scheme y'all want. I haven't really done
> much other than cleaning up the packaging and patches and a small
> bit of
> additional code, so whatever licensing y'all come up with is fine
> by me.
>
> I am sympathetic. Guys, what do you reckon?
>
> What I am hearing so far is that LGPL makes sense, since it can be
> linked with any code, not just GPL...

My apologies for not chiming in in anything resembling a reasonable
timeframe.

I'd also suggest the LGPL, for the reason you noted above. Alternately,
GPLv2 with the option of using the library under a later version of the
GPL would permit applications which were released under version 3 of the
GPL to use the library, too, which would be sufficient for the packages
which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

Cheers,

Nalin

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-05 21:27

Attachments: Message as HTML
On Monday 28 January 2008, Nalin Dahyabhai wrote:
> On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:
> Seems like the ideal thing here would be for you and the other distro
> maintainers to get together with Alec in a conversation and come to a
> decision as to what licensing scheme y'all want. I haven't really done
> much other than cleaning up the packaging and patches and a small
> bit of
> additional code, so whatever licensing y'all come up with is fine
> by me.
>
> I am sympathetic. Guys, what do you reckon?
>
> What I am hearing so far is that LGPL makes sense, since it can be
> linked with any code, not just GPL....
> My apologies for not chiming in in anything resembling a reasonable
> timeframe.
>
> I'd also suggest the LGPL, for the reason you noted above. Alternately,
> GPLv2 with the option of using the library under a later version of the
> GPL would permit applications which were released under version 3 of the
> GPL to use the library, too, which would be sufficient for the packages
> which are included in Fedora. FWIW, I'd personally lean toward LGPL.
>
> In any case, I thank you both for working on sorting this out.

looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
the change now?

-mike

Re: [Cracklib-devel] cracklib license
From: Alec Muffett <alecm@cr...> - 2008-10-05 23:18

> In any case, I thank you both for working on sorting this out.
>
> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
> the change now?

yes. go for it. thanks++

-a

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-25 22:34

Attachments: Message as HTML
On Sunday 05 October 2008, Alec Muffett wrote:
> In any case, I thank you both for working on sorting this out.
> looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we
> make the change now?
> yes. go for it. thanks++

Nathan Neulinger is the only one who can actually make said change ...

-mike

----------

BELOW IS ORIGINAL LICENSING DISCUSSION RE CHANGING TO GPL from Artistic.
----------
CrackLib was originally licensed with a variant of the Artistic license. In the interests of wider acceptance and more modern licensing, it was switched with the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed as of 2007-10-01 with the following email from Alec Muffett.

The below email references nneul@umr.edu address, as that is the address that was used at the time. For any future emails regarding this, please use nneul@neulinger.org.

From alecm@crypticide.com Mon Oct  1 12:26:03 2007
Received: from umr-exproto2.cc.umr.edu ([131.151.0.192]) by UMR-CMAIL1.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
    Mon, 1 Oct 2007 12:26:03 -0500
Received: from scansrv2.srv.mst.edu ([131.151.1.114]) by umr-exproto2.cc.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
    Mon, 1 Oct 2007 12:26:02 -0500
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-a

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1.686 pycparser 2.21

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pycparser -- A C parser in Python

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Theodore Ts'o <tytso@mit.edu> on Fri Dec 14 22:24:35 EST 2007

It is part of the main e2fsprogs distribution, which can be found at:

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Upstream Author: Theodore Ts'o <tytso@mit.edu>

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*/
Index: tdbsa/tdb.c
===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
/*
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
- 
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Theodore Ts'o
23-June-2007

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c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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<signature of Ty Coon>, 1 April 1990
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That's all there is to it!
This package was added to the e2fsprogs debian source package by
Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

http://sourceforge.net/projects/e2fsprogs

Upstream Author: Theodore Ts'o <tytso@mit.edu>

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This is the Debian GNU/Linux prepackaged version of the ss
command-line interface parsing library. It is currently
distributed together with the EXT2 file system utilities, which are
otherwise packaged as "e2fsprogs".
This package was put together by Yann Dirson <dirson@debian.org>,
from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

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#
# This is a Makefile stub which handles the creation of BSD shared
# libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:$(BSD_LIB)

$(BSD_LIB): $(OBS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
This is the Debian GNU/Linux prepackaged version of the Common Error Description library. It is currently distributed together with the EXT2 file system utilities, which are otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>, from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

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Gadi Oxman, August 1995
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1.692 cron 3.0pl1 134+deb10u1

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* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/cron.h
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/do_command.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/cron.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/user.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/database.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/crontab.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/compat.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/entry.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/job.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/misc.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/config.h

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*  
* $Id: crontab.1,v 2.4 1993/12/31 10:47:33 vixie Exp $  
*  
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$Id: INSTALL,v 2.5 1994/01/15 20:43:43 vixie Exp $  

Read the comments at the top of the Makefile, then edit the area marked
'configurable stuff'.

Edit config.h. The stuff I expect you to change is down a bit from the
top of the file, but it's clearly marked. Also look at pathnames.h.

You don't have to create the /var/cron or /var/cron/tabs directories, since
both the daemon and the `crontab' program will do this the first time they
run if they don't exist. You do need to have a /var, though -- just "mkdir
/var" if you don't have one, or you can "mkdir /usr/var; ln -s /usr/var /var"
if you expect your /var to have a lot of stuff in it.

You will also need /usr/local/etc and /usr/local/bin directories unless you
change the Makefile. These will have to be created by hand, but if you are
a long-time Usenet user you probably have them already. /usr/local/man is
where I keep my man pages, but I have the source for `man' and you probably
do not. Therefore you may have to put the man pages into /usr/man/manl,
which will be hard since there will be name collisions. (Note that the man command was originally written by Bill Joy before he left Berkeley, and it contains no AT&T code, so it is in UUNET’s archive of freely-distributable BSD code.)

LINUX note: /usr/include/paths.h on some linux systems shows _PATH_SENDMAIL to be /usr/bin/sendmail even though sendmail is installed in /usr/lib. you should check this out.

say:
make all

su and say:
make install

Note that if I can get you to “su and say” something just by asking, you have a very serious security problem on your system and you should look into it.

Edit your /usr/lib/crontab file into little pieces -- see the CONVERSION file for help on this.

Use the `crontab' command to install all the little pieces you just created. Some examples (see below before trying any of these!)

crontab -u uucp -r /usr/lib/uucp/crontab.src
crontab -u news -r /usr/lib/news/crontab.src
crontab -u root -r /usr/adm/crontab.src

Notes on above examples: (1) the .src files are copied at the time the command is issued; changing the source files later will have no effect until they are reinstalled with another `crontab -r' command. (2) The crontab command will affect the crontab of the person using the command unless `-u USER' is given; `-u' only works for root. When using most `su' commands under most BSD’s, `crontab' will still think of you as yourself even though you may think of yourself as root -- so use `-u' liberally. (3) the `r' option stands for `replace'; check the man page for crontab(1) for other possibilities.

Kill your existing cron daemon -- do `ps aux' and look for /etc/cron.

Edit your /etc/rc or /etc/rc.local, looking for the line that starts up /etc/cron. Comment it out and add a line to start the new cron daemon -- usually /usr/local/etc/cron, unless you changed it in the Makefile.

Start up this cron daemon yourself as root. Just type /usr/local/etc/cron (or whatever); no ‘&’ is needed since the daemon forks itself and the process you executed returns immediately.
ATT notes: for those people unfortunate enough to be stuck on a AT&T UNIX, you will need the public-domain "libndir", found in the B News source and in any comp.sources.unix archive. You will also need to hack the code some.

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# */

Vixie Cron V3.0
December 27, 1993
[V2.2 was some time in 1992]
[V2.1 was May 29, 1991]
[V2.0 was July 5, 1990]
[V2.0-beta was December 9, 1988]
[V1.0 was May 6, 1987]
Paul Vixie

This is a version of 'cron' that is known to run on BSD 4.[23] systems. It
is functionally based on the SysV cron, which means that each user can have
their own crontab file (all crontab files are stored in a read-protected
directory, usually /var/cron/tabs). No direct support is provided for
'at'; you can continue to run 'atrun' from the crontab as you have been
doing. If you don't have atrun (i.e., System V) you are in trouble.

A messages is logged each time a command is executed; also, the files
"allow" and "deny" in /var/cron can be used to control access to the
"crontab" command (which installs crontabs). It hasn't been tested on
SysV, although some effort has gone into making the port an easy one.

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To use this: Sorry, folks, there is no cutesy 'Configure' script. You'll
have to go edit a couple of files... So, here's the checklist:

Read all the FEATURES, INSTALL, and CONVERSION files
Edit config.h
Edit Makefile
(both of these files have instructions inside; note that
some things in config.h are definable in Makefile and are
therefore surrounded by #ifndef...#endif)
'make'
'su' and 'make install'
(you may have to install the man pages by hand)
kill your existing cron process
(actually you can run your existing cron if you want, but why?)
build new crontabs using /usr/lib/{crontab,crontab.local}
(either put them all in "root"'s crontab, or divide it up
and rip out all the ‘su’ commands, collapse the lengthy
lists into ranges with steps -- basically, this step is
as much work as you want to make it)
start up the new cron
(must be done as root)
watch it. test it with ‘crontab -r’ and watch the daemon track your
changes.
if you like it, change your /etc/[rc,rc.local] to use it instead of
the old one.

$Id: README,v 2.3 1993/12/28 08:34:43 vixie Exp $

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* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/pathnames.h
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-
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" 

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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Certificate data from Mozilla as of: Thu Nov  3 19:04:19 2011#
This is a bundle of X.509 certificates of public Certificate Authorities
(CA). These were automatically extracted from Mozilla's root certificates
file (certdata.txt). This file can be found in the mozilla source tree:
It contains the certificates in PEM format and therefore
can be directly used with curl / libcurl / php_curl, or with
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History
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1.704 safe-buffer 5.1.2

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1.705 com_signalfx_metrics_protobuf 0.0.2

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1.706 shallowequal 1.1.0

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1.708 ghodss-yaml 1.0.1-0.20190212211648-25d852aebe32

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1.710 grok 1.0.1

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1.711 css-to-react-native 3.0.0

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Patch for Ubuntu bug #399071

Display the contents of /etc/legal as part of the MOTD, the first time the user logs in, and set a flag in the user's homedir if possible to prevent repeat displays.

Authors: Dustin Kirkland <kirkland@canonical.com>

Upstream status: Ubuntu-specific, maybe submit to Debian

Index: pam.ubuntu/modules/pam_motd/pam_motd.c
===================================================================
--- pam.ubuntu.orig/modules/pam_motd/pam_motd.c
+++ pam.ubuntu/modules/pam_motd/pam_motd.c
@@ -73,6 +73,61 @@
close(fd);
}

+int display_legal(pam_handle_t *pamh)
+{
+    int retval = PAM_IGNORE, rc;
+    char *user = NULL;
+    char *dir = NULL;
+    char *flag = NULL;
+    struct passwd *pwd = NULL;
+    struct stat s;
+    int f;
+    /* Get the user name to determine if we need to print the disclaimer */
rc = pam_get_item(pamh, PAM_USER, &user);
if (rc == PAM_SUCCESS && user != NULL && *(const char *)user != '0') {
    PAM_MODUTIL_DEF_PRIVS(privs);

    /* Get the password entry */
pwd = pam_modutil_getpwnam(pamh, user);
if (pwd != NULL) {
    if (pam_modutil_drop_priv(pamh, &privs, pwd)) {
        pam_syslog(pamh, LOG_ERR,
                  "Unable to change UID to %d temporarily\n",
                  pwd->pw_uid);
        retval = PAM_SESSION_ERR;
        goto finished;
    }

    if (asprintf(&dir, "%s/.cache", pwd->pw_dir) == -1 || !dir)
        goto finished;
    if (asprintf(&flag, "%s/motd.legal-displayed", dir) == -1 || !flag)
        goto finished;

    if (stat(flag, &s) != 0)
        
    display_file(pamh, "/etc/legal");
    mkdir(dir, 0700);
    f = open(flag, O_WRONLY|O_CREAT|O_EXCL,
             S_IRUSR|S_IWUSR|S_IROTH);
    if (f>=0) close(f);
}

    if (pam_modutil_regain_priv(pamh, &privs)) {
        pam_syslog(pamh, LOG_ERR,
                  "Unable to change UID back to %d\n", privs.old_uid);
        retval = PAM_SESSION_ERR;
    }

    _pam_drop(flag);
    _pam_drop(dir);
}

    return retval;
}

PAM_EXTERN
int pam_sm_open_session(pam_handle_t *pamh, int flags,
int argc, const char **argv)
/* Display the updated motd */
display_file(pamh, motd_path);

+    /* Display the legal disclaimer only if necessary */
+    retval = display_legal(pamh);
+
    return retval;
}

This package was debianized by J.H.M. Dassen (Ray) jdassen@debian.org on

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1.721 olivere-elastic 5.0.70

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// Based on ISO/IEC 9899:TC2 Committee draft (May 6, 2005) WG14/N1124
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LLVM-exception
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creates an executable that is a derivative of the Library (because it 
contains portions of the Library), rather than a "work that uses the 
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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Version 3, 29 June 2007
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* DNS resolver (dirmngr/dns.c)

# DNS resolver (dirmngr/dns.c)

dns.c - Recursive, Reentrant DNS Resolver.

---------------------------------------------------------------------

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under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.
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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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* DNS resolver (dirmngr/dns.c)

dns.c - Recursive, Reentrant DNS Resolver.

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10. [11]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in sntp
11. [12]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
12. [13]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
13. [14]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
14. [15]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
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17. [18]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
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19. [20]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
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22. [23]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
23. [24]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
24. [25]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
25. [26]Jeff Johnson <bjj@chatham.usdesign.com> massive prototyping overhaul
26. [27]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or [28]<H.Lambermont@chello.nl> ntpsweep
27. [29]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
28. [30]Frank Kardel [31]<kardel (at) ntp (dot) org> PARSE <GENERIC> (driver 14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
29. [32]Johannes Maximilian Kuehn <kuehn@ntp.org> Rewrote ntp to comply with NTPv4 specification, ntpq saveconfig
30. [33]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
31. [34]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
32. [35]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
33. [36]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
34. [37]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
35. [38]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation
code for Version 3 as specified in RFC-1305
Maintenance
37. [40]David L. Mills <mills@udel.edu> Version 4 foundation,
precision kernel; clock drivers: 1, 3, 4, 6, 7, 11, 13, 18, 19,
22, 36
38. [41]Wolfgang Moeller <moeller@gwdg1.dnet.gwdg.de> VMS port
39. [42]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
40. [43]Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port
41. [44]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
42. [45]Derek Mulcahy <derek@toybox.demon.co.uk> and [46] Damon
            Hart-Davis <d@hd.org> ARCRON MSF clock driver
43. [47] Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code
            maintenance
44. [48] Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de>
            monitoring/trap scripts, statistics file handling
45. [49] Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
46. [50] Wilfredo Sanchez <wsanchez@apple.com> added support for
            NetInfo
47. [51] Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
48. [52] Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of
            space on the stuff in the html/pic/ subdirectory
49. [53] Ray Schnitzler <schnitz@unipress.com> Unixware1 port
50. [54] Michael Shields <shields@tembel.org> USNO clock driver
51. [55] Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock
driver
52. [56] Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure
            makeover, various other bits (see the ChangeLog)
53. [57] Kenneth Stone <ken@sdd.hp.com> HP-UX port
54. [58] Ajit Thyagarajan <ajit@ee.udel.edu> IP multicast/anycast
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55. [59] Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp> TRAK clock
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56. [60] Brian Utterback <brian.utterback@oracle.com> General codebase,
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57. [61] Loganaden Velvindron <loganaden@gmail.com> Sandboxing
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58. [62] Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic
            TrueTime clock driver
59. [63] Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and
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References
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long-opts;
config-header = config.h;
environrc;
no-misuse-usage;
version-proc = ntpOptionPrintVersion;

version = `eval VERSION=`\ sed -e 's/\.*\([// -e 's/\]\.*\)\/$' < ../sntp/m4/version.m4`\n[ -z "$VERSION" ] && echo "Cannot determine VERSION" && kill -TERM $AG_pid
echo "$VERSION";

version-value = "'.*/ Don't use -v as a shortcut for --version */

/*
* HMS: man pages already have this, and texi pages can use the
* values in sntp/include/version.texi .
*/
// doc-sub = {
//  sub-name = version;
//  sub-text = 's/#VERSION#/<<version>>/g';
// }
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- Viraj Bais &lt;vbais@mailman1.intel.co&gt; and Clayton Kirkwood &lt;kirkwood@striderfm.intel.com&gt; port to WindowsNT 3.5
- Michael Barone &lt;michael.barone@lmco.com&gt; GPSVME fixes
- Karl Berry &lt;karl@owl.HQ.ileaf.com&gt; syslog to file option
- Greg Brackley &lt;greg.brackley@bigfoot.com&gt; Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules
- Marc Brett &lt;Marc.Brett@westgeo.com&gt; Magnavox GPS clock drivers
- Piete Brooks &lt;Piete.Brooks@cl.cam.ac.uk&gt; MSF clock driver, Trimble PARSE support
- Nelson Bolyard &lt;nelson@bolyard.me&gt; update and complete broadcast and crypto features in sntp
- Jean-Francois Boudreault &lt;Jean-Francois.Boudreault@viagation.qc.ca&gt;
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Reg Clemens &lt;reg@dwf.com&gt; Oncore driver (Current maintainer)
Steve Clift &lt;clift@ml.csiro.au&gt; OMEGA clock driver
Casey Crellin &lt;casey@csc.ca&gt; vxWorks (Tornado) port and help with target configuration
Sven Dietrich &lt;sven_dietrich@trimble.com&gt; Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
John A. Dundas III &lt;dundas@salt.jpl.nasa.gov&gt; Apple A/UX port
Torsten Duwe &lt;duwe@immd4.informatik.uni-erlangen.de&gt; Linux port
Dennis Ferguson &lt;dennis@mrbill.canet.ca&gt; Foundation code for NTP Version 2 as specified in RFC-1119
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Dave Hart &lt;davehart@davehart.com&gt; General maintenance, Windows port interpolation rewrite
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Frank Kardel &lt;kardel@ntp.org&gt; PARSE &lt;GENERIC&gt; (driver 14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
Rewrote &lt;tt&gt;snntp&lt;/tt&gt; to comply with NTPv4 specification, &lt;tt&gt;ntpq saveconfig&lt;/tt&gt;
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George Lindholm &lt;lidholm@ucs.ubc.ca&gt; SunOS 5.1 port
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Lars H. Mathiesen &lt;thorinn@diku.dk&gt; adaptation of
foundation code for Version 3 as specified in RFC-1305

Danny Mayer &lt;mayer@ntp.org&gt; Network I/O, Windows Port, Code Maintenance

David L. Mills &lt;mills@udel.edu&gt; Version 4 foundation, precision kernel; clock drivers: 1, 3, 4, 6, 7, 11, 13, 18, 19, 22, 36

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Jeffrey Mogul &lt;mogul@pa.dec.com&gt; nttrace utility

Tom Moore &lt;tmoore@fievel.daytonoh.ncr.com&gt; i386 svr4 port

Kamal A Mostafa &lt;kamal@whence.com&gt; SCO OpenServer port

Derek Mulcahy &lt;d@hd.org&gt; and Damon Hart-Davis &lt;d@hd.org&gt; ARCRON MSF clock driver

Rob Neal &lt;neal@ntp.org&gt; Bancomm refclock and config/parse code maintenance

Rainer Pruy &lt;Rainer.Pruy@informatik.uni-erlangen.de&gt; monitoring/trap scripts, statistics file handling

Dirce Richards &lt;dirce@zk3.dec.com&gt; Digital UNIX V4.0 port

Wilfredo Sánchez &lt;wsanchez@apple.com&gt; added support for NetInfo

Nick Sayer &lt;mrapple@quack.kfu.com&gt; SunOS streams modules

Jack Sasportas &lt;jack@innovativeinternet.com&gt; Saved a Lot of space on the stuff in the html/pic/ subdirectory

Ray Schnitzler &lt;schnitz@unipress.com&gt; Unixware1 port

Michael Shields &lt;shields@tembel.org&gt; USNO clock driver

Jeff Steinman &lt;jss@pebbles.jpl.nasa.gov&gt; Datum PTS clock driver

Harlan Stenn &lt;harlan@pfcs.com&gt; GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)

Kenneth Stone &lt;ken@sdd.hp.com&gt; HP-UX port

Ajit Thyagarajan &lt;ajit@ee.udel.edu&gt; IP multicast/anycast support

Tomoaki TSURUOKA &lt;tsuruoka@nc.fukuoka-u.ac.jp&gt; TRAK clock driver

Brian Utterback &lt;brian.utterback@oracle.com&gt; General codebase, Solaris issues

Loganaden Velvindron &lt;loganaden@gmail.com&gt; SambaBoxing (libseccomp) support

Paul A Vixie &lt;vixie@vix.com&gt; TrueTime GPS driver, generic TrueTime clock driver

Ulrich Windl &lt;Ulrich.Windl@rz.uni-regensburg.de&gt; corrected and validated HTML documents according to the HTML DTD
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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

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therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.
6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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Ty Coon, President of Vice

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The End

1.768 libusb-compat 0.1.12-32

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1.770 armon-go-metrics 0.3.3

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1.772 django-fsm 2.7.1

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Metadata-Version: 1.1
Name: django-fsm
Version: 2.7.1
Summary: Django friendly finite state machine support.
Home-page: http://github.com/kmmbvnr/django-fsm
Author: Mikhail Podgurskiy
Author-email: kmmbvnr@gmail.com
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Keywords: django
Platform: any
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Classifier: Programming Language :: Python :: 3.4
Classifier: Programming Language :: Python :: 3.5
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{signature of Ty Coon}, 1 April 1989
from setuptools import setup

try:
    long_description = open('README.rst').read()
except IOError:
    long_description = ''

setup(
    name='django-fsm',
    version='2.7.1',
    description='Django friendly finite state machine support.',
    author='Mikhail Podgurskiy',
    author_email='kmmbvnr@gmail.com',
    url='http://github.com/kmmbvnr/django-fsm',
    keywords='django',
    packages=['django_fsm', 'django_fsm.management', 'django_fsm.management.commands'],
    include_package_data=True,
    zip_safe=False,
    license='MIT License',
    platforms=['any'],
    classifiers=[
        'Development Status :: 5 - Production/Stable',
        'Environment :: Web Environment',
        'Intended Audience :: Developers',
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        'Operating System :: OS Independent',
        'Framework :: Django',
        'Framework :: Django :: 1.6',
        'Framework :: Django :: 1.8',
        'Framework :: Django :: 1.9',
        'Framework :: Django :: 1.10',
        'Framework :: Django :: 1.11',
        'Framework :: Django :: 2.0',
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1.773 mariadb 10.3.31-0+deb10u1

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The MariaDB packages were initially made by http://ourdelta.org/, and are now managed by the MariaDB development team, maria-developers@lists.launchpad.net

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1.779 moo-color 1.0.2

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1.780 python27-jmespath 0.9.4-1

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1.781 rdma-core 22.1-1

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1.784 kerberos 1.16-2ubuntu0.2

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lib/gssapi/mechglue/g_compare_name.c
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lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
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1.789 numactl 2.0.11-2.1ubuntu0.1

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**CAST128**

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**DES**

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SHA224, SHA256, SHA384, and SHA512
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file (certdata.txt). This file can be found in the mozilla source tree:
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base64.cpp and base64.h

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Ren Nyffenegger rene.nyffenegger@adp-gmbh.ch

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md5.hpp is a reformulation of the md5.h and md5.c code from http://www.opensource.apple.com/source/cups/cups-59/cups/md5.c to allow it to function as a component of a header only library. This conversion was done by Peter Thorson (webmaster@zaphoyd.com) in 2012 for the WebSocket++ project. The changes are released under the same license as the original (listed below)

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L. Peter Deutsch
ghost@aladdin.com

****** UTF8 Validation logic (utf8_validation.hpp) *****
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1.809 kubernetes-apimachinery 0.20.6
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1.813 chalk 2.4.2

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1.815 pexpect 4.6.0-3.ph3

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1.816 tdb 1.44.1

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1.818 olefile 0.46-1

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This is a non-exhaustive list of all the people who helped me improve the olefile project (formerly OleFileIO_PL), in approximative chronological order. Please contact me if I forgot to mention your name.

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localtime.go, localtime_test.go:

Originals:
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Changes:
  * Renamed files from civil* to localtime*.
  * Package changed from civil to toml.
  * 'Local' prefix added to all structs.
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1.830 ipnormalize 1.0.1
1.830.1 Available under license :

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Found in path(s):
* /opt/cola/permits/1140310845_1614802880.67/0/ipv6-normalize-1-0-1-1-tgz/package/index.js
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ipv6-normalize

=============

```bash
$ npm i --save ipv6-normalize
```
Extract `bestPresentation` function from [URI.js/src/IPv6.js](https://github.com/medialize/URI.js) into independent module.

```javascript
'use strict';
var assert = require('assert');
var n = require('./');

assert(n('2001:db8:0:0:0:0:cafe:1111') === '2001:db8::cafe:11111');
assert(n('2001:0DB8:AAAA:0000:0000:0000:0000:000C') === '2001:db8:aaaa::c');
assert(n('2001:db8::1:0:0:0:4') === '2001:db8:0:1::4');
```
1.832 iptables 1.8.2 4

1.832.1 Available under license:

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were written by Theodore Ts'o <tytso@mit.edu> and Remy Card
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Index: tdbsa/tdb.c

===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
 */
 */
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
-
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Theodore Ts'o
23-June-2007

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That's all there is to it!
This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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This is the Debian GNU/Linux prepackaged version of the ss
command-line interface parsing library. It is currently
distributed together with the EXT2 file system utilities, which are
otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>,
from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

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# This is a Makefile stub which handles the creation of BSD shared libraries.

# In order to use this stub, the following makefile variables must be defined.

# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)

all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:$(BSD_LIB)

$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) `echo $(my_dir) | sed -e 's;lib/;;'`/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$((INSTALL_PROGRAM) $(BSD_LIB) \ $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB) \ @-$(LDCONFIG)

install-strip: install

install-shlibs-strip: install-shlibs
uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)/$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(/BSD_LIB)
This is the Debian GNU/Linux prepackaged version of the Common Error
Description library. It is currently distributed together with the EXT2 file
system utilities, which are otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>,
from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

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Gadi Oxman, August 1995

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1.859 lolex 5.1.2

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1.861 logr 0.2.0

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1.863 firmware-free 3.4
1.863.1 Available under license:

Driver: keyspan_pda -- USB Keyspan PDA single-port serial device

File: keyspan_pda/keyspan_pda.fw
Source: keyspan_pda/keyspan_pda.S

File: keyspan_pda/xircom_pgs.fw
Source: keyspan_pda/xircom_pgs.S

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Compiled from original 8051 source into Intel HEX, used in our binary ihex form.

Driver: dsp56k - Atari DSP56k support
File: dsp56k/bootstrap.bin
Source: dsp56k/bootstrap.asm

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DSP56001 assembler, possibly buildable with a56 from
http://www.zdomain.com/a56.html

--------------------------------------------------------------------------

Driver: pcnet_cs - NE2000 compatible PCMCIA adapter

File: cis/LA-PCM.cis
File: cis/PCMLM28.cis
File: cis/DP83903.cis
File: cis/NE2K.cis
File: cis/tamarack.cis
File: cis/PE-200.cis
File: cis/PE520.cis
Source: cis/src/LA-PCM.cis
Source: cis/src/PCMLM28.cis
Source: cis/src/DP83903.cis
Source: cis/src/NE2K.cis
Source: cis/src/tamarack.cis
Source: cis/src/PE-200.cis
Source: cis/src/PE520.cis

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--------------------------------------------------------------------------

Driver: 3c589_cs - 3Com PCMCIA adapter

File: cis/3CXEM556.cis
Source: cis/src/3CXEM556.cis

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--------------------------------------------------------------------------
Driver: 3c574_cs - 3Com PCMCIA adapter

File: cis/3CCFEM556.cis
Source: cis/src/3CCFEM556.cis

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--------------------------------------------------------------------------

Driver: serial_cs - Serial PCMCIA adapter

File: cis/MT5634ZLX.cis
File: cis/RS-COM-2P.cis
File: cis/COMpad2.cis
File: cis/COMpad4.cis
Source: cis/src/MT5634ZLX.cis
Source: cis/src/RS-COM-2P.cis
Source: cis/src/COMpad2.cis
Source: cis/src/COMpad4.cis

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--------------------------------------------------------------------------

Driver: serial_cs - Serial PCMCIA adapter

File: cis/SW_555_SER.cis
File: cis/SW_7xx_SER.cis
File: cis/SW_8xx_SER.cis

Licence: GPLv3

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These binary files are understood to be the preferred form for modification. They follow the PCMCIA Card Information Structure (CIS) format. The pcmcia-cs project <http://pcmcia-cs.sourceforge.net/> distributes tools for converting between CIS and an unofficial text format.

--------------------------------------------------------------------------
Driver: dvb-tpci -- AV7110 cards

File: av7110/bootcode.bin
Source: av7110/Boot.S

Licence: GPLv2 or later

------------------------------------------------------------------------

Driver: usbdux/usbduxfast/usbduxsigma - usbdux data acquisition cards

File: usbdux_firmware.bin
File: usbduxfast_firmware.bin
File: usbduxsigma_firmware.bin
Source: usbdux/fx2-include.asm
Source: usbdux/usbduxfast_firmware.asm
Source: usbdux/usbdux_firmware.asm
Source: usbdux/usbduxsigma_firmware.asm

Licence: GPLv2. See source code in usbdux/ subdirectory.

Provided from the author, Bernd Porr <BerndPorr@f2s.com>

------------------------------------------------------------------------

Driver: isci -- Intel C600 SAS controller driver

File: isci/isci_firmware.bin
Source: isci/create_fw.c
Source: isci/create_fw.h
Source: isci/probe_roms.h
Source: isci/Makefile

Licence: GPLv2

------------------------------------------------------------------------

Driver: carl9170 -- Atheros AR9170 802.11 draft-n USB driver

File: carl9170-1.fw
Version: 1.9.6
Source: carl9170fw/

Downloaded from http://linuxwireless.org/en/users/Drivers/carl9170

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File: keyspan_pda/xircom_pgs.fw
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Compiled from original 8051 source into Intel HEX, used in our binary ihex form.

Driver: dsp56k - Atari DSP56k support

File: dsp56k/bootstrap.bin
Source: dsp56k/bootstrap.asm

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DSP56001 assembler, possibly buildable with a56 from http://www.zdomain.com/a56.html

Driver: pcnet_cs - NE2000 compatible PCMCIA adapter

File: cis/LA-PCM.cis
File: cis/PCMLM28.cis
File: cis/DP83903.cis
File: cis/NE2K.cis
File: cis/tamarack.cis
File: cis/PE-200.cis
File: cis/PE520.cis
Source: cis/src/LA-PCM.cis
Source: cis/src/PCMLM28.cis
Source: cis/src/DP83903.cis
Source: cis/src/NE2K.cis
Source: cis/src/tamarack.cis
Source: cis/src/PE-200.cis
Source: cis/src/PE520.cis

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-------------------------------------------------------------------------

Driver: 3c589_cs - 3Com PCMCIA adapter

File: cis/3CXEM556.cis
Source: cis/src/3CXEM556.cis

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Driver: 3c574_cs - 3Com PCMCIA adapter

File: cis/3CCFEM556.cis
Source: cis/src/3CCFEM556.cis

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Driver: serial_cs - Serial PCMCIA adapter

File: cis/MT5634ZLX.cis
File: cis/RS-COM-2P.cis
File: cis/COMpad2.cis
File: cis/COMpad4.cis
Source: cis/src/MT5634ZLX.cis
Source: cis/src/RS-COM-2P.cis
Source: cis/src/COMpad2.cis
Source: cis/src/COMpad4.cis

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Driver: serial_cs - Serial PCMCIA adapter

File: cis/SW_555_SER.cis
File: cis/SW_7xx_SER.cis
File: cis/SW_8xx_SER.cis

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These binary files are understood to be the preferred form for
modification. They follow the PCMCIA Card Information Structure (CIS)
format. The pcmcia-cs project <http://pcmcia-cs.sourceforge.net/>
distributes tools for converting between CIS and an unofficial text
format.

-----------------------------------------------------------------------

Driver: dvb-ttpci -- AV7110 cards

File: av7110/bootcode.bin
Source: av7110/Boot.S

Licence: GPLv2 or later

-----------------------------------------------------------------------

Driver: usbdux/usbduxfast/usbduxsigma - usbdux data acquisition cards

File: usbdux_firmware.bin
File: usbduxfast_firmware.bin
File: usbduxsigma_firmware.bin
Source: usbdux/fx2-include.asm
Source: usbdux/usbduxfast_firmware.asm
Source: usbdux/usbdux_firmware.asm
Source: usbdux/usbduxsigma_firmware.asm

Licence: GPLv2. See source code in usbdux/ subdirectory.

Provided from the author, Bernd Porr <BerndPorr@f2s.com>
Driver: isci -- Intel C600 SAS controller driver

File: isci/isci_firmware.bin
Source: isci/create_fw.c
Source: isci/create_fw.h
Source: isci/probe_roms.h
Source: isci/Makefile

Licence: GPLv2

Driver: carl9170 -- Atheros AR9170 802.11 draft-n USB driver

File: carl9170-1.fw
Version: 1.9.6
Source: carl9170fw/

Downloaded from http://linuxwireless.org/en/users/Drivers/carl9170

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Atheros carl9170 firmware - used by the ar9170 wireless device

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1.868 go-colorable 0.1.6

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1.869 jcmturner-aescts 1.0.1

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**1.873 wrappy 1.0.2**

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Go support for Protocol Buffers - Google's data interchange format

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goproperties - properties file decoder for Go

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procsfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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1.890 aws-sdk-cpp 1.9.36

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add_project(aws-cpp-sdk-license-manager "C++ SDK for the AWS license-manager service" aws-cpp-sdk-core)

file(GLOB AWS_LICENSE-MANAGER_HEADERS
 "include/aws/license-manager/*.h"
 )

file(GLOB AWS_LICENSE-MANAGER_MODEL_HEADERS
 "include/aws/license-manager/model/*.h"
 )

file(GLOB AWS_LICENSE-MANAGER_SOURCE
 "source/*.*.cpp"
 )

file(GLOB AWS_LICENSE-MANAGER_MODEL_SOURCE
 "source/model/*.*.cpp"
 )

file(GLOB LICENSE-MANAGER_UNIFIED_HEADERS
 $(AWS_LICENSE-MANAGER_HEADERS)
 $(AWS_LICENSE-MANAGER_MODEL_HEADERS)
 )
file(GLOB LICENSE-MANAGERUNITY_SRC
    ${AWS_LICENSE-MANAGER_SOURCE}
    ${AWS_LICENSE-MANAGER_MODEL_SOURCE}
)

if(ENABLE_UNITY_BUILD)
    enable_unity_build("LICENSE-MANAGER" LICENSE-MANAGERUNITY_SRC)
endif()

file(GLOB LICENSE-MANAGER_SRC
    ${LICENSE-MANAGER_UNIFIED_HEADERS}
    ${LICENSE-MANAGERUNITY_SRC}
)

if(WIN32)
    #if we are compiling for visual studio, create a sane directory tree.
    if(MSVC)
        source_group("Header Files\aws\license-manager" FILES ${AWS_LICENSE-MANAGER_HEADERS})
        source_group("Header Files\aws\license-manager\model" FILES ${AWS_LICENSE-MANAGER_MODEL_HEADERS})
        source_group("Source Files" FILES ${AWS_LICENSE-MANAGER_SOURCE})
        source_group("Source Files\model" FILES ${AWS_LICENSE-MANAGER_MODEL_SOURCE})
    endif(MSVC)
endif()

set(LICENSE-MANAGER_INCLUDES
    "${CMAKE_CURRENT_SOURCE_DIR}/include/"
)

add_library(${PROJECT_NAME} ${LICENSE-MANAGER_SRC})
add_library(AWS::${PROJECT_NAME} ALIAS ${PROJECT_NAME})

set_compiler_flags(${PROJECT_NAME})
set_compiler_warnings(${PROJECT_NAME})

if(USE_WINDOWS_DLL_SEMANTICS AND BUILD_SHARED_LIBS)
    target_compile_definitions(${PROJECT_NAME} PRIVATE "AWS_LICENSEMANAGER_EXPORTS")
endif()

target_include_directories(${PROJECT_NAME} PUBLIC
    ${<BUILD_INTERFACE}:${CMAKE_CURRENT_SOURCE_DIR}/include>
    ${<INSTALL_INTERFACE:include>})

target_link_libraries(${PROJECT_NAME} PRIVATE ${PLATFORM_DEP_LIBS} ${PROJECT_LIBS})

setup_install()
install (FILES {AWS_LICENSE-MANAGER_HEADERS} DESTINATION ${INCLUDE_DIRECTORY}/aws/license-manager)
install (FILES {AWS_LICENSE-MANAGER_MODEL_HEADERS} DESTINATION ${INCLUDE_DIRECTORY}/aws/license-manager/model)

do_packaging()

AWS SDK for C++

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1.900 debianutils 4.11.2

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1.902 goprotobuf 1.4.3

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1.903 coreutils 8.32-4+b1

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1.907 jest-canvas-mock 2.3.0

1.907.1 Available under license:

No license file was found, but licenses were detected in source scan.

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Open Source Used In Cisco Telemetry Broker On Prem 1.2 8295
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- Being respectful of differing viewpoints and experiences
- Gracefully accepting constructive criticism
- Focusing on what is best for the community
- Showing empathy towards other community members

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- The use of sexualized language or imagery and unwelcome sexual attention or advances
- Trolling, insulting/derogatory comments, and personal or political attacks
- Public or private harassment
- Publishing others' private information, such as a physical or electronic address, without explicit permission
- Other conduct which could reasonably be considered inappropriate in a professional setting

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treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the program's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:
Gnomovision version 69, Copyright (C) year name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.

The hypothetical commands `show w' and `show c' should show the appropriate
parts of the General Public License. Of course, the commands you use may
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school, if any, to sign a "copyright disclaimer" for the program, if
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`Gnomovision' (which makes passes at compilers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989
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Version 1, February 1989

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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

Gnomovision version 69, Copyright (C) 19xx name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it under certain conditions; type `show c' for details.

The hypothetical commands `show w' and `show c' should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w' and `show c'; they could even be mouse-clicks or menu items--whatever suits your program.

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<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!

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Version 3, 29 June 2007

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copy of the Corresponding Source for all the software in the
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1.910 selinux 2.7-2build2

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arch-tag: d4250e44-a0e0-4ee0-adb9-2bd74f6eeb27

1.911 @emotion/weak-memoize 0.2.5

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**1.912 zlib 1.2.3**

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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017

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1.913 kronosnet 1.8-2

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/**
* A specialized version of `baseIsEqual` for arrays and objects which performs
* deep comparisons and tracks traversed objects enabling objects with circular
* references to be compared.
*
* @param {Object} object The object to compare.
* @param {Object} other The other object to compare.
* @param {Function} equalFunc The function to determine equivalents of values.
* @param {Function} [customizer] The function to customize comparisons.
* @param {number} [bitmask] The bitmask of comparison flags. See `baseIsEqual`
* for more details.
* @param {Object} [stack] Tracks traversed `object` and `other` objects.
* @returns {boolean} Returns `true` if the objects are equivalent, else `false`.
*/
// for more details.
/**
* Creates an array of the own enumerable property names of `object`.
* 
* **Note:** Non-object values are coerced to objects. See the
* [ES spec](http://ecma-international.org/ecma-262/7.0/#sec-object.keys)
* for more details.
* 
* @static
* @since 0.1.0
* @memberOf _
* @category Object
* @param {Object} object The object to query.
* @returns {Array} Returns the array of property names.
* @example
* 
* function Foo() {
*   this.a = 1;
*   this.b = 2;
* }
* 
* Foo.prototype.c = 3;
* 
* _.keys(new Foo);
* // => ['a', 'b'] (iteration order is not guaranteed)
* 
* _.keys('hi');
* // => ['0', '1']
*/
1.930 sg3-utils 1.44-1
1.930.1 Available under license:

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1.931 libidn 2.3.0-5

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compiler/cpp/src/generate/t_java_generator.cc
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1.944 udev 241-7~deb10u6

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1.964 yup 0.28.5

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1.966 go-capability 0.0.0-20200815063812-42c35b437635

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1.967 value-equal 1.0.1

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1.968 python-fasteners 0.12.0-3

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1.971 hoist-non-react-statics 3.3.2

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1.972 open-ldap 2.4.47+dfsg-3+deb10u6

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To: Stephen Frost <sfrost@debian.org>
X-Mailer: Lotus Notes Release 5.0.2a (Intl) 23 November 1999
Subject: Re: Juan C. Gomez license in OpenLDAP Source

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To: Stephen Frost <sfrost@debian.org>
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---
To: Stephen Frost <sfrost@snowman.net>
Subject: Re: OpenLDAP Licenseing issues
From: "Kurt D. Zeilenga" <Kurt@OpenLDAP.org>
Date: Wed, 28 May 2003 10:55:44 -0700
Cc: Steve Langasek <vorlon@netexpress.net>, debian-legal@lists.debian.org, openldap-devel@OpenLDAP.org
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Regards, Kurt

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1.973 wget 1.20.1 1.1

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<one line to give the program's name and a brief idea of what it does.>
Copyright (C) <year> <name of author>

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1.980 libalgorithm-diff-xs-perl 0.04 5+b1

1.980.1 Available under license:
No license file was found, but licenses were detected in source scan.

This library is free software; you can redistribute it and/or modify it under the same terms as Perl itself.

Found in path(s):
* /opt/ws_local/PERMITS_SQL/1068209212_1595544847.99/0/libalgorithm-diff-xs-perl-0-04-orig-targz/Algorithm-Diff-XS-0.04/README
No license file was found, but licenses were detected in source scan.

=head1 NAME

Algorithm::Diff::XS - Algorithm::Diff with XS core loop

=head1 SYNOPSIS

# Drop-in replacement to Algorithm::Diff, but "compact_diff"
# and C<LCSidx> will run much faster for large data sets.
use Algorithm::Diff::XS qw( compact_diff LCSidx );

=head1 DESCRIPTION

This module is a simple re-packaging of Joe Schaefer's excellent but not very well-known L<Algorithm::LCS> with a drop-in interface that simply re-uses the installed version of the L<Algorithm::Diff> module.

Note that only the C<LCSidx> function is optimized in XS at the moment, which means only C<compact_diff> will get significantly faster for large data sets, while C<diff> and C<sdiff> will run in identical speed as C<Algorithm::Diff>.

=head1 BENCHMARK
The benchmarking script is as below:

```perl
my @data = ([qw/a b d/ x 50], [qw/b a d c/ x 50]);
cmpthese( 500, {
  'Algorithm::Diff' => sub {
    Algorithm::Diff::compact_diff(@data)
  },
  'Algorithm::Diff::XS' => sub {
    Algorithm::Diff::XS::compact_diff(@data)
  }
});
```

=head1 SEE ALSO

L<Algorithm::Diff>, L<Algorithm::LCS>.

=head1 AUTHORS

Audrey Tang E<lt>cpan@audreyt.orgE<gt>

=head1 COPYRIGHT

Copyright 2008 by Audrey Tang E<lt>cpan@audreyt.orgE<gt>.

Contains derived code copyrighted 2003 by Joe Schaefer, 
E<lt>joe+cpan@sunstarsys.comE<gt>.

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it under the same terms as Perl itself.

=cut

---

abstract: Algorithm::Diff with XS core loop
author: Audrey Tang <cpan@audreyt.org>
distribution_type: module
generated_by: Module::Install version 0.67
license: perl
meta-spec:
url: http://module-build.sourceforge.net/META-spec-v1.3.html
version: 1.3
name: Algorithm-Diff-XS
no_index:
directory:
  - inc
  - t
requires:
  Algorithm::Diff: 1.19
perl: 5.6.0
version: 0.01

Found in path(s):
* /opt/ws_local/PERMITS_SQL/1068209212_1595544847.99/0/libalgorithm-diff-xs-perl-0-04-orig-targ.gz/Algorithm-Diff-XS-0.04/META.yml
No license file was found, but licenses were detected in source scan.

'GNU public license' => 'gpl', 1,
'GNU lesser public license' => 'gpl', 1,
'BSD license' => 'bsd', 1,
'proprietary' => 'proprietary', 0,
if ( $osi and $license_text =~ /All rights reserved/i ) {
  warn "LEGAL WARNING: 'All rights reserved' may invalidate Open Source licenses. Consider removing it.";
}

Found in path(s):
* /opt/ws_local/PERMITS_SQL/1068209212_1595544847.99/0/libalgorithm-diff-xs-perl-0-04-orig-targ.gz/Algorithm-Diff-XS-0.04/inc/Module/Install/Metadata.pm
No license file was found, but licenses were detected in source scan.

/*
   ---------------------------------------------------------------

ppport.h -- Perl/Pollution/Portability Version 3.06_01

Automatically created by Devel::PPPort running under

Do NOT edit this file directly! -- Edit PPPort_pm.PL and the
includes in parts/inc/ instead.

Use 'perldoc ppport.h' to view the documentation below.

---------------------------------------------------------------

SKIP

=pod
=head1 NAME

ppport.h - Perl/Pollution/Portability version 3.06_01

=head1 SYNOPSIS

perl ppport.h [options] [source files]

Searches current directory for files if no [source files] are given

--help                      show short help
--patch=file                write one patch file with changes
--copy=suffix               write changed copies with suffix
--diff=program             use diff program and options
--compat-version=version    provide compatibility with Perl version
--cplusplus                accept C++ comments
--quiet                    don't output anything except fatal errors
--nodiag                   don't show diagnostics
--nohints                  don't show hints
--nochanges                don't suggest changes
--nofilter                 don't filter input files
--list-provided            list provided API
--list-unsupported         list unsupported API
--api-info=name            show Perl API portability information

=head1 COMPATIBILITY

This version of F<ppport.h> is designed to support operation with Perl installations back to 5.003, and has been tested up to 5.9.3.

=head1 OPTIONS

=head2 --help

Display a brief usage summary.

=head2 --patch=I<file>

If this option is given, a single patch file will be created if any changes are suggested. This requires a working diff program to be installed on your system.

=head2 --copy=I<suffix>
If this option is given, a copy of each file will be saved with the given suffix that contains the suggested changes. This does not require any external programs.

If neither `--patch` or `--copy` are given, the default is to simply print the diffs for each file. This requires either `Text::Diff` or a `diff` program to be installed.

```
=head2 --diff=I<program>
```

Manually set the diff program and options to use. The default is to use `Text::Diff`, when installed, and output unified context diffs.

```
=head2 --compat-version=I<version>
```

Tell `ppport.h` to check for compatibility with the given Perl version. The default is to check for compatibility with Perl version 5.003. You can use this option to reduce the output of `ppport.h` if you intend to be backward compatible only up to a certain Perl version.

```
=head2 --cplusplus
```

Usually, `ppport.h` will detect C++ style comments and replace them with C style comments for portability reasons. Using this option instructs `ppport.h` to leave C++ comments untouched.

```
=head2 --quiet
```

Be quiet. Don't print anything except fatal errors.

```
=head2 --nodiag
```

Don't output any diagnostic messages. Only portability alerts will be printed.

```
=head2 --nohints
```

Don't output any hints. Hints often contain useful portability notes.

```
=head2 --nochanges
```

Don't suggest any changes. Only give diagnostic output and hints unless these are also deactivated.
Don't filter the list of input files. By default, files not looking like source code (i.e. not *.xs, *.c, *.cc, *.cpp or *.h) are skipped.

Lists the API elements for which compatibility is provided by F<ppport.h>. Also lists if it must be explicitly requested, if it has dependencies, and if there are hints for it.

Lists the API elements that are known not to be supported by F<ppport.h> and below which version of Perl they probably won't be available or work.

Show portability information for API elements matching I<name>. If I<name> is surrounded by slashes, it is interpreted as a regular expression.

In order for a Perl extension (XS) module to be as portable as possible across differing versions of Perl itself, certain steps need to be taken.

Including this header is the first major one. This alone will give you access to a large part of the Perl API that hasn't been available in earlier Perl releases. Use

```perl
perl ppport.h --list-provided
```

to see which API elements are provided by ppport.h.

You should avoid using deprecated parts of the API. For example, using global Perl variables without the C<PL_> prefix is deprecated. Also, some API functions used to have a C<perl_> prefix. Using this form is also deprecated. You can safely use the supported API, as F<ppport.h> will provide wrappers for older Perl versions.
If you use one of a few functions that were not present in earlier versions of Perl, and that can't be provided using a macro, you have to explicitly request support for these functions by adding one or more C<#define>s in your source code before the inclusion of F<ppport.h>.

These functions will be marked C<explicit> in the list shown by C<--list-provided>.

Depending on whether you module has a single or multiple files that use such functions, you want either C<static> or global variants.

For a C<static> function, use:

```c
#define NEED_function
```

For a global function, use:

```c
#define NEED_function_GLOBAL
```

Note that you mustn't have more than one global request for one function in your project.

<table>
<thead>
<tr>
<th>Function</th>
<th>Static Request</th>
<th>Global Request</th>
</tr>
</thead>
<tbody>
<tr>
<td>eval_pv()</td>
<td>NEED_eval_pv</td>
<td>NEED_eval_pv_GLOBAL</td>
</tr>
<tr>
<td>grok_bin()</td>
<td>NEED_grok_bin</td>
<td>NEED_grok_bin_GLOBAL</td>
</tr>
<tr>
<td>grok_hex()</td>
<td>NEED_grok_hex</td>
<td>NEED_grok_hex_GLOBAL</td>
</tr>
<tr>
<td>grok_number()</td>
<td>NEED_grok_number</td>
<td>NEED_grok_number_GLOBAL</td>
</tr>
<tr>
<td>grok_numeric_radix()</td>
<td>NEED_grok_numeric_radix</td>
<td>NEED_grok_numeric_radix_GLOBAL</td>
</tr>
<tr>
<td>grok_oct()</td>
<td>NEED_grok_oct</td>
<td>NEED_grok_oct_GLOBAL</td>
</tr>
<tr>
<td>newCONSTSUB()</td>
<td>NEED_newCONSTSUB</td>
<td>NEED_newCONSTSUB_GLOBAL</td>
</tr>
<tr>
<td>newRV_noinc()</td>
<td>NEED_newRV_noinc</td>
<td>NEED_newRV_noinc_Global</td>
</tr>
<tr>
<td>sv_2pv_nolen()</td>
<td>NEED_sv_2pv_nolen</td>
<td>NEED_sv_2pv_nolen_GLOBAL</td>
</tr>
<tr>
<td>sv_2pvbyte()</td>
<td>NEED_sv_2pvbyte</td>
<td>NEED_sv_2pvbyte_GLOBAL</td>
</tr>
<tr>
<td>sv_catpvf_mg()</td>
<td>NEED_sv_catpvf_mg</td>
<td>NEED_sv_catpvf_mg_Global</td>
</tr>
<tr>
<td>sv_catpvf_mg_nocontext()</td>
<td>NEED_sv_catpvf_mg_nocontext</td>
<td>NEED_sv_catpvf_mg_nocontext_Global</td>
</tr>
<tr>
<td>sv_setpvf_mg()</td>
<td>NEED_sv_setpvf_mg</td>
<td>NEED_sv_setpvf_mg_Global</td>
</tr>
<tr>
<td>sv_setpvf_mg_nocontext()</td>
<td>NEED_sv_setpvf_mg_nocontext</td>
<td>NEED_sv_setpvf_mg_nocontext_Global</td>
</tr>
<tr>
<td>vnewSVpvf()</td>
<td>NEED_vnewSVpvf</td>
<td>NEED_vnewSVpvf_GLOBAL</td>
</tr>
</tbody>
</table>

To avoid namespace conflicts, you can change the namespace of the explicitly exported functions using the C<DPPP_NAMESPACE> macro. Just C<#define> the macro before including C<ppport.h>:

```c
#define DPPP_NAMESPACE MyOwnNamespace_
#include "ppport.h"
```
The default namespace is C<\DPPP_>.

=back

The good thing is that most of the above can be checked by running F<ppport.h> on your source code. See the next section for details.

=head1 EXAMPLES

To verify whether F<ppport.h> is needed for your module, whether you should make any changes to your code, and whether any special defines should be used, F<ppport.h> can be run as a Perl script to check your source code. Simply say:

perl ppport.h

The result will usually be a list of patches suggesting changes that should at least be acceptable, if not necessarily the most efficient solution, or a fix for all possible problems.

If you know that your XS module uses features only available in newer Perl releases, if you're aware that it uses C++ comments, and if you want all suggestions as a single patch file, you could use something like this:

perl ppport.h --compat-version=5.6.0 --cplusplus --patch=test.diff

If you only want your code to be scanned without any suggestions for changes, use:

perl ppport.h --nochanges

You can specify a different C<diff> program or options, using the C<--diff> option:

perl ppport.h --diff='diff -C 10'

This would output context diffs with 10 lines of context.

To display portability information for the C<newSVpvn> function, use:

perl ppport.h --api-info=newSVpvn

Since the argument to C<--api-info> can be a regular expression, you can use
perl ppport.h --api-info=/_nomg$/

to display portability information for all C<_nomg> functions or

perl ppport.h --api-info=/

to display information for all known API elements.

=head1 BUGS

If this version of F<ppport.h> is causing failure during
the compilation of this module, please check if newer versions
of either this module or C<Devel::PPPort> are available on CPAN
before sending a bug report.

If F<ppport.h> was generated using the latest version of
C<Devel::PPPort> and is causing failure of this module, please
file a bug report using the CPAN Request Tracker at L<http://rt.cpan.org/>.

Please include the following information:

=over 4

=item 1.

The complete output from running "perl -V"

=item 2.

This file.

=item 3.

The name and version of the module you were trying to build.

=item 4.

A full log of the build that failed.

=item 5.

Any other information that you think could be relevant.

=back

For the latest version of this code, please get the C<Devel::PPPort>
module from CPAN.
use strict;

my %opt = (
    quiet => 0,
    diag  => 1,
    hints => 1,
    changes => 1,
    cplusplus => 0,
    filter  => 1,
);

my($ppport) = $0 =~ /\[(\w.]+)/;$
my $LF = '(?:\r\n|\[\r\n\])';  # line feed
my $HS = "[ \t\]";             # horizontal whitespace

eval {
    require Getopt::Long;
    Getopt::Long::GetOptions(%opt, qw(
        help quiet diag! filter! hints! changes! cplusplus
        patch=s copy=s diff=s compat-version=s
        list-provided list-unsupported api-info=s
    )) or usage();
};

if ($@ and grep /^-/, @ARGV) {
    usage() if "@ARGV" =~ /-[^-]\?
    die "Getopt::Long not found. Please don't use any options.

    usage() if $opt{help};
if (exists $opt{'compat-version'}) {
  my($r,$v,$s) = eval { parse_version($opt{'compat-version'}) };
  die "Invalid version number format: $opt{'compat-version'}\n";
}

die "Only Perl 5 is supported" if $r != 5;
die "Invalid version number: $opt{'compat-version'}\n" if $v >= 1000 || $s >= 1000;
$opt{'compat-version'} = sprintf "%.d.%.3d%.3d", $r, $v, $s;
} else {
  $opt{'compat-version'} = 5;
}

# Never use C comments in this file!!!!!
my $ccs = '/.*/';
my $cce = '*/.*/';
my $rccs = quotemeta $ccs;
my $rcce = quotemeta $cce;

my %API = map { /^(\w+)\|\([^|\]*\)\|\([^|\]*\)\|\w*$/
    ? ( $1 =>
        ($2                  ? ( base => $2 ) : ()),
        ($3                  ? ( todo => $3 ) : ()),
        (index($4, 'v') >= 0 ? ( varargs => 1 ) : ()),
        (index($4, 'p') >= 0 ? ( provided => 1 ) : ()),
        (index($4, 'n') >= 0 ? ( nothxarg => 1 ) : ())
    )
    : die "invalid spec: $_" } qw(
AvFILLp5.004050||p
AvFILLp||
CLASS|||n
CX_CURPAD_SAVE|||
CX_CURPAD_SV|||
CopFILEAV5.006000||p
CopFILEGV_set5.006000||p
CopFILEGV5.006000||p
CopFILE_set5.006000||p
CopFILE5.006000||p
CopSTASHPV_set5.006000||p
CopSTASHPV5.006000||p
CopSTASH_eq5.006000||p
CopSTASH_set5.006000||p
CopSTASH5.006000||p
CopyD5.009002||p
Copy||
CvPADLIST|||
POPx5.005030
POPp
POPs
PTR21V5.006000
PTR2NV5.006000
PTR2UV5.006000
PTR2uI5.007001
PTRV5.006000
PUSHMARK
PUSHi
PUSHmortal5.009002
PUSHn
PUSHp
PUSHs
PUSHu5.004000
PUTBACK
PerlIO_clearerr5.007003
PerlIO_close5.007003
PerlIO_eof5.007003
PerlIO_error5.007003
PerlIO_fileno5.007003
PerlIO_fill5.007003
PerlIO_flush5.007003
PerlIO_get_base5.007003
PerlIO_get_bufsiz5.007003
PerlIO_get_cnt5.007003
PerlIO_get_ptr5.007003
PerlIO_read5.007003
PerlIO_seek5.007003
PerlIO_set_cnt5.007003
PerlIO_set_ptrcnt5.007003
PerlIO_setlinebuf5.007003
PerlIO_stderr5.007003
PerlIO_stdin5.007003
PerlIO_stdout5.007003
PerlIO_tell5.007003
PerlIO_unread5.007003
Poison5.008000
RETVAL
Renewc
Renew
SAVECLEARSV
SAVECOMPPAD
SAVEPADSV
SAVETMPS
SAVE_DEFSV5.004050
SPAGAIN
bad_type||
bind_match||
block_end||
block_gimme||5.004000||
block_start||
boolSV||5.004000||p
boot_core_PerlIO||
boot_core_UNIVERSAL||
boot_core_xsutils||
bytes_from_utf8||5.007001|
bytes_to_utf8||5.006001|
cache_re||
call_argv||5.006000||p
call_atexit||5.006000|
call_body||
call_list_body||
call_list||5.004000|
call_method||5.006000||p
call_pv||5.006000||p
call_sv||5.006000||p
calloc||5.007002|n
cando||
cast_i32||5.006000|
cast_iv||5.006000|
cast_ulong||5.006000|
cast_uv||5.006000|
check_uni||
checkcomma||
checkposixcc||
ck_anoncode||
ck_bitop||
ck_concat||
ck_defined||
ck_delete||
ck_die||
ck_eof||
ck_eval||
ck_exec||
ck_exists||
ck_exit||
ck_ftst||
ck_fun||
ck_glob||
ck_grep||
ck_index||
ck_join||
ck_lengthconst||
ck_ifun||
ck_listiob
ck_match
ck_method
ck_null
ck_open
ck_repeat
ck_require
ck_retarget
ck_return
ck_rfun
ck_rvconst
ck_sassign
ck_select
ck_shift
ck_sort
ck_spair
ck_split
ck_subr
ck_substr
ck_svconst
ck_trunc
ck_unpack
cl_and
clAnything
cl_init
cl_init_zero
cl_or
closest_cop
convert
cop_free
cr_textfilter
croak_nocontext
croak
csighandler
custom_op_desc
custom_op_name
cv_ckproto
cv_clone
cv_const_sv
cv_dump
cv_undef
cx_dump
cx_inc
dAXMARK
dAX
dITEMS

Open Source Used In Cisco Telemetry Broker On Prem 1.2  8723
do_execfree||
do_exec||
do_gv_dump||5.006000|
do_gvgv_dump||5.006000|
do_hv_dump||5.006000|
do_ipcctl||
do_ipcget||
do_join||
do_kv||
do_magic_dump||5.006000|
do_msgrcv||
do_msgsnd||
do_oddball||
do_op_dump||5.006000|
do_open9||5.006000|
do_opens||5.007001|
do_open||5.004000|
do_pipe||
do_pmop_dump||5.006000|
do_print||
do.readline||
do.seek||
do_semop||
do_shmio||
do_spawn_nowait||
do_spawn||
do_sprintf||
do_sv_dump||5.006000|
do_sysseek||
do.tell||
do_trans_complex_utf8||
do_trans_complex||
do_trans_count_utf8||
do_trans_count||
do_trans_simple_utf8||
do_trans_simple||
do_trans||
do_vecget||
do_vecset||
do_vop||
docatch_body||
docatch||
doeval||
dofile||
dofindlabel||
doform||
doing_taint||5.008001|
doone liner||
doopen_pmmm

doparseformm

dopoptoevalm

dopoptolabelm

dopoptoloom

dopoptosub_at

dopoptosub

douwind

downantarray

dump_all|5.006000|
dump_eval|5.006000|
dump_fds

dump_form|5.006000|
dump_indent|5.006000|
dump_mstats

dump_packsubs|5.006000|
dump_sub|5.006000|
dump_vindent|5.006000|
dumpuntil

dup_attrlist

evaluate_eaccess

eval_pv|5.006000|
eval_sv|5.006000|
expect_number

fbm_compile|5.005000|
fbm_instr|5.005000|
fd_on_nosuid_fs

filter_add

filter_del

filter_gets

filter_read

find_beginning

find_byclass

find_in_my_stash

find_runcv

find_rundefsvoffset|5.009002|
find_script

find_uninit_var

fold_constants

forbid_setid

force_ident

force_list

force_next

force_version

force_word

form|5.004000|
fp_dup
fprintf_nocontext|||vn
free_global_struct||
free_tied_hv_pool||
free_tmps||
gen_constant_list||
get_av|5.006000|p
g_get_context|5.006000|n
g_get_cv|5.006000|p
g_get_db_sub||
g_get_debug_opts||
g_get_hash_seed||
g_get_hv|5.006000|p
g_get_mstats||
g_get_no_modify||
g_get_num||
g_get_op_descs|5.005000|
g_get_op_names|5.005000|
g_get_opargs||
g_get_ppaddr|5.006000|
g_get_sv|5.006000|p
g_get_vtbl|5.005030|
g_getcwd_sv|5.007002|
g_getenv_len||
gp_dup||
gp_free||
gp_ref||
grok_bin|5.007003|p
grok_hex|5.007003|p
grok_number|5.007002|p
grok_numeric_radix|5.007002|p
grok_oct|5.007003|p
group_end||
gv_AVadd||
gv_HVadd||
gv_IOadd||
gv_autoload4|5.004000|
gv_check||
gv_dump|5.006000|
gv_efullname3|5.004000|
gv_efullname4|5.006001|
gv_efullname||
gv_ename||
gv_fetchfile||
gv_fetchmeth_autoload|5.007003|
gv_fetchmethod_autoload|5.004000|
gv_fetchmethod||
gv_fetchmeth||
gv_fetchpvn_flags|5.009002|
gv_fetchpv||
gv_fetchsv||5.009002
gv_fetchsv||5.004000
gv_fullname3||5.004000
gv_fullname4||5.006001
gv_fullname||
gv_handler||5.007001

gv_init_sv||
gv_init||
gv_share||
gv_stashpvn||5.006000

gv_stashpv||
gv_stashsv||

he_dup||
hek_dup||
hfreeentries||
hsplit||
hv_assert||5.009001

hv_auxinit||
hv_clear_placeholders||5.009001

hv_clear||
hv_delayfree_ent||5.004000

hv_delete_common||
hv_delete_ent||5.004000

hv_delete||
hv_eiter_p||5.009003

hv_eiter_set||5.009003

hv_exists_ent||5.004000

hv_exists||
hv_fetch_common||
hv_fetch_ent||5.004000

hv_fetch||
hv_free_ent||5.004000

hv_iterinit||
hv_iterkeysv||5.004000

hv_iterkey||
hv_iternext_flags||5.008000

hv_iternextsv||
hv_iternext||
hv_iterval||
hv_ksplit||5.004000

hv_magic_check||
hv_magic||
hv_name_set||5.009003

hv_notallowed||
hv_placeholders_get||5.009003

hv_placeholders_p||5.009003

hv_placeholders_set||5.009003

hv_riter_p||5.009003
hv_riter_set[5.009003]
hv_scalar[5.009001]
hv_store_ent[5.004000]
hv_store_flags[5.008000]
hv_store[

hv_undef[

ibcmp_locale[5.004000]
ibcmp_utf8[5.007003]
ibcmp[

incl_perldb[

incline[

incpush[

ingroup[

init_argv_symbols[

init_debugger[

init_global_struct[

init_i18n10n[5.006000]
init_i18n14n[5.006000]
init_ids[

init_interp[

init_lexer[

init_main_stash[

init_perllib[

init_postdump_symbols[

init_predump_symbols[

init_stacks[5.005000]
init_tm[5.007002]

instr[

intro_my[

intuit_method[

intuit_more[

invert[

io_close[

isALNUM[

isALPHA[

isDIGIT[

isLOWER[

isSPACE[

isUPPER[

is_an_int[

is_gv_magical_sv[

is_gv_magical[

is_handle_constructor[

is_list_assignment[

is_lvalue_sub[5.007001]

is_uni_alnum[lc]5.006000]

is_uni_alnumc[lc]5.006000]

is_uni_alnumc[5.006000]
mg_get|||
mg_length[5.005000]
mg_localize|||
mg_magical|||
mg_set|||
mg_size[5.005000]
mini_mktime[5.007002]
missingterm|||
mode_from_discipline|||
modkids|||
mod|||
moreswitches|||
mul128|||
mulexp[10]|n
my_atof2[5.007002]
my_atof[5.006000]
my_atrns|||
my_bcropy|||n
my_betoh16|||n
my_betoh32|||n
my_betoh64|||n
my_betoh|||n
my_betohi|||n
my_betohs|||n
my_bzero|||n
my_chgsize|||
my_chsize|||
my_exit_jump|||
my_exit|||
my_failure_exit[5.004000]
my_fflush_all[5.006000]
my_fork[5.007003]|n
my_htobe16|||n
my_htobe32|||n
my_htobe64|||n
my_htobe|||n
my_htobes|||n
my_htole16|||n
my_htole32|||n
my_htole64|||n
my_htolei|||n
my_htolel|||n
my_htoles|||n
my_htonl|||
my_kid|||
my_letoh16|||n
my_letoh32|||n
my_letoh64|||n
my_letoh|||n
my_letohes|||n
my_letohe|||n
my_letohole|||n
my_letohol|||n
my_letoholes|||n
oopsCV|||
oopsHV|||
op_clear|||
op_const_sv|||
op_dump[5.006000]
op_free|||
op_null[5.007002]
op_refcnt_lock[5.009002]
op_refcnt_unlock[5.009002]
open_script|||
pMY_CTX_[5.007003][p
pMY_CTX[5.007003][p
pTHX_[5.006000][p
pTHX[5.006000][p
pack_cat[5.007003]
pack_rec|||
package|||
packlist[5.008001]
pad_add_anon|||
pad_add_name|||
pad_alloc|||
pad_block_start|||
pad_check_dup|||
pad_compname_type|||
pad_findlex|||
pad_findmy|||
pad_fixup_inner_anons|||
pad_free|||
pad_leavemy|||
pad_new|||
pad_push|||
pad_reset|||
pad_setsv|||
pad_sv|||
pad_swipe|||
pad_tidy|||
pad_undef|||
parse_body|||
parse_unicode_opts|||
path_is_absolute|||
peep|||
pending_ident|||
perl_alloc_using[ln
perl_alloc][n
perl_clone_using[ln
perl_clone][n
perl_construct][n
perl_destruct[5.007003][n
scalarkids||
scalarseq||
scalarvoid||
scalar||
scan_bin||5.006000|
scan_commit||
scan_const||
scan_formline||
scan_heredoc||
scan_hex||
scan_ident||
scan_inputsymbol||
scan_num||5.007001|
scan_oct||
scan_pat||
scan_str||
scan_subst||
scan_trans||
scan_version||5.009001|
scan_vstring||5.008001|
scan_word||
scope||
screaminstr||5.005000|
seed||
set_context||5.006000|n
set_csh||
set_numeric_local||5.006000|
set_numeric_radix||5.006000|
set_numeric_standard||5.006000|
setdefout||
setenv_getix||
share_hek_flags||
share_hek||
si_dup||
sighandler||n
simplify_sort||
skipspace||
sortsv||5.007003|
ss_dup||
stack_grow||
start_glob||
start_subparse||5.004000|
stashpv_hvname_match||5.009003|
stdize_locale||
strEQ||
strGE||
strGT||
strLE||
yyerror|||
yylex|||
yyparse|||
yywarn|||
);
if (exists $opt{'list-unsupported'}) {
my $f;
for $f (sort { lc $a cmp lc $b } keys %API) {
next unless $API{$f}{todo};
print "$f ", '.'x(40-length($f)), " ", format_version($API{$f}{todo}), "\n";
}
exit 0;
}
# Scan for possible replacement candidates
my(%replace, %need, %hints, %depends);
my $replace = 0;
my $hint = '';
while (<DATA>) {
if ($hint) {
if (m{^\s*\*\s(.*?)\s*$}) {
$hints{$hint} ||= ''; # suppress warning with older perls
$hints{$hint} .= "$1\n";
}
else {
$hint = '';
}
}
$hint = $1 if m{^\s*$rccs\sHint:\s+(\w+)\s*$};
$replace = $1 if m{^\s*$rccs\s+Replace:\s+(\d+)\s+$rcce\s*$};
$replace{$2} = $1 if $replace and m{^\s*#\s*define\s+(\w+)(?:\([^)]*\))?\s+(\w+)};
$replace{$2} = $1 if m{^\s*#\s*define\s+(\w+)(?:\([^)]*\))?\s+(\w+).*$rccs\s+Replace\s+$rcce};
$replace{$1} = $2 if m{^\s*$rccs\s+Replace (\w+) with (\w+)\s+$rcce\s*$};
if (m{^\s*$rccs\s+(\w+)\s+depends\s+on\s+(\w+(\s*,\s*\w+)*)\s+$rcce\s*$}) {
push @{$depends{$1}}, map { s/\s+//g; $_ } split /,/, $2;
}
$need{$1} = 1 if m{^#if\s+defined\(NEED_(\w+)(?:_GLOBAL)?\)};
}
if (exists $opt{'api-info'}) {
my $f;
my $count = 0;

Open Source Used In Cisco Telemetry Broker On Prem 1.2 8746


my $match = $opt{'api-info'} =~ m!^/(.*)/$! ? $1 : "^\Q$opt{'api-info'}\E\$";
for $f (sort { lc $a cmp lc $b } keys %API) {
    next unless $f =~ /$match/;
    print "\n\n=== $f ===\n\n";
    my $info = 0;
    if ($API{$f}{base} || $API{$f}{todo}) {
        my $base = format_version($API{$f}{base} || $API{$f}{todo});
        print "Supported at least starting from perl-$base\n";
        $info++;
    }
    if ($API{$f}{provided}) {
        my $todo = $API{$f}{todo} ? format_version($API{$f}{todo}) : "5.003";
        print "Support by $ppport provided back to perl-$todo\n";
        print "Support needs to be explicitly requested by NEED_$f\n" if exists $need{$f};
        print "Depends on: ", join(\', ', @{$depends{$f}}), ",\n" if exists $depends{$f};
        print "Shints{$f}" if exists $hints{$f};
        $info++;
    }
    unless ($info) {
        print "No portability information available\n";
    }
    $count++;
}
if ($count > 0) {
    print "\n";
} else {
    print "Found no API matching \'$opt{\'api-info\'}\n";
}
exit 0;

if (exists $opt{\'list-provided\'}) {
    my $f;
    for $f (sort { lc $a cmp lc $b } keys %API) {
        next unless $API{$f}{provided};
        my @flags;
        push @flags, 'explicit' if exists $need{$f};
        push @flags, 'depend'   if exists $depends{$f};
        push @flags, 'hint'     if exists $hints{$f};
        my $flags = @flags ? '\['.join(', ', @flags).']' : "";
        print "$f$flags\n";
    }
    exit 0;
}

my @files;
my @srcext = qw( xs c h cc cpp );
my $srcext = join '|', @srcext;
if (@ARGV) {
my %seen;
@files = grep { -f && !exists $seen{$_} } map { glob $_ } @ARGV;
}
else {
eval {
require File::Find;
File::Find::find(sub {
$File::Find::name =~ /\.($srcext)$/i
and push @files, $File::Find::name;
}, '.');
};
if ($@) {
@files = map { glob "*.$_" } @srcext;
}
}
if (!@ARGV || $opt{filter}) {
my(@in, @out);
my %xsc = map { /(.*)\.xs$/ ? ("$1.c" => 1, "$1.cc" => 1) : () } @files;
for (@files) {
my $out = exists $xsc{$_} || /\b\Q$ppport\E$/i || !/\.($srcext)$/i;
push @{ $out ? \@out : \@in }, $_;
}
if (@ARGV && @out) {
warning("Skipping the following files (use --nofilter to avoid this):\n| ", join "\n| ", @out);
}
@files = @in;
}
unless (@files) {
die "No input files given!\n";
}
my(%files, %global, %revreplace);
%revreplace = reverse %replace;
my $filename;
my $patch_opened = 0;
for $filename (@files) {
unless (open IN, "<$filename") {
warn "Unable to read from $filename: $!\n";
next;
}
info("Scanning $filename ...");

Open Source Used In Cisco Telemetry Broker On Prem 1.2 8748


my $c = do { local $/; <IN> };
close IN;
my %file = (orig => $c, changes => 0);
# temporarily remove C comments from the code
my @ccom;
$c =~ s{
(
[^"'/]+
|
(?:"[^"\\]*(?:\\.[^"\\]*)*" [^"'/]*)+
|
(?:'[^'\\]*(?:\\.[^'\\]*)*' [^"'/]*)+
)
|
(/ (?:
\*[^*]*\*+(?:[^$ccs][^*]*\*+)* /
|
/[^\r\n]*
))
}{
defined $2 and push @ccom, $2;
defined $1 ? $1 : "$ccs$#ccom$cce";
}egsx;
$file{ccom} = \@ccom;
$file{code} = $c;
$file{has_inc_ppport} = ($c =~ /#.*include.*\Q$ppport\E/);
my $func;
for $func (keys %API) {
my $match = $func;
$match .= "|$revreplace{$func}" if exists $revreplace{$func};
if ($c =~ /\b(?:Perl_)?($match)\b/) {
$file{uses_replace}{$1}++ if exists $revreplace{$func} && $1 eq $revreplace{$func};
$file{uses_Perl}{$func}++ if $c =~ /\bPerl_$func\b/;
if (exists $API{$func}{provided}) {
if (!exists $API{$func}{base} || $API{$func}{base} > $opt{'compat-version'}) {
$file{uses}{$func}++;
my @deps = rec_depend($func);
if (@deps) {
$file{uses_deps}{$func} = \@deps;
for (@deps) {
$file{uses}{$_} = 0 unless exists $file{uses}{$_};
}

Open Source Used In Cisco Telemetry Broker On Prem 1.2 8749


}
for ($func, @deps) {
if (exists $need{$_}) {
$file{needs}{$_} = 'static';
}
}
}
}
if (exists $API{$func}{todo} && $API{$func}{todo} > $opt{'compat-version'}) {
if ($c =~ /\b$func\b/) {
$file{uses_todo}{$func}++;
}
}
}
}
while ($c =~ /^$HS*#$HS*define$HS+(NEED_(\w+?)(_GLOBAL)?)\b/mg) {
if (exists $need{$2}) {
$file{defined $3 ? 'needed_global' : 'needed_static'}{$2}++;
}
else {
warning("Possibly wrong #define $1 in $filename");
}
}
for (qw(uses needs uses_todo needed_global needed_static)) {
for $func (keys %{$file{$_}}) {
push @{$global{$_}{$func}}, $filename;
}
}
$files{$filename} = \%file;
}
# Globally resolve NEED_'s
my $need;
for $need (keys %{$global{needs}}) {
if (@{$global{needs}{$need}} > 1) {
my @targets = @{$global{needs}{$need}};
my @t = grep $files{$_}{needed_global}{$need}, @targets;
@targets = @t if @t;
@t = grep /\.xs$/i, @targets;
@targets = @t if @t;
my $target = shift @targets;
$files{$target}{needs}{$need} = 'global';
for (@{$global{needs}{$need}}) {
$files{$_}{needs}{$need} = 'extern' if $_ ne $target;
}

Open Source Used In Cisco Telemetry Broker On Prem 1.2 8750


for $filename (@files) {
    exists $files{$filename} or next;

    info("=== Analyzing $filename ===");

    my %file = %{$files{$filename}};
    my $func;
    my $c = $file{code};

    for $func (sort keys %{$file{uses_Perl}}) {
        if ($API{$func}{varargs}) {
            my $changes = ($c =~ s{\b(Perl_$func\s*\(\s*)(?!aTHX_?)(\)|\[\^\s)*\})\s*\w\s*\} {$1 . ($2 eq ')' ? 'aTHX' : 'aTHX_') . $2 }ge);
            if ($changes) {
                warning("Doesn't pass interpreter argument aTHX to Perl_$func");
                $file{changes} += $changes;
            }
        } else {
            warning("Uses Perl_$func instead of $func");
            $file{changes} += ($c =~ s{\bPerl_$func\(\s*\)\?aTHX_\?s\*\} {$func$1\}g);
        }
    }

    for $func (sort keys %{$file{uses_replace}}) {
        warning("Uses $func instead of $replace{$func}");
        $file{changes} += ($c =~ s\b$func\/\?aTHX_\?s\*\} {$func$1\}g);
    }

    for $func (sort keys %{$file{uses}}) {
        next unless $file{uses}->{$func};     # if it's only a dependency
        if (exists $file{uses_deps}->{$func}) {
            diag("Uses $func, which depends on ", join(’,’, @{$file{uses_deps}->{$func}}));
        } elsif (exists $replace{$func}) {
            warning("Uses $func instead of $replace{$func}");
            $file{changes} += ($c =~ s\b$func/b/$replace{$func}/g);
        } else {
            diag("Uses $func");
        }
    }
}
for $func (sort keys %{file{uses_todo}}) {
  warning("Uses $func, which may not be portable below perl ",
          format_version($API{$func}{todo}));
}

for $func (sort keys %{file{needed_static}}) {
  my $message = "",
  if (not exists $file{uses}->{$func}) {
    $message = "No need to define NEED_$func if $func is never used";
  }
  elsif (exists $file{needs}->{$func} && $file{needs}->{$func} ne 'static') {
    $message = "No need to define NEED_$func when already needed globally";
  }
  if ($message) {
    diag($message);
    $file{changes} += ($c =~ s/^$HS*#$HS*define$HS+NEED_$func\b.*$LF//mg);
  }
}

for $func (sort keys %{file{needed_global}}) {
  my $message = "",
  if (not exists $global{uses}->{$func}) {
    $message = "No need to define NEED_$func_GLOBAL if $func is never used";
  }
  elsif (exists $file{needs}->{$func}) {
    if ($file{needs}->{$func} eq 'extern') {
      $message = "No need to define NEED_$func_GLOBAL when already needed globally";
    }
    elsif ($file{needs}->{$func} eq 'static') {
      $message = "No need to define NEED_$func_GLOBAL when only used in this file";
    }
  }
  if ($message) {
    diag($message);
    $file{changes} += ($c =~ s/^$HS*#$HS*define$HS+NEED_$func_GLOBAL\b.*$LF//mg);
  }
}

$file{needs_inc_ppport} = keys %{file{uses}};

if ($file{needs_inc_ppport}) {
  my $pp = "",

  for $func (sort keys %{file{needs}}) {
    my $type = $file{needs}->{$func};
    next if $type eq 'extern';
    my $suffix = $type eq 'global' ? '_GLOBAL' : "",
    unless (exists $file{"needed_$type"}->{$func}) {

if ($type eq 'global') {
    diag("Files [@ {global{needs}{$func}] need $func, adding global request");
} else {
    diag("File needs $func, adding static request");
}
$pp .= "#define NEED_$func$suffix\n";
}

if ($pp && ($c =~ s/^(?=$HS*#$HS*define$HS+NEED_\w+)+/$1$pp/m)) {
    $pp = ";
    $file{changes}++;
}

unless ($file{has_inc_ppport}) {
    diag("Needs to include \'$ppport\'");
    $pp .= qq(#include "$ppport\n"
    }
    if ($pp) {
        $file{changes} += ($c =~ s/^($HS*#$HS*define$HS+NEED_\w+.*)/$1$pp/ms)
            || ($c =~ s/^($HS*#$HS*include.*\Q$ppport\E)/$pp/m)
            || ($c =~ s/^($HS*#$HS*include.*XSUB.\"\s*?)/$1$pp/m)
            || ($c =~ s/^/$pp/);
    }
}
else {
    if ($file{has_inc_ppport}) {
        diag("No need to include \'$ppport\'");
        $file{changes} += ($c =~ s/^($HS*#$HS*include.*\Q$ppport\E.*?$LF//m);
        }
    }
}

# put back in our C comments
my $ix;
my $cppc = 0;
my @ccom = @{$file{ccom}};
for $ix (0 .. $#ccom) {
    if (!$opt{cplusplus} && $ccom[$ix] =~ s!^//!!) {
        $cppc++;
        $file{changes} += $c =~ s/$rccs$ix$rcce/$ccs$ccom[$ix] $cce/;
    } else {
        $c =~ s/$rccs$ix$rcce/$ccom[$ix]/;
    }
}
if ($cppc) {
    my $s = $cppc != 1 ? 's' : '';
    warning("Uses $cppc C++ style comment$s, which is not portable");
}

if ($file{changes}) {
    if (exists $opt{copy}) {
        my $newfile = "$filename$opt{copy}";
        if (-e $newfile) {
            error("$newfile' already exists, refusing to write copy of "$filename"");
        } else {
            local \*F;
            if (open F, ">$newfile") {
                info("Writing copy of "$filename' with changes to "$newfile"");
                print F $c;
                close F;
            } else {
                error("Cannot open "$newfile' for writing: $!");
            }
        }
    } elsif (exists $opt{patch} || $opt{changes}) {
        if (exists $opt{patch}) {
            unless ($patch_opened) {
                if (open PATCH, ">$opt{patch}") {
                    $patch_opened = 1;
                } else {
                    error("Cannot open '$opt{patch}' for writing: $!");
                    delete $opt{patch};
                    $opt{changes} = 1;
                    goto fallback;
                }
            }
            mydiff(*PATCH, $filename, $c);
        } else {
            fallback:
            info("Suggested changes:");
            mydiff(*STDOUT, $filename, $c);
        }
    } else {
        my $s = $file{changes} == 1 ? '' : 's';
        info("$file{changes} potentially required change$s detected");
    }
}
} else {
  info("Looks good");
}

close PATCH if $patch_opened;

exit 0;

sub mydiff
{
  local *F = shift;
  my($file, $str) = @_;
  my $diff;

  if (exists $opt{diff}) {
    $diff = run_diff($opt{diff}, $file, $str);
  }

  if (!defined $diff and can_use('Text::Diff')) {
    $diff = Text::Diff::diff($file, $str, { STYLE => 'Unified' });
    $diff = <<HEADER . $diff;
    --- $file
    +++ $file.patched
    HEADER
  }

  if (!defined $diff) {
    $diff = run_diff('diff -u', $file, $str);
  }

  if (!defined $diff) {
    $diff = run_diff('diff', $file, $str);
  }

  if (!defined $diff) {
    error("Cannot generate a diff. Please install Text::Diff or use --copy.");
    return;
  }

  print F $diff;
}

sub run_diff
{

my($prog, $file, $str) = @_;  
my $tmp  = 'dppptemp';    
my $suf  = 'aaa';              
my $diff = '';              
local *F;  

while (-e "$tmp.$suf") { $suf++; }  
$tmp = "$tmp.$suf";    

if (open F, ">$tmp") {  
print F $str;  
close F;  

if (open F, "$prog $file $tmp |") {  
while (<F>) {  
s/Q$tmp/E/$file.patched/;  
$diff .= $_;  
}  
close F;  
unlink $tmp;  
return $diff;  
}  

unlink $tmp;  
}  
else {  
error("Cannot open '$tmp' for writing: $!");  
}

return undef;  
}  

sub can_use  
{  
eval "use @_;";    
return $@ eq "";  
}

sub rec_depend  
{  
my $func = shift;    
my %seen;  
return () unless exists $depends{$func};  
grep !$seen{$_}++, map { ($_, rec_depend($_)) } @{$depends{$func}};  
}

sub parse_version  
{
my $ver = shift;

if ($ver =~ /\d+(\d+).\d+/) {
    return ($1, $2, $3);
}
elsif ($ver !~ /\d+\[\d_]+$/) {
    die "cannot parse version '$ver'\n";
}

$ver =~ s/_//g;
$ver =~ s/$/000000/;

my($r,$v,$s) = $ver =~ /\d+/(\d{3})\d{3}/;

$v = int $v;
$s = int $s;

if ($r < 5 || ($r == 5 && $v < 6)) {
    if ($s % 10) {
        die "cannot parse version '$ver'\n";
    }
}

return ($r, $v, $s);
}

sub format_version {
    my $ver = shift;

    $ver =~ s/$/000000/;
    my($r,$v,$s) = $ver =~ /\d+/(\d{3})\d{3}/;

    $v = int $v;
    $s = int $s;

    if ($r < 5 || ($r == 5 && $v < 6)) {
        if ($s % 10) {
            die "invalid version '$ver'\n";
        }
        $s /= 10;
    }

    $ver = sprintf "\d.\d{3}\", $r, $v;
    $s > 0 and $ver .= sprintf "_\d", $s;

    return $ver;
}
return sprintf "%d.%d.%d", $r, $v, $s;
}
sub info
{
$opt{quiet} and return;
print @_, "\n";
}
sub diag
{
$opt{quiet} and return;
$opt{diag} and print @_, "\n";
}
sub warning
{
$opt{quiet} and return;
print "*** ", @_, "\n";
}
sub error
{
print "*** ERROR: ", @_, "\n";
}
my %given_hints;
sub hint
{
$opt{quiet} and return;
$opt{hints} or return;
my $func = shift;
exists $hints{$func} or return;
$given_hints{$func}++ and return;
my $hint = $hints{$func};
$hint =~ s/^/ /mg;
print " --- hint for $func ---\n", $hint;
}
sub usage
{
my($usage) = do { local(@ARGV,$/)=($0); <> } =~ /^=head\d$HS+SYNOPSIS\s*^(.*?)\s*^=/ms;
my %M = ( 'I' => '*' );
$usage =~ s/^\s*perl\s+\S+/$^X $0/;
$usage =~ s/([A-Z])<([^>]+)>/$M{$1}$2$M{$1}/g;
print <<ENDUSAGE;

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Usage: $usage

See perldoc $0 for details.

ENDUSAGE

exit 2;
}

__DATA__
*/

Found in path(s):
* /opt/ws_local/PERMITS_SQL/1068209212_1595544847.99/0/libalgorithm-diff-xs-perl-0-04-orig-tar-gz/Algorithm-Diff-XS-0.04/ppport.h

1.981 js-cookie 2.2.1

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
    ; CHECK-LABEL: PR41535:
    ; CHECK:       // %bb.0:
    ; CHECK-NEXT:    ext v0.8b, v0.8b, v1.8b, #4
    ; CHECK-NEXT:    mov v0.d[1], v0.d[0]
    ; CHECK-NEXT:    ret
    %cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
    %cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
    %r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
    ret <4 x i32> %r
}
; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right
; register classes.

define void @autogen_SD4739(i8*) {
    ; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
    BB:
    %L34 = load i8, i8* %0
    %Cmp56 = icmp sgt i8 undef, %L34
    br label %CF246
    CF246:                                            ; preds = %CF246, %BB
    %S1163 = select i1 %Cmp56, i8 %L34, i8 undef
    br i1 undef, label %CF246, label %CF248
    CF248:                                            ; preds = %CF248, %CF246
    store i8 %S1163, i8* %0
    br label %CF248
}

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; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
  ; CHECK-LABEL: test_sitofp_fixed:
  ; First, extend each i32 to i64
  ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
  ; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
  ; Next, convert each to double.
  ; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
  ; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
  ; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
  ; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
  ; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
  ; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
  ; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
  ; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]
  ; CHECK: ret
  %flt = sitofp <16 x i32> %in to <16 x double>
  %res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
  ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {

; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}
; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
;
; D31946
; Check that we dont end up with the "LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)
define fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK: # %bb.0:
; CHECK-NEXT: pushq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: subq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa_offset 48
; CHECK-NEXT: .cfi_offset %rbx, -16
; CHECK-NEXT: movaps %xmm1, [ %[-0-9]+ ]( %r{{[sb]}}p) # 16-byte Spill
; CHECK-NEXT: movaps %xmm0, ( %rsp) # 16-byte Spill
; CHECK-NEXT: callq __gttf2
; CHECK-NEXT: movl %eax, %ebx
; CHECK-NEXT: xorps %xmm0, %xmm0
; CHECK-NEXT: .LBB0_2:
; CHECK-NEXT: addq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa_offset 16
; CHECK-NEXT: popq %rbx
; CHECK-NEXT: .cfi_def_cfa_offset 8
; CHECK-NEXT: retq
%cmp = fcmp ogt fp128 %a, %b
%sub = fsub fp128 %a, %b
%res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
define fp128 @TestFabs(fp128 %a) {
 ; CHECK-LABEL: TestFabs:
 ; CHECK:    # %bb.0:
 ; CHECK-NEXT: andps {{.*}}(%rip), %xmm0
 ; CHECK-NEXT: retq
 %res = call fp128 @llvm.fabs.f128(fp128 %a)
 ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
 ; CHECK-LABEL: TestCopysign:
 ; CHECK:    # %bb.0:
 ; CHECK-NEXT: andps {{.*}}(%rip), %xmm1
 ; CHECK-NEXT: andps {{.*}}(%rip), %xmm0
 ; CHECK-NEXT: orps %xmm1, %xmm0
 ; CHECK-NEXT: retq
 %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
 ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
 ; CHECK-LABEL: TestFneg:
 ; CHECK:    # %bb.0:
 ; CHECK-NEXT: pushq %rax
 ; CHECK-NEXT: .cfi_def_cfa_offset 16
 ; CHECK-NEXT: movaps %xmm0, %xmm1
 ; CHECK-NEXT: callq __multf3
 ; CHECK-NEXT: xorps {{.*}}(%rip), %xmm0
 ; CHECK-NEXT: popq %rax
 ; CHECK-NEXT: .cfi_def_cfa_offset 8
 ; CHECK-NEXT: retq
 %mul = fmul fp128 %a, %a
 %res = fsub fp128 0xL0000000000000000, %mul
 ret fp128 %res
}

; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
 ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
 ret void
}
define internal void @innerNoAttribute() {
    ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
    call void @innerSmall()
    ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
    call void @innerNoAttribute()
    ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
; RUN: opt -consthoist -S -o - %s | FileCheck %s
  target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
define void @foo_i8() {
entry:
  %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
  %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
  %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
  %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
  %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
  store i8 %4, i8* inttoptr(i32 805873688 to i8*)
  store i8 %3, i8* inttoptr(i32 805873719 to i8*)
  store i8 %2, i8* inttoptr(i32 805873720 to i8*)
  store i8 %1, i8* inttoptr(i32 805873727 to i8*)
  store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?!]] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?!]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[C1:const[0-9]?] = bitcast i32 805874816 to i32
; CHECK-DAG: %[C2:const[0-9]?] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[C2] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %2 = inttoptr i32 %[M1] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %4 = inttoptr i32 %[M2] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[C1] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %8 = inttoptr i32 %[M3] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %10 = inttoptr i32 %[M4] to i32*
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}
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de Minimis description of such content.
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py
UTC_ARGS: --function-signature --
scrub-attributes
; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
 ; CHECK-LABEL: define ([^@]+)@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
 ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
 ; CHECK-NEXT: ret void
 ;
; bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
 ; CHECK-LABEL: define ([^@]+)@avx512_legal512_prefer512_call_avx512_legal512_prefer512
 ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [TMP:%.*] = alloca <8 x i64>, align 32
 ; CHECK-NEXT: [TMP2:%.*] = alloca <8 x i64>, align 32
 ; CHECK-NEXT: [TMP3:%.*] = bitcast <8 x i64>* [[TMP]] to i8*
 ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [TMP3], i8 0, i64 32, i1 false)
 ; CHECK-NEXT: [TMP Valle%.*] = load <8 x i64>, <8 x i64>* [[TMP]]
 ; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* [TMP2]), <8 x i64> [[TMP_VAL]])
 ; CHECK-NEXT: [TMP4:%.*] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
 ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
 ; CHECK-NEXT: ret void
 ;
; bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg,<8 x i64>* readonly %arg1) #1 {
    ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
    ; CHECK-NEXT: ret void
    ;
    bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
    ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
    ; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
    ; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
    ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
    ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    ; CHECK-NEXT: ret void
    ;
    bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg,
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:.%*]]
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; CHECK-NEXT:    call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT:    [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]]
  ; CHECK-NEXT:    call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* [[TMP_VAL]], <8 x i64>* [[ARG]])
  ; CHECK-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT:    ret void
  ;
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1)
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT:    ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
  ; CHECK-SAME: (<8 x i64>* [[ARG:.%*]], <8 x i64> [[ARG1_VAL:.%*]])
  ; CHECK-NEXT:  bb:
  ; CHECK-NEXT:    store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT:    ret void
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
    %tmp = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
    %tmp = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}
define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
 CHECK-LABEL: define {{[^@]+}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
 CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
 CHECK-NEXT: bb:
 CHECK-NEXT: [[TMP:.*]] = alloca <8 x i64>, align 32
 CHECK-NEXT: [[TMP2:.*]] = alloca <8 x i64>, align 32
 CHECK-NEXT: [[TMP3:.*]] = bitcast <8 x i64>* [[TMP]] to i8*
 CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
 CHECK-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP]])
 CHECK-NEXT: [[TMP4:.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
 CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
 CHECK-NEXT: ret void

 bb:
 %tmp = alloca <8 x i64>, align 32
 %tmp2 = alloca <8 x i64>, align 32
 %tmp3 = bitcast <8 x i64>* %tmp to i8*
 call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
 call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
 %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
 store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
 ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {
 CHECK-LABEL: define {{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
 CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
 CHECK-NEXT: bb:
 CHECK-NEXT: [[TMP:.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
 CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
 CHECK-NEXT: ret void

 bb:
 %tmp = load <8 x i64>, <8 x i64>* %arg1
 store <8 x i64> %tmp, <8 x i64>* %arg
 ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
 CHECK-LABEL: define {{[^@]+}} @avx512_legal512_prefer256_call_avx512_legal512_prefer256
 CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
 CHECK-NEXT: bb:
; CHECK-NEXT:  [[TMP:.%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:  [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:  [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT:  call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT:  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP]])
; CHECK-NEXT:  [[TMP4:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT:  ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
; CHECK-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:.%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:.%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:.%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:.%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
; CHECK-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; This should promote

define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
    ; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal512_prefer256_call_avx2_legal512_prefer256
    ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1_VAL:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: store <8 x i64> %tmp, <8 x i64>* %arg
    ; CHECK-NEXT: ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #3 {
    ; CHECK-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal512_prefer256
    ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP2]] to i8*
    ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
    ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
    ; CHECK-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal512_prefer256(<8 x i64>* [[TMP4]], <8 x i64>* [[ARG]])
    ; CHECK-NEXT: [[TMP5:%.*]] = load <8 x i64>, <8 x i64>* [[ARG]], align 2
    ; CHECK-NEXT: ret void
}

bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
    ; CHECK-LABEL: define
    ; {{[^@]+}}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
    ; CHECK-NEXT:    [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
    ; CHECK-NEXT:    ret i32 [[C]]
    ;
    %A = load i32, i32* %X
    %B = load i32, i32* %Y
    %C = add i32 %A, %B
    ret i32 %C
}
define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
    ; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32* [[B:%.*]])
    ; CHECK-NEXT:    [[A:%.*]] = alloca i32
    ; CHECK-NEXT:    store i32 1, i32* [[A]]
    ; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, i32* [[A]]
    ; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, i32* [[B]]
    ; CHECK-NEXT:    [[C:%.*]] = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
    ; CHECK-NEXT:    ret i32 [[C]]
    ;
    %A = alloca i32
    store i32 1, i32* %A
    %C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
    ret i32 %C
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
    ; CHECK-LABEL: define
    ; {{[^@]+}}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
    ; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256
    ; CHECK-SAME: (i32* [[B:%.*]])
    ; CHECK-NEXT:    [[A:%.*]] = alloca i32
    ; CHECK-NEXT:    store i32 1, i32* [[A]]
    ; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, i32* [[A]]
    ; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, i32* [[B]]
    ; CHECK-NEXT:    [[C:%.*]] = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
    ; CHECK-NEXT:    ret i32 [[C]]
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }
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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for vpmaddwd+vpadddd
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=cascadelake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=cooperlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=cannonlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=icelake-client | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=icelake-server | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vnni -mcpu=tigerlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
; CHECK-LABEL: add256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpaddd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT:  vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%d = load <16 x i32>*, <16 x i32>*, %a
%e = load <16 x i32>*, <16 x i32>*, %b
}
define void @add512(%a, %b, %c) "min-legal-vector-width"="512" {
    %d = load <16 x i32>, <16 x i32>* %a
    %e = load <16 x i32>, <16 x i32>* %b
    %f = add <16 x i32> %d, %e
    store <16 x i32> %f, <16 x i32>* %c
    ret void
}

define void @avg_v64i8_256(%a, %b) "min-legal-vector-width"="256" {
    %1 = load <64 x i8>, <64 x i8>* %a
    %2 = load <64 x i8>, <64 x i8>* %b
    %3 = zext <64 x i8> %1 to <64 x i32>
    %4 = zext <64 x i8> %2 to <64 x i32>
    %5 = add nuw nsw <64 x i32> %3, %4
    %6 = add nuw nsw <64 x i32> %5, %4
    %7 = lshr <64 x i32> %6, %7
    %8 = trunc <64 x i32> %7 to <64 x i8>
    store <64 x i8> %8, <64 x i8>* undef, align 4
    ret void
}
define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}
define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vectorwidth"="256" {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32

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define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <32 x i16>* %CPtr) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: pmaddwd_32_512:
  ; CHECK: # %bb.0:
  ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
  ; CHECK-NEXT: vpmaddwd (%rsi), %zmm0, %zmm0
  ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
  ; CHECK-NEXT: vzeroupper
  ; CHECK-NEXT: retq
  %A = load <32 x i16>, <32 x i16>* %APtr
  %B = load <32 x i16>, <32 x i16>* %BPtr
  %a = sext <32 x i16> %A to <32 x i32>
  %b = sext <32 x i16> %B to <32 x i32>
  %m = mul nsw <32 x i32> %a, %b
  %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
  %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
  %ret = add <16 x i32> %odd, %even
  store <16 x i32> %ret, <16 x i32>* %CPtr
  ret void
}

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: psubus_64i8_max_256:
  ; CHECK: # %bb.0:
  ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT: vmovdqa32 (%rdi), %ymm1
  ; CHECK-NEXT: vpsubusb32 (%rsi), %ymm1, %ymm1
  ; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
  ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
  ; CHECK-NEXT: vzeroupper
  ; CHECK-NEXT: retq
  %x = load <64 x i8>, <64 x i8>* %xptr
  %y = load <64 x i8>, <64 x i8>* %yptr
  %cmp = icmp ult <64 x i8> %x, %y
  %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
  %res = sub <64 x i8> %max, %y
  store <64 x i8> %res, <64 x i8>* %zptr
  ret void
define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
 ; CHECK-LABEL: psubus_64i8_max_512:
 ; CHECK:       # %bb.0:
 ; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
 ; CHECK-NEXT:  vpsubusb (%rsi), %zmm0, %zmm0
 ; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
 ; CHECK-NEXT:  vzeroupper
 ; CHECK-NEXT:  retq
 %x = load <64 x i8>, <64 x i8>* %xptr
 %y = load <64 x i8>, <64 x i8>* %yptr
 %cmp = icmp ult <64 x i8> %x, %y
 %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
 %res = sub <64 x i8> %max, %y
 store <64 x i8> %res, <64 x i8>* %zptr
 ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: _Z9test_charPcS_i_256:
 ; CHECK:       # %bb.0: # %entry
 ; CHECK-NEXT:  movl %edx, %eax
 ; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
 ; CHECK-NEXT:  xorl %ecx, %ecx
 ; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
 ; CHECK-NEXT:  vpxor %xmm2, %xmm2, %xmm2
 ; CHECK-NEXT:  .p2align 4, 0x90
 ; CHECK-NEXT:  .LBB8_1: # %vector.body
 ; CHECK-NEXT:  # =>This Inner Loop Header: Depth=1
 ; CHECK-NEXT:  vpmovsxbw 16(%rdi,%rcx), %ymm3
 ; CHECK-NEXT:  vpmovsxbw (%rdi,%rcx), %ymm3
 ; CHECK-NEXT:  vpmovsxbw (rsi,%rcx), %ymm5
 ; CHECK-NEXT:  vpmaddwd %ymm3, %ymm5, %ymm3
 ; CHECK-NEXT:  vpadd %ymm2, %ymm3, %ymm2
 ; CHECK-NEXT:  vpmovsxbw (rsi,%rcx), %ymm3
 ; CHECK-NEXT:  vpmaddwd %ymm4, %ymm3, %ymm3
 ; CHECK-NEXT:  vpadd %ymm1, %ymm3, %ymm1
 ; CHECK-NEXT:  addq $32, %rcx
 ; CHECK-NEXT:  cmpq %rcx, %rax
 ; CHECK-NEXT:  jne .LBB8_1
 ; CHECK-NEXT:  # %bb.2: # %middle.block
 ; CHECK-NEXT:  vpadd %ymm0, %ymm1, %ymm1
 ; CHECK-NEXT:  vpadd %ymm0, %ymm2, %ymm0
 ; CHECK-NEXT:  vpadd %ymm0, %ymm1, %ymm0
 ; CHECK-NEXT:  vextracti128 %s1, %ymm0, %xmm1
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {  
; CHECK-LABEL: _Z9test_charPcS_i_512:  
; CHECK:       # %bb.0: # %entry  
; CHECK-NEXT: movl %edx, %eax  
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0  
; CHECK-NEXT: xorl %ecx, %ecx  
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1  
; CHECK-NEXT: .p2align 4, 0x90  
; CHECK-NEXT: .LBB9_1: # %vector.body  
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1  
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2  
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3  
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2  
; CHECK-NEXT: vpaddd %zmm1, %zmm2, %zmm1  
; CHECK-NEXT: addq $32, %rcx  
; CHECK-NEXT: cmpq %rcx, %rax  
; CHECK-NEXT: jne .LBB9_1  
; CHECK-NEXT: vpaddd %zmm0, %zmm1, %zmm0  
; CHECK-NEXT: vextracti64x4 s1, %zmm0, %ymm1  
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0  
; CHECK-NEXT: vextracti128 s1, %ymm0, %xmm1  
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0  
; CHECK-NEXT: vpsubufd {{.*#+}} xmm1 = xmm0[2,3,0,1]  
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0  
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0  
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0  
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0  
; CHECK-NEXT: vmovd %xmm0, %eax  
; CHECK-NEXT: vzeroupper  
; CHECK-NEXT: retq  
entry:  
%3 = zext i32 %2 to i64  
br label %vector.body  

vector.body:  
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]

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%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %8
%11 = add nsw <32 x i32> %10, %vec.phi
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> %i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> %i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx3, <32 x i32> undef, <32 x i32> %i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> %i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> %i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13

@a = global [1024 x i8] zeroinitializer, align 16
@b = global [1024 x i8] zeroinitializer, align 16
define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sad_16i8_256:
    ; CHECK:     # %bb.0: # %entry
    ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
    ; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT: .p2align 4, 0x90
    ; CHECK-NEXT: :LBB10_1: # %vector.body
    ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm2
    ; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm2
    ; CHECK-NEXT: vpaddd %ymm1, %ymm2, %ymm1
    ; CHECK-NEXT: addq $4, %rax
    ; CHECK-NEXT: jne .LBB10_1
    ; CHECK-NEXT: :LBB11_1: # %middle.block
    ; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm0
    ; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
    ; CHECK-NEXT: vpaddd %xmm1, %ymm0, %ymm0
    ; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
    ; CHECK-NEXT: vpaddd %xmm1, %ymm0, %ymm0
    ; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
    ; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
    ; CHECK-NEXT: vmovd %xmm0, %eax
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    entry:
    br label %vector.body
}

vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%2 = load <16 x i8>, <16 x i8>* %1, align 4
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%5 = load <16 x i8>, <16 x i8>* %4, align 4
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> %2, %5
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body
middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}
define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
 ; CHECK-LABEL: sad_16i8_512:
 ; CHECK:       # %bb.0: # %entry
 ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
 ; CHECK-NEXT:    movq $-1024, %rax # imm = 0xFC00
 ; CHECK-NEXT:  .p2align 4, 0x90
 ; CHECK-NEXT:  .LBB11_1: # %vector.body
 ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
 ; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm1
 ; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
 ; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
 ; CHECK-NEXT:    addq $4, %rax
 ; CHECK-NEXT:    jne .LBB11_1
 ; CHECK-NEXT:  # %bb.2: # %middle.block
 ; CHECK-NEXT:    vextracti64x4 %1, %zmm0, %zmm1
 ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
 ; CHECK-NEXT:    vextracti128 %1, %zmm0, %zmm1
 ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
 ; CHECK-NEXT:    vpslutfd 
 ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
 ; CHECK-NEXT:    vpslutfd 
 ; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
 ; CHECK-NEXT:    vmovd %zmm0, %eax
 ; CHECK-NEXT:    vzeroupper
 ; CHECK-NEXT:    retq
entry:
br label %vector.body
vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> undef, <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> undef, <i32 4, i32 5, i32 6, i32 7, i32 8, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> undef, <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> undef, <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define void @sbtol6f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbtol6f32_256:
; CHECK:  # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtdq2ps %ymm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm1
; CHECK-NEXT: vcvtdq2ps %ymm1, %ymm1
; CHECK-NEXT: vmovaps %ymm1, (%rdi)
; CHECK-NEXT: vcmovaps %ymm0, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitialize
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:       # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitialize
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f64_256:
; CHECK:       # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k0, %ymm0
; CHECK-NEXT: vcvtdq2ps %xmm0, %ymm1
; CHECK-NEXT: vextracti128 %1, %ymm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 %1, %ymm2, %xmm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, (%rdi)
; CHECK-NEXT: vcmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitialize

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%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
 ; CHECK-LABEL: sbto16f64_512:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vpmovw2m %ymm0, %k0
 ; CHECK-NEXT: vpmovm2d %k0, %zmm0
 ; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm1
 ; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
 ; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
 ; CHECK-NEXT: vpmovaps %zmm0, 64(%rdi)
 ; CHECK-NEXT: vpmovaps %zmm1, (%rdi)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %mask = icmp slt <16 x i16> %a, zero initializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: ubto16f32_256:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vpmovw2m %ymm0, %k0
 ; CHECK-NEXT: kshiftrw $8, %k0, %k1
 ; CHECK-NEXT: vpmovm2d %k1, %ymmm0
 ; CHECK-NEXT: vpmovm2d %k0, %ymmm0
 ; CHECK-NEXT: vpsrld $31, %ymmm0, %ymmm0
 ; CHECK-NEXT: vcvtdq2ps %ymmm0, %ymmm0
 ; CHECK-NEXT: vpsrld $31, %ymmm1, %ymmm1
 ; CHECK-NEXT: vcvtdq2ps %ymmm1, %ymmm1
 ; CHECK-NEXT: vpmovaps %ymmm1, (%rdi)
 ; CHECK-NEXT: vpmovaps %ymmm0, 32(%rdi)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %mask = icmp slt <16 x i16> %a, zero initializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
 ; CHECK-LABEL: ubto16f32_512:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vpmovw2m %ymm0, %k0
 ; CHECK-NEXT: vpmovm2d %k0, %zmm0
 ; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm1
 ; CHECK-NEXT: vextracti64x4 %1, %zmm0, %ymm0
 ; CHECK-NEXT: vcvtdq2pd %ymm0, %zmm0
 ; CHECK-NEXT: vpmovaps %zmm0, 64(%rdi)
 ; CHECK-NEXT: vpmovaps %zmm1, (%rdi)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %mask = icmp slt <16 x i16> %a, zero initializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: ubto16f64_256:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
  ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
  ; CHECK-NEXT:    vpmovm2d %k1, %ymm0
  ; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
  ; CHECK-NEXT:    vcvtdq2pd %ymm0, %ymm1
  ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm0
  ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0
  ; CHECK-NEXT:    vextracti128 $1, %ymm2, %xmm2
  ; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm2
  ; CHECK-NEXT:    vmovaps %ymm2, 32(%rdi)
  ; CHECK-NEXT:    vmovaps %ymm3, (%rdi)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %mask = icmp slt <16 x i16> %a, zeroinitializer
  %1 = uitofp <16 x i1> %mask to <16 x float>
  store <16 x float> %1, <16 x float>* %res
  ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: ubto16f64_512:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
  ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
  ; CHECK-NEXT:    vpmovm2d %k1, %ymm0
  ; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
  ; CHECK-NEXT:    vcvtdq2pd %ymm0, %ymm1
  ; CHECK-NEXT:    vextracti64x4 $1, %ymm0, %ymm0
  ; CHECK-NEXT:    vcvtdq2pd %ymm0, %ymm0
  ; CHECK-NEXT:    vzerosycr %ymm1
  ; CHECK-NEXT:    vmovaps %ymm2, 32(%rdi)
  ; CHECK-NEXT:    vmovaps %ymm3, (%rdi)
  ; CHECK-NEXT:    vzeroupper
  ; CHECK-NEXT:    retq
  %mask = icmp slt <16 x i16> %a, zeroinitializer
  %1 = uitofp <16 x i1> %mask to <16 x float>
  store <16 x float> %1, <16 x float>* %res
  ret void
}
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
  %a = load <16 x float>, <16 x float>* %ptr
  %mask = fptosi <16 x float> %a to <16 x i1>
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
  ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
  %a = load <64 x i8>* %a, <64 x i8>* %ptr
  %mask = fptosi <64 x float> %a to <64 x i1>
  %select = select <64 x i1> %mask, <64 x i8> %passthru, <64 x i8> zeroinitializer
  ret void
}
; CHECK-AVX512-NEXT:  vpckuswb %ymm4, %ymm1, %ymm1
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} ymm3 =
  ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
  ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpmullw %ymm3, %ymm4, %ymm3
; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} ymm2 =
  ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} ymm0 =
  ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-AVX512-NEXT:  vpmullw %ymm2, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpckuswb %ymm3, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-AVX512-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-AVX512-NEXT:  vzeroupper
; CHECK-AVX512-NEXT:  retq

; CHECK-VBMI-LABEL: mul256:
; CHECK-VBMI:  # %bb.0:
; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-VBMI-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-VBMI-NEXT:  vmovdqa (%rsi), %ymm2
; CHECK-VBMI-NEXT:  vmovdqa 32(%rsi), %ymm3
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
  ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm5 =
  ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} ymm2 =
  ymm2[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} ymm0 =
  ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpmullw %ymm2, %ymm0, %ymm0
; CHECK-VBMI-NEXT:  vpermt2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm3 =
  [0,2,4,6,8,10,12,14,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62]
; CHECK-VBMI-NEXT:  vperm2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
  ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm5 =
  ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpmullw %ymm4, %ymm5, %ymm4
; CHECK-VBMI-NEXT:  vpmullw %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm3 =
  0,2,4,6,8,10,12,14,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
; CHECK-VBMI-NEXT:  vperm2b %ymm4, %ymm3, %ymm1
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
  ymm2[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm5 =
  ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-VBMI-NEXT: vpermt2b %ymm4, %ymm3, %ymm0
; CHECK-VBMI-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-VBMI-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-VBMI-NEXT: vzeroupper
; CHECK-VBMI-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
; CHECK-AVX512-LABEL: mul512:
; CHECK-AVX512:
# %bb.0:
; CHECK-AVX512-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-AVX512-NEXT: vmovdqa64 (%rsi), %zmm1
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} zmm2 =
zmm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} zmm3 =
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-AVX512-NEXT: vpmullw %zmm2, %zmm3, %zmm2
; CHECK-AVX512-NEXT: vmovdqa64 {{.*#+}} zmm3 =
55,255,255,255,255,255]
; CHECK-AVX512-NEXT: vpandq %zmm3, %zmm2, %zmm2
; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} zmm1 =
zmm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-AVX512-NEXT: vpunpcklbw {{.*#+}} zmm0 =
zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,3
6,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-AVX512-NEXT: vpmullw %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT: vpandq %zmm3, %zmm0, %zmm0
; CHECK-AVX512-NEXT: vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-AVX512-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-AVX512-NEXT: vzeroupper
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: mul512:
; CHECK-VBMI:
# %bb.0:
; CHECK-VBMI-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-VBMI-NEXT: vmovdqa64 (%rsi), %zmm1
; CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} zmm2 =
zmm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]

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This threw an assertion at one point.

define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {
    CHECK-LABEL: mload_v4i32:
    CHECK:       # %bb.0:
    CHECK-NEXT:  vptestnmd %xmm0, %xmm0, %k1
    CHECK-NEXT:  vpblendmd (%rdi), %xmm1, %xmm0 {%k1}
    CHECK-NEXT:  retq
    %mask = icmp eq <4 x i32> %trigger, zeroinitializer
    %res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>* %addr, i32 4, <4 x i31> %mask, <4 x i32> %dst)
    ret <4 x i32> %res
}
define <16 x i32> @trunc_v16i64_v16i32(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    CHECK-LABEL: trunc_v16i64_v16i32:
    CHECK:       # %bb.0:
    CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
    CHECK-NEXT:  vmovdqa 64(%rdi), %ymm2
    CHECK-NEXT:  vmovdqa 96(%rdi), %ymm3
    CHECK-NEXT:  vpmovqd %ymm0, %xmm0
}
define <16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
    %a = load <16 x i64>, <16 x i64>* %x  
    %b = trunc <16 x i64> %a to <16 x i32>  
    ret <16 x i8> %b  
}

define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {  
    %a = load <16 x i32>, <16 x i32>* %x  
    %b = trunc <16 x i32> %a to <16 x i8>  
    ret <16 x i8> %b  
}

define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i16:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpmovq %ymm1, %xmm1
    ; CHECK-NEXT: vpmovq %ymm0, %xmm0
    ; CHECK-NEXT: vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = trunc <8 x i64> %a to <8 x i8>
    ret <8 x i8> %b
}

define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpmovq %ymm1, %xmm1
    ; CHECK-NEXT: vpmovq %ymm0, %xmm0
    ; CHECK-NEXT: vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = trunc <8 x i64> %a to <8 x i16>
    ret <8 x i16> %b
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vpshrlq %48, 32(%rdi), %ymm1
    ; CHECK-NEXT: vpshrlq %48, (%rdi), %ymm2
    ; CHECK-NEXT: vmovdqa {{.*#+}} ymm0 = [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30]
    ; CHECK-NEXT: vpmovi2w %ymm1, %ymm2, %ymm0
    ; CHECK-NEXT: retq
    %a = load <16 x i64>, <16 x i64>* %x
    %b = lshr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
    %c = trunc <8 x i64> %b to <8 x i32>
    ret <8 x i32> %c
}

define <16 x i32> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm1
    ; CHECK-NEXT: vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
define <32 x i8> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
CHECK-LABEL: trunc_v8i64_v8i32_sign:
CHECK: # %bb.0:
CHECK-NEXT: vpsraq $48, 32(%rdi), %ymm0
CHECK-NEXT: vpsraq $48, (%rdi), %ymm1
CHECK-NEXT: vpmovqd %ymm1, %xmm1
CHECK-NEXT: vpmovqd %ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
CHECK-NEXT: vpmovqd %ymm1, %ymm0
CHECK-NEXT: vinserti128 $1, %xmm0, %ymm1, %ymm0
CHECK-NEXT: retq
%a = load <32 x i16>, <32 x i16>* %x
%b = lshr <32 x i16> %a, <16 x i32>* %x
%c = trunc <32 x i16> %b to <32 x i16>
ret <32 x i8> %c
}

define <16 x i16> @trunc_v16i32_v16i16_sign(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
CHECK-LABEL: trunc_v16i32_v16i16_sign:
CHECK: # %bb.0:
CHECK-NEXT: vpsra2w 32(%rdi), %ymm1, %ymm0
CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %x
%b = lshr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>
%c = trunc <16 x i32> %b to <16 x i16>
ret <16 x i16> %c
}

define <8 x i32> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
CHECK-LABEL: trunc_v8i64_v8i32_sign:
CHECK: # %bb.0:
CHECK-NEXT: vpsrlw $8, 32(%rdi), %ymm0
CHECK-NEXT: vpsrlw $8, (%rdi), %ymm1
CHECK-NEXT: vpackuswb %ymm0, %ymm1, %ymm0
CHECK-NEXT: vpermq [.+#+] ymm0 = ymm0[0,2,1,3]
CHECK-NEXT: retq
%a = load <32 x i16>, <32 x i16>* %x
%b = lshr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
}

define <16 x i16> @trunc_v16i32_v16i16_sign(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
CHECK-LABEL: trunc_v16i32_v16i16_sign:
CHECK: # %bb.0:
CHECK-NEXT: vpermi2w 32(%rdi), %ymm1, %ymm0
CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %x
%b = lshr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>
%c = trunc <16 x i32> %b to <16 x i16>
ret <16 x i16> %c
}
定义用于计算 <16 x i32> 的 <16 x i32> 矢量的函数:

```assembly
; CHECK-LABEL: trunc_v16i32_v16i16_sign:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-NEXT:  vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT:  vpermi2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT:  retq

%a = load <16 x i32>, <16 x i32>* %x
%b = ashr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>  
%c = trunc <16 x i32> %b to <16 x i16>
ret <16 x i16> %c
```

定义用于计算 <32 x i16> 的 <32 x i16> 矢量的函数:

```assembly
; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_sign:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:  vpsraw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT:  vpsraw $8, (%rdi), %ymm1
; CHECK-AVX512-NEXT:  vpacksswb %ymm0, %ymm1, %ymm0
; CHECK-AVX512-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-AVX512-NEXT:  retq

; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_sign:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT:  vpermi2b 32(%rdi), %ymm1, %ymm0
; CHECK-VBMI-NEXT:  retq

%a = load <32 x i16>, <32 x i16>* %x
%b = ashr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>  
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
```

定义用于计算 <32 x i8> 的 <32 x i64> 矢量的函数:

```assembly
define <32 x i8> @zext_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: zext_v16i8_v16i64:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovzxbw {{.*#+}} ymm1 = xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
; CHECK-NEXT:  vpshufd {{.*#+}} xmm2 = xmm1[2,3,0,1]
; CHECK-NEXT:  vpmovzxwq {{.*#+}} ymm2 = xmm2[0],zero,zero,xmm2[1],zero,zero,zero,xmm2[2],zero,zero,zero,xmm2[3],zero,zero
; CHECK-NEXT:  vextracti128 $1, %ymm1, %xmm1
; CHECK-NEXT:  vpshufd {{.*#+}} xmm3 = xmm1[2,3,0,1]
```
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; CHECK-NEXT: vpmovzxwq {{.*#+}} ymm3 = xmm3[0],zero,zero,xmm3[1],zero,zero,xmm3[2],zero,zero,xmm3[3],zero,zero,zero
; CHECK-NEXT: vpmovzxwq {{.*#+}} ymm1 = xmm1[0],zero,zero,xmm1[1],zero,zero,xmm1[2],zero,zero,xmm1[3],zero,zero,zero
; CHECK-NEXT: vpmovzxwq {{.*#+}} ymm0 = xmm0[0],zero,zero,xmm0[1],zero,zero,xmm0[2],zero,zero,xmm0[3],zero,zero,zero
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm2, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq

%a = zext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:       # %bb.0:
; CHECK-NEXT: vpmovsxbw %xmm0, %ymm0
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpmovsxwq %ymm1, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT: vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]
; CHECK-NEXT: vpmovsxwq %ymm3, %ymm3
; CHECK-NEXT: vpmovsxwq %xmm0, %ymm0
; CHECK-NEXT: vpmovsxwq %xmm2, %ymm2
; CHECK-NEXT: vmovdqa %ymm2, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = sext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i16_setcc:
; CHECK:       # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %xmm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqw %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrb $4, %k1, %k2

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define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" { 
    ; CHECK-LABEL: vselect_split_v8i32_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:    vpcmpeqd %ymm1, %ymm0, %k1
    ; CHECK-NEXT:    kshiftrb $4, %k1, %k2
    ; CHECK-NEXT:    vmovdqa64 32(%rdi), %ymm3 [%k2]
    ; CHECK-NEXT:    vmovdqa64 (%rdi), %ymm2 [%k1]
    ; CHECK-NEXT:    vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:    vmovdqa %ymm3, 32(%rdx)
    ; CHECK-NEXT:    retq
    ; CHECK-NEXT:    %x = load <8 x i64>, <8 x i64>* %p
    ; CHECK-NEXT:    %y = load <8 x i64>, <8 x i64>* %q
    ; CHECK-NEXT:    %a = icmp eq <8 x i32> %s, %t
    ; CHECK-NEXT:    %b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
    ; CHECK-NEXT:    store <8 x i64> %b, <8 x i64>* %r
    ; CHECK-NEXT:    ret void
} 

define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" { 
    ; CHECK-LABEL: vselect_split_v16i8_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:    vmovdqa32 (%rsi), %ymm3
    ; CHECK-NEXT:    vpcmpeqb %xmm1, %ymm0, %k1
    ; CHECK-NEXT:    kshiftrw $8, %k1, %k2
    ; CHECK-NEXT:    vmovdqa32 32(%rdi), %ymm3 [%k2]
    ; CHECK-NEXT:    vmovdqa32 (%rdi), %ymm2 [%k1]
    ; CHECK-NEXT:    vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:    vmovdqa %ymm3, 32(%rdx)
    ; CHECK-NEXT:    retq
    ; CHECK-NEXT:    %x = load <16 x i8>, <16 x i8>* %p
    ; CHECK-NEXT:    %y = load <16 x i8>, <16 x i8>* %q
    ; CHECK-NEXT:    %a = icmp eq <16 x i32> %s, %t
    ; CHECK-NEXT:    %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
    ; CHECK-NEXT:    store <16 x i32> %b, <16 x i32>* %r
    ; CHECK-NEXT:    ret void
} 

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; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i8> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: vselect_split_v16i16_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:    vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:    vpcmpeqw %ymm1, %ymm0, %k1
    ; CHECK-NEXT:    kshiftrw $8, %k1, %k2
    ; CHECK-NEXT:    vmovdqa32 32(%rdi), %ymm3 [%k2]
    ; CHECK-NEXT:    vmovdqa32 (%rdi), %ymm2 [%k1]
    ; CHECK-NEXT:    vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:    vzerooupper
    ; CHECK-NEXT:    retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i16> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_packus_v16i32_v16i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:    vpackusdw 32(%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:    vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-NEXT:    vpmovuswb %ymm0, %xmm0
    ; CHECK-NEXT:    vzerooupper
    ; CHECK-NEXT:    retq
%a = load <16 x i32>, <16 x i32>* %p
%b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
%a = select <16 x i1> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a
%c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a, <16 x i32> %a
%d = icmp sgt <16 x i32> %c, zeroinitializer
%e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer

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define void @trunc_packus_v16i32_v16i8_store(<16 x i32> %p, <16 x i8> %q) "min-legal-vector-width"="256" {
; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vpacksdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT: vpmovuswb %ymm0, (%rsi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %p
%b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
%c = select <16 x i1> %b, <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
%d = icmp sgt <16 x i32> %c, zeroinitializer
%e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
%f = trunc <16 x i32> %e to <16 x i8>
store <16 x i8> %f, <16 x i8>* %q
ret void
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_argument_return:
; CHECK: # %bb.0:
; CHECK-NEXT: retq
ret <64 x i1> %x
}

define void @v64i1_shuffle(<64 x i8> %x, <64 x i8> %y) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_shuffle:
; CHECK: # %bb.0: # %entry
; CHECK-NEXT: vmovdqa (%rdi), %ymm1
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm0
; CHECK-NEXT: vptestnmb %ymm1, %ymm1, %k0
; CHECK-NEXT: kshiftrd $1, %k0, %k1
; CHECK-NEXT: movq $-3, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrq $63, %k0, %k2
; CHECK-NEXT: kshiftrq $62, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-5, %rax
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $8, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $54, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $11, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $53, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $10, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $52, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-4097, %rax # imm = 0xEFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $13, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $51, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-8193, %rax # imm = 0xDFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $12, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $50, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-16385, %rax # imm = 0xBFFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $15, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $49, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT: movq $-32769, %rax # imm = 0xFFFF7FFF
; CHECK-NEXT: kmovq %rax, %k2
; CHECK-NEXT: kandq %k2, %k1, %k1
; CHECK-NEXT: kshiftrd $14, %k0, %k2
; CHECK-NEXT: kshiftlq $63, %k2, %k2
; CHECK-NEXT: kshiftrq $48, %k2, %k2
; CHECK-NEXT: korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-65537, %rax # imm = 0xFFFEEEEF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshift rd $17, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftq $47, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-131073, %rax # imm = 0xFFFFDFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshift rd $16, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftq $46, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-262145, %rax # imm = 0xFFFFBFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshift rd $19, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftq $45, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-524289, %rax # imm = 0xFFF7FFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshift rd $21, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftq $43, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1048577, %rax # imm = 0xFFEFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshift rd $20, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftq $42, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-2097153, %rax # imm = 0xFFDFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshift rd $23, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftq $41, %k2, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $19, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $13, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.2251799813685249, %rax # imm = 0x7FFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $18, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $12, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.4503599627370497, %rax # imm = 0x7FFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $21, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $11, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.9007199254740993, %rax # imm = 0x7FFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $20, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $10, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.18014398509481985, %rax # imm = 0x7FFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $23, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $9, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.3602879701863969, %rax # imm = 0x7FFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $22, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $8, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.72057594037927937, %rax # imm = 0x7FFFFFFFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k0, %k0
; CHECK-NEXT:    kshiftrd $25, %k1, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $7, %k2, %k2
; CHECK-NEXT:    korq %k2, %k0, %k0
; CHECK-NEXT:    movabsq $.144115188075855873, %rax # imm = 0x7FFFFFFFFFFFFF
entry:
%a = load <64 x i8>, <64 x i8>* %x
%b = icmp eq <64 x i8> %a, zeronitializer
%shuf = shufflevector <64 x i1> %b, undef, <64 x i1> undef
%a = load <64 x i8>, <64 x i8>* %x
%y = load <64 x i8>, <64 x i8>* %y
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, 1, <64 x i1> %shuf)
ret void
}
define void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
%1 = load <8 x i64>, <8 x i64>* %xptr
%2 = load <8 x i64>, <8 x i64>* %yptr
%3 = load <8 x i64>, <8 x i64>* %zptr
vzeroupper
retq
}
define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: cmp_v8i64_zext:
  ; CHECK:      # %bb.0:
  ; CHECK-NEXT: vmovdqa (%rsi), %ymm0
  ; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
  ; CHECK-NEXT: vpcmpgtq 32(%rdi), %ymm1, %ymm1
  ; CHECK-NEXT: vpcmpgtq (%rdi), %ymm0, %ymm0
  ; CHECK-NEXT: vpsrlq $63, %ymm1, %ymm1
  ; CHECK-NEXT: vpsrlq $63, %ymm0, %ymm0
  ; CHECK-NEXT: vmovdqa %ymm0, 0(%rdx)
  ; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
  ; CHECK-NEXT: vzeroupper
  ; CHECK-NEXT: retq

  %x = load <8 x i64>, <8 x i64>* %xptr
  %y = load <8 x i64>, <8 x i64>* %yptr
  %cmp = icmp slt <8 x i64> %x, %y
  %ext = zext <8 x i1> %cmp to <8 x i64>
  store <8 x i64> %ext, <8 x i64>* %zptr
  ret void
}

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: var_rotate_v16i8:
  ; CHECK:      # %bb.0:
  ; CHECK-NEXT: vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
  ; CHECK-NEXT: vpsubb %xmm1, %xmm2, %xmm2
  ; CHECK-NEXT: vmovzxbw {{.*#+}} ymm1 =
  ; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm0 =
  ; CHECK-NEXT: vpsllvw %ymm1, %ymm1, %ymm1
  ; CHECK-NEXT: vpmovzxbw {{.*#+}} ymm2 =
  ; CHECK-NEXT: vpsrlvw %ymm2, %ymm2, %ymm2
  ; CHECK-NEXT: vpor %ymm0, %ymm1, %ymm0

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {

  ; CHECK-NEXT:  vpmovwb %ymm0, %xmm0
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq

  %b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
  %shl = shl <16 x i8> %a, %b
  %lshr = lshr <16 x i8> %a, %b8
  %or = or <16 x i8> %shl, %lshr
  ret <16 x i8> %or

}

define <32 x i8> @splatvar_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {

  ; CHECK-NEXT:  vpsllw $4, %ymm0, %ymm2
  ; CHECK-NEXT:  vpsrlw $4, %ymm0, %ymm3
  ; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
  ; CHECK-NEXT:  vpsllw $5, %ymm1, %ymm1
  ; CHECK-NEXT:  vpbroadcastb %ymm1, %ymm1
  ; CHECK-NEXT:  vpsllw $6, %ymm0, %ymm3
  ; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
  ; CHECK-NEXT:  vpsllw $7, %ymm0, %ymm0
  ; CHECK-NEXT:  vpsrlw $6, %ymm0, %ymm3
  ; CHECK-NEXT:  vpternlogq $248, {{.*}}(%rip), %ymm2, %ymm3
  ; CHECK-NEXT:  vpsllw %ymm1, %ymm1
  ; CHECK-NEXT:  vpsllw %ymm1, %ymm3
  ; CHECK-NEXT:  retq

  %b8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
  %shl = shl <32 x i8> %a, %b
  %lshr = lshr <32 x i8> %a, %b8
  %or = or <32 x i8> %shl, %lshr
  ret <32 x i8> %or

}

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {

  ; CHECK-NEXT:  vpbroadcastb %xmm1, %xmm1
  ; CHECK-NEXT:  vpand {{.*}}(%rip), %xmm1, %xmm1
  ; CHECK-NEXT:  vpmovzxvb {.*} xmm2 = 
  ; CHECK-NEXT:  vpslub %xmm2, %ymm0, %ymm3
  ; CHECK-NEXT:  vpsubb %xmm1, %xmm4, %xmm1

}}
; CHECK-NEXT: vpcmpeqd %xmm4, %xmm4, %xmm4
; CHECK-NEXT: vpsllw %xmm2, %xmm4, %xmm2
; CHECK-NEXT: vpbroadcastb %xmm2, %ymm2
; CHECK-NEXT: vpmovzxbq {{.*#+}} xmm1 =
%mm1[0],zero,zero,zero,zero,zero,zero,zero,xmm1[1],zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT: vpsrlw %xmm1, %ymm0, %ymm5
; CHECK-NEXT: vpand %ymm2, %ymm3, %ymm2
; CHECK-NEXT: vpsrlw $8, %ymm0, %ymm0
; CHECK-NEXT: vpbroadcastb %xmm0, %ymm0
; CHECK-NEXT: vpternlogq $236, %ymm5, %ymm2, %ymm0
; CHECK-NEXT: retq
%splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
%splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8> , %splat
%splats8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8> , %splats8
%shl = shl <32 x i8> %a, %splat
%lsr = lshr <32 x i8> %a, %splats8
%or = or <32 x i8> %shl, %lsr
ret <32 x i8> %or
}

define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: constant_rotate_v32i8:
; CHECK-AVX512: # %bb.0:
; CHECK-AVX512-NEXT: vpsllw $4, %ymm0, %ymm1
; CHECK-AVX512-NEXT: vpand {{.*}}(%rip), %ymm1, %ymm1
; CHECK-AVX512-NEXT: vbpexchangei128 {{.*#+}} %ymm2 = [8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]
; CHECK-AVX512-NEXT: # %ymm2 = mem[0,1,0,1]
; CHECK-AVX512-NEXT: vpbroadcastb %ymm2, %ymm1, %ymm0, %ymm1
; CHECK-AVX512-NEXT: vpsllw $2, %ymm1, %ymm3
; CHECK-AVX512-NEXT: vpand {{.*}}(%rip), %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpadddb %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT: vpbroadcastb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpadddb %ymm1, %ymm1, %ymm3
; CHECK-AVX512-NEXT: vpadddb %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT: vpbroadcastb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-AVX512-NEXT: vunpckhbw {{.*#+}} %ymm3 =
%ymm0[8],%ymm2[8],%ymm0[9],%ymm2[9],%ymm0[10],%ymm2[10],%ymm0[11],%ymm2[11],%ymm0[12],%ymm2[12],%ymm0[13]
,ymmm2[13],ymmm0[14],ymmm2[14],ymmm0[15],ymmm2[15],ymmm0[24],ymmm2[24],ymmm0[25],ymmm2[25],ymmm0[26],ymmm2[26],ymmm0[27],ymmm2[27],ymmm0[28],ymmm2[28],ymmm0[29],ymmm2[29],ymmm0[30],ymmm2[30],ymmm0[31],ymmm2[31]
; CHECK-AVX512-NEXT: vpsllw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-AVX512-NEXT: vpsrlw $8, %ymm3, %ymm3
; CHECK-AVX512-NEXT: vunpcklbw {{.*#+}} %ymm0 =
%ymm0[0],%ymm2[0],%ymm0[1],%ymm2[1],%ymm0[2],%ymm2[2],%ymm0[3],%ymm2[3],%ymm0[4],%ymm2[4],%ymm0[5],%ymm2[5]
,ymmm0[6],ymmm2[6],ymmm0[7],ymmm2[7],ymmm0[16],ymmm2[16],ymmm0[17],ymmm2[17],ymmm0[18],ymmm2[18],ymmm0[18]
define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: splatconstant_rotate_mask_v32i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpsllw $4, %ymm0, %ymm1
    ; CHECK-NEXT:    vpsrlw $4, %ymm0, %ymm0
    ; CHECK-NEXT:    vpternlogq $216, {{.*}}(%rip), %ymm1, %ymm0
    ; CHECK-NEXT:    retq
    %shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>
    %lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>
    %rmask = and <32 x i8> %lshr, <i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55>
    %lmask = and <32 x i8> %shl, <i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33>
    %or = or <32 x i8> %lmask, %rmask
    ret <32 x i8> %or
}

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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp

LINK_LIBS
  llodbCore
  llodbHost
  llodbSymbol
LINK_COMPONENTS
  Support
)
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; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
entry:
br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
%lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
switch i32 undef, label %exit ]
i32 1, label %sw.bb.i
i32 2, label %sw.bb3.i
]

sw.bb.i: ; preds = %while.body
unreachable

sw.bb3.i: ; preds = %while.body
unreachable

exit: ; preds = %while.body
switch i32 undef, label %exit.2 [i32 1, label %sw.bb.i17
i32 2, label %sw.bb3.i20 ]

sw.bb.i17: ; preds = %.exit
%0 = bitcast %struct.0* %lsr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lsr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable
exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=4 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS________OP M,IS__TUNIT_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=3 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_OPM,NOT_CGSCC_NPM,NOT_TUNIT_OPM,IS__TUNIT____,IS________NP M,IS__TUNIT_NPM
; RUN: opt -attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_NPM,IS__CGSCC____,IS________OP M,IS__CGSCC_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS________NP M,IS__CGSCC_NPM
; Test that we only promote arguments when the caller/callee have compatible
; function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}


bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

;
; NOT_TUNIT_NPM-LABEL: define
{[^@]+}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:  ret void
;
; IS_TUNIT_NPM-LABEL: define
{[^@]+}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT:  ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {

;
; IS_TUNIT_OPM-LABEL: define {[^@]+}@avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS_TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS_TUNIT_OPM-NEXT: bb:
; IS_TUNIT_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 64
; IS_TUNIT_OPM-NEXT:  store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_OPM-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_OPM-NEXT:  store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_OPM-NEXT:  ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT:  call fastcc void
@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT:  ret void
;
; IS__TUNIT_NPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT:  bb:
; IS__TUNIT_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT:  [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT:  call fastcc void
@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT:  ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT:  bb:
; IS__CGSCC_OPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:  call fastcc void
@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:  ret void
;
; IS__CGSCC_NPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT:  bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void
@calleee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonfree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)

; IS__CGSCC_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocaapture nolfree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocaapture nolfree nonnull readonly
align 64 dereferenceable(64) [[TMP]])

; IS__CGSCC_NPM-NEXT: [[TMP4:.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

; bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocaapture nolfree nonnull writeonly align 64 dereferenceable(64)
[[ARG:.*]], <8 x i64>* nocaapture nolfree nonnull readonly align 64 dereferenceable(64) [[ARG1:.]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:.]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocaapture nolfree nonnull writeonly align 64 dereferenceable(64)
[[ARG:.*]], <8 x i64> [[TMP0:.]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:.]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:.]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(8 x i64)* %arg) #1 {

};

@avx512_legal512_prefer256_call_avx512_legal512_prefer512(8 x i64)* %arg) #1 {

};

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(8 x i64)* %arg) #1 {

};

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(8 x i64)* %arg) #1 {

};
This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, 
<8 x i64>* %tmp) #1 {

bb:

%tmp = load <8 x i64>, <8 x i64>* %tmp, align 64
%tmp2 = load <8 x i64>, <8 x i64>* %tmp2, align 64
%tmp3 = bitcast <8 x i64>* %tmp3 to i8*
call void @llvm.memset.p0i8.i64(i8* %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x 
i64>* %tmp), <8 x i64>* %arg, align 2
ret void

};
```c
define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg1
        store <8 x i64> %tmp, <8 x i64>* %arg
        ret void
    }
```
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapturenonnull writeonlyalign 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapturenonnull writeonlyalign 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapturenonnull writeonlyalign 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapturenonnull readonlyalign 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal256_prefer256_call_avx512Legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapturenonnull writeonlyalign 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapturenonnull writeonlyalign 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapturenonnull readonlyalign 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {

; NOT_TUNIT_NPM-LABEL: define
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>*readonly %arg1) #3 {

;

; NOT_TUNIT_NPM-LABEL: define
( {{[@]+}} )@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:

;

; IS__TUNIT_NPM-LABEL: define ( {{[@]+}} )@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {

;

; IS__TUNIT_OPM-LABEL: define ( {{[@]+}} )@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>*readonly %arg1) #3 {

;

; NOT_TUNIT_NPM-LABEL: define
( {{[@]+}} )@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:

;

; IS__TUNIT_NPM-LABEL: define ( {{[@]+}} )@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
; IS__TUNIT_OPM-SAME: (&<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[TMP;%.*]] = alloca &<8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP2;%.*]] = alloca &<8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [[TMP3;%.*]] = bitcast &<8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@calleee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(&<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], &<8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4;%.*]] = load &<8 x i64>, &<8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store &<8 x i64> [[TMP4]], &<8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
call fastcc void
@calleee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(&<8 x i64>* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0;%.*]] = load &<8 x i64>, &<8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@calleee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(&<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], &<8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4;%.*]] = load &<8 x i64>, &<8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store &<8 x i64> [[TMP4]], &<8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
call fastcc void
@calleee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(&<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG;%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP;%.*]] = alloca &<8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2;%.*]] = alloca &<8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3;%.*]] = bitcast &<8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@calleee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(&<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], &<8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4;%.*]] = load &<8 x i64>, &<8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store &<8 x i64> [[TMP4]], &<8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8*, i8, i64, i1) #5

attributes #0 = { inlinelint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinelint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinelint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinelint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinelint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; Verify that fptosi(42.1) isn't simplified when the rounding mode is unknown.
; Verify that no gross errors happen.
; CHECK-LABEL: @f20
; COMMON: cfdctsiz
define i32 @f20(double %a) strictfp {
  entry:
  %result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1, metadata !"fpexcept.strict")
  strictfp
  ret i32 %result
}

@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)
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define void @zext256() "min-legal-vector-width"="256" {
  ;VEC256-LABEL: 'zext256'
  ;VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ;VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ;VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ;VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ;VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ;VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;AVX512VL512-LABEL: 'zext256'
  ;AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ;AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ;AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ;AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ;AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ;AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;SKX512-LABEL: 'zext256'
  ;SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ;SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
define void @zext512() "min-legal-vector-width"="512" {
  ; AVX-LABEL: 'zext512'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ret void
}

define void @zext512() "min-legal-vector-width"="512" {
  ; AVX512VL256-LABEL: 'zext512'
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ret void
}

define void @zext512() "min-legal-vector-width"="512" {
  ; AVX512VL512-LABEL: 'zext512'
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ret void
}
define void @sext256() "min-legal-vector-width"="256" {
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <16 x i8> undef to <16 x i32>
  %D = sext <16 x i16> undef to <16 x i32>
  %E = sext <32 x i8> undef to <32 x i16>
  ret void
}

define void @sext512() "min-legal-vector-width"="512" {
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <16 x i8> undef to <16 x i32>
  %D = sext <16 x i16> undef to <16 x i32>
  %E = sext <32 x i8> undef to <32 x i16>
  ret void
}

define void @sext128() "min-legal-vector-width"="128" {
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <16 x i8> undef to <16 x i32>
  %D = sext <16 x i16> undef to <16 x i32>
  %E = sext <32 x i8> undef to <32 x i16>
  ret void
}
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'sext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {  
; AVX-LABEL: 'sext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
skx256: sext512
  : SKX256-LABEL: 'sext512'
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL256-LABEL: 'sext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL512-LABEL: 'sext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
define void @trunc256() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'trunc256'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
  ret void
}

; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
; ; SKX512-LABEL: 'sext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; ; AVX512VL256-LABEL: 'trunc256'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
%A = trunc <8 x i64> undef to <8 x i32>
%B = trunc <8 x i64> undef to <8 x i16>
%C = trunc <8 x i64> undef to <8 x i8>
%D = trunc <16 x i32> undef to <16 x i16>
%E = trunc <16 x i32> undef to <16 x i8>
%F = trunc <32 x i16> undef to <32 x i8>
ret void
}

define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
    ; AVX-LABEL: 'zext256_vXi1'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
    ; AVX512VL256-LABEL: 'zext256_vXi1'
    ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef
to <2 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 25 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 50 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; AVX512VL512-LABEL: 'zext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
define i32 @sext256_vXi1() "min-legal-vector-width"="256" {
    ; AVX-LABEL: 'sext256_vXi1'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>

    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>

    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>

    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 16 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 32 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>

    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 16 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 32 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 64 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>

    ret i32 undef
}

define i32 sext256_vXi1() "min-legal-vector-width"="256" {
    ; AVX-LABEL: 'sext256_vXi1'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V8i32 = sext <8 x i1> unde
i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>
%V64i16 = sext <64 x i1> undef to <64 x i16>
%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>
%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 = sext <8 x i1> undef to <8 x i64>
%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>
%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1>
define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
    ; AVX-LABEL: 'trunc_vXi1'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to
; AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; AVX512VL256-LABEL: 'trunc_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; SKX256-LABEL: 'trunc_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
x i1>

; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; SKX512-LABEL: ‘trunc_vXi1’
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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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procfs provides functions to retrieve system, kernel and process
metrics from the pseudo-filesystem proc.

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1.1006 deepmerge 2.2.1

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1.1007 sys-mountinfo 0.4.1

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1.1012 fvbommel-sortorder 1.0.1

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1.1019 mitchellh-mapstructure 1.2.2
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1.1020 pynacl 1.4.0
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1.1026 libdumbnet 1.12-8

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Metadata-Version: 1.1
Name: roman
Version: 2.0.0
Summary: Integer to Roman numerals converter
Home-page: http://pypi.python.org/pypi/roman
Author: Mark Pilgrim
Author-email: f8dy@diveintopython.org
License: Python 2.1.1
Description: ======

CHANGES

2.0.0 (2013-02-25)

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- Added Python 3.3 and PyPy support.

- Added tests.

1.4.0 (2009-07-23)

--------------

- Initial PyPI release.

Keywords: roman
Platform: UNKNOWN
Classifier: Development Status :: 5 - Production/Stable
Classifier: Intended Audience :: Developers
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
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1.1043 net-snmp 5.7.3+dfsg-5+deb10u2

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Protocol Buffers for Go with Gadgets

Go support for Protocol Buffers - Google's data interchange format

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https://github.com/golang/protobuf

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ASN.1 library for Python
------------------------
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This is a free and open source implementation of ASN.1 types and codecs
as a Python package. It has been first written to support particular
protocol (SNMP) but then generalized to be suitable for a wide range
of protocols based on
[ASN.1 specification](https://www.itu.int/rec/dologin_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items).

Features
-------

* Generic implementation of ASN.1 types (X.208)
* Standards compliant BER/CER/DER codecs
* Dumps/loads ASN.1 structures from Python types
* 100% Python, works with Python 2.4 up to Python 3.6
* MT-safe
* Contributed ASN.1 compiler [Asn1ate](https://github.com/kimgr/asn1ate)

Why using pyasn1
----------------

ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1. [Communication between heterogeneous systems](http://www.oss.com/asn1/dubuisson.html) by Olivier Dubuisson is one of those high quality books freely available on the Internet.

The pyasn1 package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

How to use pyasn1
-----------------  

With pyasn1 you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```
```bash
Record ::= SEQUENCE {
   id       INTEGER,
   room [0] INTEGER OPTIONAL,
   house [1] INTEGER DEFAULT 0
}
```
```

Could be expressed in pyasn1 like this:

```
```python
class Record(Sequence):
```
componentType = NamedTypes(
    NamedType('id', Integer()),
    OptionalNamedType('room', Integer().subtype(
        implicitTag=Tag(tagClassContext, tagFormatSimple, 0)
    ),
    ),
    DefaultedNamedType('house', Integer(0).subtype(
        implicitTag=Tag(tagClassContext, tagFormatSimple, 1)
    ),
    )
)
'''

It is in the spirit of ASN.1 to take abstract data description
and turn it into a programming language specific form.
Once you have your ASN.1 data structure expressed in Python, you
can use it along the lines of similar Python type (e.g. ASN.1
`SET` is similar to Python `dict`, `SET OF` to `list`):

```python
>>> record = Record()
>>> record['id'] = 123
>>> record['room'] = 321
>>> str(record)
Record:
id=123
room=321
```

Part of the power of ASN.1 comes from its serialisation features. You
can serialise your data structure and send it over the network.

```python
>>> from pyasn1.codec.der.encoder import encode
>>> substrate = encode(record)
>>> hexdump(substrate)
00000: 30 07 02 01 7B 80 02 01 41
```

Conversely, you can turn serialised ASN.1 content, as received from
network or read from a file, into a Python object which you can
introspect, modify, encode and send back.

```python
>>> from pyasn1.codec.der.decoder import decode
```
```python
>>> received_record, rest_of_substrate = decode(substrate, asn1Spec=Record())

>>> for field in received_record:
...    print('{} is {}'.format(field, received_record[field]))
id is 123
room is 321
house is 0

>>> record == received_record
True

>>> received_record.update(room=123)

>>> substrate = encode(received_record)

>>> hexdump(substrate)
00000: 30 06 02 01 7B 80 01 7B

```

The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, 
list, dict etc.). But ASN.1 types exhibit more complicated behaviour.

To make life easier for a Pythonista, they can turn their pyasn1 
classes into Python built-ins:

```
```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)

```

Or vice-versa -- you can initialize an ASN.1 structure from a tree of
Python objects:

```
```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id': 123, 'room': 321, 'house': 0}, asn1Spec=Record())
```

With ASN.1 design, serialisation codecs are decoupled from data objects,
so you could turn every single ASN.1 object into many different
serialised forms. As of this moment, pyasn1 supports BER, DER, CER and
Python built-ins codecs. The extremely compact PER encoding is expected
to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the
[documentation](http://snmplabs.com/pyasn1/),
compiled ASN.1 modules for different protocols and file formats could be found in the pyasn1-modules [repo](https://github.com/etingof/pyasn1-modules).

How to get pyasn1
-----------------

The pyasn1 package is distributed under terms and conditions of 2-clause BSD [license](http://snmplabs.com/pyasn1/license.html). Source code is freely available as a GitHub [repo](https://github.com/etingof/pyasn1).

You could `pip install pyasn1` or download it from [PyPI](https://pypi.python.org/pypi/pyasn1).

If something does not work as expected, [open an issue](https://github.com/etingof/pyasn1/issues) at GitHub or post your question [on Stack Overflow](http://stackoverflow.com/questions/ask) or try browsing pyasn1 [mailing list archives](https://sourceforge.net/p/pyasn1/mailman/pyasn1-users/).

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ASN.1 library for Python
========================

.. toctree::
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Abstract Syntax Notation One (ASN.1)

<http://en.wikipedia.org/wiki/Abstract_Syntax_Notation_1x>`_ is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.
The `pyasn1 <https://pypi.python.org/pypi/pyasn1/>`_ library implements
ASN.1 support in pure-Python.

What is ASN.1
--------------

ASN.1 is a large, arguably over-engineered and extremely old data modelling and
serialisation tool. It is probably among the first serialisation protocols in
the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first
electronic mail (known as X.400). Later on if was split off the e-mail application
and become a stand-alone tech still being actively supported by its designers
and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the
first, unsuccessful, version of the Internet. You can read many interesting
`discussions <https://news.ycombinator.com/item?id=8871453>`_ on that topic.

In the following years, generations of software engineers tackled the serialisation
problem many times. We can see that in Google's `ProtoBuffers <https://developers.google.com/protocol-
buffers/>`_ or `FlatBuffers <https://google.github.io/flatbuffers/>`_, for example.
Interestingly, many new takes on binary protocol design do not depart
far from ASN.1 from technical perspective. It's more of a matter of striking
a balance between processing overhead, wire format overhead and human
readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

* Data types: the standard introduces a collection of basic data types
  (integers, bits, strings, arrays and records) that can be used for describing
  arbitrarily complex, nested data structures.

* Serialisation protocols: the above data structures could be converted into a
  series of octets for storage or transmission over the wire as well as
  recovered back into their structured form. The system is fully agnostic
to hardware architectures differences.

* Schema language: ASN.1 data structures could be described in terms
  of a schema language for ASN.1 compiler to turn it into platform-specific
  implementation.

ASN.1 applications
------------------

Being an old and generally successful standard, ASN.1 is widely
adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:

* Signaling standards for the public switched telephone network (SS7 family)
* Network management standards (SNMP, CMIP)
* Directory standards (X.500 family, LDAP)
* Public Key Infrastructure standards (X.509, etc.)
* PBX control (CSTA)
* IP-based Videoconferencing (H.323 family)
* Biometrics (BIP, CBEFF, ACBio)
* Intelligent transportation (SAE J2735)
* Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

ASN.1 gotchas
--------------

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the `statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>`_ on discovered security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

Documentation
--------------

.. toctree::
   :maxdepth: 2

/pyasn1/contents

Use case
--------

.. toctree::
   :maxdepth: 2

/example-use-case

Download & Install
------------------

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Changes
-------

All changes and release history is maintained in changelog. There you could also download the latest unreleased pyasn1 tarball containing the latest fixes and improvements.

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/changelog

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Getting help
------------


Books on ASN.1
--------------

The pyasn1 implementation is largely based on reading up the following awesome books:

* `ASN.1 - Communication between heterogeneous systems <http://www.oss.com/asn1/dubuisson.html>`_ by Olivier Dubuisson

Here you can get the official standards which is hard to read:

On the other end of the readability spectrum, here is a quick and sweet write up:


If you are working with ASN.1, we'd highly recommend reading a proper book on the subject.

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Metadata-Version: 1.1
Name: pyasn1
Version: 0.4.2
Summary: ASN.1 types and codecs
Home-page: https://github.com/etingof/pyasn1
Author: Ilya Etingof <etingof@gmail.com>
Author-email: etingof@gmail.com
License: BSD
Description: Pure-Python implementation of ASN.1 types and DER/BER/CER codecs (X.208)
Platform: any
Classifier: Development Status :: 5 - Production/Stable
Classifier: Environment :: Console
Classifier: Intended Audience :: Developers
Classifier: Intended Audience :: Education
Classifier: Intended Audience :: Information Technology
Classifier: Intended Audience :: System Administrators
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* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/codec/cer/encoder.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/compat/binary.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/codec/ber/decoder.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/type/__main__.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/type/test_constraint.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/compat/integer.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/compat/test_octets.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/codec/cer/__main__.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/compat/test_integer.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/compat/test_useful.py
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* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/codec/ber/decoder.py
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* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/type/test_useful.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/codec/cer/test_encoder.py
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set yrange [0:]
set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
set xtics rotate by -45
set style histogram errorbars gap 2 lw 1
set style fill solid border -1
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1.1094 sanitized-anchor-name 1.0.0

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1.1098 dimchansky-utfbom 1.1.1

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A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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c-a-bundle.crt -- Bundle of CA Root Certificates

Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#
This is a bundle of X.509 certificates of public Certificate Authorities
(CA). These were automatically extracted from Mozilla's root certificates
file (certdata.txt). This file can be found in the mozilla source tree:
It contains the certificates in PEM format and therefore can be directly used with curl / libcurl / php_curl, or with an Apache+mod_ssl webserver for SSL client authentication. Just configure this file as the SSLCACertificateFile.

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A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for
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History
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1.1110 console-setup 1.193~deb10u1

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1.1115 x-sys 0.0.0-20210616094352-59db8d763f22

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1.1117 keyutils 1.6-6

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lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
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lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
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lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
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and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
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arch-tag: d4250e44-a0e0-4ee0-adb9-2bd74f6e9eb27

1.1122 units 0.0.0-20180711220420-6950e57a87ea

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1.1124 cdebconf 0.213ubuntu1

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-- vile: txtmode file-encoding=utf-8
Upstream source https://invisible-island.net/ncurses/ncurses-examples.html

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```
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```

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AES in libhcrypto

rijndael-alg-fst.c

@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
@author Paulo Barreto <paulo.barreto@terra.com.br>

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lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
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lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
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lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

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1.1141 x-sync 0.0.0-20201207232520-09787c993a3a

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1.1146 busybox 1.30.1-4

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```makefile
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,
```
## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<

%.$(SO): $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@

%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s ${*F}.$(SO).$(SO_TARGET_VERSION) $@

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.c) $< \
| sed "\"s/\([^*\[]\*\]*\)/\1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec '$(GEN_DEPS.cc) $< \
| sed "\"s/\([^*\[]\*\]*\)/\1.o $@ : /g\" > $@; \
[ -s $@ ] || rm -f $@

## Versioned libraries rules
%.$(SO).$($(SO_TARGET_VERSION_MAJOR)).$($(SO_TARGET_VERSION)):
$($(RM) $@ && ln -s $< $@)

%.$(SO).$($(SO_TARGET_VERSION_MAJOR)):
$($(RM) $@ && ln -s $<.*$($(SO).$($(SO_TARGET_VERSION_MAJOR))) $@

## Bind internal references
# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic
# Dependencies [i.e. map files] for the final library

BIR_DEPS=

## Remove shared library 's'

STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup

### 1.1164 @emotion/sheet 1.0.1

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Here is a list of the main contributors to lm-sensors version 3.

* Frodo Looijaard
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-- vile: txmode file-encoding=utf-8
Upstream source https://invisible-island.net/ncurses/ncurses.html
This package is used for testing builds of ncurses.

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## Commands to generate dependency files 
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS) 
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS) 

## Flags for position independent code 
SHAREDLIBCFLAGS = -fPIC 
SHAREDLIBCXXFLAGS = -fPIC 
SHAREDLIBCPPFLAGS = -DPIC 

## Additional flags when building libraries and with threads 
THREADSCPFLAGS = -D_REENTRANT 
LIBCPPFLAGS = 

## Compiler switch to embed a runtime search path 
LD_RPATH= 
LD_RPATH_PRE=-Wl,-rpath, 

## Compiler switch to embed a library name 
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET)) 

## Shared library options 
LD_SOOPTIONS= -Wl,-Bsymbolic 

## Shared object suffix 
SO = so 

## Non-shared intermediate object suffix 
STATIC_O = ao 

## Compilation rules 
%.$(STATIC_O): $(srcdir)/%.c
Open Source Used In Cisco Telemetry Broker On Prem 1.2 10443

1.1185 path-exists 3.0.0
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--------------------------------------------------
For lib/py/compat/win32/stdint.h

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// Based on ISO/IEC 9899:TC2 Committee draft (May 6, 2005) WG14/N1124
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If you think you belong on this list, please let me know! --Benjamin

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For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in
non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does
and what the program that uses the Library does.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based
on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:
a) Accompany the combined library with a copy of the same work based on the Library, uncombined with any other library facilities. This must be distributed under the terms of the Sections above.

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Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

TERMS AND CONDITIONS

0. Definitions.

"This License" refers to version 3 of the GNU General Public License.

"Copyright" also means copyright-like laws that apply to other kinds of
works, such as semiconductor masks.

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The "System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major
Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

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++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for gdb-specific debug information and the 68k series machines, most of the preprocessing pass, and extensive changes in messages.c, input-file.c, write.c.
K. Richard Pixley maintained gas for a while, adding various enhancements and many bug fixes, including merging support for several processors, breaking gas up to handle multiple object file format backends (including heavy rewrite, testing, an integration of the coff and b.out backends), adding configuration including heavy testing and verification of cross assemblers and file splits and renaming, converted gas to strictly ansi C including full prototypes, added support for m680[34]0 & cpu32, considerable work on i960 including a coff port (including considerable amounts of reverse engineering), a sparc opcode file rewrite, decstation, rs6000, and hp300hpux host ports, updated "know" assertions and made them work, much other reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan. Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn’t been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola’s opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.
Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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   d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or
collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.
When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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   Version 2.1, February 1999

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library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.
Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)
These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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This distribution replaces my earlier GetDate distribution, which was
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The parser contained here will only parse absolute dates, if you want a
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=head1 NAME

Date::Language::Hungarian - Magyar format for Date::Format

=head1 SYNOPSIS

my $lang = Date::Language->new('Hungarian');
print $lang->time2str("%a %b %e %T %Y", time);

@lt = localtime(time);
print $lang->time2str($template, time);
print $lang->strftime($template, @lt);
print $lang->time2str($template, time, $zone);
print $lang->strftime($template, @lt, $zone);
print $lang->ctime(time);
print $lang->asctime(@lt);

print $lang->ctime(time, $zone);
print $lang->asctime(@lt, $zone);

See L<Date::Format>.

=head1 AUTHOR

Paula Goddard (paula -at- paulacska -dot- com)

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=encoding utf8

=head1 NAME

Date::Language::Bulgarian - localization for Date::Format

=head1 DESCRIPTION

This is Bulgarian localization for Date::Format.
It is important to note that this module source code is in utf8.
All strings which it outputs are in utf8, so it is safe to use it
currently only with English. You are left alone to try and convert
the output when using different Date::Language::* in the same application.
This should be addressed in the future.

=head1 SYNOPSIS

use strict;
use warnings;
use Date::Language;
local $/=$/;
my $template = '%a %b %e %T %Y (%Y-%m-%d %H:%M:%S);
my $time = 1290883821; # or just use time();
my $ltimes = localtime($time);
my %languages = qw(English GMT German EEST Bulgarian EET);
binmode(select, ':utf8');

foreach my $l(keys %languages){
  my $lang = Date::Language->new($l);
  my $zone = $languages{$l};
  print $/. "$l $zone"
  print $lang->time2str($template, $time);
  print $lang->time2str($template, $time, $zone);
  print $lang->strftime($template, $ltimes);
}

=head1 AUTHOR

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1.1250 go-runtime 1.17.2

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[This is the first released version of the library GPL. It is
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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

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Appendix: How to Apply These Terms to Your New Libraries

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1.1260 util-linux 2.36.1-8
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1 32 7679 7648 3.7M 8f8378c0-01
2 7680 16383 8704 4.3M 8f8378c0-02
5 7936 12799 4864 2.4M
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The “source code” for a work means the preferred form of the work for making modifications to it. "Object code” means any non-source form of a work.

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arch/arm64/boot/dts/socionext/uniphier-ld-.dts*
arch/arm64/boot/dts/zte/zx296718*.dts*
include/dt-bindings/clock/sun*.h
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1.1268 deprecated 1.2.13

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* /opt/cola/permits/1201203897_1631116519.19/0/deprecated-1-2-13-tar-gz/Deprecated-1.2.13/Deprecated.egg-info/PKG-INFO
* /opt/cola/permits/1201203897_1631116519.19/0/deprecated-1-2-13-tar-gz/Deprecated-1.2.13/PKG-INFO

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%global srcname Deprecated
%global pkgname deprecated

Name: python-%{pkgname}
Version: 1.2.13
Release: 1%{?dist}
Summary: Python decorator to deprecate old python classes, functions or methods
License: MIT
URL: https://github.com/tantale/%{pkgname}
Source0: % {pypi_source}
BuildArch: noarch

description
Python @deprecated decorator to deprecate old python classes, functions or methods.

package -n python3-%{pkgname}
Summary: %{summary}
BuildRequires: python3-devel
BuildRequires: python3-setuptools
%{?python_provide:%python_provide python3-%{pkgname}}

description -n python3-%{pkgname}
Python @deprecated decorator to deprecate old python classes, functions or methods.

prep
%autosetup -n % {srcname}-% {version}
rm -rf %{pkgname}.egg-info
%build
%py3_build

%install
%py3_install

%files -n python3-%{pkgname}
%license LICENSE.rst
%doc README.md
%{python3_sitelib}/%{pkgname}/
%{python3_sitelib}/%{srcname}-*.egg-info/

%changelog
* Fri Jul 26 2019 Petr Hracek <phracek@redhat.com> - 1.2.6-2
  - Fix python3_sitelib issue

* Fri Jul 26 2019 Petr Hracek <phracek@redhat.com> - 1.2.6-1
  - Initial package

Found in path(s):
* /opt/cola/permits/1201203897_1631116519.19/0/deprecated-1-2-13-tar-gz/Deprecated-1.2.13/python-deprecated.spec
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* Showing empathy towards other community members
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1.1270 opencontainers-image-spec 1.0.1

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1.1271 libusb 0.1.12-32

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1.1287 pretty 1.1.0

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Linus Torvalds

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John R. Hauser
2018 January 20

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1.1301 keyutils 1.5.9 9.2ubuntu2

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1.1302 django-appconf 1.0.5

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define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
; CHECK-LABEL: PR41535:
; CHECK: // %bb.0:
; CHECK-NEXT: ext v0.8b, v0.8b, v1.8b, #4
; CHECK-NEXT: mov v0.d[1], v0.d[0]
; CHECK-NEXT: ret
%cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
%cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
%r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
ret <4 x i32> %r
}
}

define void @autogen_SD4739(i8*) {
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
%L34 = load i8, i8* %0
%Cmp56 = icmp sgt i8 undef, %L34
br label %CF246
CF246: ; preds = %CF246, %BB
%SI163 = select i1 %Cmp56, i8 %L34, i8 undef
br i1 undef, label %CF246, label %CF248
CF248: ; preds = %CF248, %CF246
store i8 %SI163, i8* %0
br label %CF248
}

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define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
    ; CHECK-LABEL: test_sitofp_fixed:
    ; First, extend each i32 to i64
    ; CHECK-DAG: sshll.2d [[[BLOCK0_HI:v[0-9]+]], v0, #0
    ; CHECK-DAG: sshll.2d [[[BLOCK1_HI:v[0-9]+]], v1, #0
    ; CHECK-DAG: sshll.2d [[[BLOCK2_HI:v[0-9]+]], v2, #0
    ; CHECK-DAG: sshll.2d [[[BLOCK3_HI:v[0-9]+]], v3, #0
    ; Next, convert each to double.
    ; CHECK-DAG: scvtf.2d v0, [[[BLOCK0_LO]]]
    ; CHECK-DAG: scvtf.2d v1, [[[BLOCK0_HI]]]
    ; CHECK-DAG: scvtf.2d v2, [[[BLOCK1_LO]]]
    ; CHECK-DAG: scvtf.2d v3, [[[BLOCK1_HI]]]
    ; CHECK-DAG: scvtf.2d v4, [[[BLOCK2_LO]]]
    ; CHECK-DAG: scvtf.2d v5, [[[BLOCK2_HI]]]
    ; CHECK-DAG: scvtf.2d v6, [[[BLOCK3_LO]]]
    ; CHECK-DAG: scvtf.2d v7, [[[BLOCK3_HI]]]
    ; CHECK: ret
    %flt = sitofp <16 x i32> %in to <16 x double>
    %res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
    ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
    ; CHECK-LABEL: test_sitofp_fixed_shortish:
    ; CHECK-DAG: scvtf.2d v0, v0
    ; CHECK-DAG: scvtf.2d v1, v1
    ; CHECK: ret
    %flt = sitofp <4 x i64> %in to <4 x double>
    %res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
    ret <4 x double> %res
}

; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc -$mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s

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Check that we don't end up with the "LLVM ERROR: Cannot select" error.
Additionally ensure that the output code actually put fp128 values in SSE registers.

```assembly
; declare fp128 @llvm.fabs.f128(fp128)
; declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
    ; CHECK-LABEL: TestSelect:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   pushq %rbx
    ; CHECK-NEXT:   .cfi_def_cfa_offset 16
    ; CHECK-NEXT:   subq $32, %rsp
    ; CHECK-NEXT:   .cfi_def_cfa_offset 48
    ; CHECK-NEXT:   .cfi_offset %rbx, -16
    ; CHECK-NEXT:   movaps %xmm1, {
    ; CHECK-NEXT:     [[-0-9]+]](%r[{{[sb]}]}p) # 16-byte Spill
    ; CHECK-NEXT:   movaps %xmm0, (%rsp) # 16-byte Spill
    ; CHECK-NEXT:   callq __gttf2
    ; CHECK-NEXT:   movl %eax, %ebx
    ; CHECK-NEXT:   movaps (%rsp), %xmm0 # 16-byte Reload
    ; CHECK-NEXT:   movaps {
    ; CHECK-NEXT:     [[(-0)-9]+]](%r[{{[sb]}]}p), %xmm1 # 16-byte Reload
    ; CHECK-NEXT:   callq __subtf3
    ; CHECK-NEXT:   testl %ebx, %ebx
    ; CHECK-NEXT:   jg .LBB0_2
    ; CHECK-NEXT:   .LBB0_1:
    ; CHECK-NEXT:   xorps %xmm0, %xmm0
    ; CHECK-NEXT:   .LBB0_2:  
    ; CHECK-NEXT:   addq $32, %rsp
    ; CHECK-NEXT:   .cfi_def_cfa_offset 16
    ; CHECK-NEXT:   popq %rbx
    ; CHECK-NEXT:   .cfi_def_cfa_offset 8
    ; CHECK-NEXT:   retq
    ; %cmp = fcmp ogt fp128 %a, %b
    ; %sub = fsub fp128 %a, %b
    ; %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
    ; ret fp128 %res
}

define fp128 @TestFabs(fp128 %a) {
    ; CHECK-LABEL: TestFabs:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   andps {
    ; CHECK-NEXT:     [[*]](%rip), %xmm0
    ; CHECK-NEXT:   retq
    ; %res = call fp128 @llvm.fabs.f128(fp128 %a)
    ; ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
```
define fp128 @TestCopysign(fp128 %a, fp128 %b) {
    .check-label: TestCopysign:
    .check:    # %bb.0:
    .check-next:    andps {{.*}}(%rip), %xmm1
    .check-next:    andps {{.*}}(%rip), %xmm0
    .check-next:    orps %xmm1, %xmm0
    .check-next:    retq
    %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
    ret fp128 %res
}
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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
  ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
  ret void
}

define internal void @innerNoAttribute() {
  ret void
}
define void @outerNoAttribute() {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeSmall("min-legal-vector-width"="128") {
    call void @innerLarge()
    ret void
}

define void @outerConflictingAttributeLarge("min-legal-vector-width"="512") {
    call void @innerSmall()
    ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute("min-legal-vector-width"="128") {
    call void @innerNoAttribute()
    ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }

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; RUN: opt -consthoist -S -o %s | FileCheck %s
target triple = "i686-unknown-linux-gnu"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG:  %[[C1:const[0-9]?>] = bitcast i32 805874720 to i32
; CHECK-DAG:  %[[C2:const[0-9]?>] = bitcast i32 805874688 to i32
; CHECK-DAG:  %[[C3:const[0-9]?>] = bitcast i32 805873720 to i32
; CHECK-DAG:  %[[C4:const[0-9]?>] = bitcast i32 805873688 to i32
; CHECK:  %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT:  %1 = load volatile i8, i8* %0
; CHECK-NEXT:  %[[M1:const_mat[0-9]?>] = add i32 %[[C2]], 4
; CHECK-NEXT:  %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT:  %3 = load volatile i8, i8* %2
; CHECK-NEXT:  %[[M2:const_mat[0-9]?>] = add i32 %[[C2]], 31
; CHECK-NEXT:  %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT:  %5 = load volatile i8, i8* %4
; CHECK-NEXT:  %[[M3:const_mat[0-9]?>] = add i32 %[[C1]], 7
; CHECK-NEXT:  %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT:  %7 = load volatile i8, i8* %6
; CHECK-NEXT:  %8 = inttoptr i32 %[[C1]], 7
; CHECK-NEXT:  %9 = load volatile i8, i8* %8
; CHECK-NEXT:  %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT:  store i8 %9, i8* %10
; CHECK-NEXT:  %[[M4:const_mat[0-9]?>] = add i32 %[[C4]], 31
; CHECK-NEXT:  %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT:  store i8 %7, i8* %11
; CHECK-NEXT:  %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT:  store i8 %5, i8* %12
; CHECK-NEXT:  %[[M5:const_mat[0-9]?>] = add i32 %[[C3]], 7
; CHECK-NEXT:  %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT:  store i8 %3, i8* %13
; CHECK-NEXT:  %[[M6:const_mat[0-9]?>] = add i32 %[[C1]], 80
define void @foo_i8() {
  entry:
    %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
    %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
    %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
    %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
    %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
  store i8 %4, i8* inttoptro(i32 805873688 to i8*)
  store i8 %3, i8* inttoptro(i32 805873719 to i8*)
  store i8 %2, i8* inttoptro(i32 805873720 to i8*)
  store i8 %1, i8* inttoptro(i32 805873727 to i8*)
  store i8* inttoptr(i32 805874800 to i8*), i8** @goo
  ret void
}

define void @foo_i16() {
  entry:
    %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*)
    %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*)
    %2 = load volatile i16, i16* inttoptr (i32 805874719 to i16*)
    %3 = load volatile i16, i16* inttoptr (i32 805874720 to i16*)
    %4 = load volatile i16, i16* inttoptr (i32 805874727 to i16*)
  store i16 %4, i16* inttoptro(i32 805873688 to i16*)
  store i16 %3, i16* inttoptro(i32 805873719 to i16*)
  store i16 %2, i16* inttoptro(i32 805873720 to i16*)
  store i16 %1, i16* inttoptro(i32 805873727 to i16*)
  store i16* inttoptr(i32 805874800 to i16*), i16** @goo
  ret void
}
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874816 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %[[M1:const_mat[0-9]??]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %[[M2:const_mat[0-9]??]] = add i32 %[[C2]], 124
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*
; CHECK-NEXT: %5 = load volatile i32, i32* %4, align 4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i32*
; CHECK-NEXT: %7 = load volatile i32, i32* %6, align 4
; CHECK-NEXT: %[[M3:const_mat[0-9]??]] = add i32 %[[C1]], 8
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i32*
; CHECK-NEXT: %9 = load volatile i32, i32* %8, align 4
; CHECK-NEXT: %[[M4:const_mat[0-9]??]] = add i32 %[[C1]], 12
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i32*
; CHECK-NEXT: %11 = load volatile i32, i32* %10, align 4

define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

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This should promote

```
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
    CHECK-LABEL: define {[^@]+}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
    CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
    CHECK-NEXT: bb:
    CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
    CHECK-NEXT: ret void
}
```

```
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
    CHECK-LABEL: define {[^@]+}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
    CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
    CHECK-NEXT: bb:
    CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
    CHECK-NEXT: %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    CHECK-NEXT: store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    CHECK-NEXT: ret void
}
```

```
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
    CHECK-LABEL: define {[^@]+}@avx512_legal512_prefer512_call_avx512_legal512_prefer512
    CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
    CHECK-NEXT: bb:
    CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
    CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
        CHECK-LABEL: define {[^@]+}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
        CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])
        CHECK-NEXT: bb:
        CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
        CHECK-NEXT: ret void
    }
```
ret void
;

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{[^@]+}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{[^@]+}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

define void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1_VAL:%.*]]) #0 {
; CHECK-LABEL: define {{\[^@\]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64>* [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
; CHECK-LABEL: define {{\[^@\]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64>* [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
; CHECK-LABEL: define {{\[^@\]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1_VAL:%.*]])
; CHECK-NEXT: bb:
; CHECK-NEXT: store <8 x i64>* [[ARG1_VAL]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void

; This should promote

bb:  
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer512
; CHECK-SAME: (<8 x i64>* [[ARG[:]*]])
; CHECK-NEXT: bb:  
; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP3]]
; CHECK-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* [[TMP2]], <8 x i64> [[TMP_VAL]])
; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT: ret void
;  
bb:  
%tmp = load <8 x i64>, align 32
%tmp2 = alloc <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:.*]], <8 x i64>* readonly [[ARG1:.*]])
; CHECK-NEXT: bb:  
; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]]
; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
; CHECK-NEXT: ret void
;  
bb:  
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
 ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
 ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
 ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
 ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
 ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
 ; CHECK-NEXT: call fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP]])
 ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
 ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
 ; CHECK-NEXT: ret void
 }

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512Legal256Prefer256CallAvx512Legal512Prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {
 ; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_over preferable
 ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 32
 ; CHECK-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]]
 ; CHECK-NEXT: ret void
 }

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
 ; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_over preferable
 ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
 ; CHECK-NEXT: bb:
 ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32

define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
  bb:
    store <8 x i64> %arg1, <8 x i64>* %arg
    ret void
}
define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
  bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
  bb:
    store <8 x i64> %arg1, <8 x i64>* %arg
    ret void
}
define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
  bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %X, i32* %Y) #2 {
    ; CHECK-LABEL: define
    {([^@]+)}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
    ; CHECK-NEXT:    [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
    ; CHECK-NEXT:    ret i32 [[C]]
    ;
    %A = load i32, i32* %X
    %B = load i32, i32* %Y
    %C = add i32 %A, %B
    ret i32 %C
}

define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
    ; CHECK-LABEL: define [[[^@]+]]@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32* [[B:%.*]])
    ; CHECK-NEXT:    [[A:%.*]] = alloca i32
    ; CHECK-NEXT:    store i32 1, i32* [[A]]
    ; CHECK-NEXT:    [[A_VAL:%.*]] = load i32, i32* [[A]]
    ; CHECK-NEXT:    [[B:%.*]] = alloca i32
    ; CHECK-NEXT:    store i32 1, i32* [[B]]
    ; CHECK-NEXT:    [[B_VAL:%.*]] = load i32, i32* [[B]]
    ; CHECK-NEXT:    [[C:%.*]] = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
    ; CHECK-NEXT:    ret i32 [[C]]
    ;
    %A = alloca i32
    store i32 1, i32* %A
    %C = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
    ret i32 %C
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %X, i32* %Y) #2 {
    ; CHECK-LABEL: define
    {([^@]+)}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256
    ; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
    ; CHECK-NEXT:    [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
    ;
define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %B) #2 {
  %A = alloca i32
  store i32 1, i32* %A
  %A = alloca i32
  store i32 1, i32* %A
  %C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 %A, i32 %B)
  ret i32 %C
}

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for vpmaddwd+vpaddd
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cascadelake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cooperlake | FileCheck %s --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=cannonlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-client | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=icelake-server | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=-avx512vnni -mcpu=tigerlake | FileCheck %s --check-prefixes=CHECK,CHECK-VBMI

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
 transparency
 ; CHECK-LABEL: add256:
 ; CHECK: # %bb.0:
 ; CHECK-NEXT: vmodvdq (%rdi), %ymm0
 ; CHECK-NEXT: vmodvdq 32(%rdi), %ymm1
 ; CHECK-NEXT: vpaddd 32(%rsi), %ymm1, %ymm1
 ; CHECK-NEXT: vpaddd (%rsi), %ymm0, %ymm0
 ; CHECK-NEXT: vmodvdq %ymm0, (%rdx)
 ; CHECK-NEXT: vmodvdq %ymm1, 32(%rdx)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 ; %d = load <16 x i32>, <16 x i32>* %a
 ; %e = load <16 x i32>, <16 x i32>* %b
 ; %f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}
define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: add512:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpaddd (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}
define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
; CHECK-LABEL: avg_v64i8_256:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpavgb (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpavgb 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vmovdqu %ymm1, (%rax)
; CHECK-NEXT: vmovdqu %ymm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

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define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}
define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vectorwidth"="256" {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32
12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32
13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>

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define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="512" {
; CHECK-LABEL: pmaddwd_32_512:
; CHECK:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: psubus_64i8_max_256:
; CHECK:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %y.ptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}
define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %y.ptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: psubus_64i8_max_512:
    ; CHECK:      # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %x = load <64 x i8>, <64 x i8>* %xptr
    %y = load <64 x i8>, <64 x i8>* %y.ptr
    %cmp = icmp ult <64 x i8> %x, %y
    %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
    %res = sub <64 x i8> %max, %y
    store <64 x i8> %res, <64 x i8>* %zptr
    ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: _Z9test_charPcS_i_256:
    ; CHECK:      # %bb.0: # %entry
    ; CHECK-NEXT: movl %edx, %eax
    ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT: xorl %ecx, %ecx
    ; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
    ; CHECK-NEXT: .p2align 4, 0x90
    ; CHECK-NEXT: .LBB8_1: # %vector.body
    ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT: vpmovxsxbw 16(%rdi,%rcx), %ymm3
    ; CHECK-NEXT: vpmovxsxbw (%rdi,%rcx), %ymm4
    ; CHECK-NEXT: vpmovxsxbw 16(%rsi,%rcx), %ymm5
    ; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT: vpaddd %ymm2, %ymm3, %ymm2
    ; CHECK-NEXT: vpmovxsxbw (%rsi,%rcx), %ymm3
    ; CHECK-NEXT: vpmaddwd %ymm4, %ymm3, %ymm3
    ; CHECK-NEXT: vpaddd %ymm1, %ymm3, %ymm1
    ; CHECK-NEXT: addq $32, %rcx
    ; CHECK-NEXT: cmpq %rcx, %rax
    ; CHECK-NEXT: jne .LBB8_1
    ; CHECK-NEXT: # %bb.2: # %middle.block
    ; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm1
    ; CHECK-NEXT: vpaddd %ymm0, %ymm2, %ymm0
    ; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm0
    ; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
    ; CHECK-NEXT: vpaddd %ymm1, %xmm0, %xmm0

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; CHECK-NEXT:  vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:  vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpshufd {{.*#+}} xmm1 = xmm0[1,2,3]
; CHECK-NEXT:  vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq

entry:
  %3 = zext i32 %2 to i64
br label %vector.body

vector.body:
  %index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
  %vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
  %4 = getelementptr inbounds i8, i8* i8, i64 %index
  %5 = bitcast i8* %4 to <32 x i8>*
  %wide.load = load <32 x i8>, <32 x i8>* %5, align 1
  %6 = sext <32 x i8> %wide.load to <32 x i32>
  %7 = getelementptr inbounds i8, i8* i8, i64 %index
  %8 = bitcast i8* %7 to <32 x i8>*
  %wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
  %9 = sext <32 x i8> %wide.load14 to <32 x i32>
  %10 = mul nsw <32 x i32> %9, %6
  %11 = add nsw <32 x i32> %10, %vec.phi
  %index.next = add i64 %index, 32
  %12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
  %rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
  %rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde...
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13
}

define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
; CHECK-LABEL: _Z9test_charPcS_i_512:
; CHECK: # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB9_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %zmm2
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %zmm3
; CHECK-NEXT: vpmaddwd %zmm2, %zmm3, %zmm2
; CHECK-NEXT: vpadd %zmm1, %zmm2, %zmm1
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
; CHECK-NEXT: jne .LBB9_1
; CHECK-NEXT: vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT: vmovd %zmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%e = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %e to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> %i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> %i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde

%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> %i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> %i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> %i32 1, i32 2, i32 3, i32 undef, i32 unde
%bin.rdx20 = add <32 x i32> %bin.rdx, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
ret i32 %13

@a = global [1024 x i8] zeroinitializer, align 16
@b = global [1024 x i8] zeroinitializer, align 16

define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
; CHECK-LABEL: sad_16i8_256:
; CHECK: # %bb.0: # %entry
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB10_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vmovdqu a+1024(%rax), %ymm2
; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm2, %ymm2
; CHECK-NEXT: vpaddd %ymm1, %ymm2, %ymm1
; CHECK-NEXT: addq $4, %rax
; CHECK-NEXT: jne .LBB10_1
; CHECK-NEXT: # %bb.2: # %middle.block
; CHECK-NEXT: vpaddd %ymm0, %ymm1, %ymm0
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT: vpaddd %xmm1, %ymm0, %xmm0
; CHECK-NEXT: vpshufd [{...*}] xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpaddd %ymm1, %ymm0, %ymm0
; CHECK-NEXT: vpshufd [{...*}] xmm1 = xmm0[1,1,2,3]
; CHECK-NEXT: vpaddd %xmm1, %ymm0, %ymm0
; CHECK-NEXT: vmovd %xmm0, %eax
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
entry:
  br label %vector.body

vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
  %2 = zext <16 x i8> %wide.load to <16 x i32>
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = zext <16 x i8> %wide.load1 to <16 x i32>
  %6 = sub nsw <16 x i32> %2, %5
  %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
  %8 = sub nsw <16 x i32> zeroinitialize, %6
  %9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
  %10 = add nsw <16 x i32> %9, %vec.phi
  %index.next = add i64 %index, 4
  %11 = icmp eq i64 %index.next, 1024
  br i1 %11, label %middle.block, label %vector.body
middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> undef, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, <16 x i32> undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
; CHECK-LABEL: sad_16i8_512:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT:  movq $-1024, %rax # imm = 0xFC00
; CHECK-NEXT:  .p2align 4, 0x90
; CHECK-NEXT:  .LBB11_1: # %vector.body
; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm1
; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpsadbw b+1024(%rax), %zmm1, %zmm1
; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vpaddd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    vmovd %zmm0, %eax
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
; entry:
entry:
br label %vector.body
vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitialize, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, %i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitialize, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12

define void @sbtol6f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbtol6f32_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw S8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtq2ps %ymm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm1
; CHECK-NEXT:  vcvtqd2ps %ymm1, %ymm1
; CHECK-NEXT:  vmovaps %ymm1, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {;
 ; CHECK-LABEL: sbto16f32_512:
 ; CHECK:    # %bb.0:
 ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
 ; CHECK-NEXT:  vpmovm2d %k0, %zmm0
 ; CHECK-NEXT:  vcvtqd2ps %zmm0, %zmm0
 ; CHECK-NEXT:  vzeroupper
 ; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {;
 ; CHECK-LABEL: sbto16f64_256:
 ; CHECK:    # %bb.0:
 ; CHECK-NEXT:  vpmovw2m %ymm0, %k0
 ; CHECK-NEXT:  vpmovm2d %k0, %zmm0
 ; CHECK-NEXT:  vcvtqd2pd %zmm0, %zmm0
 ; CHECK-NEXT:  vzeroupper
 ; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @sbtol64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}

define void @ubtol64f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubtol64f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}
define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: ubto16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:    vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:    vpsrld $31, %ymm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm1
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm0
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0
    ; CHECK-NEXT:    vpmovm2d %k0, %ymm2
    ; CHECK-NEXT:    vpsrld $31, %ymm2, %ymm2
    ; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0
    ; CHECK-NEXT:    vextracti128 $1, %ymm2, %xmm2
    ; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymm2
    ; CHECK-NEXT:    vmovaps %ymm2, 32(%rdi)
    ; CHECK-NEXT:    vmovaps %ymm3, 64(%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: ubto16f64_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %ymm0
    ; CHECK-NEXT:    vpsrld $31, %ymm0, %zmm0
    ; CHECK-NEXT:    vcvtdq2pd %zmm0, %zmm0
    ; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
    ; CHECK-NEXT: vcvtdq2pd %zmm0, %zmm1
    ; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
    ; CHECK-NEXT: vcvtdq2pd %zmm0, %zmm0
    ; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}
define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: test_16f32toub_256:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:   vcvttps2dq (%rdi), %ymm1
  ; CHECK-NEXT:   vpslld $31, %ymm1, %ymm1
  ; CHECK-NEXT:   vpmovd2m %ymm1, %k0
  ; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1
  ; CHECK-NEXT:   vpslld $31, %ymm1, %ymm1
  ; CHECK-NEXT:   vpmovd2m %ymm1, %k1
  ; CHECK-NEXT:   kunpckbw %k0, %k1, %k1
  ; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 {%k1} {z}
  ; CHECK-NEXT:   retq
  %a = load <16 x float>, <16 x float>* %ptr
  %mask = fptoui <16 x float> %a to <16 x i1>
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
  ret <16 x i16> %select
}

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: test_16f32toub_512:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:   vcvttps2dq (%rdi), %zmm1
  ; CHECK-NEXT:   vpslld $31, %zmm1, %zmm1
  ; CHECK-NEXT:   vpmovd2m %zmm1, %k1
  ; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 {%k1} {z}
  ; CHECK-NEXT:   retq
  %a = load <16 x float>, <16 x float>* %ptr
  %mask = fptoui <16 x float> %a to <16 x i1>
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
  ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: test_16f32tosb_256:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:   vcvttps2dq (%rdi), %zmm1
  ; CHECK-NEXT:   vpslld $31, %zmm1, %zmm1
  %a = load <16 x float>, <16 x float>* %ptr
  %mask = fptoui <16 x float> %a to <16 x i1>
  %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
  ret <16 x i16> %select
}
; CHECK-NEXT:  vpmovd2m %ymm1, %k0
; CHECK-NEXT:  vcvttps2dq 32(%rdi), %ymm1
; CHECK-NEXT:  vpmovd2m %ymm1, %k1
; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 [%k1] [z]
; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1>, <16 x i16> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
 ; CHECK-LABEL:  test_16f32tosb_512:
 ; CHECK:       # %bb.0:
 ; CHECK-NEXT:  vcvttps2dq (%rdi), %zmm1
 ; CHECK-NEXT:  vpmovd2m %zmm1, %k1
 ; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 [%k1] [z]
 ; CHECK-NEXT:  retq
%a = load <16 x float>, <16 x float>* %ptr
%mask = fptosi <16 x float> %a to <16 x i1>
%select = select <16 x i1>, <16 x i16> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret <16 x i16> %select
}
define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
 ; CHECK-AVX512-LABEL:  mul256:
 ; CHECK-AVX512:       # %bb.0:
 ; CHECK-AVX512-NEXT:  vmovdqa (%rdi), %ymm0
 ; CHECK-AVX512-NEXT:  vmovdqa 32(%rdi), %ymm1
 ; CHECK-AVX512-NEXT:  vmovdqa (%rsi), %ymm2
 ; CHECK-AVX512-NEXT:  vmovdqa 32(%rsi), %ymm3
 ; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} ymm4 = ymm3[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
 ; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} ymm5 = ymm1[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
 ; CHECK-AVX512-NEXT:  vpmullw %ymm4, %ymm5, %ymm5
 ; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm4, %ymm4
 ; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} ymm3 = ymm3[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
 ; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} ymm1 = ymm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
 ; CHECK-AVX512-NEXT:  vpmullw %ymm3, %ymm1, %ymm1
 ; CHECK-AVX512-NEXT:  vpand %ymm5, %ymm1, %ymm1
 ; CHECK-AVX512-NEXT:  vpackuswb %ymm4, %ymm1, %ymm1
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
    CHECK-AVX512-LABEL: mul512:
    CHECK-AVX512:     # %bb.0:
    CHECK-AVX512-NEXT: vmovdqa64 (%rdi), %zmm0
    CHECK-AVX512-NEXT: vmovdqa64 (%rsi), %zmm1
    CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} %zmm2 = zmm1[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
    CHECK-AVX512-NEXT: vpunpckhbw {{.*#+}} %zmm3 = zmm0[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
    CHECK-AVX512-NEXT: vpmullw %zmm2, %zmm3, %zmm2
    CHECK-AVX512-NEXT: vpandq %zmm3, %zmm2
    CHECK-AVX512-NEXT: vpackuswb %zmm2, %zmm0, %zmm0
    CHECK-AVX512-NEXT: vmovdqa64 (%rdx)
    CHECK-AVX512-NEXT: vzeroupper
    CHECK-AVX512-NEXT: retq
    
    CHECK-VBMI-LABEL: mul512:
    CHECK-VBMI:     # %bb.0:
    CHECK-VBMI-NEXT: vmovdqa64 (%rdi), %zmm0
    CHECK-VBMI-NEXT: vmovdqa64 (%rsi), %zmm1
    CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} %zmm2 = zmm1[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
    CHECK-VBMI-NEXT: vpunpckhbw {{.*#+}} %zmm3 = zmm0[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
    CHECK-VBMI-NEXT: vpackuswb %zmm2, %zmm0, %zmm0
    CHECK-VBMI-NEXT: vmovdqa64 (%rdx)
    CHECK-VBMI-NEXT: vzeroupper
    CHECK-VBMI-NEXT: retq
    
    Open Source Used In Cisco Telemetry Broker On Prem 1.2 11291
zmm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT: vpminuw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} zmm1 =
  zmm1[0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,20,20,21,21,22,22,23,23,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT: vpunpcklbw {{.*#+}} zmm0 =
  zmm0[0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,20,20,21,21,22,22,23,23,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
; CHECK-VBMI-NEXT: vpmullw %zmm1, %zmm0, %zmm0
; CHECK-VBMI-NEXT: vmovdqa64 {{.*#+}} zmm1 =
  [0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72,74,76,78,80,82,84,86,88,90,92,94,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72,74,76,78,80,82,84,86,88,90,92,94,96,100,102,104,106,108,110,112,114,116,118,120,122,124,126]
; CHECK-VBMI-NEXT: vpmovdqa64 %zmm1, (%rdx)
; CHECK-VBMI-NEXT: vzeroupper
; CHECK-VBMI-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {  
; CHECK-LABEL: mload_v4i32:  
; CHECK:   # %bb.0:  
; CHECK-NEXT: vptestnmld %xmm0, %xmm0, %k1  
; CHECK-NEXT: vpmovq (%rdi), %ymm0  
; CHECK-NEXT: vpmovq 32(%rdi), %ymm1  
; CHECK-NEXT: vpmovq 64(%rdi), %ymm2  
; CHECK-NEXT: vpmovq 96(%rdi), %ymm3  
; CHECK-NEXT: vpmovq %ymm0, %ymm0  
; CHECK-NEXT: vpmovq %ymm1, %ymm1  
}
define <16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i64_v16i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:  vmovdqa 64(%rdi), %ymm2
    ; CHECK-NEXT:  vmovdqa 96(%rdi), %ymm3
    ; CHECK-NEXT:  vpmovqb %ymm3, %xmm3
    ; CHECK-NEXT:  vpmovqb %ymm2, %xmm2
    ; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm2 = xmm2[0],xmm3[0],xmm2[1],xmm3[1]
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <16 x i64>, <16 x i64>* %x
    %b = trunc <16 x i64> %a to <16 x i32>
    ret <16 x i8> %b
}

define <16 x i8> @trunc_v16i32_v16i8(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i8:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpmovdb %ymm1, %xmm1
    ; CHECK-NEXT:  vpmovdb %ymm0, %xmm0
    ; CHECK-NEXT:  vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <16 x i32>, <16 x i32>* %x
    %b = trunc <16 x i32> %a to <16 x i8>
    ret <16 x i8> %b
}

define <8 x i8> @trunc_v8i64_v8i8(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i8:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpmovdb %ymm1, %xmm1
    ; CHECK-NEXT:  vpmovdb %ymm0, %xmm0
    ; CHECK-NEXT:  vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = trunc <8 x i64> %a to <8 x i8>
    ret <8 x i8> %b
}
define <8 x i16> @trunc_v8i64_v8i16(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: trunc_v8i64_v8i16:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
  ; CHECK-NEXT:  vpmovqb %ymm1, %xmm1
  ; CHECK-NEXT:  vpmovqb %ymm0, %xmm0
  ; CHECK-NEXT:  vpunpckldq {{.*#+}} xmm0 = xmm0[0],xmm1[0],xmm0[1],xmm1[1]
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %a = load <8 x i64>, <8 x i64>* %x
  %b = trunc <8 x i64> %a to <8 x i8>
  ret <8 x i8> %b
}

define <8 x i16> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  vpsrlq $48, 32(%rdi), %ymm2
  ; CHECK-NEXT:  vpsrlq $48, (%rdi), %ymm0
  ; CHECK-NEXT:  vmovdqa %ymm0, %ymm1
  ; CHECK-NEXT:  vpmovqw %ymm1, %xmm1
  ; CHECK-NEXT:  vpmovqw %ymm0, %xmm0
  ; CHECK-NEXT:  vpunpcklqdq {{.*#+}} xmm0 = xmm0[0],xmm1[0]
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %a = load <8 x i64>, <8 x i64>* %x
  %b = trunc <8 x i64> %a to <8 x i16>
  ret <8 x i16> %b
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  vpsrlq %48, 32(%rdi), %ymm2
  ; CHECK-NEXT:  vpsrlq %48, (%rdi), %ymm0
  ; CHECK-NEXT:  vmovdqa %ymm0, %ymm1
  ; CHECK-NEXT:  vpmovqw %ymm1, %xmm1
  ; CHECK-NEXT:  vpmovqw %ymm0, %xmm0
  ; CHECK-NEXT:  vpmovq %ymm0, %ymm1
  ; CHECK-NEXT:  vpermi2w %ymm1, %ymm0
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %a = load <16 x i64>, <16 x i64>* %x
  %b = lshr <16 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
  %c = trunc <16 x i64> %a to <16 x i32>
  ret <16 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
  ; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  vmovdqa (%rdi), %ymm1
  ; CHECK-NEXT:  vmovdqa %ymm1, %ymm0
  ; CHECK-NEXT:  vpmovq %ymm0, %ymm1
  ; CHECK-NEXT:  vpermi2w 32(%rdi), %ymm1, %ymm0
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %a = load <16 x i64>, <16 x i64>* %x
  %b = lshr <16 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
  %c = trunc <16 x i64> %a to <16 x i32>
  ret <16 x i32> %c
}
define <32 x i8> @trunc_v32i16_v32i8_zeroes(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_zeroes:
    ; CHECK-AVX512:     # %bb.0:
    ; CHECK-AVX512-NEXT: vpsrlw $8, 32(%rdi), %ymm0
    ; CHECK-AVX512-NEXT: vpsrlw $8, (%rdi), %ymm1
    ; CHECK-AVX512-NEXT: vpackuswb %ymm0, %ymm1, %ymm0
    ; CHECK-AVX512-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-AVX512-NEXT: retq
}
}

define <8 x i32> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i32_sign:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vpsraq $48, 32(%rdi), %ymm0
    ; CHECK-NEXT: vpsraq $48, (%rdi), %ymm1
    ; CHECK-NEXT: vpmovqd %ymm0, %xmm0
    ; CHECK-NEXT: vpmovqd %ymm1, %xmm1
    ; CHECK-NEXT: vinserti128 $1, %xmm0, %ymm1, %ymm0
    ; CHECK-NEXT: retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = ashr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
    %c = trunc <8 x i64> %b to <8 x i32>
    ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_sign(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_sign:
    ; CHECK-NEXT: retq
    %a = load <16 x i32>, <16 x i32>* %x
    %b = lshr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>
    %c = trunc <16 x i32> %b to <16 x i16>
    ret <16 x i16> %c
}
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm1
; CHECK-NEXT: vmovdqa {{.*+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT: vperm2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %x
%b = ashr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>
%c = trunc <16 x i32> %b to <16 x i16>
ret <16 x i16> %c
}
define <32 x i8> @trunc_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_sign:
; CHECK-AVX512: # %bb.0:
; CHECK-AVX512-NEXT: vpsraw $8, 32(%rdi), %ymm0
; CHECK-AVX512-NEXT: vpsraw $8, (%rdi), %ymm1
; CHECK-AVX512-NEXT: vpacksswb %ymm0, 0, %ymm1, %ymm0
; CHECK-AVX512-NEXT: vpermq {{.*+}} ymm0 = ymm0[0,2,1,3]
; CHECK-AVX512-NEXT: retq
;
; CHECK-VBMI-LABEL: trunc_v32i16_v32i8_sign:
; CHECK-VBMI: # %bb.0:
; CHECK-VBMI-NEXT: vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT: vmovdqa {{.*+}} ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT: vpermq {{.*+}} ymm0 = ymm0[0,2,1,3]
; CHECK-VBMI-NEXT: retq
%
%a = load <32 x i16>, <32 x i16>* %x
%b = ashr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
}
define void @zext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL: zext_v16i8_v16i64:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovzxbw {{.*+}} ymm1 = xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
; CHECK-NEXT: vpshufd {{.*+}} xmm2 = xmm1[2,3,0,1]
; CHECK-NEXT: vpmovzxqw {{.*+}} ymm2 = xmm2[0],zero,zero,xmm2[1],zero,zero,xmm2[2],zero,zero,xmm2[3],zero,zero,zero
; CHECK-NEXT: vextracti128 $1, %ymm1, %ymm1
; CHECK-NEXT: vpshufd {{.*+}} xmm3 = xmm1[2,3,0,1]
; CHECK-NEXT: vpmovzxqw {{.*+}} ymm3 =
define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {  
; CHECK-LABEL: sext_v16i8_v16i64:  
; CHECK:      # %bb.0:  
; CHECK-NEXT: vpmovsxwb %xmm0, %ymm0  
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]  
; CHECK-NEXT: vpmovsxwq %xmm1, %ymm1  
; CHECK-NEXT: vextACTI128 $1, %ymm0, %xmm2  
; CHECK-NEXT: vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]  
; CHECK-NEXT: vpmovsxwq %xmm3, %ymm3  
; CHECK-NEXT: vpmovsxwq %xmm0, %ymm0  
; CHECK-NEXT: vpmovsxwq %xmm2, %ymm2  
; CHECK-NEXT: vpmovsxwq %ymm0, %ymm0  
; CHECK-NEXT: vmovdqa %ymm2, (%rdi)  
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)  
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)  
; CHECK-NEXT: vmovdqa %ymm1, 64(%rdi)  
; CHECK-NEXT: vzeroupper  
; CHECK-NEXT: retq  

%a = sext <16 x i8> %x to <16 x i64>  
store <16 x i64> %a, <16 x i64>* %y  
ret void  
}  

define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {  
; CHECK-LABEL: vselect_split_v8i16_setcc:  
; CHECK:      # %bb.0:  
; CHECK-NEXT: vmovdqa64 (%rsi), %ymm3  
; CHECK-NEXT: vpmpeqw %ymm1, %ymm0, %k1  
; CHECK-NEXT: vextrACTI128 $4, %k1, %k2  
; CHECK-NEXT: vpmpeqw %xmm1, %xmm0, %k1  
; CHECK-NEXT: vshiftlb $4, %k1, %k2  
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 [%k2]
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i16> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v8i32_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqd %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrb %ymm1, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i32> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
; CHECK-LABEL: vselect_split_v16i8_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqb %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrw %xmm1, %k1, %k2
; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {  
; CHECK-LABEL: vselect_split_v16i16_setcc:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqw %ymm1, %ymm0, %k1
; CHECK-NEXT: kshiftrw $8, %k1, %k2
; CHECK-NEXT: vmovdqa32 32(%rdi), %ymm3 {%k2}
; CHECK-NEXT: vmovdqa32 (%rdi), %ymm2 {%k1}
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <16 x i32>, <16 x i32>* %p
%y = load <16 x i32>, <16 x i32>* %q
%a = icmp eq <16 x i8> %s, %t
%b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
store <16 x i32> %b, <16 x i32>* %r
ret void
}

define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {  
; CHECK-LABEL: trunc_packus_v16i32_v16i8:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vpackusdw 32(%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
; CHECK-NEXT: vpmovuswb %ymm0, %xmm0
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <16 x i32>, <16 x i32>* %p
%b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>  
%c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>  
%d = icmp sgt <16 x i32> %c, zeroinitializer
%e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
%f = trunc <16 x i32> %e to <16 x i8>
ret <16 x i8> %f
}

define void @trunc_packus_v16i32_v16i8_store(<16 x i32>* %p, <16 x i8>* %q) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: trunc_packus_v16i32_v16i8_store:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT:  vpackusdw 32(%rdi), %ymm0, %ymm0
  ; CHECK-NEXT:  vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
  ; CHECK-NEXT:  vpmovuswb %ymm0, (%rsi)
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %a = load <16 x i32>, <16 x i32>* %p
  %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
  %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> %c
  %d = icmp sgt <16 x i32> %c, zeroinitializer
  %e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeroinitializer
  %f = trunc <16 x i32> %e to <16 x i8>
  store <16 x i8> %f, <16 x i8>* %q
  ret void
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: v64i1_argument_return:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  retq
  ret <64 x i1> %x
}

define void @v64i1_shuffle(<64 x i8>* %x, <64 x i8>* %y) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: v64i1_shuffle:
  ; CHECK:       # %bb.0: # %entry
  ; CHECK-NEXT:  vmovdqa (%rdi), %ymm1
  ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm0
  ; CHECK-NEXT:  vptestnmb %ymm1, %ymm1, %k0
  ; CHECK-NEXT:  kshiftrd $1, %k0, %k1
  ; CHECK-NEXT:  movq $-3, %rax
  ; CHECK-NEXT:  kmovq %rax, %k2
  ; CHECK-NEXT:  kandq %k2, %k1
  ; CHECK-NEXT:  kshiftlq $63, %k0, %k2
  ; CHECK-NEXT:  kshiftrq $62, %k2, %k2
  ; CHECK-NEXT:  korq %k2, %k1, %k1
  ; CHECK-NEXT:  movq $-5, %rax
  ; CHECK-NEXT:  kmovq %rax, %k2
  ; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $17, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $47, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-131073, %rax # imm = 0xFFFFDFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $16, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $46, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-262145, %rax # imm = 0xFFFFBFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $19, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $45, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-524289, %rax # imm = 0xFFFF7FFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $18, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $44, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-1048577, %rax # imm = 0xFFEFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $21, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $43, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-2097153, %rax # imm = 0xFFDFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $20, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $42, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-4194305, %rax # imm = 0xFFFBBBBB
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $23, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $41, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-8388609, %rax # imm = 0xFF7FFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $22, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $40, %k2, %k2
; CHECK-NEXT:    korrq %k2, %k1, %k1
; CHECK-NEXT:    movq $-16777217, %rax # imm = 0xFEFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $25, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $39, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-33554433, %rax # imm = 0xFDFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $24, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $38, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-67108865, %rax # imm = 0xFBFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $27, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $37, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-134217729, %rax # imm = 0xF7FFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $26, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $36, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-268435457, %rax # imm = 0xEFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $29, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $35, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-536870913, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftrd $28, %k0, %k2
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $34, %k2, %k2

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; CHECK-NEXT:  kshiftrq $27, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-137438953473, %rax # imm = 0xFFFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $4, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $26, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-274877906945, %rax # imm = 0xFFFFFFFFBFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftd $7, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $25, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-549755813889, %rax # imm = 0xFFFFFFFF7FFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $6, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $24, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-1099511627777, %rax # imm = 0xFFFFFFFFEFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $9, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $23, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-2199023255553, %rax # imm = 0xFFFFFFFFDFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $8, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $22, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-4398046511105, %rax # imm = 0xFFFFFFFFDFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $11, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $21, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-8796093022209, %rax # imm = 0xFFFFFFFF7FFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $10, %k1, %k2

Open Source Used In Cisco Telemetry Broker On Prem 1.2  11306
; CHECK-NEXT:  kshiftrq $20, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-17592186044417, %rax # imm = 0xFFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $13, %k1, %k2
; CHECK-NEXT:  kshifltq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $19, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-35184372088833, %rax # imm = 0xFFFFDFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $12, %k1, %k2
; CHECK-NEXT:  kshifltq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $17, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-70368744177665, %rax # imm = 0xFFFFBFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $15, %k1, %k2
; CHECK-NEXT:  kshifltq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $17, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-140737488355329, %rax # imm = 0xFFFF7FFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $14, %k1, %k2
; CHECK-NEXT:  kshifltq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $15, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-281474976710657, %rax # imm = 0xFFFEFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $16, %k1, %k2
; CHECK-NEXT:  kshifltq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $14, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-562949953421313, %rax # imm = 0xFFFDFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $17, %k1, %k2
; CHECK-NEXT:  kshifltq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $15, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-1125899906842625, %rax # imm = 0xFFFBBBBBBBBBBBBB
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $19, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $13, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-2251799813685249, %rax # imm = 0xFFF7FFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $18, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $12, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-4503599627370497, %rax # imm = 0xFFEFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $21, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $11, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-9007199254740993, %rax # imm = 0xFFFFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $20, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $10, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-18014398509481985, %rax # imm = 0xFFBFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $23, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $9, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-36028797018963969, %rax # imm = 0xFF7FFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $22, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $8, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-72057594037927937, %rax # imm = 0xFDFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $25, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $7, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-144115188075855873, %rax # imm = 0xFDFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftr $24, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $6, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-288230376151711745, %rax # imm = 0xFBFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $27, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $5, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $26, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $4, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-1152921504606846977, %rax # imm = 0xEFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $29, %k1, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $3, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-2305843009213693953, %rax # imm = 0xDFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $31, %k1, %k2
; CHECK-NEXT:  kshiftlq $62, %k2, %k2
; CHECK-NEXT:  kshiftrq $2, %k2, %k2
; CHECK-NEXT:  korq %k2, %k0, %k0
; CHECK-NEXT:  movabsq $-4611686018427387905, %rax # imm = 0xBFFFFFFFFFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k0, %k0
; CHECK-NEXT:  kshiftrd $30, %k1, %k1
; CHECK-NEXT:  kshiftlq $1, %k0, %k0
; CHECK-NEXT:  kshiftrq $1, %k0, %k0
; CHECK-NEXT:  korq %k1, %k0, %k1
; CHECK-NEXT:  vmovdqu8 %ymm1, (%rsi) {%k1}
; CHECK-NEXT:  kshiftrq $32, %k1, %k1
; CHECK-NEXT:  vmovdqu8 %ymm0, 32(%rsi) {%k1}
; CHECK-NEXT:  vzeroupper
entry:
  %a = load <64 x i8>, <64 x i8>* %x
  %b = icmp eq <64 x i8> %a, zeroinitializer
  %shuf = shufflevector <64 x i8> %b, undef, <64 x i32> <i32 1, i32 0, i32 2, i32 5, i32 4, i32 7, i32 6, i32 9, i32 8, i32 11, i32 10, i32 13, i32 12, i32 15, i32 14, i32 17, i32 16, i32 19, i32 18, i32 21, i32 20, i32 23, i32 22, i32 25, i32 24, i32 27, i32 26, i32 29, i32 28, i32 31, i32 30, i32 33, i32 32, i32 35, i32 34, i32 37, i32 36, i32 39, i32 38, i32 41, i32 40, i32 43, i32 42, i32 45, i32 44, i32 47, i32 46, i32 49, i32 48, i32 51, i32 50, i32 53, i32 52, i32 55, i32 54, i32 57, i32 56, i32 59, i32 58, i32 61, i32 60, i32 63, i32 62>
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, i32 %shuf)
retq
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>* %x)
}
@mem64_dst = global i64 0, align 8
@mem64_src = global i64 0, align 8
declare void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
; CHECK: # %bb.0:
; CHECK-NEXT: kmovq {%.*}(%rip), %k0
; CHECK-NEXT: #APP
; CHECK-NEXT: #NO_APP
; CHECK-NEXT: kmovq %k0, {%.*}(%rip)
; CHECK-NEXT: movl -{{[0-9]+}}(%rsp), %eax
; CHECK-NEXT: retq
%1 = alloca i32, align 4
%2 = load i64, i64* @mem64_src, align 8
%3 = call i64 asm "", "=k,k,~{dirflag},~{fpsr},~{flags}"(i64 %2)
store i64 %3, i64* @mem64_dst, align 8
%4 = load i32, i32* %1, align 4
ret i32 %4
}
define void @cmp_v8i64_sext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: cmp_v8i64_sext:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpcmgtq 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT: vpcmgtq (%rdi), %ymm0, %ymm1
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <8 x i64>, <8 x i64>* %xptr
%y = load <8 x i64>, <8 x i64>* %yptr
%cmp = icmp slt <8 x i64> %x, %y
define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" { ; CHECK-LABEL: cmp_v8i64_zext: ; CHECK: # %bb.0: ; CHECK-NEXT: vmovdqa (%rsi), %ymm0 ; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1 ; CHECK-NEXT: vpcmpltq 32(%rdi), %ymm1, %ymm0 ; CHECK-NEXT: vpcmpltq (%rdi), %ymm0, %ymm0 ; CHECK-NEXT: vpsrlq $63, %ymm1, %ymm1 ; CHECK-NEXT: vpsrlq $63, %ymm0, %ymm0 ; CHECK-NEXT: vmovdqa %ymm0, (%rdx) ; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx) ; CHECK-NEXT: vzeroupper ; CHECK-NEXT: retq ; %x = load <8 x i64>, <8 x i64>* %xptr ; %y = load <8 x i64>, <8 x i64>* %yptr ; %cmp = icmp slt <8 x i64> %x, %y ; %ext = zext <8 x i1> %cmp to <8 x i64> ; store <8 x i64> %ext, <8 x i64>* %zptr ; ret void }

define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" { ; CHECK-LABEL: var_rotate_v16i8: ; CHECK: # %bb.0: ; CHECK-NEXT: vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8] ; CHECK-NEXT: vpsubb %xmm1, %xmm2, %xmm2 ; CHECK-NEXT: vmovzxvw {{.*#+}} ymm1 = xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zero,xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xmm1[15],zero ; CHECK-NEXT: vmovzxwbw {{.*#+}} ymm0 = xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero ; CHECK-NEXT: vpsllvw %ymm1, %ymm0, %ymm1 ; CHECK-NEXT: vmovzxwbw {{.*#+}} ymm2 = xmm2[0],zero,xmm2[1],zero,xmm2[2],zero,xmm2[3],zero,xmm2[4],zero,xmm2[5],zero,xmm2[6],zero,xmm2[7],zero,xmm2[8],zero,xmm2[9],zero,xmm2[10],zero,xmm2[11],zero,xmm2[12],zero,xmm2[13],zero,xmm2[14],zero,xmm2[15],zero ; CHECK-NEXT: vpsrlvw %ymm2, %ymm0, %ymm0 ; CHECK-NEXT: vpor %ymm0, %ymm1, %ymm0 ; CHECK-NEXT: vmovwb %ymm0, %xmm0
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
%shl = shl <16 x i8> %a, %b
%lshr = lshr <16 x i8> %a, %b8
%or = or <16 x i8> %shl, %lshr
ret <16 x i8> %or }

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL:  var_rotate_v32i8:
; CHECK:  # %bb.0:
; CHECK-NEXT:  vpsllw $4, %ymm0, %ymm2
; CHECK-NEXT:  vpsrlw $4, %ymm0, %ymm3
; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT:  vpsllw $5, %ymm1, %ymm1
; CHECK-NEXT:  vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
; CHECK-NEXT:  vpsllw $2, %ymm0, %ymm2
; CHECK-NEXT:  vpsrlw $6, %ymm0, %ymm3
; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT:  vpaddb %ymm1, %ymm1, %ymm1
; CHECK-NEXT:  vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
; CHECK-NEXT:  vpsrlw $7, %ymm0, %ymm2
; CHECK-NEXT:  vpaddb %ymm0, %ymm0, %ymm3
; CHECK-NEXT:  vpternlogq $248, {{.*}}(%rip), %ymm2, %ymm3
; CHECK-NEXT:  vpaddb %ymm1, %ymm1, %ymm1
; CHECK-NEXT:  vpblendvb %ymm1, %ymm3, %ymm0, %ymm0
; CHECK-NEXT:  retq
%b8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
%shl = shl <32 x i8> %a, %b
%lshr = lshr <32 x i8> %a, %b8
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or }

define <32 x i8> @splatvar_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL:  splatvar_rotate_v32i8:
; CHECK:  # %bb.0:
; CHECK-NEXT:  vpbroadcastb %xmm1, %xmm1
; CHECK-NEXT:  vpand {{.*}}(%rip), %xmm1, %xmm1
; CHECK-NEXT:  vpmovzxbq {{.*#+}} xmm2 =
xmm1[0],zero,zero,zero,zero,zero,zero,zero,xmm1[1],zero,zero,zero,zero,zero,zero
; CHECK-NEXT:  vpsllw %xmm2, %ymm0, %ymm3
; CHECK-NEXT:  vmovdqa {{.*++}} xmm4 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
; CHECK-NEXT:  vpsubb %xmm1, %xmm4, %xmm1
; CHECK-NEXT:  vpcmpeqd %xmm4, %xmm4, %xmm4
; CHECK-NEXT:  vpsllw %xmm2, %xmm4, %xmm2
; CHECK-NEXT:  vbroadcastb %xmm2, %ymm2
; CHECK-NEXT:  vpmovzxbq {{.*#+}} xmm1 = xmm1[0],zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero,zero
; CHECK-NEXT:  vpsrlw %xmm1, %ymm0, %ymm5
; CHECK-NEXT:  vpand %ymm2, %ymm3, %ymm2
; CHECK-NEXT:  vpsrlw %xmm1, %xmm4, %xmm0
; CHECK-NEXT:  vpbroadcast %xmm0, %ymm0
; CHECK-NEXT:  vpternlogq $236, %ymm5, %ymm2, %ymm0
; CHECK-NEXT:  retq
%splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
%splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %splat
%shl = shl <32 x i8> %a, %splat
%ishr = ishr <32 x i8> %a, %splat8
%or = or <32 x i8> %shl, %ishr
ret <32 x i8> %or
}
define <32 x i8> @constant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {  
; CHECK-AVX512-LABEL:  constant_rotate_v32i8:
; CHECK-AVX512:    # %bb.0:
; CHECK-AVX512-NEXT:    vpsllw $4, %ymm0, %ymm1
; CHECK-AVX512-NEXT:    vpand {{.*}}(%rip), %ymm1, %ymm1
; CHECK-AVX512-NEXT:    vbroadcast128 {{.*#+}} ymm2 = [8192,24640,41088,57536,57344,41152,24704,8256,8192,24640,41088,57536,57344,41152,24704,8256]
; CHECK-AVX512-NEXT:    # ymm2 = mem[0,1,0,1]
; CHECK-AVX512-NEXT:    vblendvb %ymm2, %ymm1, %ymm0, %ymm1
; CHECK-AVX512-NEXT:    vpsllw $2, %ymm1, %ymm3
; CHECK-AVX512-NEXT:    vpaddb %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT:    vblendvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:    vpaddb %ymm1, %ymm1, %ymm3
; CHECK-AVX512-NEXT:    vblendvb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-AVX512-NEXT:    vpxor %xmm2, %xmm2, %xmm2
; CHECK-AVX512-NEXT:    vpunpckhbw {{.*#+}} ymm3 = ymm0[8],ymm2[8],ymm0[9],ymm2[9],ymm0[10],ymm2[10],ymm0[11],ymm2[11],ymm0[12],ymm2[12],ymm0[13],ymm2[13],ymm0[14],ymm2[14],ymm0[15],ymm2[15],ymm0[24],ymm2[24],ymm0[25],ymm2[25],ymm0[26],ymm2[26],ymm0[27],ymm2[27],ymm0[28],ymm2[28],ymm0[29],ymm2[29],ymm0[30],ymm2[30],ymm0[31],ymm2[31]
; CHECK-AVX512-NEXT:    vpsllw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-AVX512-NEXT:    vpsrlw $8, %ymm3, %ymm3
; CHECK-AVX512-NEXT:    vpunpcklbw {{.*#+}} ymm0 = ymm0[0],ymm2[0],ymm0[1],ymm2[1],ymm0[2],ymm2[2],ymm0[3],ymm2[3],ymm0[4],ymm2[4],ymm0[5],ymm2[5],ymm0[6],ymm2[6],ymm0[7],ymm2[7],ymm0[16],ymm2[16],ymm0[17],ymm2[17],ymm0[18],ymm2[18],ymm0[19],ymm2[19],ymm0[20],ymm2[20],ymm0[21],ymm2[21],ymm0[22],ymm2[22],ymm0[23],ymm2[23]}
define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: splatconstant_rotate_mask_v32i8:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:  vpsllw $4, %ymm0, %ymm1  
    ; CHECK-NEXT:  vpsrlw $4, %ymm0, %ymm0  
    ; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm1, %ymm0  
    ; CHECK-NEXT:  retq  
    %shl = shl <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>  
    %lshr = lshr <32 x i8> %a, <i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4, i8 4>  
    %rmask = and <32 x i8> %lshr, <i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55, i8 55>  
    %lmask = and <32 x i8> %shl, <i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33, i8 33>  
    %or = or <32 x i8> %lmask, %rmask  
    ret <32 x i8> %or  
}  

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Developed by:

LLVM Team

University of Illinois at Urbana-Champaign

http://llvm.org

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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp

LINK_LIBS
  lldbCore
  lldbHost
  lldbSymbol

LINK_COMPONENTS
  Support
)

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Tobias Ulvgrd <tobias.ulvgard@dirac.se>
Tom Madams <tom.ej.madams@gmail.com> <tmadams@google.com>
Yixuan Qiu <yixuanq@gmail.com>
Yusuke Suzuki <utatane.tea@gmail.com>
Zbigniew Skowron <zbychs@gmail.com>
; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
entry:
br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
%lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
switch i32 undef, label %exit [
i32 1, label %sw.bb.i
i32 2, label %sw.bb3.i
]

sw.bb.i: ; preds = %while.body
unreachable

sw.bb3.i: ; preds = %while.body
unreachable

exit: ; preds = %while.body
switch i32 undef, label %exit.2 [
i32 1, label %sw.bb.i17
i32 2, label %sw.bb3.i20
]

sw.bb.i17: ; preds = %exit
%0 = bitcast %struct.0* %lsr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lsr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable
exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -attributor-max-iterations=4 -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS________OP M,IS__TUNIT_OPM
; RUN: opt -attributor-cgscc -attributor-manifest-internal -attributor-annotate-decl-cs -S < %s | FileCheck %s --check-prefixes=CHECK,NOT_TUNIT_NPM,NOT_TUNIT_OPM,NOT_CGSCC_OPM,IS__CGSCC____,IS________OP M,IS__CGSCC_OPM

define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
...
define void @avx512_legal512_prefer512_call_avx512 Legal512_prefer512(<8 x i64>* %arg) #0 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg
        store <8 x i64> %tmp, <8 x i64>* %arg
        ret void
    }

define void @avx512_legal512_prefer512_call_avx512 Legal512_prefer512(<8 x i64>* %arg) #0 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg
        store <8 x i64> %tmp, <8 x i64>* %arg
        ret void
    }

define void @avx512_legal512_prefer512_call_avx512 Legal512_prefer512(<8 x i64>* %arg) #0 {
    bb:
        %tmp = load <8 x i64>, <8 x i64>* %arg
        store <8 x i64> %tmp, <8 x i64>* %arg
        ret void
    }
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1)

This should promote

define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {
  .IS__TUNIT_OPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256
  .IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
  .IS__TUNIT_OPM-NEXT: bb:
    .IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    .IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    .IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    .IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    .IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256(<8 x i64>* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonnull readonly align 64 dereferenceable(64) [[ARG:%.*]])
    .IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
    .IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    .IS__TUNIT_OPM-NEXT: ret void
  
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256 call_avx512_legal512_prefer256 call_avx512_legal512_prefer256 call_avx512_legal512_prefer256 call_avx512 Legal512 Prefer256 call_avx512 Legal512 Prefer256 call_avx512 Legal512 Prefer256 call_avx512 Legal512 Prefer256 call_avx512 Legal512 Prefer256 call_avx512 Legal512 Prefer256 call_avx512 Legal512 Prefer256
bb:
    %tmp = alloca <8 x i64>, align 32
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*

@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg)
    %tmp = bitcast %arg to i64*
    call fastcc void
    @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg)
        %tmp = bitcast %arg to i64*
        store %arg, %tmp, align 2
        ret void

@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg)
    %arg = load %arg, %arg, align 2
    call fastcc void
    @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(%arg)
        %arg = load %arg, %arg, align 2
        store %arg, %arg, align 2
        ret void
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512 Legal512_Prefer512_CallAvx512_Legal512_Prefer256(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #1 {
;
; NOT_TUNIT_NPM-LABEL: define
{ {{[@]+]} } @callee_avx512 Legion512_Prefer512_CallAvx512_Legal512_Prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define
{ {{[@]+]} } @callee_avx512 Legion512_Prefer512_CallAvx512_Legal512_Prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_Legion512_Prefer512_CallAvx512_Legal512_Prefer256(<8 x i64>* %arg) #0 {
;
; IS__TUNIT_OPM-LABEL: define { {{[@]+]} } @avx512 Legion512_Prefer512_CallAvx512_Legal512_Prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* no capture write only [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 32
; IS__TUNIT_OPM-NEXT: [[ARG1_PRIV:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 32
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 32
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* no capture nonnull write only align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callees_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull
writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64
dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {[^@]+} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.malloc.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callees_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP]])
; IS__TUNIT_NPM-NEXT: load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {[^@]+} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.malloc.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callees_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* [[TMP]])
; IS__CGSCC_OPM-NEXT: load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;
; IS__CGSCC_NPM-LABEL: define {[^@]+} @avx512_legal512_prefer512_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.malloc.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void
@calleexav512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly
align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x
i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
;
; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; NOT_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; NOT_TUNIT_NPM-NEXT: [[ARG:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
; IS_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
[[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT: ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
  ; IS__TUNIT_OPM-LABEL: define { 
    ; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
    ; IS__TUNIT_OPM-NEXT: bb:
    ; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; IS__TUNIT_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull
readonly align 64 dereferenceable(64) [[TMP]])
  ; IS__UNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
  ; IS__UNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; IS__UNIT_OPM-NEXT: ret void
  ; IS__UNIT_NPM-LABEL: define { 
    ; IS__UNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
    ; IS__UNIT_NPM-NEXT: bb:
    ; IS__UNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; IS__UNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; IS__UNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__UNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__UNIT_NPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
  ; IS__UNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
  ; IS__UNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; IS__UNIT_NPM-NEXT: ret void
  ; IS__CGSCC_OPM-LABEL: define { 
    ; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
    ; IS__CGSCC_OPM-NEXT: bb:
    ; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
    ; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree
nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nonfree nonnull
readonly align 64 dereferenceable(64) [[TMP]])
}
define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) {  
  %tmp = load <8 x i64>, <8 x i64>* %arg  
  store <8 x i64> %tmp, <8 x i64>* %arg  
  ret void  
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(8 x i64)* %arg) #2 {
  %tmp = load <8 x i64>, %arg  
  store <8 x i64> %tmp, <8 x i64>* %arg  
  ret void  
}
@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofreenonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* [[TMP]] to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* [[TMP]] to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
;
; NOT_TUNIT_NPM-LABEL: define
{{[^@]+}} @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT:   [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT:   store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT:   ret void
;
; IS__TUNIT_NPM-LABEL: define {{[^@]+}} @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT:   [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT:   store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:   [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT:   store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT:   ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
;
; IS__TUNIT_OPM-LABEL: define {{[^@]+}} @avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT:   [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT:   [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT:   [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT:   call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT:   call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #4 {
  bb:
  [[ARG:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
  store <8 x i64> [[ARG]], <8 x i64>* [[ARG]], align 64
  ret void
}

; NOT_TUNIT_NPM-LABEL: define internal fastcc void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {
  bb:
  %tmp = alloca <8 x i64>, align 64
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}
; IS__CGSCC_NPM-NEXT:  bb:
; IS__CGSCC_NPM-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:  call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:  call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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    ; VEC256-LABEL: 'zext256'
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; AVX512VL512-LABEL: 'zext256'
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; SKX512-LABEL: 'zext256'
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; OPENX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; OPENX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; OPENX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; OPENX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; %A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void

define void @zext512() "min-legal-vector-width"="512" {
  ; AVX-LABEL: 'zext512'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

  ; AVX512VL256-LABEL: 'zext512'
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

  ; AVX512VL512-LABEL: 'zext512'
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'zext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'zext512'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; %A = zext <8 x i16> undef to <8 x i64>
%B = zext <8 x i32> undef to <8 x i64>
%C = zext <16 x i8> undef to <16 x i32>
%D = zext <16 x i16> undef to <16 x i32>
%E = zext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext256() "min-legal-vector-width"="256" {
; VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x
i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; SKX512-LABEL: 'sext256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

; %A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {
; AVX-LABEL: 'sext512'
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL256-LABEL: 'sext512'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext512'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'sext512'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
define void @trunc256() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'trunc256'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
  ret void
}

define void @trunc128() "min-legal-vector-width"="128" {
  ; AVX512VL256-LABEL: 'trunc256'
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
  ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
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11382

; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'trunc256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 8 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX256-LABEL: 'trunc256'
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; SKX512-LABEL: 'trunc256'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = trunc <8 x i64> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %B = trunc <8 x i64> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %C = trunc <8 x i64> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %D = trunc <16 x i32> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %E = trunc <16 x i32> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %F = trunc <32 x i16> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
%A = trunc <8 x i64> undef to <8 x i32>
%B = trunc <8 x i64> undef to <8 x i16>
%C = trunc <8 x i64> undef to <8 x i8>
%D = trunc <16 x i32> undef to <16 x i16>
%E = trunc <16 x i32> undef to <16 x i8>
%F = trunc <32 x i16> undef to <32 x i8>
ret void
}

define i32 @zext256_vXi1() "min-legal-vector-width"="256" {

; AVX-LABEL: 'zext256_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; AVX512VL256-LABEL: 'zext256_vXi1'
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 12 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 6 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 25 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 50 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
; SKX256-LABEL: 'zext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

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; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>

define i32 @sext256_vXi1() "min-legal-vector-width"="256" {
    ; AVX-LABEL: 'sext256_vXi1'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
    ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>

    ret i32 undef
}
to <4 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-LABEL: `sext256_vXi1'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; SKX256-LABEL: 'sext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

; SKX512-LABEL: 'sext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16
x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef 
;
%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 = sext <8 x i1> undef to <8 x i64>
%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>

%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>

%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>

ret i32 undef
}
define i32 @trunc_vXi1() "min-legal-vector-width"="256" [ 
; AVX-LABEL: 'trunc_vXi1'
; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
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undef to <16 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 4 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 8 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 14 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 3 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 7 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; AVX512VL512-NEXT:  Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'trunc_vXi1'
; SKX256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
; SKX256-NEXT:  Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'trunc_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2
x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 47 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

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%V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>

%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>

%V2i16 = trunc <2 x i16> undef to <2 x i1>
%V4i16 = trunc <4 x i16> undef to <4 x i1>
%V8i16 = trunc <8 x i16> undef to <8 x i1>
%V16i16 = trunc <16 x i16> undef to <16 x i1>
%V32i16 = trunc <32 x i16> undef to <32 x i1>
%V64i16 = trunc <64 x i16> undef to <64 x i1>

%V2i8 = trunc <2 x i8> undef to <2 x i1>
%V4i8 = trunc <4 x i8> undef to <4 x i1>
%V8i8 = trunc <8 x i8> undef to <8 x i1>
%V16i8 = trunc <16 x i8> undef to <16 x i1>
%V32i8 = trunc <32 x i8> undef to <32 x i1>
%V64i8 = trunc <64 x i8> undef to <64 x i1>

ret i32 undef
}

LLVM System Interface Library

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1.1305 protobuf 1.26.0

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1.1306 libtext-iconv-perl 1.7 5+b7

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Text::Iconv Version 1.6

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Description

This module provides a Perl interface to the iconv() codeset conversion function, as defined by the Single UNIX Specification. For more details see the POD documentation embedded in the file Iconv.pm, which will also be installed as Text::Iconv(3) man page.

Prerequisites

Your system should be compliant with the Single UNIX Specification or at least provide iconv_open(), iconv() and iconv_close() functions compliant with it.

Note that the possible conversions and the quality of the conversions depend on the available iconv conversion tables and algorithms, which are in most cases supplied by the operating system vendor. Some systems also allow you to build your own tables (e.g., HP-UX, Tru64 UNIX, and AIX provide the genxlt(1) command).

It is also possible to use a separate iconv library such as GNU libiconv package, just make sure that the desired library is found (see <http://www.gnu.org/directory/localization/libiconv.html>).
Building the module
-------------------

Since the Single UNIX Specification only specifies an interface, several issues are left to the implementation to define, namely:

1. The supported codesets
2. The supported conversions
3. The names for the supported codesets

Please check your system documentation for the above points.

The module can be built using this sequence of commands:

    perl Makefile.PL
    make
    make test

On some systems, the iconv functions are in libc; on others (and when you want to use a separate iconv library, such as GNU libiconv), you have to link with -liconv. Makefile.PL tries to automatically detect this. However, if your iconv library is in a non-standard path, you might have to help it. You can specify library and include paths like this:

    perl Makefile.PL LIBS=-L/path/to/lib INC=-I/path/to/include

You might also have to do this if you have multiple iconv implementations installed and want to use a specific one.

The purpose of the test scripts (run by "make test") is to check if an iconv library can be found, and if the iconv functions can be called. It is neither intended to find out which conversions are supported, nor to test the quality of the conversions provided by your iconv library. However, since the iconv functions can only be tested by calling them, which in turn requires the specification of codesets to be converted. The test scripts try some conversions which seem to be widely supported. Conversions not supported by your iconv implementation are skipped, but for the purpose outlined above this is no problem, as it shows that the iconv functions are working. There is no standard way to find out which conversions are supported by an implementation. This is also the reason why Text::Iconv can't provide platform-independent support for codeset identifiers (even if it it would be nice to have).

Installation
------------
make install

Feedback
-------

Text::Iconv was tested on various platforms and with different iconv implementations.

If you built Text::Iconv on a platform where you needed to specify additional libraries, or where none of the codeset identifiers in the test scripts are supported, please drop me a note.

Thanks
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Thanks go to all those who reported bugs and suggested features.

Michael Piotrowski <mxp@dynalabs.de>

Found in path(s):
* /opt/cola/permits/1144640811_1616146132.37/0/libtext-iconv-perl-1-7-orig-tar-bz2/Text-Iconv-1.7/README

1.1307 tonistiigi-vt100 0.0.0-20210615222946-8066bb97264f

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1.1308 open-ldap 2.4.57+dfsg-3

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1.1309 attr 2.4.47-2build1

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goproperties - properties file decoder for Go

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Go support for Protocol Buffers - Google's data interchange format

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// For more information: http://goo.gl/8us55a#licenseObject
type License struct {
    Name string `json:"name,omitempty"
    URL  string `json:"url,omitempty"
}
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*/

package license

import (  
    "context"
    "flag"
    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
    "github.com/vmware/govmomi/vim25/types"
)

type assign struct {  
  *flags.ClientFlag
  *flags.OutputFlag
  *flags.HostSystemFlag
  *flags.ClusterFlag

  name   string
  remove bool
}

func init() {  
  cli.Register("license.assign", &assign{})
}

func (cmd *assign) Register(ctx context.Context, f *flag.FlagSet) {  
  cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
  cmd.ClientFlag, ctx = flags.NewOutputFlag(ctx, cmd.OutputFlag, cmd.ClientFlag, "" consultancy"
  cmd.ClientFlag, ctx = flags.NewHostSystemFlag(ctx, cmd.HostSystemFlag)
  cmd.ClientFlag, ctx = flags.NewClusterFlag(ctx, cmd.ClusterFlag)
}

func (cmd *assign) Run(ctx context.Context, args []string) error {
  if len(args) != 1 {
    return cmd.ShowHelp()
  }

  name := args[0]

  // Code for the assign command
}
func (cmd *assign) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.HostSystemFlag.Process(ctx); err != nil {
        return err
    }
    return cmd.ClusterFlag.Process(ctx)
}

func (cmd *assign) Usage() string {
    return "KEY"
}

func (cmd *assign) Description() string {
    return `Assign licenses to HOST or CLUSTER.
Examples:
govc license.assign $VCSA_LICENSE_KEY
govc license.assign -host a_host.example.com $SESX_LICENSE_KEY
govc license.assign -cluster a_cluster $VSAN_LICENSE_KEY`
}

func (cmd *assign) Run(ctx context.Context, f *flag.FlagSet) error {
    if f.NArg() != 1 {
        return flag.ErrHelp
    }
    key := f.Arg(0)
client, err := cmd.Client()
if err != nil {
    return err
}

m, err := license.NewManager(client).AssignmentManager(ctx)
if err != nil {
    return err
}

host, err := cmd.HostSystemIfSpecified()
if err != nil {
    return err
}

var id string

if host == nil {
    cluster, cerr := cmd.ClusterIfSpecified()
    if cerr != nil {
        return cerr
    }
    if cluster == nil {
        // Default to vCenter UUID
        id = client.ServiceContent.About.InstanceUuid
    } else {
        id = cluster.Reference().Value
    }
    if cmd.remove {
        return m.Remove(ctx, id)
    }
}

info, err := m.Update(ctx, id, key, cmd.name)
if err != nil {
    return err
}

return cmd.WriteResult(licenseOutput([]types.LicenseManagerLicenseInfo{*info}))

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*/

package license

import (  
    "context"  
    "flag"

    "github.com/vmware/govmomi/govc/cli"  
    "github.com/vmware/govmomi/govc/flags"  
    "github.com/vmware/govmomi/license"
)

var featureUsage = "List licenses with given feature"

type ls struct {  
    *flags.ClientFlag  
    *flags.OutputFlag

    feature string
}

func init() {  
    cli.Register("license.ls", &ls{}))
}

func (cmd *ls) Register(ctx context.Context, f *flag.FlagSet) {  
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)  
    cmd.ClientFlag.Register(ctx, f)  
    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)  
    cmd.OutputFlag.Register(ctx, f)

    f.StringVar(&cmd.feature, "feature", ",", featureUsage)
}

func (cmd *ls) Process(ctx context.Context) error {  
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
if err := cmd.OutputFlag.Process(ctx); err != nil {
    return err
}
return nil
}

func (cmd *ls) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }
    m := license.NewManager(client)
    result, err := m.List(ctx)
    if err != nil {
        return err
    }
    if cmd.feature != "" {
        result = result.WithFeature(cmd.feature)
    }
    return cmd.WriteResult(licenseOutput(result))
}
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*/

package license

import (
    "context"
    "flag"
    "fmt"
type add struct {
    *flags.ClientFlag
    *flags.OutputFlag
}

func init() {
    cli.Register("license.add", &add{ })
}

func (cmd *add) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)
    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)
}

func (cmd *add) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    return nil
}

func (cmd *add) Usage() string {
    return "KEY..."
}

func (cmd *add) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }
    m := license.NewManager(client)

    // From the vSphere 5.5 documentation:
    //
    // To specify the edition type and any optional functions, use
// updateLicense for ESX Server and addLicense follow by
// LicenseAssingmentManager.updateAssignedLicense for VirtualCenter.
//
var addFunc func(ctx context.Context, key string, labels map[string]string) (types.LicenseManagerLicenseInfo, error)
switch t := client.ServiceContent.About.ApiType; t {
    case "HostAgent":
        addFunc = m.Update
    case "VirtualCenter":
        addFunc = m.Add
    default:
        return fmt.Errorf("unsupported ApiType: %s", t)
}

result := make(licenseOutput, 0)
for _, v := range f.Args() {
    license, err := addFunc(ctx, v, nil)
    if err != nil {
        return err
    }

    result = append(result, license)
}

return cmd.WriteResult(licenseOutput(result))
}

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package simulator

import (  
    "context"  
    "reflect"  
    "testing"  

    "github.com/vmware/govmomi"  
    "github.com/vmware/govmomi/find"  
    "github.com/vmware/govmomi/license"  
)

func TestLicenseManagerVPX(t *testing.T) {  
    ctx := context.Background()  
    m := VPX()  
    defer m.Remove()  
    err := m.Create()  
    if err != nil {  
        t.Fatal(err)  
    }  
    s := m.Service.NewServer()  
    defer s.Close()  
    c, err := govmomi.NewClient(ctx, s.URL, true)  
    if err != nil {  
        t.Fatal(err)  
    }  
    lm := license.NewManager(c.Client)  
    am, err := lm.AssignmentManager(ctx)  
    if err != nil {  
        t.Fatal(err)  
    }  
    la, err := am.QueryAssigned(ctx, "enoent")  
    if err != nil {  
        t.Fatal(err)  
    }
}
if len(la) != 0 {
    t.Errorf("unexpected license")
}

finder := find.NewFinder(c.Client, false)
hosts, err := finder.HostSystemList(ctx, "/...")
if err != nil {
    t.Fatal(err)
}

host := hosts[0].Reference().Value

for _, name := range []string{"", host, vcid} {
    la, err = am.QueryAssigned(ctx, name)
    if err != nil {
        t.Fatal(err)
    }
}

if len(la) != 1 {
    t.Fatal("no licenses")
}

if !reflect.DeepEqual(la[0].AssignedLicense, EvalLicense) {
    t.Fatal("invalid license")
}

func TestLicenseManagerESX(t *testing.T) {
    ctx := context.Background()
c := ESX()
    defer c.Remove()

er := c.Create()
    if err != nil {
        t.Fatal(err)
    }

d := c.Service.NewServer()
    defer d.Close()

c, err := govmomi.NewClient(ctx, s.URL, true)
    if err != nil {
        t.Fatal(err)
    }
}
lm := license.NewManager(c.Client)
_, err = lm.AssignmentManager(ctx)
if err == nil {
    t.Fatal("expected error")
}

la, err := lm.List(ctx)
if err != nil {
    t.Fatal(err)
}
if len(la) != 1 {
    t.Fatal("no licenses")
}

if !reflect.DeepEqual(la[0], EvalLicense) {
    t.Fatal("invalid license")
}

func TestAddRemoveLicense(t *testing.T) {
    ctx := context.Background()
    m := ESX()

derive m.Remove()

er := m.Create()
if err != nil {
    t.Fatal(err)
}

s := m.Service.NewServer()
derive s.Close()

c, err := govmomi.NewClient(ctx, s.URL, true)
if err != nil {
    t.Fatal(err)
}

lm := license.NewManager(c.Client)
key := "00000-00000-00000-00000-11111"
lables := map[string]string{"key": "value"}

info, err := lm.Add(ctx, key, labels)
if err != nil {
    t.Fatal(err)
}
if info.LicenseKey != key {
    t.Fatalf("expect info.LicenseKey equal to %q; got %q", key, info.LicenseKey)
}

if len(info.Labels) != len(labels) {
    t.Fatalf("expect len(info.Labels) equal to %d; got %d", len(labels), len(info.Labels))
}

if info.Labels[0].Key != "key" || info.Labels[0].Value != "value" {
    t.Fatalf("expect label to be {key:value}; got {%s:%s}", info.Labels[0].Key, info.Labels[0].Value)
}

la, err := lm.List(ctx)
if err != nil {
    t.Fatal(err)
}

if len(la) != 2 {
    t.Fatal("no licenses")
}

if la[1].LicenseKey != key {
    t.Fatalf("expect info.LicenseKey equal to %q; got %q", key, la[1].LicenseKey)
}

if len(la[1].Labels) != len(labels) {
    t.Fatalf("expect len(info.Labels) equal to %d; got %d", len(labels), len(la[1].Labels))
}

if la[1].Labels[0].Key != "key" || la[1].Labels[0].Value != "value" {
    t.Fatalf("expect label to be {key:value}; got {%s:%s}", la[1].Labels[0].Key, la[1].Labels[0].Value)
}

err = lm.Remove(ctx, key)
if err != nil {
    t.Fatal(err)
}

la, err = lm.List(ctx)
if err != nil {
    t.Fatal(err)
}
if len(la) != 1 {
    t.Fatal("no licenses")
}

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package simulator

import {
  "github.com/vmware/govmomi/object"
  "github.com/vmware/govmomi/vim25/methods"
  "github.com/vmware/govmomi/vim25/mo"
  "github.com/vmware/govmomi/vim25/soap"
  "github.com/vmware/govmomi/vim25/types"
}

// EvalLicense is the default license
var EvalLicense = types.LicenseManagerLicenseInfo{
  LicenseKey: "00000-00000-00000-00000-00000",
  EditionKey: "eval",
  Name: "Evaluation Mode",
  Properties: []types.KeyAnyValue{
    {Key: "feature",
     Value: types.KeyValue{
      Key: "serialuri:2",
      Value: "Remote virtual Serial Port Concentrator",
    },},
    {Key: "feature",
     Value: types.KeyValue{
      Key: "dvs",
      Value: "vSphere Distributed Switch",
    },},
  },
}

type LicenseManager struct {
  mo.LicenseManager
}
func NewLicenseManager(ref types.ManagedObjectReference) object.Reference {
    m := &LicenseManager{
        Self: ref,
        Licenses: []types.LicenseManagerLicenseInfo{EvalLicense}
    }
    if Map.IsVPX() {
        am := Map.Put(&LicenseAssignmentManager{}).Reference()
        m.LicenseAssignmentManager = &am
    }
    return m
}

func (m *LicenseManager) AddLicense(req *types.AddLicense) soap.HasFault {
    body := &methods.AddLicenseBody{
        Res: &types.AddLicenseResponse{},
    }
    for _, license := range m.Licenses {
        if license.LicenseKey == req.LicenseKey {
            body.Res.Returnval = licenseInfo(license.LicenseKey, license.Labels)
            return body
        }
    }
    m.Licenses = append(m.Licenses, types.LicenseManagerLicenseInfo{
        LicenseKey: req.LicenseKey,
        Labels:    req.Labels,
    })
    body.Res.Returnval = licenseInfo(req.LicenseKey, req.Labels)
    return body
}

func (m *LicenseManager) RemoveLicense(req *types.RemoveLicense) soap.HasFault {
    body := &methods.RemoveLicenseBody{
        Res: &types.RemoveLicenseResponse{},
    }
    for i, license := range m.Licenses {
        if req.LicenseKey == license.LicenseKey {
            m.Licenses = append(m.Licenses[:i], m.Licenses[i+1:]...)
            return body
        }
    }
}
type LicenseAssignmentManager struct {
    mo.LicenseAssignmentManager
}

func (m *LicenseAssignmentManager) QueryAssignedLicenses(req *types.QueryAssignedLicenses) soap.HasFault {
    body := &methods.QueryAssignedLicensesBody{
        Res: &types.QueryAssignedLicensesResponse{},
    }

    // EntityId can be a HostSystem or the vCenter InstanceUuid
    if req.EntityId != "" {
        if req.EntityId != Map.content().About.InstanceUuid {
            id := types.ManagedObjectReference{
                Type: "HostSystem",
                Value: req.EntityId,
            }
        }
    }

    if Map.Get(id) == nil {
        return body
    }

    body.Res.Returnval = []types.LicenseAssignmentManagerLicenseAssignment{
        {
            EntityId: req.EntityId,
            AssignedLicense: EvalLicense,
        },
    }

    return body
}

func licenseInfo(key string, labels []types.KeyValue) types.LicenseManagerLicenseInfo {
    info := EvalLicense

    info.LicenseKey = key
    info.Labels = labels

    return info
}
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*/

package license

import (
    "context"
    "flag"

    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
)

type decode struct {
    *flags.ClientFlag
    *flags.OutputFlag

    feature string
}

func init() {
    cli.Register("license.decode", &decode{})
}

func (cmd *decode) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)
    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)

    f.StringVar(&cmd.feature, "feature", "", featureUsage)
}

func (cmd *decode) Process(ctx context.Context, f *flag.FlagSet) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }

    return nil
}
if err := cmd.OutputFlag.Process(ctx); err != nil {
    return err
}
return nil
}

func (cmd *decode) Usage() string {
    return "KEY..."
}

func (cmd *decode) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }

    var result license.InfoList
    m := license.NewManager(client)
    for _, v := range f.Args() {
        license, err := m.Decode(ctx, v)
        if err != nil {
            return err
        }
        result = append(result, license)
    }

    if cmd.feature != "" {
        result = result.WithFeature(cmd.feature)
    }

    return cmd.WriteResult(licenseOutput(result))
}

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*/

package license

import (  "context"  "strconv"

type Manager struct {
    object.Common
}

func NewManager(c *vim25.Client) *Manager {
    m := Manager{
        object.NewCommon(c, *c.ServiceContent.LicenseManager),
    }
    return &m
}

func mapToKeyValueSlice(m map[string]string) []types.KeyValue {
    var r []types.KeyValue
    for k, v := range m {
        r = append(r, types.KeyValue{Key: k, Value: v})
    }
    return r
}

func (m Manager) Add(ctx context.Context, key string, labels map[string]string) (types.LicenseManagerLicenseInfo, error) {
    req := types.AddLicense{
        This:       m.Reference(),
        LicenseKey: key,
        Labels:     mapToKeyValueSlice(labels),
    }
    res, err := methods.AddLicense(ctx, m.Client(), &req)
    if err != nil {
        return types.LicenseManagerLicenseInfo{}, err
    }
    return res.Returnval, nil
}

func (m Manager) Decode(ctx context.Context, key string) (types.LicenseManagerLicenseInfo, error) {
    req := types.DecodeLicense{
        This:       m.Reference(),
    }
    res, err := methods.DecodeLicense(ctx, m.Client(), &req)
    if err != nil {
        return types.LicenseManagerLicenseInfo{}, err
    }
    return res.Returnval, nil
}
LicenseKey: key,
}

res, err := methods.DecodeLicense(ctx, m.Client(), &req)
if err != nil {
    return types.LicenseManagerLicenseInfo{}, err
}

return res.Returnval, nil
}

func (m Manager) Remove(ctx context.Context, key string) error {
    req := types.RemoveLicense{
        This:       m.Reference(),
        LicenseKey: key,
    }

    _, err := methods.RemoveLicense(ctx, m.Client(), &req)
    return err
}

func (m Manager) Update(ctx context.Context, key string, labels map[string]string) (types.LicenseManagerLicenseInfo, error) {
    req := types.UpdateLicense{
        This:       m.Reference(),
        LicenseKey: key,
        Labels:     mapToKeyValueSlice(labels),
    }

    res, err := methods.UpdateLicense(ctx, m.Client(), &req)
    if err != nil {
        return types.LicenseManagerLicenseInfo{}, err
    }

    return res.Returnval, nil
}

func (m Manager) List(ctx context.Context) (InfoList, error) {
    var mlm mo.LicenseManager

    err := m.Properties(ctx, m.Reference(), []string{"licenses"}, &mlm)
    if err != nil {
        return nil, err
    }

    return InfoList(mlm.Licenses), nil
}
func (m Manager) AssignmentManager(ctx context.Context) (*AssignmentManager, error) {
    var mlm mo.LicenseManager

    err := m.Properties(ctx, m.Reference(), []string{"licenseAssignmentManager"}, &mlm)
    if err != nil {
        return nil, err
    }

    if mlm.LicenseAssignmentManager == nil {
        return nil, object.ErrNotSupported
    }

    am := AssignmentManager{
        object.NewCommon(m.Client(), *mlm.LicenseAssignmentManager),
    }

    return &am, nil
}

type licenseFeature struct {
    name  string
    level int
}

func parseLicenseFeature(feature string) *licenseFeature {
    lf := new(licenseFeature)
    f := strings.Split(feature, ":")

    lf.name = f[0]

    if len(f) > 1 {
        var err error
        lf.level, err = strconv.Atoi(f[1])
        if err != nil {
            lf.name = feature
        }
    }

    return lf
}

func HasFeature(license types.LicenseManagerLicenseInfo, key string) bool {
    feature := parseLicenseFeature(key)

    for _, p := range license.Properties {
        if p.Key != "feature" {
            continue
        }
    }
}

Open Source Used In Cisco Telemetry Broker On Prem 1.2 11533
kv, ok := p.Value.(types.KeyValue)

if !ok {
    continue
}

lf := parseLicenseFeature(kv.Key)

if lf.name == feature.name && lf.level >= feature.level {
    return true
}

return false

// InfoList provides helper methods for []types.LicenseManagerLicenseInfo
type InfoList []types.LicenseManagerLicenseInfo

func (l InfoList) WithFeature(key string) InfoList {
    var result InfoList

    for _, license := range l {
        if HasFeature(license, key) {
            result = append(result, license)
        }
    }

    return result
}

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*/
package license

import (
    "context"
    "flag"
    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
)

type remove struct {
    *flags.ClientFlag
    *flags.OutputFlag
}

func init() {
    cli.Register("license.remove", &remove{ })
}

func (cmd *remove) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)
    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)
}

func (cmd *remove) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    return nil
}

func (cmd *remove) Usage() string {
    return "KEY..."
}

func (cmd *remove) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }
}
m := license.NewManager(client)
for _, v := range f.Args() {
    err = m.Remove(ctx, v)
    if err != nil {
        return err
    }
}
return nil

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*/

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import (  
    "context"  
    "flag"  
    "fmt"  
    "io"  
    "os"  
    "text/tabwriter"  
    "
github.com/vmware/govmomi/govc/cli"  
    "github.com/vmware/govmomi/govc/flags"  
    "github.com/vmware/govmomi/license"  
    "github.com/vmware/govmomi/vim25/types"
)

type assigned struct {
    *flags.ClientFlag
    *flags.OutputFlag
    id string
}
func init() {
cli.Register("license.assigned.ls", &assigned{})
}

func (cmd *assigned) Register(ctx context.Context, f *flag.FlagSet) {
cli.ClientFlag, ctx = flags.NewClientFlag(ctx)
cli.ClientFlag.Register(ctx, f)

cli.OutputFlag, ctx = flags.NewOutputFlag(ctx)
cli.OutputFlag.Register(ctx, f)

f.StringVar(&cmd.id, "id", "", "Entity ID")
}

func (cmd *assigned) Process(ctx context.Context) error {
if err := cmd.ClientFlag.Process(ctx); err != nil {
return err
}
if err := cmd.OutputFlag.Process(ctx); err != nil {
return err
}
return nil
}

func (cmd *assigned) Run(ctx context.Context, f *flag.FlagSet) error {
client, err := cmd.Client()
if err != nil {
return err
}

m, err := license.NewManager(client).AssignmentManager(ctx)
if err != nil {
return err
}

assigned, err := m.QueryAssigned(ctx, cmd.id)
if err != nil {
return err
}

return cmd.WriteResult(assignedOutput(assigned))
}

type assignedOutput []types.LicenseAssignmentManagerLicenseAssignment

func (res assignedOutput) Write(w io.Writer) error {
tw := tabwriter.NewWriter(os.Stdout, 4, 0, 2, ' ', 0)
fmt.Fprintf(tw, "Id:\tScope:\tName:\tLicense:\n")
for _, v := range res {
    fmt.Fprintf(tw, "%s\t", v.EntityId)
    fmt.Fprintf(tw, "%s\t", v.Scope)
    fmt.Fprintf(tw, "%s\t", v.EntityDisplayName)
    fmt.Fprintf(tw, "%s\t", v.AssignedLicense.LicenseKey)
    fmt.Fprintf(tw, "\n")
} return tw.Flush()
}
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package license

import (
    "fmt"
    "io"
    "os"
    "text/tabwriter"

    "github.com/vmware/govmomi/vim25/types"
)

type licenseOutput []types.LicenseManagerLicenseInfo

func (res licenseOutput) Write(w io.Writer) error {
    tw := tabwriter.NewWriter(os.Stdout, 4, 0, 2, ' ', 0)
    fmt.Fprintf(tw, "Key:	Edition:	Used:	Total:
")
    for _, v := range res {
        fmt.Fprintf(tw, "%s	", v.LicenseKey)
        fmt.Fprintf(tw, "%s	", v.EditionKey)
        fmt.Fprintf(tw, "%d	", v.Used)
        fmt.Fprintf(tw, "%d	", v.Total)
        fmt.Fprintf(tw, "\n")
    }
    return tw.Flush()
}

#!/usr/bin/env bats
load test_helper

# These tests should only run against a server running an evaluation license.
verify_evaluation() {
    if [ "$govc license.ls -json | jq -r '[.EditionKey]' != "eval" ]; then

skip "requires evaluation license"
fi
}

get_key() {
jq ".[] | select(.LicenseKey == "$1")"
}

get_property() {
jq -r ".Properties[1].Select (.Key == "\$1") | .Value"
}

@test "license.add" {
esx_env
verify_evaluation
run govc license.add -json 00000-00000-00000-00000-00001 00000-00000-00000-00000-00002
assert_success

# Expect to see an entry for both the first and the second key
assert_equal "License is not valid for this product" "$(get_key 00000-00000-00000-00000-00001 <<<${output} | get_property diagnostic)"
assert_equal "License is not valid for this product" "$(get_key 00000-00000-00000-00000-00002 <<<${output} | get_property diagnostic)"
}

@test "license.remove" {
esx_env
verify_evaluation
run govc license.remove -json 00000-00000-00000-00000-00001
assert_success
}

@test "license.ls" {
vcsim_env
verify_evaluation
run govc license.ls -json
assert_success

# Expect the test instance to run in evaluation mode
assert_equal "Evaluation Mode" "$(get_key 00000-00000-00000-00000-00000 <<<${output} | jq -r ".Name")"
}
@test "license.decode" {
    esx_env

    verify_evaluation

    key=00000-00000-00000-00000-00000
    assert_equal "eval" $(govc license.decode $key | grep $key | awk '{print $2}')
}

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limitations under the License.
 */

package license

import (
    "context"
    "github.com/vmware/govmomi/object"
    "github.com/vmware/govmomi/vim25/methods"
    "github.com/vmware/govmomi/vim25/types"
)

type AssignmentManager struct {
    object.Common
}

func (m AssignmentManager) QueryAssigned(ctx context.Context, id string) ([]types.LicenseAssignmentManagerLicenseAssignment, error) {
    req := types.QueryAssignedLicenses{
        This:     m.Reference(),
        EntityId: id,
    }
    res, err := methods.QueryAssignedLicenses(ctx, m.Client(), &req)
    if err != nil {
        return nil, err
    }
    return res.Returnval, nil
}

func (m AssignmentManager) Remove(ctx context.Context, id string) error {
    req := types.RemoveAssignedLicense{
        This:     m.Reference(),
        EntityId: id,
    }

_, err := methods.RemoveAssignedLicense(ctx, m.Client(), &req)
return err
}

func (m AssignmentManager) Update(ctx context.Context, id string, key string, name string) (*types.LicenseManagerLicenseInfo, error) {
req := types.UpdateAssignedLicense{
    This: m.Reference(),
    Entity: id,
    LicenseKey: key,
    EntityDisplayName: name,
}
res, err := methods.UpdateAssignedLicense(ctx, m.Client(), &req)
if err != nil {
    return nil, err
}

return &res.Returnval, nil
}

1.1317 keyutils 1.6.1-2

1.1317.1 Available under license:

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Version 2.1, February 1999

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Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
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Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
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copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
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associated interface definition files, plus the scripts used to
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special exception, the source code distributed need not include
anything that is normally distributed (in either source or binary
form) with the major components (compiler, kernel, and so on) of the
operating system on which the executable runs, unless that component
itself accompanies the executable.

If distribution of executable or object code is made by offering
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distribution of the source code, even though third parties are not
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## 1.1318 icu 67.1-r2

### 1.1318.1 Available under license:

```
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.%(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<
```
%.$(STATIC_O): $(srcdir)/%_.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%_.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\$(GEN_DEPS.c) $< \n | sed '\s/
\o : ]*\o $@ : /g\n" > $@; \n [ -s $@ ] || rm -f $@' \n%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<"
@$(SHELL) -ec "$\$(GEN_DEPS.cc) $< \n | sed '\s/
\o : ]*\o $@ : /g\n" > $@; \n [ -s $@ ] || rm -f $@' \n
## Versioned libraries rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s {*F}.$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup
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