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1.2 libxext 1.3.3-1+b2

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file or class name and description of purpose be included on the
same "printed page" as the copyright notice for easier
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1.18 load-json-file 4.0.0
1.18.1 Available under license :

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1.19 libalgorithm-diff-xs-perl 0.04 5+b1
1.19.1 Available under license :

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Found in path(s):
* /opt/ws_local/PERMITS_SQL/1068209212_159554847.99/0/libalgorithm-diff-xs-perl-0-04-orig-tar-
gz/Algorithm-Diff-XS-0.04/README
No license file was found, but licenses were detected in source scan.

=head1 NAME
Algorithm::Diff::XS - Algorithm::Diff with XS core loop

=head1 SYNOPSIS

# Drop-in replacement to Algorithm::Diff, but "compact_diff"
# and C<LCSidx> will run much faster for large data sets.
use Algorithm::Diff::XS qw( compact_diff LCSidx );

=head1 DESCRIPTION

This module is a simple re-packaging of Joe Schaefer's excellent
but not very well-known L<Algorithm::LCS> with a drop-in interface
that simply re-uses the installed version of the L<Algorithm::Diff>
module.

Note that only the C<LCSidx> function is optimized in XS at the
moment, which means only C<compact_diff> will get significantly
faster for large data sets, while C<diff> and C<sdiff> will run
in identical speed as C<Algorithm::Diff>.

=head1 BENCHMARK

<table>
<thead>
<tr>
<th>Rate</th>
<th>Algorithm::Diff</th>
<th>Algorithm::Diff::XS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Algorithm::Diff</td>
<td>14.7/s</td>
<td>--</td>
</tr>
<tr>
<td>Algorithm::Diff::XS</td>
<td>806/s</td>
<td>5402%</td>
</tr>
</tbody>
</table>

The benchmarking script is as below:

my @data = ([qw/a b d/ x 50], [qw/b a d c/ x 50]);
cmpthses( 500, {
  'Algorithm::Diff' => sub {
    Algorithm::Diff::compact_diff(@data)
  },
  'Algorithm::Diff::XS' => sub {
    Algorithm::Diff::XS::compact_diff(@data)
  }
});

=head1 SEE ALSO

L<Algorithm::Diff>, L<Algorithm::LCS>.

=head1 AUTHORS
Audrey Tang E\lt\texttt{cpan@audreyt.org}\gt

=head1 COPYRIGHT

Copyright 2008 by Audrey Tang E\lt\texttt{cpan@audreyt.org}\gt.

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=cut

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---

abstract: Algorithm::Diff with XS core loop
author: Audrey Tang \texttt{<cpan@audreyt.org>}
distribution_type: module
generated_by: Module::Install version 0.67
license: perl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.3.html
  version: 1.3
name: Algorithm-Diff-XS
no_index:
directory:
  - inc
  - t
requires:
  Algorithm::Diff: 1.19
  perl: 5.6.0
  version: 0.01

No license file was found, but licenses were detected in source scan.

'GNU public license' => 'gpl', 1,
'GNU lesser public license' => 'gpl', 1,
'BSD license' => 'bsd', 1,
'proprietary' => 'proprietary', 0,
if ( $osi and $license_text =~ /All rights reserved/i ) {
   warn "LEGAL WARNING: 'All rights reserved' may invalidate Open Source licenses. Consider removing it."
}

Found in path(s):
* /opt/ws_local/PERMITS_SQL/1068209212_1595544847.99/0/libalgorithm-diff-xs-perl-0-04-orig-tar-gz/Algorithm-Diff-XS-0.04/inc/Module/Install/Metadata.pm
No license file was found, but licenses were detected in source scan.

/*
   --------------------------------------------------------------

   ppport.h -- Perl/Pollution/Portability Version 3.06_01


   Do NOT edit this file directly! -- Edit PPPort.pm.PL and the includes in parts/inc/ instead.

   Use 'perldoc ppport.h' to view the documentation below.

   --------------------------------------------------------------

   SKIP

   =pod

   =head1 NAME

   ppport.h - Perl/Pollution/Portability version 3.06_01

   =head1 SYNOPSIS

   perl ppport.h [options] [source files]

   Searches current directory for files if no [source files] are given

   --help       show short help

   --patch=file write one patch file with changes
   --copy=suffix write changed copies with suffix
   --diff=program use diff program and options

   --compat-version=version provide compatibility with Perl version
   --cplusplus accept C++ comments

   --quiet      don't output anything except fatal errors
   --nodiag     don't show diagnostics

   --------------------------------------------------------------
--nohints       don't show hints
--nochanges     don't suggest changes
--nofilter      don't filter input files

--list-provided list provided API
--list-unsupported list unsupported API
--api-info=name show Perl API portability information

=head1 COMPATIBILITY

This version of F<ppport.h> is designed to support operation with Perl
installations back to 5.003, and has been tested up to 5.9.3.

=head1 OPTIONS

=head2 --help

Display a brief usage summary.

=head2 --patch=I<file>

If this option is given, a single patch file will be created if
any changes are suggested. This requires a working diff program
to be installed on your system.

=head2 --copy=I<suffix>

If this option is given, a copy of each file will be saved with
the given suffix that contains the suggested changes. This does
not require any external programs.

If neither C<--patch> or C<--copy> are given, the default is to
simply print the diffs for each file. This requires either
C<Text::Diff> or a C<diff> program to be installed.

=head2 --diff=I<program>

Manually set the diff program and options to use. The default
is to use C<Text::Diff>, when installed, and output unified
context diffs.

=head2 --compat-version=I<version>

Tell F<ppport.h> to check for compatibility with the given
Perl version. The default is to check for compatibility with Perl
version 5.003. You can use this option to reduce the output
of F<ppport.h> if you intend to be backward compatible only
up to a certain Perl version.
=head2 --cplusplus

Usually, F<ppport.h> will detect C++ style comments and replace them with C style comments for portability reasons. Using this option instructs F<ppport.h> to leave C++ comments untouched.

=head2 --quiet

Be quiet. Don't print anything except fatal errors.

=head2 --nodiag

Don't output any diagnostic messages. Only portability alerts will be printed.

=head2 --nohints

Don't output any hints. Hints often contain useful portability notes.

=head2 --nochanges

Don't suggest any changes. Only give diagnostic output and hints unless these are also deactivated.

=head2 --nofilter

Don't filter the list of input files. By default, files not looking like source code (i.e. not *.xs, *.c, *.cc, *.cpp or *.h) are skipped.

=head2 --list-provided

Lists the API elements for which compatibility is provided by F<ppport.h>. Also lists if it must be explicitly requested, if it has dependencies, and if there are hints for it.

=head2 --list-unsupported

Lists the API elements that are known not to be supported by F<ppport.h> and below which version of Perl they probably won't be available or work.

=head2 --api-info=I<name>

Show portability information for API elements matching I<name>. If I<name> is surrounded by slashes, it is interpreted as a regular
expression.

=head1 DESCRIPTION

In order for a Perl extension (XS) module to be as portable as possible across differing versions of Perl itself, certain steps need to be taken.

=over 4

=item *

Including this header is the first major one. This alone will give you access to a large part of the Perl API that hasn't been available in earlier Perl releases. Use

perl ppport.h --list-provided

to see which API elements are provided by ppport.h.

=item *

You should avoid using deprecated parts of the API. For example, using global Perl variables without the C<PL_> prefix is deprecated. Also, some API functions used to have a C<perl_> prefix. Using this form is also deprecated. You can safely use the supported API, as F<ppport.h> will provide wrappers for older Perl versions.

=item *

If you use one of a few functions that were not present in earlier versions of Perl, and that can't be provided using a macro, you have to explicitly request support for these functions by adding one or more C<#define>s in your source code before the inclusion of F<ppport.h>.

These functions will be marked C<explicit> in the list shown by C<--list-provided>.

Depending on whether you module has a single or multiple files that use such functions, you want either C<static> or global variants.

For a C<static> function, use:

    #define NEED_function

For a global function, use:

    #define NEED_function_GLOBAL
Note that you mustn’t have more than one global request for one function in your project.

<table>
<thead>
<tr>
<th>Function</th>
<th>Static Request</th>
<th>Global Request</th>
</tr>
</thead>
<tbody>
<tr>
<td>eval_pv()</td>
<td>NEED_eval_pv</td>
<td>NEED_eval_pv_GLOBAL</td>
</tr>
<tr>
<td>grok_bin()</td>
<td>NEED_grok_bin</td>
<td>NEED_grok_bin_GLOBAL</td>
</tr>
<tr>
<td>grok_hex()</td>
<td>NEED_grok_hex</td>
<td>NEED_grok_hex_GLOBAL</td>
</tr>
<tr>
<td>grok_number()</td>
<td>NEED_grok_number</td>
<td>NEED_grok_number_GLOBAL</td>
</tr>
<tr>
<td>grok_numeric_radix()</td>
<td>NEED_grok_numeric_radix</td>
<td>NEED_grok_numeric_radix_GLOBAL</td>
</tr>
<tr>
<td>grok_oct()</td>
<td>NEED_grok_oct</td>
<td>NEED_grok_oct_GLOBAL</td>
</tr>
<tr>
<td>newCONSTSUB()</td>
<td>NEED_newCONSTSUB</td>
<td>NEED_newCONSTSUB_GLOBAL</td>
</tr>
<tr>
<td>newRV_noinc()</td>
<td>NEED_newRV_noinc</td>
<td>NEED_newRV_noinc_GLOBAL</td>
</tr>
<tr>
<td>sv_2pv_nolen()</td>
<td>NEED_sv_2pv_nolen</td>
<td>NEED_sv_2pv_nolen_GLOBAL</td>
</tr>
<tr>
<td>sv_2pvbyte()</td>
<td>NEED_sv_2pvbyte</td>
<td>NEED_sv_2pvbyte_GLOBAL</td>
</tr>
<tr>
<td>sv_catpvf_mg()</td>
<td>NEED_sv_catpvf_mg</td>
<td>NEED_sv_catpvf_mg_GLOBAL</td>
</tr>
<tr>
<td>sv_catpvf_mg_nocontext()</td>
<td>NEED_sv_catpvf_mg_nocontext</td>
<td>NEED_sv_catpvf_mg_nocontext_GLOBAL</td>
</tr>
<tr>
<td>sv_setpvf_mg()</td>
<td>NEED_sv_setpvf_mg</td>
<td>NEED_sv_setpvf_mg_GLOBAL</td>
</tr>
<tr>
<td>sv_setpvf_mg_nocontext()</td>
<td>NEED_sv_setpvf_mg_nocontext</td>
<td>NEED_sv_setpvf_mg_nocontext_GLOBAL</td>
</tr>
<tr>
<td>vnewSVpvf()</td>
<td>NEED_vnewSVpvf</td>
<td>NEED_vnewSVpvf_GLOBAL</td>
</tr>
</tbody>
</table>

To avoid namespace conflicts, you can change the namespace of the explicitly exported functions using the C<DPPP_NAMESPACE> macro. Just C<#define> the macro before including C<ppport.h>:

```
#define DPPP_NAMESPACE MyOwnNamespace_
#include "ppport.h"
```

The default namespace is C<DPPP_>.

=back

The good thing is that most of the above can be checked by running F<ppport.h> on your source code. See the next section for details.

=head1 EXAMPLES

To verify whether F<ppport.h> is needed for your module, whether you should make any changes to your code, and whether any special defines should be used, F<ppport.h> can be run as a Perl script to check your source code. Simply say:

```
perl ppport.h
```

The result will usually be a list of patches suggesting changes that should at least be acceptable, if not necessarily the most efficient solution, or a fix for all possible problems.
If you know that your XS module uses features only available in newer Perl releases, if you're aware that it uses C++ comments, and if you want all suggestions as a single patch file, you could use something like this:

    perl ppport.h --compat-version=5.6.0 --cplusplus --patch=test.diff

If you only want your code to be scanned without any suggestions for changes, use:

    perl ppport.h --nochanges

You can specify a different C<diff> program or options, using the C<--diff> option:

    perl ppport.h --diff='diff -C 10'

This would output context diffs with 10 lines of context.

To display portability information for the C<newSVpvn> function, use:

    perl ppport.h --api-info=newSVpvn

Since the argument to C<--api-info> can be a regular expression, you can use

    perl ppport.h --api-info=/_nomg$/

to display portability information for all C<_nomg> functions or

    perl ppport.h --api-info=/./

to display information for all known API elements.

=head1 BUGS

If this version of F<ppport.h> is causing failure during the compilation of this module, please check if newer versions of either this module or C<Devel::PPPort> are available on CPAN before sending a bug report.

If F<ppport.h> was generated using the latest version of C<Devel::PPPort> and is causing failure of this module, please file a bug report using the CPAN Request Tracker at L<http://rt.cpan.org/>.

Please include the following information:
The complete output from running "perl -V"

This file.

The name and version of the module you were trying to build.

A full log of the build that failed.

Any other information that you think could be relevant.

For the latest version of this code, please get the C<Devel::PPPort> module from CPAN.

=head1 COPYRIGHT

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=head1 SEE ALSO

See L<Devel::PPPort>.

=cut

use strict;

my %opt = (
quiet => 0,
diag => 1,
hints => 1,
changes => 1,
cplusplus => 0,
filter => 1,
);

my($ppport) = $0 =~ /\[(.\]+)/;
my $LF = '(?:\r\n|\[\r\n\])';  # line feed
my $HS = "[\t\ ]";  # horizontal whitespace

eval {
require Getopt::Long;
Getopt::Long::GetOptions(%opt, qw(
    help quiet diag! filter! hints! changes! cplusplus
    patch=s copy=s diff=s compat-version=s
    list-provided list-unsupported api-info=s
)) or usage();

if ($@ and grep /^-/, @ARGV) {
    usage() if "$ARGV" =~ /^--?h(?:elp)?$/;
die "Getopt::Long not found. Please don't use any options.\n";
}

usage() if $opt{help};

if (exists $opt{compat-version}) {
    my($r,$v,$s) = eval { parse_version($opt{compat-version}) };
    if ($@) {
        die "Invalid version number format: $opt{compat-version}\n";
    }
die "Only Perl 5 is supported\n" if $r != 5;
die "Invalid version number: $opt{compat-version}\n" if $v >= 1000 || $s >= 1000;
$opt{compat-version} = sprintf "%d.%03d%03d", $r, $v, $s;
} else {
    $opt{compat-version} = 5;
}

# Never use C comments in this file!!!!!
my $ccs = '/\*\*';
my $cce = '\*\/';
my $rcce = quotemeta $cce;
my $rccs = quotemeta $ccs;
my $API = map { /\$(w+)\[(\^[\*]|\^)[\^]|\^[\^]|\^[0-9]*\$(w+)\$/}
? ( $1 => {
  ($2    ? ( base => $2 ) : () ),
  ($3    ? ( todo => $3 ) : () ),
  (index($4, 'v') >= 0 ? ( varargs => 1 ) : () ),
  (index($4, 'p') >= 0 ? ( provided => 1 ) : () ),
  (index($4, 'n') >= 0 ? ( nothxarg => 1 ) : () ),
} )
: die "invalid spec: $_" ) qw( 
AvFILLp|5.004050||p 
AvFILL||n 
CLASS||n 
CX_CURPAD_SAVE||n 
CX_CURPAD_SV||n 
CopFILEAV|5.006000||p 
CopFILEGV_set|5.006000||p 
CopFILEGV|5.006000||p 
CopFILESV|5.006000||p 
CopFILE_set|5.006000||p 
CopFILE|5.006000||p 
CopSTASHPV_set|5.006000||p 
CopSTASHPV|5.006000||p 
CopSTASH_eq|5.006000||p 
CopSTASH_set|5.006000||p 
CopSTASH|5.006000||p 
CopyD|5.009002||p 
Copy||n 
CvPADLIST||n 
CvSTASH||n 
CvWEAKOUTSIDE||n 
DEFSV|5.004050||p 
END_EXTERN_C|5.005000||p 
ENTER||n 
ERRSV|5.004050||p 
EXTEND||n 
EXTERN_C|5.005000||p 
FREETMPS||n 
GIMME_V|5.004000||n 
GIMME||n 
GROK_NUMERIC_RADIX|5.007002||p 
G_ARRAY||n 
G_DISCARD||n 
G_EVAL||n 
G_NOARGS||n 
G_SCALAR||n 
G_VOID|5.004000||n 
GetVars||n 
GvSV||n 
Gv_AMupdate||n 
)
HEf_SVKEY|5.004000|
HeHASH|5.004000|
HeKEY|5.004000|
HeKLEN|5.004000|
HePV|5.004000|
HeSVKEY_force|5.004000|
HeSVKEY_set|5.004000|
HeSVKEY|5.004000|
HeVAL|5.004000|
HvNAME||
INT2PTR|5.006000|p
IN_LOCALE_COMPILETIME|5.007002|p
IN_LOCALE_RUNTIME|5.007002|p
IN_LOCALE|5.007002|p
IN_PERL_COMPILETIME|5.008001|p
IS_NUMBER_GREATER_THAN_UV_MAX|5.007002|p
IS_NUMBER_INFINITY|5.007002|p
IS_NUMBER_IN_UV|5.007002|p
IS_NUMBER_NAN|5.007003|p
IS_NUMBER_NEG|5.007002|p
IS_NUMBER_NOT_INT|5.007002|p
IVSIZE|5.006000|p
IVTYPE|5.006000|p
IVdf|5.006000|p
LEAVE||
LVRET||
MARK||
MY_CXT_CLONE|5.009002|p
MY_CXT_INIT|5.007003|p
MY_CXT|5.007003|p
MoveD|5.009002|p
Move||
NEWSV||
NOOP|5.005000|p
NUM2PTR|5.006000|p
NVTYPE|5.006000|p
NVer|5.006001|p
NVtr|5.006001|p
NVgr|5.006001|p
Newc||
Newz||
New||
Nullav||
Nullch||
Nullcv||
Nullhv||
Nullsv||
ORIGMARK||
PAD_BASE_SV||
PAD_CLONE_VARS||
PAD_COMPNAME_FLAGS||
PAD_COMPNAME_GEN_set||
PAD_COMPNAME_GEN||
PAD_COMPNAME_OURSTASH||
PAD_COMPNAME_PV||
PAD_COMPNAME_TYPE||
PAD_RESTORE_LOCAL||
PAD_SAVE_LOCAL||
PAD_SAVE_SETNULLPAD||
PAD_SETSV||
PAD_SET_CUR_NOSAVE||
PAD_SET_CUR||
PAD_SV||
PAD_SV||
PERL_BCDVERSION|5.009003||p
PERL_GCC_BRACE_GROUPS_FORBIDDEN|5.008001||p
PERL_INT_MAX|5.004000||p
PERL_INT_MIN|5.004000||p
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if (exists $opt{'list-unsupported'}) {
    my $f;
    for $f (sort { lc $a cmp lc $b } keys %API) {
        next unless $API{$f}{todo};
        print "$f ", 'x(40-length($f)), " ", format_version($API{$f}{todo}), "\n";
    }
    exit 0;
}

# Scan for possible replacement candidates

my(%replace, %need, %hints, %depends);
my $replace = 0;
my $hint = "";
while (<DATA>) {
    if (Shint) {
        if (m{^s*\s*\$hint\s*$}) {
            $hints{$hint} ||= '';  # suppress warning with older perls
            $hints{$hint} .= "$1\n";
        }
        else {
            Shint = "";
        }
    }
    $hint = $1 if m{^s*$rcces\sHint:\s+\(w+\)s*\$};

    $replace = $1 if m{^s*$rcces\sReplace:\s+\(d+\)s+\$rcces\s*$};
    $replace{S2} = $1 if $replace and m{^s*\s*$rcces\s+Replace\s+(\w+)(?<![:\s])\s*$};
    $replace{S1} = $2 if m{^s*$rcces\s+Replace \(w+\) with \(w+\)s+\$rcces\s*$};

    if (m{^s*$rcces\s+(\w+)\s+depends\s+on\s+(\w+)(s*\w+)*)s+\$rcces\s*$}) {
        push @$depends{S1}, map { s/\s+/\g{} s/\s+/\_ } split /\s+/, $2;
    }

    $need{S1} = 1 if m{^s*\$rcces\s+\$need\(\w+\)(?:_GLOBAL)?\$};
}

if (exists $opt{api-info}) {
    my $f;
    my $count = 0;
    my $match = $opt{api-info} =~ m!^/(.*)$/! ? $1 : "^Q$opt{api-info}E";
    for $f (sort { lc $a cmp lc $b } keys %API) {
        next unless $f =~ /$match/;
        print "\n=== $f ===\n\n";
        my $info = 0;
        if (SAPI{$f}{base} || SAPI{$f}{todo}) {
            my $base = format_version(SAPI{$f}{base} || SAPI{$f}{todo});
            print "Supported at least starting from perl-$base\n";
            $info++;
        }
        if (SAPI{$f}{provided}) {
            my $todo = SAPI{$f}{todo} ? format_version(SAPI{$f}{todo}) : "5.003";
            print "Support by $ppport provided back to perl-$todo\n";
            print "Support needs to be explicitly requested by NEED_$f\n" if exists $need{$f};
            print "Depends on: \", join\('\,\', @$depends{$f})), \":\n" if exists $depends{$f};
            print "$hints{$f}" if exists $hints{$f};
            $info++;
        }
        unless ($info) {
            print "No portability information available\n";
        }
    }
}
$count++;
}
if ($count > 0) {
print "\n";
}
else {
print "Found no API matching '$opt{'api-info'}'.\n";
}
exit 0;
}
if (exists $opt{'list-provided'}) {
my $f;
for $f (sort { lc $a cmp lc $b } keys %API) {
next unless $API{$f}{provided};
my @flags;
push @flags, 'explicit' if exists $need{$f};
push @flags, 'depend' if exists $depends{$f};
push @flags, 'hint' if exists $hints{$f};
my $flags = @flags ? ' ['.join(', ', @flags).']' : '';
print "$f$flags\n";
}
exit 0;
}
my @files;
my @srcext = qw( xs c h cc cpp );
my $srcext = join '|', @srcext;
if (@ARGV) {
my %seen;
@files = grep { -f && !exists $seen{$_} } map { glob $_ } @ARGV;
}
else {
eval {
require File::Find;
File::Find::find(sub {
$File::Find::name =~ /\.($srcext)$/i
and push @files, $File::Find::name;
}, '.');
};
if ($@) {
@files = map { glob "*.$_" } @srcext;
}
}
if (!@ARGV || $opt{filter}) {
my(@in, @out);

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my %xsc = map { /(.*)\.xs$/ ? ("$1.c" => 1, "$1.cc" => 1) : () } @files;
for (@files) {
my $out = exists $xsc{$_} || /\b\Q$ppport\E$/i || !/\.($srcext)$/i;
push @{ $out ? \@out : \@in }, $_;
}
if (@ARGV && @out) {
warning("Skipping the following files (use --nofilter to avoid this):\n| ", join "\n| ", @out);
}
@files = @in;
}
unless (@files) {
die "No input files given!\n";
}
my(%files, %global, %revreplace);
%revreplace = reverse %replace;
my $filename;
my $patch_opened = 0;
for $filename (@files) {
unless (open IN, "<$filename") {
warn "Unable to read from $filename: $!\n";
next;
}
info("Scanning $filename ...");
my $c = do { local $/; <IN> };
close IN;
my %file = (orig => $c, changes => 0);
# temporarily remove C comments from the code
my @ccom;
$c =~ s{
(
[^"'/]+
|
(?:"[^"\\]*(?:\\.[^"\\]*)*" [^"'/]*)+
|
(?:'[^'\\]*(?:\\.[^'\\]*)*' [^"'/]*)+
)
|
(/ (?:
\*[^*]*\*+(?:[^$ccs][^*]*\*+)* /
|
/[^\r\n]*

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defined $2 and push @ccom, $2;
defined $1 ? $1 : "$ccs#$ccom$cece";
}egsx;

$file{ccom} = \@ecom;
ofile{code} = $c;
$file{has_inc_ppport} = ($c =~ /\#.*include.*\Q$ppport\E/);

my $func;

for $func (keys %API) {
    my $match = $func;
    $match .= "$revreplace[$func]" if exists $revreplace[$func];
    if ($c =~ /\b(?:Perl_)?($match)\b/) {
        $file{uses_replace}{$1}++ if exists $revreplace{$func} && $1 eq $revreplace{$func};
        $file{uses_Perl}{$func}++ if $c =~ /\bPerl_$func\b/;
    }
    if (exists $API{$func}[provided]) {
        if (exists $API{$func}[base] || $API{$func}[base] > $opt{compat-version}) {
            $file{uses}{$func}++;
            my @deps = rec_depend($func);
            if (@deps) {
                $file{uses_deps}{$func} = \@deps;
                for (@deps) {
                    $file{uses}{$_} = 0 unless exists $file{uses}{$_};
                }
            }
        }
    }
    if (exists $API{$func}[todo] && $API{$func}[todo] > $opt{compat-version}) {
        if ($c =~ /\b$func\b/) {
            $file{uses_todo}{$func}++;
        }
    }
}

while ($c =~ /^\$HS*#$HS*define\$HS+(NEED_(\w+?)(_GLOBAL)?)\b/mg) {
    if (exists $need{$2}) {
        $file{defined $3 ? 'needed_global' : 'needed_static'}{$2}++;
    } else {

warning("Possibly wrong #define $1 in $filename");
}
}
for (qw(uses needs uses_todo needed_global needed_static)) {
for $func (keys %{$file{$_}}) {
push @{$global{$_}{$func}}, $filename;
}
}
$files{$filename} = \%file;
}
# Globally resolve NEED_'s
my $need;
for $need (keys %{$global{needs}}) {
if (@{$global{needs}{$need}} > 1) {
my @targets = @{$global{needs}{$need}};
my @t = grep $files{$_}{needed_global}{$need}, @targets;
@targets = @t if @t;
@t = grep /\.xs$/i, @targets;
@targets = @t if @t;
my $target = shift @targets;
$files{$target}{needs}{$need} = 'global';
for (@{$global{needs}{$need}}) {
$files{$_}{needs}{$need} = 'extern' if $_ ne $target;
}
}
}
for $filename (@files) {
exists $files{$filename} or next;
info("=== Analyzing $filename ===");
my %file = %{$files{$filename}};
my $func;
my $c = $file{code};
for $func (sort keys %{$file{uses_Perl}}) {
if ($API{$func}{varargs}) {
my $changes = ($c =~ s{\b(Perl_$func\s*\(\s*)(?!aTHX_?)(\)|[^\s)]*\))}
{ $1 . ($2 eq ')' ? 'aTHX' : 'aTHX_ ') . $2 }ge);
if ($changes) {
warning("Doesn't pass interpreter argument aTHX to Perl_$func");
$file{changes} += $changes;
}
}

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else {
  warning("Uses Perl_$func instead of $func");
  $file{changes} += ($c =~ s{\bPerl_$func\(\s*\)\((\s*aTHX_?)?\s*} {$func\$1\(}g);
}
}

for $func (sort keys %{$file{uses_replace}}) {
  warning("Uses $func instead of $replace{$func}");
  $file{changes} += ($c =~ s/b$func/b$replace{$func}/g);
}

for $func (sort keys %{$file{uses}}) {
  next unless $file{uses}[$func]; # if it's only a dependency
  if (exists $file{uses_deps}{$func}) {
    diag("Uses $func, which depends on ", join(’, ’, @{$file{uses_deps}{$func}}));
  }
  elsif (exists $replace{$func}) {
    warning("Uses $func instead of $replace{$func}");
    $file{changes} += ($c =~ s/b$func/b$replace{$func}/g);
  }
  else {
    diag("Uses $func");
  }
  hint($func);
}

for $func (sort keys %{$file{uses_todo}}) {
  warning("Uses $func, which may not be portable below perl ",
    format_version($API{$func}{todo}));
}

for $func (sort keys %{$file{needed_static}}) {
  my $message = ";
  if (not exists $file{uses}{$func}) {
    $message = "No need to define NEED_$func if $func is never used";
  } elsif (exists $file{needs}{$func} && $file{needs}{$func} ne 'static') {
    $message = "No need to define NEED_$func when already needed globally";
  } if ($message) {
    diag($message);
    $file{changes} += ($c =~ s/^$HS*#$HS*define$HS+NEED_$func\b.*$LF//mg);
  }
}

for $func (sort keys %{$file{needed_global}}) {
  my $message = "
  if (not exists $file{uses}{$func}) {
    $message = "No need to define NEED_$func if $func is never used";
  } elsif (exists $file{needs}{$func} && $file{needs}{$func} ne 'static') {
    $message = "No need to define NEED_$func when already needed globally";
  } if ($message) {
    diag($message);
    $file{changes} += ($c =~ s/^$HS*#$HS*define$HS+NEED_$func\b.*$LF//mg);
  }
}
if (not exists $global{uses}[$func]) {
    $message = "No need to define NEED_$func_GLOBAL if $func is never used";
}
elsif (exists $file{needs}[$func]) {
    if ($file{needs}[$func] eq 'extern') {
        $message = "No need to define NEED_$func_GLOBAL when already needed globally";
    } elsif ($file{needs}[$func] eq 'static') {
        $message = "No need to define NEED_$func_GLOBAL when only used in this file";
    }
}
if ($message) {
    diag($message);
    $file{changes} += ($c =~ s/^$HS*#$HS*define$HS+NEED_$func_GLOBAL\b.*$LF//mg);
}
}

$file{needs_inc_ppport} = keys %{ $file{uses} };

if ($file{needs_inc_ppport}) {
    my $pp = "";

    for $func (sort keys %{ $file{needs} }) {
        my $type = $file{needs}[$func];
        next if $type eq 'extern';
        my $suffix = $type eq 'global' ? '_GLOBAL' : '';
        unless (exists $file{needed_$type}[$func]) {
            if ($type eq 'global') {
                diag("Files [@ $global{needs}[$func]] need $func, adding global request");
            } else {
                diag("File needs $func, adding static request");
            }
            $pp .= "#define NEED_$func$suffix\n";
        }
    }

    if ($pp && ($c =~ s/^(?=$HS*#$HS*define$HS+NEED_\w+/\$pp/m)) {
        $pp = "";
        $file{changes}++;
    }

    unless ($file{has_inc_ppport}) {
        diag("Needs to include '$ppport'");
        $pp .= qq(#include "$ppport\n")
    }

    if ($pp) {
$file{changes} += ($c =~ s/^($HS*#$HS*define$HS+NEED_\w+.*)$/1$ppp/m)
|| ($c =~ s/^\(?=\$HS*\$HS*include.\Q$ppport\E)/$pp/m)
|| ($c =~ s/^\(?=\$HS*\$HS*include.\Q$SUB.*ss.*?\E)/$1$ppp/m)
|| ($c =~ s/^/$ppp/);
}
}
else {
  if ($file{has_inc_ppport}) {
    diag("No need to include '$ppport'");
    $file{changes} += ($c =~ s/^$HS*?#$HS*include.*\Q$ppport\E.*?$LF//m);
  }
}

# put back in our C comments
my $ix;
my $cppc = 0;
my @ccom = @{$file{ccom}};
for $ix (0 .. $#ccom) {
  if (!$opt{cplusplus} && $ccom[$ix] =~ s!^//!!) {
    $cppc++;
    $file{changes} += $c =~ s/$rccs$ix$rcce/$ccs$ccom[$ix] $cce/;
  }
  else {
    $c =~ s/$rccs$ix$rcce/$ccom[$ix]/;
  }
}

if ($cppc) {
  my $s = $cppc != 1 ? 's' : ';
  warning("Uses $cppc C++ style comment$s, which is not portable");
}

if ($file{changes}) {
  if (exists $opt{copy}) {
    my $newfile = "$filename$opt{copy}";
    if (-e $newfile) {
      error("'$newfile' already exists, refusing to write copy of '$filename'");
    }
    else {
      local *F;
      if (open F, ">$newfile") {
        info("Writing copy of '$filename' with changes to '$newfile'");
        print F $c;
        close F;
      }
      else {
        error("Cannot open '$newfile' for writing: $!");
      }
    }
  }
}
elsif (exists $opt{patch} || $opt{changes}) {
    if (exists $opt{patch}) {
        unless ($patch_opened) {
            if (open PATCH, ">$opt{patch}") {
                $patch_opened = 1;
            } else {
                error("Cannot open '$opt{patch}' for writing: $!");
                delete $opt{patch};
                $opt{changes} = 1;
                goto fallback;
            }
            mydiff(*PATCH, $filename, $c);
        } else {
            fallback:
            info("Suggested changes:");
            mydiff(*STDOUT, $filename, $c);
        } else {
            my $s = $file{changes} == 1 ? '' : 's';
            info("$file{changes} potentially required change$s detected");
        }
    } else {
        info("Looks good");
    }
}

close PATCH if $patch_opened;

exit 0;

sub mydiff
{
    local *F = shift;
    my($file, $str) = @_;
    my $diff;

    if (exists $opt{diff}) {
        $diff = run_diff($opt{diff}, $file, $str);
    }
if (defined $diff and can_use('Text::Diff')) {
    $diff = Text::Diff::diff($file, $str, { STYLE => 'Unified' });
    $diff = <<HEADER . $diff;
    --- $file
    +++ $file.patched
    HEADER
}

if (defined $diff) {
    $diff = run_diff('diff -u', $file, $str);
}

if (defined $diff) {
    $diff = run_diff('diff', $file, $str);
}

if (defined $diff) {
    error("Cannot generate a diff. Please install Text::Diff or use --copy.");
    return;
}

print F $diff;

sub run_diff {
    my($prog, $file, $str) = @_; 
    my $tmp = 'dppptemp';
    my $suf = 'aaa';
    my $diff = "
    local *F;

    while (-e "$tmp.$suf") { $suf++ }
    $tmp = "$tmp.$suf";

    if (open F, ">$tmp") {
        print F $str;
        close F;

        if (open F, "$prog $file $tmp") {
            while (<F>) {
                s/Q$tmp/E/$file.patched/;
                $diff .= ";
            }
            close F;
            unlink $tmp;
            return $diff;
        }
    }
}
unlink $tmp;
}
else {
    error("Cannot open '$tmp' for writing: $!");
}
return undef;
}

sub can_use {
    eval "use @_;";
    return $@ eq "";
}

sub rec_depend {
    my $func = shift;
    my %seen;
    return () unless exists $depends{$func};
    grep !$seen{$_}++, map { ($_, rec_depend($_)) } @{$depends{$func}};
}

sub parse_version {
    my $ver = shift;
    if ($ver =~ /^\d+\.(\d+\.)\d+$/) {
        return ($1, $2, $3);
    }
    elsif ($ver !~ /^\d+\.(\d+\.)\d+$/) {
        die "cannot parse version '$ver\n";
    }
    $ver =~ s/_//g;
    $ver =~ s/$/000000/;
    my($r,$v,$s) = $ver =~ /\d+\.(\d+\.)\d+/;
    $v = int $v;
    $s = int $s;
    if ($r < 5 || ($r == 5 && $v < 6)) {
        if ($s % 10) {
            die "cannot parse version '$ver\n";
        }
return ($r, $v, $s);
}

sub format_version
{
my $ver = shift;

$ver =~ s/$/000000/;
my($r,$v,$s) = $ver =~ /\d+(\d{3})\d{3}\d*/;

$v = int $v;
$s = int $s;

if ($r < 5 || ($r == 5 && $v < 6)) {
  if ($s % 10) {
    die "invalid version '$ver'\n";
  }
  $s /= 10;

  $ver = sprintf "%d.%03d", $r, $v;
  $s > 0 and $ver .= sprintf "_\%02d", $s;

  return $ver;
}

return sprintf "%d.%d.%d", $r, $v, $s;
}

sub info
{
$opt{quiet} and return;
print @_, "\n";
}

sub diag
{
$opt{quiet} and return;
$opt{diag} and print @_, "\n";
}

sub warning
{
$opt{quiet} and return;
print "***", @_, "\n";
}
sub error
{
    print "*** ERROR: ", @_, "\n";
}

my %given_hints;
sub hint
{
    $opt{quiet} and return;
    $opt{hints} or return;
    my $func = shift;
    exists $hints{$func} or return;
    $given_hints{$func}++ and return;
    my $hint = $hints{$func};
    $hint =~ s/^/   /mg;
    print "   --- hint for $func ---\n", $hint;
}

sub usage
{
    my($usage) = do { local(@ARGV,$/)=($0); <> } =~ /^=head\d$HS+SYNOPSIS\s*^(.*?)(.*♥)s*\=/ms;
    my %M = ( 'I' => '*' );
    $usage =~ s/\^\S*perl\S+/S+/g;
    $usage =~ s/\S+$/S+$/g;
    print "Usage: $usage
See perldoc $0 for details.
ENDUSAGE

exit 2;
}

__DATA__
/*

Found in path(s):
*/

1.20 llvm-toolchain-7 7.0.1-8+deb10u2
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---------------------------------------------------------------------------

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1.27 spdx-license-ids 3.0.5

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charset = utf-8
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indent_style = tab
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[*.md,yml]]
indent_style = space

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trim_trailing_whitespace = false
workflow "Test" {
on = "push"
resolves = ["npm"]
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```sh
npm install spdx-license-ids
```

## [Node.js](https://nodejs.org/) API

### require('spdx-license-ids')

Type: `string[]`

All license IDs except for the currently deprecated ones.

```javascript
const ids = require('spdx-license-ids');
//=> ['0BSD', 'AAL', 'ADSL', 'AFL-1.1', 'AFL-1.2', 'AFL-2.0', 'AFL-2.1', 'AFL-3.0', 'AGPL-1.0-only', ...]

ids.includes('BSD-3-Clause'); //=> true
ids.includes('CC-BY-1.0'); //=> true

ids.includes('GPL-3.0'); //=> false
```

### require('spdx-license-ids/deprecated')

Type: `string[]`

Deprecated license IDs.

```javascript
```
const deprecatedIds = require('spdx-license-ids/deprecated');
//=> ['AGPL-1.0', 'AGPL-3.0', 'GFDL-1.1', 'GFDL-1.2', 'GFDL-1.3', 'GPL-1.0', 'GPL-2.0', ...]

deprecatedIds.includes('BSD-3-Clause'); //=> false
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[*.workflow]
indent_style = space

1.28 libnuma 2.0.12-1

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1.29 openssl 1.1.1k-r0

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1.30 urllib3 1.26.6

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1.31 kronosnet 1.8-2

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prepared so as to be conveniently linked with application programs
(which use some of those functions and data) to form executables.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

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>>> freebsd-base64-4.8
>>> unicode-5.0

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base64.c -- routines to encode/decode base64 data
$OpenLDAP: pkg/ldap/libraries/liblutil/base64.c,v 1.15 2006/01/03 22:12:11 kurt Exp $ /
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1.51 six 1.12.0

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1.52 python-uritemplate 0.6-3

1.52.1 Available under license:
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uritemplate
==========

.. image:: https://secure.travis-ci.org/uri-templates/uritemplate-py.png?branch=master
   :alt: build status
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This is a Python implementation of `RFC6570`_, URI Template, and can expand templates up to and including Level 4 in that specification.

It exposes a method, *expand*. For example:

.. code-block:: python

   >>> from uritemplate import expand
   >>> expand("http://www.{domain}/", {"domain": "foo.com"})
   'http://www.foo.com/

It also exposes a method *variables* that returns all variables used in a uritemplate. For example:

.. code-block:: python

   >>> from uritemplate import variables
   >>> variables('http://www{.domain*}{/top,next}{?q:20}')
   set(["domain", 'next', 'q', 'top'])

This function can be useful to determine what keywords are available to be expanded.


Requirements
------------

uritemplate works with Python 2.5+.

.. note:: You need to install `simplejson`_ module for Python 2.5.

.. _simplejson: https://pypi.python.org/pypi/simplejson/
Install

The easiest way to install uritemplate is with pip:

    $ pip install uritemplate

See its `Python Package Index entry`_ for more.

.. _Python Package Index entry: http://pypi.python.org/pypi/uritemplate

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1.54 sysv-init 2.93-8
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1.55 jcmtturner-aescts v1.0.1

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1.57 nginx 1.21.0-1~buster

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   * Paul Vixie <paul@vix.com> uunet!decwrl!vixie!paul
*/

Found in path(s):
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/env.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/cron.h
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/do_command.c
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/cron.c
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" * Paul Vixie <paul@vix.com> uunet!decwrl!vixie!paul
" */
"
$Id: crontab.1,v 2.4 1993/12/31 10:47:33 vixie Exp $
```

Found in path(s):
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.org/crontab.1

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```
Read the comments at the top of the Makefile, then edit the area marked 'configurable stuff'.

Edit config.h. The stuff I expect you to change is down a bit from the top of the file, but it's clearly marked. Also look at pathnames.h.

You don't have to create the /var/cron or /var/cron/tabs directories, since both the daemon and the `crontab' program will do this the first time they run if they don't exist. You do need to have a /var, though -- just "mkdir /var" if you don't have one, or you can "mkdir /usr/var; ln -s /usr/var /var" if you expect your /var to have a lot of stuff in it.

You will also need /usr/local/etc and /usr/local/bin directories unless you change the Makefile. These will have to be created by hand, but if you are a long-time Usenet user you probably have them already. /usr/local/man is where I keep my man pages, but I have the source for `man' and you probably do not. Therefore you may have to put the man pages into /usr/man/manl, which will be hard since there will be name collisions. (Note that the man command was originally written by Bill Joy before he left Berkeley, and it contains no AT&T code, so it is in UUNET's archive of freely-distributable BSD code.)

LINUX note: /usr/include/paths.h on some linux systems shows _PATH_SENDMAIL to be /usr/bin/sendmail even though sendmail is installed in /usr/lib. you should check this out.

say:
make all

su and say:
make install

Note that if I can get you to "su and say" something just by asking, you have a very serious security problem on your system and you should look into it.
Edit your /usr/lib/crontab file into little pieces -- see the CONVERSION file for help on this.

Use the `crontab' command to install all the little pieces you just created. Some examples (see below before trying any of these!)

crontab -u uucp -r /usr/lib/uucp/crontab.src
crontab -u news -r /usr/lib/news/crontab.src
crontab -u root -r /usr/adm/crontab.src

Notes on above examples: (1) the .src files are copied at the time the command is issued; changing the source files later will have no effect until they are reinstalled with another `crontab -r' command. (2) The crontab command will affect the crontab of the person using the command unless `-u USER' is given; `-u' only works for root. When using most `su' commands under most BSD's, `crontab' will still think of you as yourself even though you may think of yourself as root -- so use `-u' liberally. (3) the `-r' option stands for `replace'; check the man page for crontab(1) for other possibilities.

Kill your existing cron daemon -- do `ps aux' and look for /etc/cron.

Edit your /etc/rc or /etc/rc.local, looking for the line that starts up /etc/cron. Comment it out and add a line to start the new cron daemon -- usually /usr/local/etc/cron, unless you changed it in the Makefile.

Start up this cron daemon yourself as root. Just type /usr/local/etc/cron (or whatever); no '&' is needed since the daemon forks itself and the process you executed returns immediately.

ATT notes: for those people unfortunate enough to be stuck on a AT&T UNIX, you will need the public-domain "libndir", found in the B News source and in any comp.sources.unix archive. You will also need to hack the code some.

Found in path(s):
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0p11-orig-tar-bz2/cron-3-0p11-orig-tar-gz/cron-3.0p11.orig/INSTALL

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  3.0pl1.orig/popen.c
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# */

Vixie Cron V3.0
December 27, 1993
[V2.2 was some time in 1992]
[V2.1 was May 29, 1991]
[V2.0 was July 5, 1990]
[V2.0-beta was December 9, 1988]
[V1.0 was May 6, 1987]
Paul Vixie

This is a version of 'cron' that is known to run on BSD 4.[23] systems. It
is functionally based on the SysV cron, which means that each user can have
their own crontab file (all crontab files are stored in a read-protected
directory, usually /var/cron/tabs). No direct support is provided for
'at'; you can continue to run 'atrun' from the crontab as you have been doing. If you don't have atrun (i.e., System V) you are in trouble.

A messages is logged each time a command is executed; also, the files "allow" and "deny" in /var/cron can be used to control access to the "crontab" command (which installs crontabs). It hasn't been tested on SysV, although some effort has gone into making the port an easy one.

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To use this: Sorry, folks, there is no cutsey 'Configure' script. You'll have to go edit a couple of files... So, here's the checklist:

Read all the FEATURES, INSTALL, and CONVERSION files
Edit config.h
Edit Makefile
(both of these files have instructions inside; note that
some things in config.h are definable in Makefile and are therefore surrounded by ifndef...#endif)
'make'
'su' and 'make install'
(you may have to install the man pages by hand)
kill your existing cron process
(actually you can run your existing cron if you want, but why?)
built new crontabs using /usr/lib/{crontab,crontab.local}
(either put them all in "root"'s crontab, or divide it up
and rip out all the 'su' commands, collapse the lengthy lists into ranges with steps -- basically, this step is as much work as you want to make it)
start up the new cron
(must be done as root)
watch it. test it with 'crontab -r' and watch the daemon track your changes.
if you like it, change your /etc/{rc,rc.local} to use it instead of the old one.

$Id: README,v 2.3 1993/12/28 08:34:43 vixie Exp $

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* /opt/cola/permits/1097313584_1611670802.240/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/README
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* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/Makefile
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* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/pathnames.h
* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-3.0pl1.orig/externs.h
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* @(#)bitstring.h 5.2 (Berkeley) 4/4/90
*/

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* /opt/cola/permits/1097313584_1611670802.24/0/cron-3-0pl1-orig-tar-bz2/cron-3-0pl1-orig-tar-gz/cron-
3.0pl1.orig/bitstring.h

1.60 gnutls 3.6.7-4+deb10u6

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##
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Here is a list of the main contributors to lm-sensors version 3.

* Frodo Looijaard
  Original author of libsensors, sensors-detect, sensors and isadump.
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  Original author of sensord.
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  Rewrite of sensors-detect.
  Support for multiple configuration files in libsensors.

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THE BASIC LIBRARY FUNCTIONS
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1.71 asn1crypto 0.24.0-1

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1.72 shadow 4.5-1.1

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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands
as a successor of a language called ABC. Guido remains Python's
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us)
in Reston, Virginia where he released several versions of the
software.

In May 2000, Guido and the Python core development team moved to
BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope
Corporation, see http://www.zope.com). In 2001, the Python Software
Foundation (PSF, see http://www.python.org/psf/) was formed, a
non-profit organization created specifically to own Python-related
Intellectual Property. Zope Corporation is a sponsoring member of
the PSF.

All Python releases are Open Source (see http://www.opensource.org
for the Open Source Definition). Historically, most, but not all, Python
releases have also been GPL-compatible; the table below summarizes
the various releases.

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This is a bundle of X.509 certificates of public Certificate Authorities (CA). These were automatically extracted from Mozilla's root certificates file (certdata.txt). This file can be found in the mozilla source tree: http://mxr.mozilla.org/mozilla/source/security/nss/lib/ckfw/builtins/certdata.txt?raw=1#
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@(#) SRCsfile: certdata.txt,v $ $Revision: 1.80 $ $Date: 2011/11/03 15:11:58 $ License

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1.74 @types/parse-json 4.0.0

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1.75 redux-saga-core 1.1.3

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or later

This distribution replaces my earlier GetDate distribution, which was
only a date parser. The date parser contained in this distribution is
far superior to the yacc based parser, and a *lot* faster.

The parser contained here will only parse absolute dates, if you want a
date parser that can parse relative dates then take a look at the Time
modules by David Muir on CPAN.

You install the library by running these commands:

perl Makefile.PL
make
make test
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=head1 NAME

Date::Language::Hungarian - Magyar format for Date::Format

=head1 SYNOPSIS

my $lang = Date::Language->new('Hungarian');
print $lang->time2str("%a %b %e %T %Y", time);

@lt = localtime(time);
print $lang->time2str($template, time);
print $lang->strftime($template, @lt);
print $lang->time2str($template, time, $zone);
print $lang->strftime($template, @lt, $zone);

print $lang->ctime(time);
print $lang->asctime(@lt);

print $lang->ctime(time, $zone);
print $lang->asctime(@lt, $zone);

See L<Date::Format>.

=head1 AUTHOR

Paula Goddard (paula -at- paulacska -dot- com)

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=encoding utf8

=head1 NAME

Date::Language::Bulgarian - localization for Date::Format

=head1 DESCRIPTION

This is Bulgarian localization for Date::Format.
It is important to note that this module source code is in utf8.
All strings which it outputs are in utf8, so it is safe to use it
Currently only with English. You are left alone to try and convert the output when using different Date::Language::* in the same application. This should be addressed in the future.

=head1 SYNOPSIS

use strict;
use warnings;
use Date::Language;
local $|=;
my $template = '%a %b %e %T %Y (%Y-%m-%d %H:%M:%S);
my $time=1290883821; #or just use time();
my $lt = localtime($time);
my %languages = qw(English GMT German EEST Bulgarian EET);
binmode(select,'utf8');

foreach my $l(keys %languages){
  my $lang = Date::Language->new($l);
  my $zone = $languages{$l};
  print $/. "$l $zone";
  print $lang->time2str($template, $time);
  print $lang->time2str($template, $time, $zone);
  print $lang->strftime($template, $lt);
}

=head1 AUTHOR

Krasimir Berov (berov@cpan.org)

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```
Metadata-Version: 1.1
Name: roman
Version: 2.0.0
Summary: Integer to Roman numerals converter
Home-page: http://pypi.python.org/pypi/roman
Author: Mark Pilgrim
Author-email: f8dy@diveintopython.org
License: Python 2.1.1
Description: =======

CHANGES

========

2.0.0 (2013-02-25)

-----------

- Added Python 3.3 and PyPy support.
```
- Added tests.

1.4.0 (2009-07-23)
------------------
- Initial PyPI release.

Keywords: roman
Platform: UNKNOWN
Classifier: Development Status :: 5 - Production/Stable
Classifier: Intended Audience :: Developers
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
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Found in path(s):
* /opt/cola/permits/1129505197_1612286462.39/0/python-roman-2.0.0-orig-1-tar-gz/roman-2.0.0/PKG-INFO
* /opt/cola/permits/1129505197_1612286462.39/0/python-roman-2.0.0-orig-1-tar-gz/roman-2.0.0/src/roman.egg-info/PKG-INFO

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1.101 go-connections v0.3.0
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1.103 @babel/helper-module-imports 7.8.3

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@end if tex
@if info
@center END OF TERMS AND CONDITIONS
@end if info
@if page
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1.108 groff 1.22.4-3+deb10u1

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This is the Debian GNU/Linux prepackaged version of the GNU groff
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GNU groff was written by James Clark <jjc@jclark.com>.

This Debian package was previously maintained by Fabrizio Polacco
<fpolacco@debian.org>.
It is now maintained by Colin Watson <cjwatson@debian.org>.

The original tarball came from <URL:ftp://groff.ffii.org/pub/groff/>:
48fa768dd6fdeb7968041dd5ae8e2b02  groff-1.20.1.tar.gz
and was simply renamed to groff_1.20.1.orig.tar.gz.

Some patches have been applied to groff outside the debian directory.

The Debian diff also appends /usr/share/groff/tmac to the default macro path
for compatibility with versions of groff earlier than 1.17 (patch by Colin
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I e-mailed the upstream maintainer to clarify the intent of this. Here is the relevant part of the reply:

From: Werner LEMBERG <wl@gnu.org>
To: cjwatson@debian.org
Subject: Re: Clarifying the scope of groff/LICENSE
Date: Thu, 18 Nov 2004 00:19:52 +0100 (CET)

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> assume that groff/LICENSE grants me permission to distribute
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> GPL?

This is the intention, of course.

> If not, is the FDL thing from yesteryear going to be resolved some
> other way? I just want to make sure that I don't get forced to
> remove the groff manual, since that would really diminish the
> quality of the Debian package IMHO.

Sigh. I haven't found time to revert the FDL stuff back to GPL. It basically affects only Bernd Warken and me, and we have both agreed to return to GPL in case FDL makes problems.

(Nota that this e-mail predates the current wording in LICENSES.)

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1.112 net-tools 1.60+git20180626.aebd88e-1

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1.115 x-crypto v0.0.0-20200204104054-c9f3fb736b72

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1.118 postcss-value-parser 4.1.0

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1.119 isl 0.20-2

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1.121 bsdmainutils 11.1.2+b1

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Then the package has been maintained by Charles Briscoe-Smith <cpbs@debian.org>. I gathered data for the 1999-2001 calendar files from various sources on the Internet, and I'd also like to thank Oliver Elphick, Julian Gilbey, Daniel Martin and Jaldhar H. Vyas for providing much useful data on the various religious calendars. I have edited the files they provided to fit calendar's requirements, so any errors should be attributed to me.

After cpbs@debian.org, Marco d'Itri <md@linux.it> maintained it for almost two years, before Tollef Fog Heen <tfheen@debian.org> took over. The package is now maintained by Graham Wilson <bob@decoy.wox.org>.

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This is the Debian GNU/Linux prepackaged version of the translation files of the EXT2 file system utilities. The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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1.126 gnutls 3.5.18-1ubuntu1.4

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##
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```

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1.127 ansi-styles 3.2.1

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ASN.1 library for Python
------------------------
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This is a free and open source implementation of ASN.1 types and codecs
as a Python package. It has been first written to support particular
protocol (SNMP) but then generalized to be suitable for a wide range
of protocols based on
[ASN.1 specification](https://www.itu.int/rec/dologin_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items).

Features
--------

* Generic implementation of ASN.1 types (X.208)
* Standards compliant BER/CER/DER codecs
* Dumps/loads ASN.1 structures from Python types
* 100% Python, works with Python 2.4 up to Python 3.6
* MT-safe
* Contributed ASN.1 compiler [Asn1ate](https://github.com/kimgr/asn1ate)

Why using pyasn1
ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1. [Communication between heterogeneous systems](http://www.oss.com/asn1/dubuisson.html) by Olivier Dubuisson is one of those high quality books freely available on the Internet.

The pyasn1 package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

### How to use pyasn1

With pyasn1 you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```bash
Record ::= SEQUENCE {
    id        INTEGER,
    room  [0] INTEGER OPTIONAL,
    house [1] INTEGER DEFAULT 0
}
```

Could be expressed in pyasn1 like this:

```python
class Record(Sequence):
    componentType = NamedTypes(
        NamedType('id', Integer()),
        OptionalNamedType('room', Integer().subtype(implicitTag=Tag(tagClassContext, tagFormatSimple, 0))
    ),
```
It is in the spirit of ASN.1 to take abstract data description and turn it into a programming language specific form. Once you have your ASN.1 data structure expressed in Python, you can use it along the lines of similar Python type (e.g. ASN.1 `SET` is similar to Python `dict`, `SET OF` to `list`):

```python
>>> record = Record()
>>> record['id'] = 123
>>> record['room'] = 321
>>> str(record)
Record:
id=123
room=321
``` Part of the power of ASN.1 comes from its serialisation features. You can serialise your data structure and send it over the network.

```python
>>> from pyasn1.codec.der.encoder import encode
>>> substrate = encode(record)
>>> hexdump(substrate)
00000: 30 07 02 01 7B 80 02 01 41
```

Conversely, you can turn serialised ASN.1 content, as received from network or read from a file, into a Python object which you can introspect, modify, encode and send back.

```python
>>> from pyasn1.codec.der.decoder import decode
>>> received_record, rest_of_substrate = decode(substrate, asn1Spec=Record())
>>> for field in received_record:
...    print('{} is {}'.format(field, received_record[field]))
id is 123
room is 321
house is 0
```
The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, list, dict etc.). But ASN.1 types exhibit more complicated behaviour. To make life easier for a Pythonista, they can turn their pyasn1 classes into Python built-ins:

```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)
{'id': 123, 'room': 321, 'house': 0}
```

Or vice-versa -- you can initialize an ASN.1 structure from a tree of Python objects:

```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id': 123, 'room': 321, 'house': 0}, asn1Spec=Record())
>>> str(record)
Record:
id=123
room=321
```

With ASN.1 design, serialisation codecs are decoupled from data objects, so you could turn every single ASN.1 object into many different serialised forms. As of this moment, pyasn1 supports BER, DER, CER and Python built-ins codecs. The extremely compact PER encoding is expected to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the [documentation](http://snmplabs.com/pyasn1/), compiled ASN.1 modules for different protocols and file formats could be found in the pyasn1-modules [repo](https://github.com/etingof/pyasn1-modules).

How to get pyasn1

------------
The pyasn1 package is distributed under terms and conditions of 2-clause BSD [license](http://snmplabs.com/pyasn1/license.html). Source code is freely available as a GitHub [repo](https://github.com/etingof/pyasn1).

You could `pip install pyasn1` or download it from [PyPI](https://pypi.python.org/pypi/pyasn1).

If something does not work as expected, [open an issue](https://github.com/etingof/pyasn1/issues) at GitHub or post your question [on Stack Overflow](http://stackoverflow.com/questions/ask) or try browsing pyasn1 [mailing list archives](https://sourceforge.net/p/pyasn1/mailman/pyasn1-users/).

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ASN.1 library for Python
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Abstract Syntax Notation One (`ASN.1`<http://en.wikipedia.org/wiki/Abstract_Syntax_Notation_1x>`) is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.

The `pyasn1`<https://pypi.python.org/pypi/pyasn1/> library implements ASN.1 support in pure-Python.

What is ASN.1
--------------
ASN.1 is a large, arguably over-engineered and extremely old data modelling and serialisation tool. It is probably among the first serialisation protocols in the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first electronic mail (known as X.400). Later on if was split off the e-mail application and become a stand-alone tech still being actively supported by its designers and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the first, unsuccessful, version of the Internet. You can read many interesting discussions on that topic.

In the following years, generations of software engineers tackled the serialisation problem many times. We can see that in Google's ProtoBuffers or FlatBuffers, for example. Interestingly, many new takes on binary protocol design do not depart far from ASN.1 from technical perspective. It's more of a matter of striking a balance between processing overhead, wire format overhead and human readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

* Data types: the standard introduces a collection of basic data types (integers, bits, strings, arrays and records) that can be used for describing arbitrarily complex, nested data structures.

* Serialisation protocols: the above data structures could be converted into a series of octets for storage or transmission over the wire as well as recovered back into their structured form. The system is fully agnostic to hardware architectures differences.

* Schema language: ASN.1 data structures could be described in terms of a schema language for ASN.1 compiler to turn it into platform-specific implementation.

ASN.1 applications
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Being an old and generally successful standard, ASN.1 is widely adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:

* Signaling standards for the public switched telephone network (SS7 family)
* Network management standards (SNMP, CMIP)
* Directory standards (X.500 family, LDAP)
* Public Key Infrastructure standards (X.509, etc.)
* PBX control (CSTA)
* IP-based Videoconferencing (H.323 family)
* Biometrics (BIP, CBEFF, ACBio)
* Intelligent transportation (SAE J2735)
* Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

ASN.1 gotchas
-------------

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the `statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>`_ on discovered security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

Documentation
-------------

.. toctree::
   :maxdepth: 2

   /pyasn1/contents

Use case
--------

.. toctree::
   :maxdepth: 2

   /example-use-case

Download & Install
------------------

.. toctree::
   :maxdepth: 2

   /download

Changes
-------

All changes and release history is maintained in changelog. There you could also download the latest unreleased pyasn1 tarball containing the latest fixes and improvements.
License

The PyASN1 software is distributed under 2-clause BSD License.

Getting help


Books on ASN.1

The pyasn1 implementation is largely based on reading up the following awesome books:

* "ASN.1 - Communication between heterogeneous systems <http://www.oss.com/asn1/dubuisson.html>" by Olivier Dubuisson

Here you can get the official standards which is hard to read:


On the other end of the readability spectrum, here is a quick and sweet write up:


If you are working with ASN.1, we'd highly recommend reading a proper
book on the subject.

Found in path(s):
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0.4.2-orig-1.tar-gz/pyasn1-0.4.2/docs/source/contents.rst

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Metadata-Version: 1.1
Name: pyasn1
Version: 0.4.2
Summary: ASN.1 types and codecs
Home-page: https://github.com/etingof/pyasn1
Author: Ilya Etingof <etingof@gmail.com>
Author-email: etingof@gmail.com
License: BSD
Description: Pure-Python implementation of ASN.1 types and DER/BER/CER codecs (X.208)
Platform: any
Classifier: Development Status :: 5 - Production/Stable
Classifier: Environment :: Console
Classifier: Intended Audience :: Developers
Classifier: Intended Audience :: Education
Classifier: Intended Audience :: Information Technology
Classifier: Intended Audience :: System Administrators
Classifier: Intended Audience :: Telecommunications Industry
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Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.4
Classifier: Programming Language :: Python :: 2.5
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.2
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* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0.4.2-orig-1.tar-gz/pyasn1-0.4.2/pyasn1.egg-info/PKG-INFO

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* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/tests/compat/test_binary.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/type/opentype.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/codec/native/decoder.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/codec/ber/encoder.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/codec/der/encoder.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/type/constraint.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/type/namedval.py
* /opt/cola/permits/1125512698_1614699302.25/0/pyasn1-0-4-2-orig-1-tar-gz/pyasn1-0.4.2/pyasn1/debug.py
1.131 google-api-go-client v0.20.0

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package api

import {
    "bytes"
    "io/ioutil"
    "os"
    "path/filepath"
    "regexp"
    "testing"
}

// Files in this package use a BSD-style license.
var sentinel = regexp.MustCompile(`(//|#) Copyright \d\d\d\d (Google LLC|The Go Authors)\(\)\*( All rights reserved\)\* `( //##) Use of this source code is governed by a BSD-style
(//##) license that can be found in the LICENSE file.
` )

const prefix = "// Copyright"

// A few files have to be skipped.
var skip = map[string]bool{
    "tools.go": true, // This file requires another comment above the license.
    "internal/third_party/uritemplates/uritemplates.go": true, // This file is licensed to an individual.
    "internal/third_party/uritemplates/uritemplates_test.go": true, // This file is licensed to an individual.
}

// This test validates that all go files in the repo start with an appropriate license.
func TestLicense(t *testing.T) {
    err := filepath.Walk(".", func(path string, fi os.FileInfo, err error) error {
        if skip[path] {
            return nil
        }

        if err != nil {
            return err
        }

        if filepath.Ext(path) != ".go" && filepath.Ext(path) != ".sh" {
            return nil
        }
    })
src, err := ioutil.ReadFile(path)
if err != nil {
    return nil
}

// Verify that the license is matched.
if !sentinel.Match(src) {
    t.Errorf("%v: license header not present", path)
    return nil
}

// Also check it is at the top of .go files (but not .sh files, because they must have a shebang first).
if filepath.Ext(path) == ".go" && bytes.HasPrefix(src, []byte(prefix)) {
    t.Errorf("%v: license header not at the top", path)
}
return nil
}
if err != nil {
    t.Fatal(err)
}

1.132 naoina-go-stringutil v0.1.0

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1.134 openssh 7.9p1-10+deb10u1
1.134.1 Available under license :
/*$OpenBSD: poll.h,v 1.11 2003/12/10 23:10:08 millert Exp $ */

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   - Zlib is now external, in a library
   - The make-ssh-known-hosts script is no longer included
   - TSS has been removed
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   * @version 3.0 (December 2000)
   *
   * Optimised ANSI C code for the Rijndael cipher (now AES)
   *
   * @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
   * @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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*/

#ifndef HAVE_SETRESGID
int setresgid(gid_t, gid_t, gid_t);
#endif
#ifndef HAVE_SETRESUID
int setresuid(uid_t, uid_t, uid_t);
#endif
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*/

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potential issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w)	(*(int*)&(w)) /* convert union wait to int */
#define WIFEXITED(w)	(!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w)	((_W_INT(w)) & 0100)
#define WIFSIGNALED(w)	(!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w)	(int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1)
#define WTERMSIG(w)	(int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1)
#define WCOREFLAG	0x80
#define WCOREDUMP(w) 	((_W_INT(w)) & WCOREFLAG)

/* Prototype */
pid_t waitpid(int, int *, int);
#endif /* !HAVE_WAITPID */
1.135 caio-go-tdigest v2.3.0+incompatible

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1.136 pythonurllib 1.24.1-1

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Upstream-Name: urllib3
Upstream-Contact: Andrey Petrov <andrey.petrov@shazow.net>
Source: https://pypi.org/project/urllib3/

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1.150 libtext-iconv-perl 1.7 5+b7

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Text::Iconv Version 1.6

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Description
-----------
This module provides a Perl interface to the iconv() codeset conversion function, as defined by the Single UNIX Specification. For more details see the POD documentation embedded in the file Iconv.pm, which will also be installed as Text::Iconv(3) man page.

Prerequisites
------------
Your system should be compliant with the Single UNIX Specification or at least provide iconv_open(), iconv() and iconv_close() functions compliant with it.

Note that the possible conversions and the quality of the conversions depend on the available iconv conversion tables and algorithms, which are in most cases supplied by the operating system vendor. Some systems also allow you to build your own tables (e.g., HP-UX, Tru64 UNIX, and AIX provide the genxlt(1) command).

It is also possible to use a separate iconv library such as GNU libiconv package, just make sure that the desired library is found (see <http://www.gnu.org/directory/localization/libiconv.html>).
Building the module
-------------------

Since the Single UNIX Specification only specifies an interface, several issues are left to the implementation to define, namely:

1. The supported codesets
2. The supported conversions
3. The names for the supported codesets

Please check your system documentation for the above points.

The module can be built using this sequence of commands:

    perl Makefile.PL
    make
    make test

On some systems, the iconv functions are in libc; on others (and when you want to use a separate iconv library, such as GNU libiconv), you have to link with -liconv. Makefile.PL tries to automatically detect this. However, if your iconv library is in a non-standard path, you might have to help it. You can specify library and include paths like this:

    perl Makefile.PL LIBS='-L/path/to/lib' INC='-I/path/to/include

You might also have to do this if you have multiple iconv implementations installed and want to use a specific one.

The purpose of the test scripts (run by "make test") is to check if an iconv library can be found, and if the iconv functions can be called. It is neither intended to find out which conversions are supported, nor to test the quality of the conversions provided by your iconv library. However, since the iconv functions can only be tested by calling them, which in turn requires the specification of codesets to be converted. The test scripts try some conversions which seem to be widely supported. Conversions not supported by your iconv implementation are skipped, but for the purpose outlined above this is no problem, as it shows that the iconv functions are working. There is no standard way to find out which conversions are supported by an implementation. This is also the reason why Text::Iconv can't provide platform-independent support for codeset identifiers (even if it it would be nice to have).

Installation
------------
make install

Feedback
--------

Text::Iconv was tested on various platforms and with different iconv implementations.

If you built Text::Iconv on a platform where you needed to specify additional libraries, or where none of the codeset identifiers in the test scripts are supported, please drop me a note.

Thanks
------

Thanks go to all those who reported bugs and suggested features.

Michael Piotrowski <mxp@dynalabs.de>

Found in path(s):
* /opt/cola/permits/1144640811_1616146132.37/0/libtext-iconv-perl-1-7-orig-tar-bz2/Text-Iconv-1.7/README

1.151 libedit 3.1-20181209-1

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1.152 linux-atm 2.5.1-2build1

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1.161 libgcrypt 1.8.4 5

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Assigns past and future changes. (new: src/libgcrypt.pc.in, 
src/Makefile.am, src/secmem.c, mpi/hppa1.1/mpih-mul3.S, 
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added SHA-224, changed cipher/sha256.c, added HMAC tests.)
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Patch for Ubuntu bug #399071

Display the contents of /etc/legal as part of the MOTD, the first time the user logs in, and set a flag in the user's homedir if possible to prevent repeat displays.

Authors: Dustin Kirkland <kirkland@canonical.com>

Upstream status: Ubuntu-specific, maybe submit to Debian

Index: pam.ubuntu/modules/pam_motd/pam_motd.c
--- pam.ubuntu.orig/modules/pam_motd/pam_motd.c
+++ pam.ubuntu/modules/pam_motd/pam_motd.c
@@ -73,6 +73,61 @@
close(fd);
}

+int display_legal(pam_handle_t *pamh)
+{
+    int retval = PAM_IGNORE, rc;
+    char *user = NULL;
+    char *dir = NULL;
+    char *flag = NULL;
+    struct passwd *pwd = NULL;
+    struct stat s;
+    int f;
+    /* Get the user name to determine if we need to print the disclaimer */
+    rc = pam_get_item(pamh, PAM_USER, &user);
+    if (rc == PAM_SUCCESS && user != NULL && *(const char *)user != '\0')
+    {
+        PAM_MODUTIL_DEF_PRIVS(privs);
+
+        /* Get the password entry */
+        pwd = pam_modutil_getpwnam (pamh, PAM_USER, &user);
+        if (pwd != NULL)
+        {
+            if (pam_modutil_drop_priv(pamh, &privs, pwd)) {
+                pam_syslog(pamh, LOG_ERR,
+                           "Unable to change UID to %d temporarily\n",
+                           pwd->pw_uid);
+                retval = PAM_SESSION_ERR;
+                goto finished;
+            }
+
+            if (asprintf(&dir, "%s/.cache", pwd->pw_dir) == -1 || !dir)
+                goto finished;
+            if (asprintf(&flag, "%s/motd.legal-displayed", dir) == -1 || !flag)
+                goto finished;
+
+            if (stat(flag, &s) != 0)
+            {
+                display_file(pamh, "/etc/legal");
+                mkdir(dir, 0700);
+                f = open(flag, O_WRONLY|O_CREAT|O_EXCL,
+                        S_IRUSR|S_IWUSR|S_IRGRP|S_IROTH);
+                if (f>=0) close(f);
+            }
+        }
+        if (asprintf(&dir, "%s/cache", pwd->pw_dir) == -1 || !dir)
+            goto finished;
+        if (asprintf(&flag, "%s/motd.legal-displayed", dir) == -1 || !flag)
+            goto finished;
+        if (stat(flag, &s) != 0)
+        {
+            display_file(pamh, "/etc/legal");
+            mkdir(dir, 0700);
+            f = open(flag, O_WRONLY|O_CREAT|O_EXCL,
+                    S_IRUSR|S_IWUSR|S_IRGRP|S_IROTH);
+            if (f>=0) close(f);
+        }
+    }
+}


+finished:
+    if (pam_modutil_regain_priv(pamh, &privs)) {
+        pam_syslog(pamh, LOG_ERR,
+                  "Unable to change UID back to %d\n", privs.old_uid);
+        retval = PAM_SESSION_ERR;
+    }
+
+    _pam_drop(flag);
+    _pam_drop(dir);
+}
+
+    return retval;
+
+
PAM_EXTERN
int pam_sm_open_session(pam_handle_t *pamh, int flags,
    int argc, const char **argv)
@@ -116,6 +171,9 @@
    /* Display the updated motd */
    display_file(pamh, motd_path);

+    /* Display the legal disclaimer only if necessary */
+    retval = display_legal(pamh);
+    return retval;
+
    display_file(pamh, motd_path);

This package was debianized by J.H.M. Dassen (Ray) jdassen@debian.org on

It was downloaded from ftp://ftp.kernel.org/pub/linux/libs/pam/pre/

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1.169 globals 11.12.0

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1.170 debconf 1.5.71

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A library to support the Internationalised Domain Names in Applications (IDNA) protocol as specified in RFC 5890 et.al. This new methodology, known as IDNA 2008, can generate materially different results to the previous standard. The library can act as a drop-in replacement for the "encodings.idna" module.

```python
import io, sys
from setuptools import setup
```
def main():
    python_version = sys.version_info[:2]
    if python_version < (2,7):
        raise SystemExit("Sorry, Python 2.7 or newer required")

    package_data = {}
    exec(open('idna/package_data.py').read(), package_data)

    arguments = {
        'name': 'idna',
        'packages': ['idna'],
        'version': package_data['__version__'],
        'description': 'Internationalized Domain Names in Applications (IDNA)',
        'long_description': io.open("README.rst", encoding="UTF-8").read(),
        'author': 'Kim Davies',
        'author_email': 'kim@cynosure.com.au',
        'license': 'BSD-like',
        'url': 'https://github.com/kjd/idna',
        'classifiers': [
            'Development Status :: 5 - Production/Stable',
            'Intended Audience :: Developers',
            'Intended Audience :: System Administrators',
            'License :: OSI Approved :: BSD License',
            'Operating System :: OS Independent',
            'Programming Language :: Python',
            'Programming Language :: Python :: 2',
            'Programming Language :: Python :: 2.7',
            'Programming Language :: Python :: 3',
            'Programming Language :: Python :: 3.4',
            'Programming Language :: Python :: 3.5',
            'Programming Language :: Python :: 3.6',
            'Programming Language :: Python :: 3.7',
            'Programming Language :: Python :: 3.8',
            'Programming Language :: Python :: Implementation :: CPython',
            'Programming Language :: Python :: Implementation :: PyPy',
            'Topic :: Internet :: Name Service (DNS)',
            'Topic :: Software Development :: Libraries :: Python Modules',
            'Topic :: Utilities',
        ],
        'python_requires': '>2.7, !=3.0.*, !=3.1.*, !=3.2.*, !=3.3.*',
    }

    setup(**arguments)

    if __name__ == '__main__':
        main()
1.173 es-to-primitive 1.2.1
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1.175 rdma-core 22.1-1

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DRuntime: Runtime Library for the D Programming Language

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GCC RUNTIME LIBRARY EXCEPTION

Version 3.1, 31 March 2009

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1.199 object.values 1.1.1

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1.200 cast v1.1.0

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1.201 dbus-python 1.2.8-3

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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1.216 libsemanage 2.8-2

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1.221 govmomi v0.19.0
1.221.1 Available under license :

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package license

import (
    "context"
    "flag"

    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
    "github.com/vmware/govmomi/vim25/types"
)

*/

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*/
type assign struct {
    *flags.ClientFlag
    *flags.OutputFlag
    *flags.HostSystemFlag
    *flags.ClusterFlag

    name   string
    remove bool
}

func init() {
    cli.Register("license.assign", &assign{}
}

func (cmd *assign) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)

    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)

    cmd.HostSystemFlag, ctx = flags.NewHostSystemFlag(ctx)
    cmd.HostSystemFlag.Register(ctx, f)

    cmd.ClusterFlag, ctx = flags.NewClusterFlag(ctx)
    cmd.ClusterFlag.Register(ctx, f)

    f.StringVar(&cmd.name, "name", ",", "Display name")
    f.BoolVar(&cmd.remove, "remove", false, "Remove assignment")
}

func (cmd *assign) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.HostSystemFlag.Process(ctx); err != nil {
        return err
    }
    return cmd.ClusterFlag.Process(ctx)
}

func (cmd *assign) Usage() string {
    return "KEY"
func (cmd *assign) Description() string {
    return `Assign licenses to HOST or CLUSTER.

Examples:
govc license.assign $VCSA_LICENSE_KEY
govc license.assign -host a_host.example.com $ESX_LICENSE_KEY
govc license.assign -cluster a_cluster $VSAN_LICENSE_KEY`
}

func (cmd *assign) Run(ctx context.Context, f *flag.FlagSet) error {
    if f.NArg() != 1 {
        return flag.ErrHelp
    }
    key := f.Arg(0)
    
    client, err := cmd.Client()
    if err != nil {
        return err
    }
    
    m, err := license.NewManager(client).AssignmentManager(ctx)
    if err != nil {
        return err
    }
    
    host, err := cmd.HostSystemIfSpecified()
    if err != nil {
        return err
    }
    
    var id string
    if host == nil {
        cluster, cerr := cmd.ClusterIfSpecified()
        if cerr != nil {
            return cerr
        }
        if cluster == nil {
            // Default to vCenter UUID
            id = client.ServiceContent.About.InstanceUuid
        } else {
            id = cluster.Reference().Value
        }
    } else {
        id = host.Reference().Value
    }
}
if cmd.remove {
    return m.Remove(ctx, id)
}

info, err := m.Update(ctx, id, key, cmd.name)
if err != nil {
    return err
}

return cmd.WriteResult(licenseOutput([]types.LicenseManagerLicenseInfo{*info}))

/*=================================================================*=

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*/

package license

import (
    "context"
    "flag"
    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
)

var featureUsage = "List licenses with given feature"

type ls struct {
    *flags.ClientFlag
    *flags.OutputFlag

    feature string
}
func init() {
    cli.Register("license.ls", &ls{})
}

func (cmd *ls) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)

    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)

    f.StringVar(&cmd.feature, "feature", "", featureUsage)
}

func (cmd *ls) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    return nil
}

func (cmd *ls) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }

    m := license.NewManager(client)
    result, err := m.List(ctx)
    if err != nil {
        return err
    }

    if cmd.feature != "" {
        result = result.WithFeature(cmd.feature)
    }

    return cmd.WriteResult(licenseOutput(result))
}

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*/

package license

import (
    "context"
    "flag"
    "fmt"

    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
    "github.com/vmware/govmomi/vim25/types"
)

type add struct {
    *flags.ClientFlag
    *flags.OutputFlag
}

func init() {
    cli.Register("license.add", &add{ })
}

func (cmd *add) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)
    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)
}

func (cmd *add) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    return nil
func (cmd *add) Usage() string {
    return "KEY..."
}

func (cmd *add) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }

    m := license.NewManager(client)

    // From the vSphere 5.5 documentation:
    //
    // To specify the edition type and any optional functions, use
    // updateLicense for ESX Server and addLicense follow by
    // LicenseAssingmentManager.updateAssignedLicense for VirtualCenter.
    //
    var addFunc func(ctx context.Context, key string, labels map[string]string) (types.LicenseManagerLicenseInfo, error)
    switch t := client.ServiceContent.About.ApiType; t {
    case "HostAgent":
        addFunc = m.Update
    case "VirtualCenter":
        addFunc = m.Add
    default:
        return fmt.Errorf("unsupported ApiType: %s", t)
    }

    result := make(licenseOutput, 0)
    for _, v := range f.Args() {
        license, err := addFunc(ctx, v, nil)
        if err != nil {
            return err
        }
        result = append(result, license)
    }

    return cmd.WriteResult(licenseOutput(result))
}

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Open Source Used In Cisco Telemetry Broker On Prem 1.1 1476
package simulator

import {
  "context"
  "reflect"
  "testing"

  "github.com/vmware/govmomi"
  "github.com/vmware/govmomi/find"
  "github.com/vmware/govmomi/license"
}

func TestLicenseManagerVPX(t *testing.T) {
  ctx := context.Background()
  m := VPX()
  defer m.Remove()

  err := m.Create()
  if err != nil {
    t.Fatal(err)
  }

  s := m.Service.NewServer()
  defer s.Close()

  c, err := govmomi.NewClient(ctx, s.URL, true)
  if err != nil {
    t.Fatal(err)
  }

  lm := license.NewManager(c.Client)
  am, err := lm.AssignmentManager(ctx)
  if err != nil {
    t.Fatal(err)
  }
}

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*/
la, err := am.QueryAssigned(ctx, "enoent")
if err != nil {
    t.Fatal(err)
}

if len(la) != 0 {
    t.Errorf("unexpected license")
}

finder := find.NewFinder(c.Client, false)
hosts, err := finder.HostSystemList(ctx, "/...")
if err != nil {
    t.Fatal(err)
}

host := hosts[0].Reference().Value

for _, name := range []string{"", host, vcid} {
    la, err = am.QueryAssigned(ctx, name)
    if err != nil {
        t.Fatal(err)
    }
    if len(la) != 1 {
        t.Fatal("no licenses")
    }
    if !reflect.DeepEqual(la[0].AssignedLicense, EvalLicense) {
        t.Fatal("invalid license")
    }
}

func TestLicenseManagerESX(t *testing.T) {
    ctx := context.Background()
m := ESX()

defer m.Remove()

er := m.Create()
if err != nil {
    t.Fatal(err)
}

s := m.Service.NewServer()
defer s.Close()
c, err := govmomi.NewClient(ctx, s.URL, true)
if err != nil {
    t.Fatal(err)
}

lm := license.NewManager(c.Client)
_, err = lm.AssignmentManager(ctx)
if err == nil {
    t.Fatal("expected error")
}

la, err := lm.List(ctx)
if err != nil {
    t.Fatal(err)
}

if len(la) != 1 {
    t.Fatal("no licenses")
}

if !reflect.DeepEqual(la[0], EvalLicense) {
    t.Fatal("invalid license")
}

func TestAddRemoveLicense(t *testing.T) {
    ctx := context.Background()
    m := ESX()
    defer m.Remove()

    err := m.Create()
    if err != nil {
        t.Fatal(err)
    }

    s := m.Service.NewServer()
    defer s.Close()

    c, err := govmomi.NewClient(ctx, s.URL, true)
    if err != nil {
        t.Fatal(err)
    }

    lm := license.NewManager(c.Client)
    key := "00000-00000-00000-00000-11111"
    labels := map[string]string{"key": "value"}
info, err := lm.Add(ctx, key, labels)
if err != nil {
    t.Fatal(err)
}

if info.LicenseKey != key {
    t.Fatalf("expect info.LicenseKey equal to %q; got %q", key, info.LicenseKey)
}

if len(info.Labels) != len(labels) {
    t.Fatalf("expect len(info.Labels) equal to %d; got %d", len(labels), len(info.Labels))
}

if info.Labels[0].Key != "key" || info.Labels[0].Value != "value" {
    t.Fatalf("expect label to be {key:value}; got {%s:%s}", info.Labels[0].Key, info.Labels[0].Value)
}

la, err := lm.List(ctx)
if err != nil {
    t.Fatal(err)
}

if len(la) != 2 {
    t.Fatal("no licenses")
}

if la[1].LicenseKey != key {
    t.Fatalf("expect info.LicenseKey equal to %q; got %q", key, la[1].LicenseKey)
}

if len(la[1].Labels) != len(labels) {
    t.Fatalf("expect len(info.Labels) equal to %d; got %d", len(labels), len(la[1].Labels))
}

if la[1].Labels[0].Key != "key" || la[1].Labels[0].Value != "value" {
    t.Fatalf("expect label to be {key:value}; got {%s:%s}", la[1].Labels[0].Key, la[1].Labels[0].Value)
}

err = lm.Remove(ctx, key)
if err != nil {
    t.Fatal(err)
}
la, err = lm.List(ctx)
if err != nil {
t.Fatal(err)
}
if len(la) != 1 {
t.Fatal("no licenses")
}

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/

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package simulator

import (  
    "github.com/vmware/govmomi/object"  
    "github.com/vmware/govmomi/vim25/methods"  
    "github.com/vmware/govmomi/vim25/mo"  
    "github.com/vmware/govmomi/vim25/soap"  
    "github.com/vmware/govmomi/vim25/types"  
)

// EvalLicense is the default license
var EvalLicense = types.LicenseManagerLicenseInfo{  
    LicenseKey: "00000-00000-00000-00000-00000",  
    EditionKey: "eval",  
    Name: "Evaluation Mode",  
    Properties: []types.KeyAnyValue{  
        {  
            Key: "feature",  
            Value: types.KeyValue{  
                Key: "serialuri:2",  
                Value: "Remote virtual Serial Port Concentrator",  
            },  
        },  
        {  
            Key: "feature",  
            Value: types.KeyValue{  
                Key: "dvs",  
                Value: "vSphere Distributed Switch",  
            },  
        },  
    },
}
type LicenseManager struct {
    mo.LicenseManager
}

func NewLicenseManager(ref types.ManagedObjectReference) object.Reference {
    m := &LicenseManager{
        Self: ref
    }
    m.Licenses = []types.LicenseManagerLicenseInfo{EvalLicense}

    if Map.IsVPX() {
        am := Map.Put(&LicenseAssignmentManager{ }).Reference()
        m.LicenseAssignmentManager = &am
    }

    return m
}

func (m *LicenseManager) AddLicense(req *types.AddLicense) soap.HasFault {
    body := &methods.AddLicenseBody{
        Res: &types.AddLicenseResponse{ },
    }

    for _, license := range m.Licenses {
        if license.LicenseKey == req.LicenseKey {
            body.Res.Returnval = licenseInfo(license.LicenseKey, license.Labels)
            return body
        }
    }

    m.Licenses = append(m.Licenses, types.LicenseManagerLicenseInfo{
        LicenseKey: req.LicenseKey,
        Labels:     req.Labels,
    })

    body.Res.Returnval = licenseInfo(req.LicenseKey, req.Labels)

    return body
}

func (m *LicenseManager) RemoveLicense(req *types.RemoveLicense) soap.HasFault {
    body := &methods.RemoveLicenseBody{
        Res: &types.RemoveLicenseResponse{ },
    }

    for i, license := range m.Licenses {
        if req.LicenseKey == license.LicenseKey {

            body.Res.Returnval = licenseInfo(license.LicenseKey, license.Labels)

            return body
        }
    }

    return body
}
m.Licenses = append(m.Licenses[:i], m.Licenses[i+1:])...
return body
}

func (m *LicenseAssignmentManager) QueryAssignedLicenses(req *types.QueryAssignedLicenses) soap.HasFault
{
body := &methods.QueryAssignedLicensesBody{
Res: &types.QueryAssignedLicensesResponse{},
}

// EntityId can be a HostSystem or the vCenter InstanceUuid
if req.EntityId != "" {
if req.EntityId != Map.content().About.InstanceUuid {
id := types.ManagedObjectReference{
Type: "HostSystem",
Value: req.EntityId,
}

if Map.Get(id) == nil {
return body
}
}

body.Res.Returnval = []types.LicenseAssignmentManagerLicenseAssignment{
{
EntityId: req.EntityId,
AssignedLicense: EvalLicense,
},

return body
}

func licenseInfo(key string, labels []types.KeyValue) types.LicenseManagerLicenseInfo {
info := EvalLicense

info-LicenseKey = key
info.Labels = labels

return info
package license

import (
    "context"
    "flag"

    "github.com/vmware/govmomi/govc/cli"
    "github.com/vmware/govmomi/govc/flags"
    "github.com/vmware/govmomi/license"
)

type decode struct {
    *flags.ClientFlag
    *flags.OutputFlag

    feature string
}

func init() {
    cli.Register("license.decode", &decode{})
}

func (cmd *decode) Register(ctx context.Context, f *flag.FlagSet) {
    cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
    cmd.ClientFlag.Register(ctx, f)

    cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
    cmd.OutputFlag.Register(ctx, f)

    f.StringVar(&cmd.feature, "feature", ",", featureUsage)
}
func (cmd *decode) Process(ctx context.Context) error {
    if err := cmd.ClientFlag.Process(ctx); err != nil {
        return err
    }
    if err := cmd.OutputFlag.Process(ctx); err != nil {
        return err
    }
    return nil
}

func (cmd *decode) Usage() string {
    return "KEY..."
}

func (cmd *decode) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }
    var result license.InfoList
    m := license.NewManager(client)
    for _, v := range f.Args() {
        license, err := m.Decode(ctx, v)
        if err != nil {
            return err
        }
        result = append(result, license)
    }
    if cmd.feature != "" {
        result = result.WithFeature(cmd.feature)
    }
    return cmd.WriteResult(licenseOutput(result))
}

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package license

import (
    "context"
    "strconv"
    "strings"
    "github.com/vmware/govmomi/object"
    "github.com/vmware/govmomi/vim25"
    "github.com/vmware/govmomi/vim25/methods"
    "github.com/vmware/govmomi/vim25/mo"
    "github.com/vmware/govmomi/vim25/types"
)

type Manager struct {
    object.Common
}

func NewManager(c *vim25.Client) *Manager {
    m := Manager{
        object.NewCommon(c, *c.ServiceContent.LicenseManager),
    }
    return &m
}

func mapToKeyValueSlice(m map[string]string) []types.KeyValue {
    var r []types.KeyValue
    for k, v := range m {
        r = append(r, types.KeyValue{Key: k, Value: v})
    }
    return r
}

func (m Manager) Add(ctx context.Context, key string, labels map[string]string) (types.LicenseManagerLicenseInfo, error) {
    req := types.AddLicense{
        This:       m.Reference(),
        LicenseKey: key,
        Labels:     mapToKeyValueSlice(labels),
    }
    return
}
res, err := methods.AddLicense(ctx, m.Client(), &req)
if err != nil {
    return types.LicenseManagerLicenseInfo{}, err
}

return res.Returnval, nil
}

func (m Manager) Decode(ctx context.Context, key string) (types.LicenseManagerLicenseInfo, error) {
    req := types.DecodeLicense{
        This:       m.Reference(),
        LicenseKey: key,
    }

    res, err := methods.DecodeLicense(ctx, m.Client(), &req)
    if err != nil {
        return types.LicenseManagerLicenseInfo{}, err
    }

    return res.Returnval, nil
}

func (m Manager) Remove(ctx context.Context, key string) error {
    req := types.RemoveLicense{
        This:       m.Reference(),
        LicenseKey: key,
    }

    _, err := methods.RemoveLicense(ctx, m.Client(), &req)
    return err
}

func (m Manager) Update(ctx context.Context, key string, labels map[string]string) (types.LicenseManagerLicenseInfo, error) {
    req := types.UpdateLicense{
        This:       m.Reference(),
        LicenseKey: key,
        Labels:     mapToKeyValueSlice(labels),
    }

    res, err := methods.UpdateLicense(ctx, m.Client(), &req)
    if err != nil {
        return types.LicenseManagerLicenseInfo{}, err
    }

    return res.Returnval, nil
}
func (m Manager) List(ctx context.Context) (InfoList, error) {
    var mlm mo.LicenseManager
    err := m.Properties(ctx, m.Reference(), []string{"licenses"}, &mlm)
    if err != nil {
        return nil, err
    }
    return InfoList(mlm.Licenses), nil
}

func (m Manager) AssignmentManager(ctx context.Context) (*AssignmentManager, error) {
    var mlm mo.LicenseManager
    err := m.Properties(ctx, m.Reference(), []string{"licenseAssignmentManager"}, &mlm)
    if err != nil {
        return nil, err
    }
    if mlm.LicenseAssignmentManager == nil {
        return nil, object.ErrNotSupported
    }
    am := AssignmentManager{
        object.NewCommon(m.Client(), *mlm.LicenseAssignmentManager),
    }
    return &am, nil
}

type licenseFeature struct {
    name  string
    level int
}

func parseLicenseFeature(feature string) *licenseFeature {
    lf := new(licenseFeature)
    f := strings.Split(feature, ":")
    lf.name = f[0]
    if len(f) > 1 {
        var err error
        lf.level, err = strconv.Atoi(f[1])
        if err != nil {
            lf.name = feature
        }
    }
    return lf
}
func HasFeature(license types.LicenseManagerLicenseInfo, key string) bool {
    feature := parseLicenseFeature(key)

    for _, p := range license.Properties {
        if p.Key != "feature" {
            continue
        }

        kv, ok := p.Value.(types.KeyValue)
        if !ok {
            continue
        }

        lf := parseLicenseFeature(kv.Key)

        if lf.name == feature.name && lf.level >= feature.level {
            return true
        }
    }

    return false
}

// InfoList provides helper methods for []types.LicenseManagerLicenseInfo
type InfoList []types.LicenseManagerLicenseInfo

func (l InfoList) WithFeature(key string) InfoList {
    var result InfoList

    for _, license := range l {
        if HasFeature(license, key) {
            result = append(result, license)
        }
    }

    return result
}

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*/

package license

import (  
    "context"  
    "flag"  
    "github.com/vmware/govmomi/govc/cli"  
    "github.com/vmware/govmomi/govc/flags"  
    "github.com/vmware/govmomi/license"
)

type remove struct {  
*flags.ClientFlag  
*flags.OutputFlag
}

func init() {  
cli.Register("license.remove", &remove{})
}

func (cmd *remove) Register(ctx context.Context, f *flag.FlagSet) {  
cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)  
cmd.ClientFlag.Register(ctx, f)  
  
cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)  
cmd.OutputFlag.Register(ctx, f)
}

func (cmd *remove) Process(ctx context.Context) error {  
if err := cmd.ClientFlag.Process(ctx); err != nil {  
return err
}
if err := cmd.OutputFlag.Process(ctx); err != nil {  
return err
}
return nil
}
func (cmd *remove) Usage() string {
    return "KEY..."
}

func (cmd *remove) Run(ctx context.Context, f *flag.FlagSet) error {
    client, err := cmd.Client()
    if err != nil {
        return err
    }

    m := license.NewManager(client)
    for _, v := range f.Args() {
        err = m.Remove(ctx, v)
        if err != nil {
            return err
        }
    }

    return nil
}

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*/

package license

import {
    "context"
    "flag"
    "fmt"
    "io"
    "os"
    "text/tabwriter"
    "github.com/vmware/govmomi/gove/cli"
type assigned struct {
  *flags.ClientFlag
  *flags.OutputFlag
  id string
}

func init() {
  cli.Register("license.assigned.ls", &assigned{})
}

func (cmd *assigned) Register(ctx context.Context, f *flag.FlagSet) {
  cmd.ClientFlag, ctx = flags.NewClientFlag(ctx)
  cmd.ClientFlag.Register(ctx, f)
  cmd.OutputFlag, ctx = flags.NewOutputFlag(ctx)
  cmd.OutputFlag.Register(ctx, f)
  f.StringVar(&cmd.id, "id", "", "Entity ID")
}

func (cmd *assigned) Process(ctx context.Context) error {
  if err := cmd.ClientFlag.Process(ctx); err != nil {
    return err
  }
  if err := cmd.OutputFlag.Process(ctx); err != nil {
    return err
  }
  return nil
}

func (cmd *assigned) Run(ctx context.Context, f *flag.FlagSet) error {
  client, err := cmd.Client()
  if err != nil {
    return err
  }
  m, err := license.NewManager(client).AssignmentManager(ctx)
  if err != nil {
    return err
  }
  assigned, err := m.QueryAssigned(ctx, cmd.id)
  if err != nil {
    return err
  }
  return nil
}
if err != nil {
    return err
}
return cmd.WriteResult(assignedOutput(assigned))

type assignedOutput []types.LicenseAssignmentManagerLicenseAssignment

func (res assignedOutput) Write(w io.Writer) error {
    tw := tabwriter.NewWriter(os.Stdout, 4, 0, 2, ' ', 0)
    fmt.Fprintf(tw, "Id:	Scope:	Name:	License:
"
    for _, v := range res {
        fmt.Fprintf(tw, "%s	", v.EntityId)
        fmt.Fprintf(tw, "%s	", v.Scope)
        fmt.Fprintf(tw, "%s	", v.EntityDisplayName)
        fmt.Fprintf(tw, "%s	", v.AssignedLicense.LicenseKey)
        fmt.Fprintf(tw, "\n")
    }
    return tw.Flush()
}
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*/

package license

import (
    "fmt"
    "io"
    "os"
    "text/tabwriter"
    
    "github.com/vmware/govmomi/vim25/types"
)
type licenseOutput []types.LicenseManagerLicenseInfo

func (res licenseOutput) Write(w io.Writer) error {
    tw := tabwriter.NewWriter(os.Stdout, 4, 0, 2, ' ', 0)
    fmt.Fprintf(tw, "Key:	Edition:	Used:	Total:
")
    for _, v := range res {
        fmt.Fprintf(tw, "%s	", v.LicenseKey)
        fmt.Fprintf(tw, "%s	", v.EditionKey)
        fmt.Fprintf(tw, "%d	", v.Used)
        fmt.Fprintf(tw, "%d	", v.Total)
        fmt.Fprintf(tw, "\n")
    }
    return tw.Flush()
}

#!/usr/bin/env bats
load test_helper

# These tests should only run against a server running an evaluation license.
verify_evaluation() {
    if !["(govc license.ls -json | jq -r .[0].EditionKey)" != "eval"] then
        skip "requires evaluation license"
    fi
}

get_key() {
    jq ".[1] | select(.LicenseKey == "$1")"
}

get_property() {
    jq -r ".Properties[] | select(.Key == "$1") | .Value"
}

@test "license.add" {
    esx_env
    verify_evaluation

    run govc license.add -json 00000-00000-00000-00000-00001 00000-00000-00000-00000-00002
    assert_success

    # Expect to see an entry for both the first and the second key
    assert_equal "License is not valid for this product" "$\$(get_key 00000-00000-00000-00000-00001 <<<\$(output) | get_property diagnostic)"
    assert_equal "License is not valid for this product" "$\$(get_key 00000-00000-00000-00000-00002 <<<\$(output) | get_property diagnostic)"
}
@test "license.remove" {
  esx_env

  verify_evaluation

  run govc license.remove -json 00000-00000-00000-00000-00001
  assert_success
}

@test "license.ls" {
  vcsim_env

  verify_evaluation

  run govc license.ls -json
  assert_success

  # Expect the test instance to run in evaluation mode
  assert_equal "Evaluation Mode" "$(get_key 00000-00000-00000-00000-00000 <<<$output | jq -r ".Name")"
}

@test "license.decode" {
  esx_env

  verify_evaluation

  key=00000-00000-00000-00000-00000
  assert_equal "eval" $(govc license.decode $key | grep $key | awk '{print $2}')
}

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package license

import (
    "context"
    
    "github.com/vmware/govmomi/object"
    "github.com/vmware/govmomi/vim25/methods"
    "github.com/vmware/govmomi/vim25/types"
)

type AssignmentManager struct {
    object.Common
}

func (m AssignmentManager) QueryAssigned(ctx context.Context, id string) ([]types.LicenseAssignmentManagerLicenseAssignment, error) {
    req := types.QueryAssignedLicenses{
        This:     m.Reference(),
        EntityId: id,
    }
    res, err := methods.QueryAssignedLicenses(ctx, m.Client(), &req)
    if err != nil {
        return nil, err
    }
    return res.Returnval, nil
}

func (m AssignmentManager) Remove(ctx context.Context, id string) error {
    req := types.RemoveAssignedLicense{
        This:     m.Reference(),
        EntityId: id,
    }
    _, err := methods.RemoveAssignedLicense(ctx, m.Client(), &req)
    return err
}

func (m AssignmentManager) Update(ctx context.Context, id string, key string, name string)
(*types.LicenseManagerLicenseInfo, error) {
 req := types.UpdateAssignedLicense{
   This:              m.Reference(),
   Entity:            id,
   LicenseKey:        key,
   EntityDisplayName: name,
 }

 res, err := methods.UpdateAssignedLicense(ctx, m.Client(), &req)
 if err != nil {
   return nil, err
 }

 return &res.Returnval, nil
 }

1.222 lolex 5.1.2
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1.236 libalgorithmm-merge-perl 0.08 3

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1.239 initramfs-tools 0.133+deb10u1
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The current ubuntu release can be found at:

The Debian tree is maintained with "git" at:
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1.240 Lua 5.1.5

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1.241 tre 0.8.0

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Version: 0.8.0
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1.242 escape-string-regexp 1.0.5

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1.243 hoist-non-react-statics 3.3.2

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1.244 react-select 4.0.2

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1.245 s3transfer 0.4.2

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1.246 string-prototype-trimend 1.0.1

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1.250 net-tools 1.60+git20161116.90da8a0

1ubuntu1

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1.253 libxslt 1.1.32 2.2~deb10u1

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1.255 eslint-plugin-import 2.20.2

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1.256 jsonref 0.2

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1.264 test-exclude 5.2.3

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1.274 python-certifi 2018.08.24-1.ph3

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1.277 immer 8.0.1

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1.284 python-requests 2.21.0

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1.287 popt 1.16-12

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1.289 prometheus-procfs v0.0.3

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procfs provides functions to retrieve system, kernel and process
metrics from the pseudo-filesystem proc.

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1.291 vpnkit v0.5.0

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// License information for the exposed API.

// For more information: http://goo.gl/8us55a#licenseObject
type License struct {
    Name string `json:"name,omitempty"
    URL  string `json:"url,omitempty"
}
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Go support for Protocol Buffers - Google's data interchange format

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goproperties - properties file decoder for Go

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==================

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1.307 fn-name 3.0.0

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1.308 soupsieve 1.8

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1.309 x-sync v0.0.0-20200317015054-43a5402ce75a

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1.310 parse-json 4.0.0

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1.312 babel-plugin-macros 2.8.0
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1.313 python-httplib2 0.11.3-2

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1.314 pyrsistent 0.18.0

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1.315 tiny-invariant 1.1.0

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1.316 yup 0.28.5

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1.319 ncurses 6.1-1ubuntu1.18.04

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1.322 tomb v1.0.0-20141024135613-dd632973f1e7

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1.324 eslint-import-resolver-node 0.3.3

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1.328 pyyaml 3.13-2

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1.329 icu 60.2 3ubuntu3.1
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIIBCFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS=-Wl,-Bsymbic

## Shared object suffix
SO = so
## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%.o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%.o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<
## Dependency rules

%.d: $(srcdir)/%.c  
@echo "generating dependency information for $<"
@$(SHELL) -ec "$(GEN_DEPS.c) $< \  
| sed "\$s/\([^/\]*/\1.o $@ : /g" > $@; \  
[ -s $@ ] || rm -f $@

%.d: $(srcdir)/%.cpp  
@echo "generating dependency information for $<"
@$(SHELL) -ec "$(GEN_DEPS.cc) $< \  
| sed "\$s/\([^/\]*/\1.o $@ : /g" > $@; \  
[ -s $@ ] || rm -f $@

## Versioned libraries rules

%.$@.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$@.$(SO).$(SO_TARGET_VERSION)  
$(RM) $@ && ln -s ${<F} $@

%.$@: %.$@.$(SO).$(SO_TARGET_VERSION_MAJOR)  
$(RM) $@ && ln -s ${*F}.$(SO).$(SO).$(SO_TARGET_VERSION_MAJOR) $@

## Bind internal references

# LDflags that pkgdata will use  
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library  
BIR_DEPS=

## Remove shared library 's'

STATIC_PREFIX_WHEN_USED =  
STATIC_PREFIX =

## End BSD-specific setup

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**
** uuid.c: PostgreSQL Binding (C part)
*/

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## uuid.pod: Perl Binding (Perl/POD part)
##
## =pod

=head1 NAME

OSSP::uuid - B<OSSP uuid> Perl Binding

=head1 DESCRIPTION
B<OSSP uuid> is a ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and RFC 4122 compliant Universally Unique Identifier (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

B<OSSP::uuid> is the Perl binding to the B<OSSP uuid> API. Three variants are provided:

=head2 TIE-STYLE API

The TIE-style API is a functionality-reduced wrapper around the OO-style API and intended for very high-level convenience programming:

=over 4
=item C<use OSSP::uuid;>
=item B<tie>C< my $uuid, 'OSSP::uuid::tie', $mode, ...;>
=item C<$uuid = [ $mode, ... ];>
=item C<print "UUID=$uuid\n";>
=item C<untie $uuid;>
=back

=head2 OO-STYLE API

The OO-style API is a wrapper around the C-style API and intended for high-level regular programming.

=over 4
=item C<use OSSP::uuid;>
=item C<my $uuid = new OSSP::uuid;>
=item C<$uuid-load($name);>
=item C<$uuid-make($mode, ...);>
Additionally, the strings C"v1", C"v3", C"v4", C"v5" and C"mc"
can be used in C<$mode> and the strings C"bin", C"str", and C"txt"
can be used for C<$fmt>.

=head2 C-STYLE API

The C-style API is a direct mapping
of the B<OSSP uuid> ISO-C API to Perl and is intended for low-level
programming. See uuid(3) for a description of the functions and
their expected arguments.

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Additionally, the following constants are exported for use in `C<$rc>`, `C<$mode>`, `C<$fmt>` and `C<$ver>`:

- `C<UUID_VERSION>`
- `C<UUID_LEN_BIN>`
- `C<UUID_LEN_STR>`
- `C<UUID_RC_OK>`
- `C<UUID_RC_ARG>`
- `C<UUID_RC_MEM>`
- `C<UUID_RC_SYS>`
- `C<UUID_RC_INT>`
- `C<UUID_RC_IMP>`
- `C<UUID_MAKE_V1>`
- `C<UUID_MAKE_V3>`
- `C<UUID_MAKE_V4>`
- `C<UUID_MAKE_V5>`
- `C<UUID_MAKE_MC>`
- `C<UUID_FMT_BIN>`
- `C<UUID_FMT_STR>`
- `C<UUID_FMT_SIV>`
- `C<UUID_FMT_TXT>`

=head1 EXAMPLES

The following two examples create the version 3 UUID `C<02d9e6d5-9467-382e-8f9b-9300a64ac3cd>`, both via the OO-style and the C-style API. Error handling is omitted here for easier reading, but has to be added for production-quality code.

```perl
# TIE-style API (very high-level)
use OSSP::uuid;
tie my $uuid, 'OSSP::uuid::tie';
$uuid = [ "v1" ];
print "UUIDs: $uuid, $uuid, $uuid\n";
$uuid = [ "v3", "ns:URL", "http://www.ossp.org/" ];
print "UUIDs: $uuid, $uuid, $uuid\n";
untie $uuid;

# OO-style API (high-level)
use OSSP::uuid;
my $uuid = new OSSP::uuid;
my $uuid_ns = new OSSP::uuid;
$uuid_ns->load("ns:URL");
$uuid->make("v3", $uuid_ns, "http://www.ossp.org/");
```
undef $uuid_ns;
my $str = $uuid->export("str");
undef $uuid;
print "$str\n";

# C-style API (low-level)
use OSSP::uuid qw(:all);
my $uuid; uuid_create($uuid);
my $uuid_ns; uuid_create($uuid_ns);
uuid_load($uuid_ns, "ns:URL");
uuid_make($uuid, UUID_MAKE_V3, $uuid_ns, "http://www.ossp.org/");
uuid_destroy($uuid_ns);
my $str; uuid_export($uuid, UUID_FMT_STR, $str, undef);
uuid_destroy($uuid);
print "$str\n";

=head1 SEE ALSO

uuid(1), uuid-config(1), uuid(3).

=head1 HISTORY

The Perl binding B<OSSP::uuid> to B<OSSP uuid> was implemented in November 2004 by Ralf S. Engelschall E<lt>rse@engelschall.comE<gt>.

=cut

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uuid++.pod: manual page for C++ API

=pod

=head1 NAME
B< OSSP uuid> - B<Universally Unique Identifier> (C++ API)

=head1 VERSION

OSSP uuid UUID_VERSION_STR

=head1 DESCRIPTION

B<uuid++> is the ISO-C++ language binding of the B<OSSP uuid> C API. It provides a thin ISO-C++ class B<uuid> wrapping the ISO-C API type B<uuid_t>.

=head1 APPLICATION PROGRAMMING INTERFACE

The ISO-C++ Application Programming Interface (API) of B<OSSP uuid> consists of the following components:

=head2 CONSTANTS

The constants are the same to those provided by the ISO-C API. See uuid(3) for details.

=head2 CLASSES

The following classes are provided:

=over 4

=item B<uuid>

This is the class corresponding to the C API type B<uuid_t>. It is the main object.

=item B<uuid_error_t>

This is the class corresponding to the C API function B<uuid_error>. It is the object thrown as an exception in case of any errors.

=back

=head2 METHODS

The following methods are provided:

=over 4

=item B<uuid>();
The standard constructor.

=item B<uuid>(const uuid &_obj);

The copy constructor for B<uuid> class.

=item B<uuid>(const uuid_t *_obj);

The import constructor for C API objects.

=item B<uuid>(const void *_bin);

The import constructor for binary representation.

=item B<uuid>(const char * _str);

The import constructor for string representation.

=item ~B<uuid>();

The standard destructor for B<uuid> class.

=item uuid &B<uuid>::operator=(const uuid &_obj);

The assignment operator corresponding to the copy constructor.

=item uuid &B<uuid>::operator=(const uuid_t * _obj);

The assignment operator corresponding to the import constructor for C API objects.

=item uuid &B<uuid>::operator=(const void * _bin);

The assignment operator corresponding to the import constructor for binary representation.

=item uuid &B<uuid>::operator=(const char * _str);

The assignment operator corresponding to the import constructor for string and single integer value representation.

=item uuid B<uuid>::clone>(void);

Regular method corresponding to the C API function B<uuid_clone>.

=item void B<uuid>::load>(const char * _name);

Regular method corresponding to the C API function B<uuid_load>.

=item void B<uuid>::make>(unsigned int _mode, ...);
Regular method corresponding to the C API function B<uuid_make>.

=item int B<uuid::isnil>(void);

Regular method corresponding to the C API function B<uuid_isnil>.

=item int B<uuid::compare>(const uuid &_obj);

Regular method corresponding to the C API function B<uuid_compare>.

=item int B<uuid::operator==>(const uuid &_obj);

The comparison operator corresponding to B<uuid_compare> usage for equality.

=item int B<uuid::operator!=(const uuid &_obj);

The comparison operator corresponding to B<uuid_compare> usage for inequality.

=item int B<uuid::operatorE<lt>>(const uuid &_obj);

The comparison operator corresponding to B<uuid_compare> usage for less-than.

=item int B<uuid::operatorE<lt>=>(const uuid &_obj);

The comparison operator corresponding to B<uuid_compare> usage for less-than-or-equal.

=item int B<uuid::operatorE<gt>>(const uuid &_obj);

The comparison operator corresponding to B<uuid_compare> usage for greater-than.

=item int B<uuid::operatorE<gt>=>(const uuid &_obj);

The comparison operator corresponding to B<uuid_compare> usage for greater-than-or-equal.

=item void B<uuid::import>(const void *_bin);

Regular method corresponding to the C API function B<uuid_import> for binary representation usage.

=item void B<uuid::import>(const char *-_str);

Regular method corresponding to the C API function B<uuid_import> for string representation usage.

=item void *B<uuid::binary>(void);

Regular method corresponding to the C API function B<uuid_export> for binary representation usage.

=item char *B<uuid::string>(void);

---

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Regular method corresponding to the C API function `B<uuid::integer>` for string representation usage.

```c
=item char *B<uuid::integer>(void);
```

Regular method corresponding to the C API function `B<uuid::export>` for single integer value representation usage.

```c
=item char *B<uuid::summary>(void);
```

Regular method corresponding to the C API function `B<uuid::export>` for textual summary representation usage.

```c
=item unsigned long B<uuid::version>(void);
```

Regular method corresponding to the C API function `B<uuid::version>`.

```c
=item B<uuid_error_t>()
```

The standard constructor for `B<uuid_error_t>` class.

```c
=item B<uuid_error_t>(uuid_rc_t _code)
```

The standard constructor for `B<uuid_error_t>` class with return code initialization.

```c
=item ~B<uuid_error_t>()
```

The standard destructor for `B<uuid_error_t>` class.

```c
=item void B<uuid_error_t::code>(uuid_rc_t _code)
```

Regular method for setting the return code.

```c
=item uuid_rc_t B<uuid_error_t::code>(void)
```

Regular method for fetching the return code.

```c
=item char *B<uuid_error_t::string>(void)
```

Regular method for fetching the string corresponding to the current return code.

```c
=back
```

=head1 EXAMPLE

The following shows an example usage of the C++ API. Exception handling is omitted for code simplification and has to be re-added for production code.

```c
/* generate a DCE 1.1 v1 UUID from system environment */
```
char *uuid_v1(void)
{
    uuid id;
    char *str;

    id.make(UUID_MAKE_V1);
    str = id.string();
    return str;
}

/* generate a DCE 1.1 v3 UUID from an URL */
char *uuid_v3(const char *url)
{
    uuid id;
    uuid id_ns;
    char *str;

    id_ns.load("ns:URL");
    id.make(UUID_MAKE_V3, &id_ns, url);
    str = id.string();
    return str;
}

=head1 SEE ALSO

uuid(3).

=cut

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**
** uuid.h: library API definition
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** uuid_mac.h: Media Access Control (MAC) resolver API definition
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ui64.c: implementation of 64-bit unsigned integer arithmetic

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uuid_time.h: Time Management API
/

uuid_str.c: string formatting functions
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 ** uuid.xs: Perl Binding (Perl/XS part)
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uuid_dce.c: DCE 1.1 compatibility API implementation

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--
-- uuid.sql: PostgreSQL Binding (SQL part)
--
--
-- prolog
--

SET search_path TO public;
SET client_min_messages TO warning;

DROP FUNCTION uuid_in(CSTRING) CASCADE;
DROP FUNCTION uuid_out(uuid) CASCADE;
DROP FUNCTION uuid_recv(INTERNAL) CASCADE;
DROP FUNCTION uuid_send(uuid) CASCADE;
DROP TYPE uuid CASCADE;
DROP CAST (CSTRING AS uuid) CASCADE;
DROP CAST (uuid AS CSTRING) CASCADE;
DROP FUNCTION uuid(CSTRING) CASCADE;
DROP FUNCTION uuid(INTEGER) CASCADE;
DROP FUNCTION uuid(INTEGER, CSTRING, CSTRING) CASCADE;
DROP FUNCTION uuid_eq(uuid) CASCADE;
DROP FUNCTION uuid_ne(uuid) CASCADE;
DROP FUNCTION uuid_lt(uuid, uuid) CASCADE;
DROP FUNCTION uuid_gt(uuid, uuid) CASCADE;
DROP FUNCTION uuid_le(uuid, uuid) CASCADE;
DROP FUNCTION uuid_ge(uuid, uuid) CASCADE;
DROP OPERATOR =(uuid,uuid) CASCADE;
DROP OPERATOR <>(uuid,uuid) CASCADE;
DROP OPERATOR <(uuid,uuid) CASCADE;
DROP OPERATOR >(uuid,uuid) CASCADE;
DROP OPERATOR <=(uuid,uuid) CASCADE;
DROP OPERATOR >=(uuid,uuid) CASCADE;
DROP FUNCTION uuid_hash(uuid) CASCADE;
DROP FUNCTION uuid_cmp(uuid, uuid) CASCADE;
DROP OPERATOR CLASS uuid_ops USING hash CASCADE;
DROP OPERATOR CLASS uuid_ops USING btree CASCADE;

BEGIN;

--
-- the UUID data type
--

CREATE FUNCTION uuid_in(CSTRING) RETURNS uuid
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_in';

CREATE FUNCTION uuid_out(uuid) RETURNS CSTRING
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_out';

CREATE FUNCTION uuid_recv(INTERNAL) RETURNS uuid
STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_recv';
CREATE FUNCTION
   uuid_send(uuid) RETURNS BYTEA
   STRICT
   LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_send';

CREATE TYPE uuid (
   INPUT   = uuid_in, -- for SQL input
   OUTPUT  = uuid_out, -- for SQL output
   RECEIVE = uuid_recv, -- for DB input
   SEND    = uuid_send, -- for DB output
   DEFAULT = 'uuid(1)',
   INTERNALLENGTH = 16,
   ALIGNMENT = char
);

COMMENT ON TYPE uuid
   IS 'UUID type';

-- CREATE CAST (CSTRING AS uuid)
--     WITH FUNCTION uuid_in(CSTRING) AS ASSIGNMENT;
--
-- CREATE CAST (uuid AS CSTRING)
--     WITH FUNCTION uuid_out(uuid) AS ASSIGNMENT;
--
-- the UUID constructor function
--
CREATE FUNCTION
   uuid(CSTRING) RETURNS uuid
   IMMUTABLE STRICT
   LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_in';

CREATE FUNCTION
   uuid(INTEGER) RETURNS uuid
   VOLATILE CALLED ON NULL INPUT
   LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_make';

CREATE FUNCTION
   uuid(INTEGER, CSTRING, CSTRING) RETURNS uuid
   VOLATILE CALLED ON NULL INPUT
   LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_make';

--
-- the UUID operators
--
CREATE FUNCTION
CREATE FUNCTION uuid_eq(uuid, uuid) RETURNS BOOL
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_eq';

CREATE FUNCTION uuid_ne(uuid, uuid) RETURNS BOOL
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_ne';

CREATE FUNCTION uuid_lt(uuid, uuid) RETURNS BOOL
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_lt';

CREATE FUNCTION uuid_gt(uuid, uuid) RETURNS BOOL
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_gt';

CREATE FUNCTION uuid_le(uuid, uuid) RETURNS BOOL
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_le';

CREATE FUNCTION uuid_ge(uuid, uuid) RETURNS BOOL
IMMUTABLE STRICT
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_ge';

CREATE OPERATOR = (
    leftarg    = uuid,
    rightarg   = uuid,
    negator    = <>,
    procedure  = uuid_eq
);  

CREATE OPERATOR <> (  
    leftarg    = uuid,
    rightarg   = uuid,
    negator    = =,
    procedure  = uuid_ne
);  

CREATE OPERATOR < (  
    leftarg    = uuid,
    rightarg   = uuid,
    commutator = >,
    negator    = >=,
    procedure  = uuid_le
);  

CREATE OPERATOR <= (  
    leftarg    = uuid,
    rightarg   = uuid,
    negator    = >,
    procedure  = uuid_ge
);  

CREATE OPERATOR >= (  
    leftarg    = uuid,
    rightarg   = uuid,
    negator    = <,
    procedure  = uuid_lt
);
CREATE OPERATOR > (  
   leftarg = uuid,  
   rightarg = uuid,  
   commutator = <,  
   negator = <=,  
   procedure = uuid_gt
);

CREATE OPERATOR <= (  
   leftarg = uuid,  
   rightarg = uuid,  
   commutator = >=,  
   negator = >,  
   procedure = uuid_le
);

CREATE OPERATOR >= (  
   leftarg = uuid,  
   rightarg = uuid,  
   commutator = <=,  
   negator = <,  
   procedure = uuid_ge
);

--
-- the UUID support for indexing
--

CREATE FUNCTION  
   uuid_hash(uuid) RETURNS INTEGER  
IMMUTABLE STRICT  
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_hash';

CREATE FUNCTION  
   uuid_cmp(uuid, uuid) RETURNS INTEGER  
IMMUTABLE STRICT  
LANGUAGE C AS 'MODULE_PATHNAME', 'pg_uuid_cmp';

CREATE OPERATOR CLASS uuid_ops  
DEFAULT FOR TYPE uuid USING hash AS  
OPERATOR 1 =, -- 1: equal  
FUNCTION 1 uuid_hash(uuid);

CREATE OPERATOR CLASS uuid_ops  
DEFAULT FOR TYPE uuid USING btree AS
OPERATOR 1 <,  -- 1: less than
OPERATOR 2 <=,  -- 2: less than or equal
OPERATOR 3 =,  -- 3: equal
OPERATOR 4 >=,  -- 4: greater than or equal
OPERATOR 5 >,  -- 5: greater than
FUNCTION 1 uuid_cmp(uuid, uuid);

--
-- epilog
--

COMMIT;

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* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/pgsql/uuid.sql.in

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 **
 ** ui128.h: API declaration
 */

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** uuid_mac.c: Media Access Control (MAC) resolver implementation
*/

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** uuid.c: library API implementation
*/

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** uuid_sha1.c: SHA-1 API implementation
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** uuid_time.c: Time Management
*/

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** uuid_sha1.h: SHA-1 API definition
*/

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uuid++.cc: library C++ API implementation
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** uuid.c: PHP/Zend API (language: C)
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** uuid_bm.c: bitmask API implementation
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## TYPEMAP

type_t * T_PTRREF

type_t ** T_PTRREF

type_rec_t T_IV

type_pmt_t T_IV

int * T_PV

size_t * T_PV

const void * T_PV

void ** T_PV

Found in path(s):

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##

## uuid.pod: manual page
### Name

**OSSP uuid** - **Universally Unique Identifier**

### Version

**OSSP uuid UUID_VERSION_Str**

### Description

**OSSP uuid** is an ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant **Universally Unique Identifier** (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood of uniqueness over space and time and are computationally difficult to guess. They are globally unique identifiers which can be locally generated without contacting a global registration authority. UUIDs are intended as unique identifiers for both mass tagging objects with an extremely short lifetime and to reliably identifying very persistent objects across a network.

This is the ISO-C application programming interface (API) of **OSSP uuid**.

### UUID Binary Representation

According to the DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 standards, a DCE 1.1 variant UUID is a 128 bit number defined out of 7 fields, each field a multiple of an octet in size and stored in network byte order:

```
[4]
  version
-->| |<--
  | |  [16]
  | |  [16]
[32]  time_low time_mid _and_version
```
An example of a UUID binary representation is the octet stream `C<0xF80x1D 0x4F 0xAE 0x7D 0xEC 0x11 0xD0 0xA7 0x65 0x00 0xA0 0xC9 0x1E 0x6B0xF6>`. The binary representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_BIN>.

=head2 UUID ASCII String Representation

According to the DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 standards, a DCE 1.1 variant UUID is represented as an ASCII string consisting of 8 hexadecimal digits followed by a hyphen, then three groups of 4 hexadecimal digits each followed by a hyphen, then 12 hexadecimal digits. Formally, the string representation is defined by the following grammar:

```
uuid     = <time_low> "-" 
         <time_mid> "-" 
         <time_high_and_version> "-" 
         <clock_seq_high_and_reserved>
```

An example of a UUID string representation is the ASCII string "C<f81d4fae-7dec-11d0-a765-00a0c91e6bf6>". The string representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_STR>.

Notice: a corresponding URL can be generated out of a ASCII string representation of an UUID by prefixing with "C<urn:uuid:" as in "C<urn:uuid:f81d4fae-7dec-11d0-a765-00a0c91e6bf6>".

=head2 UUID Single Integer Value Representation

According to the ISO/IEC 11578:1996 and ITU-T Rec. X.667 standards, a DCE 1.1 variant UUID can be also represented as a single integer value consisting of a decimal number with up to 39 digits.

An example of a UUID single integer value representation is the decimal number "C<329800735698586629295641978511506172918>". The string representation format is exactly what the B<OSSP uuid> API functions B<uuid_import>() and B<uuid_export>() deal with under C<UUID_FMT_SIV>.

Notice: a corresponding ISO OID can be generated under the "{joint-iso-itu-t(2) uuid(25)}" arc out of a single integer value representation of a UUID by prefixing with "C<2.25.". An example OID is "C<2.25.329800735698586629295641978511506172918>". Additionally, an URL can be generated by further prefixing with "C<urn:oid:" as in "C<urn:oid:2.25.329800735698586629295641978511506172918>".

=head2 UUID Variants and Versions

A UUID has a variant and version. The variant defines the layout of the UUID. The version defines the content of the UUID. The UUID variant supported in B<OSSP uuid> is the DCE 1.1 variant only. The DCE 1.1 UUID variant versions supported in B<OSSP uuid> are:
Version 1 UUIDs are guaranteed to be unique through combinations of hardware addresses, time stamps and random seeds. There is a reference in the UUID to the hardware (MAC) address of the first network interface card (NIC) on the host which generated the UUID -- this reference is intended to ensure the UUID will be unique in space as the MAC address of every network card is assigned by a single global authority (IEEE) and is guaranteed to be unique. The next component in a UUID is a timestamp which, as clock always (should) move forward, will be unique in time. Just in case some part of the above goes wrong (the hardware address cannot be determined or the clock moved steps backward), there is a random clock sequence component placed into the UUID as a "catch-all" for uniqueness.

Version 3 and version 5 UUIDs are guaranteed to be inherently globally unique if the combination of namespace and name used to generate them is unique.
Version 4 UUIDs are not guaranteed to be globally unique, because they are generated out of locally gathered pseudo-random numbers only. Nevertheless there is still a high likelihood of uniqueness over space and time and that they are computationally difficult to guess.

=head2 Nil UUID

There is a special I<Nil> UUID consisting of all octets set to zero in the binary representation. It can be used as a special UUID value which does not conflict with real UUIDs.

=head1 APPLICATION PROGRAMMING INTERFACE

The ISO-C Application Programming Interface (API) of B<OSSP uuid> consists of the following components.

=head2 CONSTANTS

The following constants are provided:

=over 4

=item B<UUID_VERSION>

The hexadecimal encoded B<OSSP uuid> version. This allows compile-time checking of the B<OSSP uuid> version. For run-time checking use B<uuid_version>() instead.

The hexadecimal encoding for a version "$I<v>.$I<r>$I<t>$I<l>" is calculated with the B<GNU sh> B<version> command and is (in Perl-style for concise description) "sprintf('0x%i%02x%02x%02x', $I<v>, $I<r>, $I<t>, $I<l>)", i.e., the version 0.9.6 is encoded as "0x009206".

=item B<UUID_LEN_BIN>, B<UUID_LEN_STR>, B<UUID_LEN_SIV>

The number of octets of the UUID binary and string representations. Notice that the lengths of the string representation (B<UUID_LEN_STR>) and the lengths of the single integer value representation (B<UUID_LEN_SIV>) does I<not> include the necessary C<NUL> termination character.

=item B<UUID_MAKE_V1>, B<UUID_MAKE_V3>, B<UUID_MAKE_V4>, B<UUID_MAKE_V5>, B<UUID_MAKE_MC>

The I<mode> bits for use with B<uuid_make>(). The B<UUID_MAKE_V>I<N> specify which UUID version to generate. The B<UUID_MAKE_MC> forces the
use of a random multi-cast MAC address instead of the real physical MAC
address in version 1 UUIDs.

=item B<UUID_RC_OK>, B<UUID_RC_ARG>, B<UUID_RC_MEM>, B<UUID_RC_SYS>,
B<UUID_RC_INT>, B<UUID_RC_IMP>

The possible numerical return-codes of API functions.
The C<UUID_RC_OK> indicates success, the others indicate errors.
Use B<uuid_error>() to translate them into string versions.

=item B<UUID_FMT_BIN>, B<UUID_FMT_STR>, B<UUID_FMT_SIV>, B<UUID_FMT_TXT>

The I<fmt> formats for use with B<uuid_import>() and B<uuid_export>().
The B<UUID_FMT_BIN> indicates the UUID binary representation (of
length B<UUID_LEN_BIN>), the B<UUID_FMT_STR> indicates the UUID string
representation (of length B<UUID_LEN_STR>), the B<UUID_FMT_SIV>
indicates the UUID single integer value representation (of maximum
length B<UUID_LEN_SIV>) and the B<UUID_FMT_TXT> indicates the textual
description (of arbitrary length) of a UUID.

=back

=head2 FUNCTIONS

The following functions are provided:

=over 4

=item uuid_rc_t B<uuid_create>(uuid_t **I<uuid>);

Create a new UUID object and store a pointer to it in C<*>I<uuid>.
A UUID object consists of an internal representation of a UUID, the
internal PRNG and MD5 generator contexts, and cached MAC address and
timestamp information. The initial UUID is the I<Nil> UUID.

=item uuid_rc_t B<uuid_destroy>(uuid_t *I<uuid>);

Destroy UUID object I<uuid>.

=item uuid_rc_t B<uuid_clone>(const uuid_t *I<uuid>, uuid_t **I<uuid_clone>);

Clone UUID object I<uuid> and store new UUID object in I<uuid_clone>.

=item uuid_rc_t B<uuid_isnil>(const uuid_t *I<uuid>, int *I<result>);

Checks whether the UUID in I<uuid> is the I<Nil> UUID.
If this is the case, it returns I<true> in C<*>I<result>.
Else it returns I<false> in C<*>I<result>.  

=close
=item uuid_rc_t B<uuid_compare>(const uuid_t *I<uuid>, const uuid_t *I<uuid2>, int *I<result>);

Compares the order of the two UUIDs in I<uuid1> and I<uuid2>
and returns the result in C<*I<result>>: C<-1> if I<uuid1> is
smaller than I<uuid2>, C<0> if I<uuid1> is equal to I<uuid2>
and C<+1> if I<uuid1> is greater than I<uuid2>.

=item uuid_rc_t B<uuid_import>(uuid_t *I<uuid>, uuid_fmt_t I<fmt>, const void *I<data_ptr>, size_t
I<data_len>);

Imports a UUID I<uuid> from an external representation of format I<fmt>.
The data is read from the buffer at I<data_ptr> which contains at least
I<data_len> bytes.

The format of the external representation is specified by I<fmt> and the
minimum expected length in I<data_len> depends on it. Valid values for
I<fmt> are B<UUID_FMT_BIN>, B<UUID_FMT_STR> and B<UUID_FMT_SIV>.

=item uuid_rc_t B<uuid_export>(const uuid_t *I<uuid>, uuid_fmt_t I<fmt>, void *I<data_ptr>, size_t
I<data_len>);

Exports a UUID I<uuid> into an external representation of format
I<fmt>. Valid values for I<fmt> are B<UUID_FMT_BIN>, B<UUID_FMT_STR>,
B<UUID_FMT_SIV> and B<UUID_FMT_TXT>.

The data is written to the buffer whose location is obtained
by dereferencing I<data_ptr> after a "cast" to the appropriate
pointer-to-pointer type. Hence the generic pointer argument I<data_ptr>
is expected to be a pointer to a "pointer of a particular type", i.e.,
it has to be of type "C<unsigned char **>" for B<UUID_FMT_BIN> and
"C<char **>" for B<UUID_FMT_STR>, B<UUID_FMT_SIV> and B<UUID_FMT_TXT>.

The buffer has to be room for at least C<*I<data_len> bytes. If the
value of the pointer after "casting" and dereferencing I<data_ptr>
is C<NULL>, I<data_len> is ignored as input and a new buffer is
allocated and returned in the pointer after "casting" and dereferencing
I<data_ptr> (the caller has to free(3) it later on).

If I<data_len> is not C<NULL>, the number of available bytes in the
buffer has to be provided in C<*I<data_len> and the number of actually
written bytes are returned in C<*I<data_len> again. The minimum
required buffer length depends on the external representation as
specified by I<fmt> and is at least B<UUID_LEN_BIN> for B<UUID_FMT_BIN>,
B<UUID_LEN_Str> for B<UUID_FMT_STR> and B<UUID_LEN_SIV> for
B<UUID_FMT_SIV>. For B<UUID_FMT_TXT> a buffer of unspecified length is
required and hence it is recommended to allow B<OSSP uuid> to allocate
the buffer as necessary.
=item uuid_rc_t B<uuid_load>(uuid_t *I<uuid>, const char *I<name>);

Loads a pre-defined UUID value into the UUID object I<uuid>. The following I<name> arguments are currently known:

=over 4

=item I<name> I<UUID>

=item nil 00000000-0000-0000-0000-000000000000

=item ns:DNS 6ba7b810-9dad-11d1-80b4-00c04fd430c8

=item ns:URL 6ba7b811-9dad-11d1-80b4-00c04fd430c8

=item ns:OID 6ba7b812-9dad-11d1-80b4-00c04fd430c8

=item ns:X500 6ba7b814-9dad-11d1-80b4-00c04fd430c8

=back

The "C<ns:>I<XXX>" are names of pre-defined name-space UUIDs for use in the generation of DCE 1.1 version 3 and version 5 UUIDs.

=item uuid_rc_t B<uuid_make>(uuid_t *I<uuid>, unsigned int I<mode>, ...);

Generates a new UUID in I<uuid> according to I<mode> and optional arguments (dependent on I<mode>).

If I<mode> contains the C<UUID_MAKE_V1> bit, a DCE 1.1 variant UUID of version 1 is generated. Then optionally the bit C<UUID_MAKE_MC> forces the use of random multi-cast MAC address instead of the real physical MAC address (the default). The UUID is generated out of the 60-bit current system time, a 12-bit clock sequence and the 48-bit MAC address.

If I<mode> contains the C<UUID_MAKE_V3> or C<UUID_MAKE_V5> bit, a DCE 1.1 variant UUID of version 3 or 5 is generated and two additional arguments are expected: first, a namespace UUID object (C<uuid_t *>). Second, a name string of arbitrary length (C<const char *>). The UUID is generated out of the 128-bit MD5 or 160-bit SHA-1 from the concatenated octet stream of namespace UUID and name string.

If I<mode> contains the C<UUID_MAKE_V4> bit, a DCE 1.1 variant UUID of version 4 is generated. The UUID is generated out of 128-bit random data.

=item char *B<uuid_error>(uuid_rc_t I<rc>);
Returns a constant string representation corresponding to the return-code \texttt{I<rc>} for use in displaying B\texttt{OSSP uuid} errors.

\texttt{=}item unsigned long B\texttt{uuid_version}(\texttt{void});

Returns the hexadecimal encoded B\texttt{OSSP uuid} version as compiled into the library object files. This allows run-time checking of the B\texttt{OSSP uuid} version. For compile-time checking use \texttt{C<UUID\_VERSION>} instead.

\texttt{=}back

\texttt{=}head\texttt{1} EXAMPLE

The following shows an example usage of the API. Error handling is omitted for code simplification and has to be re-added for production code.

\texttt{/* generate a DCE 1.1 v1 UUID from system environment */}
char *\texttt{uuid\_v1}(void)
{
    uuid_t *uuid;
    char *str;

    \texttt{uuid\_create(&uuid);};
    \texttt{uuid\_make(uuid, UUID\_MAKE\_V1);};
    \texttt{str = NULL;};
    \texttt{uuid\_export(uuid, UUID\_FMT\_STR, &str, NULL);};
    \texttt{uuid\_destroy(uuid);};
    \texttt{return str;};
}

\texttt{/* generate a DCE 1.1 v3 UUID from an URL */}
char *\texttt{uuid\_v3}(const char *url)
{
    uuid_t *uuid;
    uuid_t *uuid\_ns;
    char *str;

    \texttt{uuid\_create(&uuid);};
    \texttt{uuid\_create(&uuid\_ns);};
    \texttt{uuid\_load(uuid\_ns, "ns:URL");}
    \texttt{uuid\_make(uuid, UUID\_MAKE\_V3, uuid\_ns, url);};
    \texttt{str = NULL;};
    \texttt{uuid\_export(uuid, UUID\_FMT\_STR, &str, NULL);};
    \texttt{uuid\_destroy(uuid\_ns);};
    \texttt{uuid\_destroy(uuid);};
    \texttt{return str;};}
The following are references to B<UUID> documentation and specifications:


- **HTTP Extensions for Distributed Authoring (WebDAV)**, section B<6.4.1 Node Field Generation Without the IEEE 802 Address>,
IETF RFC-2518,
February 1999, 94 pages,
http://www.ietf.org/rfc/rfc2518.txt

=item

B<DCE 1.1 compliant UUID functions>,
FreeBSD manual pages uuid(3) and uuidgen(2),
http://www.freebsd.org/cgi/man.cgi?query=uuid&manpath=FreeBSD+6.0-RELEASE

=back

=head1 HISTORY

B<OSSP uuid> was implemented in January 2004 by Ralf S. Engelschall
E<lt>rse@engelschall.comE<gt>. It was prompted by the use of UUIDs
in the B<OSSP> as> and B<OpenPKG> projects. It is a clean room
implementation intended to be strictly standards compliant and maximum
portable.

=head1 SEE ALSO

uuid(1), uuid-config(1), OSSP::uuid(3).

=cut

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid.pod
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 **
 ** This file is part of OSSP uuid, a library for the generation
 ** of UUIDs which can be found at http://www.ossp.org/pkg/lib/uuid/
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**
** uuid++.hh: library C++ API definition
*/

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** uuid_prng.h: PRNG API definition
*/

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**
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** which can be found at http://www.ossp.org/pkg/lib/ui64/.
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**
** ui64.h: API declaration
*/

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##
## This file is part of OSSP uuid, a library for the generation
## of UUIDs which can found at http://www.ossp.org/pkg/lib/uuid/
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## NAME

B< OSSP uuid> - B<Universally Unique Identifier Command-Line Tool>

## VERSION

OSSP uuid UUID_VERSION_STR

## SYNOPSIS

B<uuid>
[B<-v> I<version>]
[B<-m>]
[B<-n> I<count>]
[B<-l>]
[B<-F> I<format>]
[B<-o> I<filename>]
[I<namespace> I<name>]

B<uuid>
B<-d>
[B<-r>]
[B<-o> I<filename>]
I<uuid>

## DESCRIPTION

B< OSSP uuid> is a ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant I<Universally Unique Identifier> (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number
based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood of uniqueness over space and time and are computationally difficult to guess. They are globally unique identifiers which can be locally generated without contacting a global registration authority. UUIDs are intended as unique identifiers for both mass tagging objects with an extremely short lifetime and to reliably identifying very persistent objects across a network.

This is the command line interface (CLI) of B<OSSP uuid>. For a detailed description of UUIDs see the documentation of the application programming interface (API) in uuid(3).

=head1 OPTIONS

=over 3

=item B<-v> I<version>

Sets the version of the generated DCE 1.1 variant UUID. Supported are I<version> "C<1>", "C<3>", "C<4>" and "C<5>". The default is "C<1>".

For version 3 and version 5 UUIDs the additional command line arguments I<namespace> and I<name> have to be given. The I<namespace> is either a UUID in string representation or an identifier for internally pre-defined namespace UUIDs (currently known are "C<ns:DNS>", "C<ns:URL>", "C<ns:OID>", and "C<ns:X500>"). The I<name> is a string of arbitrary length.

=item B<-m>

Forces the use of a random multi-cast MAC address when generating version 1 UUIDs. By default the real physical MAC address of the system is used.

=item B<-n> I<count>

Generate I<count> UUIDs instead of just a single one (the default).

=item B<-1>

If option B<-n> is used with a I<count> greater than C<1>, then this option can enforce the reset the UUID context for each generated UUID. This makes no difference for I<version> C<3>, C<4> and C<5> UUIDs. But
version C<1> UUIDs are based on the previously generated UUID which is remembered in the UUID context of the API. Option B<-1> deletes the remembered UUID on each iteration.

=item B<-F> I<format>

Representation format for importing or exporting an UUID. The following (case insensitive) format identifiers are currently recognized:

=over 4

=item C<BIN> (binary representation)

This is the raw 128 bit network byte order binary representation of a UUID. Example is the octet stream C<0xF8 0x1D 0x4F 0xAE 0x7D 0xEC 0x11 0xD0 0xA7 0x65 0x00 0xA0 0xC9 0x1E 0x6B 0xF6>.

=item C<STR> (string representation)

This is the 36 character hexadecimal ASCII string representation of a UUID. Example is the string "C<f81d4fae-7dec-11d0-a765-00a0c91e6bf6>".

=item C<SIV> (single integer value representation)

This is the maximum 39 character long single integer value representation of a UUID. Example is the string "C<3298007356985866629295641978511506172918>".

=back

=item B<-o> I<filename>

Write output to I<filename> instead of to F<stdout>.

=item B<-d>

Decode a given UUID (given as a command line argument or if the command line argument is "C<->" the UUID is read from F<stdin>) and dump textual information about the UUID.

=back

=head1 EXAMPLES

# generate DCE 1.1 v1 UUID (time and node based)
$ uuid -v1
01c47915-4777-11d8-bc70-0090272f725
# decode and dump DCE 1.1 v1 UUID (time and node based)
$ uuid -d 01c47915-4777-11d8-bc70-0090272ff725
encode: STR: 01c47915-4777-11d8-bc70-0090272ff725
SIV: 2349374037528578887923094374772111141
version: 1 (time and node based)
clock: 15472 (usually random)
node: 00:90:27:2f:f7:25 (global unicast)

# generate DCE 1.1 v3 UUID (name based)
$ uuid -v3 ns:URL http://www.ossp.org/
02d9e6d5-9467-382e-8f9b-9300a64ac3cd

# decode and dump DCE 1.1 v3 UUID (name based)
$ uuid -v3 02d9e6d5-9467-382e-8f9b-9300a64ac3cd
encode: STR: 02d9e6d5-9467-382e-8f9b-9300a64ac3cd
SIV: 37898662856079108881008183850376205
version: 3 (name based, MD5)
(not decipherable: MD5 message digest only)

# generate DCE 1.1 v4 UUID 4 (random data based)
$ uuid -v4
eb424026-6f54-4ef8-a4d0-bb658a1fc6cf

# decode and dump DCE 1.1 v4 UUID 4 (random data based)
$ uuid -d eb424026-6f54-4ef8-a4d0-bb658a1fc6cf
encode: STR: eb424026-6f54-4ef8-a4d0-bb658a1fc6cf
SIV: 312712571721458096795100956955942831823
version: 4 (random data based)
(no semantics: random data only)

=head1 SEE ALSO

uuid(3), OSSP::uuid(3).

=cut

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 **
 ** uuid_md5.h: MD5 API definition
 */

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid_md5.h
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##
## uuid_compat.pod: Data::UUID Backward Compatibility Perl API (Perl/POD part)
##
## =pod

=head1 NAME

Data::UUID - B<OSSP uuid> Backward Compatibility Perl Binding

=head1 DESCRIPTION

B<Data::UUID> is the B<OSSP uuid> backward compatibility Perl binding to the API of the original B<Data::UUID> module. It allows other B<Data::UUID> based Perl modules to run with B<OSSP::uuid> without changes.

=head1 SEE ALSO

B<OSSP::uuid>.

=head1 HISTORY

The backward compatibility Perl binding B<Data::UUID> for B<OSSP uuid> was originally implemented in 2004 by Piotr Roszatycki E<dexter@debian.orgE<gt>. It was later cleaned up and speed optimized in December 2005 by David Wheeler E<david@justatheory.comE<gt>.

=cut

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/perl/uuid_compat.pod

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config.m4: PHP/Zend API build-time configuration (language: m4)

PHP_ARG_ENABLE(uuid, OSSP uuid module,
[  --enable-uuid             Enable OSSP uuid extension module.])

if test "$PHP_UUID" != "no"; then
  PHP_NEW_EXTENSION(uuid, uuid.c, $ext_shared)
  AC_DEFINE(HAVE_UUID, 1, [Have OSSP uuid library])
  PHP_ADD_LIBPATH([..], )
  PHP_ADD_LIBRARY([uuid],, UUID_SHARED_LIBADD)
  PHP_ADD_INCLUDE([..])
  PHP_SUBST(UUID_SHARED_LIBADD)

  dnl avoid linking conflict with a potentially existing uuid_create(3) in libc
  AC_CHECK_FUNC(uuid_create,[
    SAVE_LDFLAGS="$LDFLAGS"
    LDFLAGS="$LDFLAGS -Wl,-Bsymbolic"
    AC_TRY_LINK([],[], [EXTRA_LDFLAGS="$EXTRA_LDFLAGS -Wl,-Bsymbolic"],
    LDFLAGS="$SAVE_LDFLAGS")]
fi

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/php/config.m4
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/*
** OSSP ui128 - 128-Bit Arithmetic
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**
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** which can be found at http://www.ossp.org/pkg/lib/ui128/.
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** ui128.c: implementation of 128-bit unsigned integer arithmetic
*/

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid_ui128.c
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OSSP uuid - Universally Unique Identifier
Version 1.6.2 (04-Jul-2008)

ABSTRACT

OSSP uuid is a ISO-C:1999 application programming interface (API)
and corresponding command line interface (CLI) for the generation of
DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant Universally
Unique Identifier (UUID). It supports DCE 1.1 variant UUIDs of version
1 (time and node based), version 3 (name based, MD5), version 4
(random number based) and version 5 (name based, SHA-1). Additional
API bindings are provided for the languages ISO-C++:1998, Perl:5 and
PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1
and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood
of uniqueness over space and time and are computationally difficult
to guess. They are globally unique identifiers which can be locally
generated without contacting a global registration authority. UUIDs
are intended as unique identifiers for both mass tagging objects
with an extremely short lifetime and to reliably identifying very
persistent objects across a network.
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HOME AND DOCUMENTATION

The documentation and latest release can be found on

- http://www.ossp.org/pkg/lib/uuid/

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##
## uuid.pc.in: pkg-config(1) specification
##

prefix=@prefix@
exec_prefix=${prefix}
includedir=${prefix}/include
libdir=${exec_prefix}/lib

Name: OSSP uuid
Description: Universally Unique Identifier (UUID) Library
Version: @UUID_VERSION_RAW@
URL: http://www.ossp.org/pkg/lib/uuid/
Cflags: -I${includedir}
Libs: -L${libdir} -luuid
Libs.private: @LIBS@

Found in path(s):
* /opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid.pc.in
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**
** uuid_md5.c: MD5 API implementation
 */
*/

/*
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 * Algorithm" in all material mentioning or referencing this software
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 */

Found in path(s):
*/

/opt/cola/permits/1129505846_1612286528.98/0/ossp-uuid-1-6-2-orig-1-tar-gz/uuid-1.6.2/uuid_md5.c
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uuid_dce.h: DCE 1.1 compatibility API definition

*/

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1.352 iconv 2.27
1.352.1 Available under license :

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notice like this when it starts in an interactive mode:

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The hypothetical commands `show w' and `show c' should show the appropriate
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might be different; for a GUI interface, you would use an "about box".

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WEV @@ WEV[B  "1

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Version 2.1, February 1999
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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
lib/libgss/g_canon_name.c
lib/libgss/g_compare_name.c
lib/libgss/g_context_time.c
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lib/libgss/g_inquire_names.c
lib/libgss/g_process_context.c
lib/libgss/g_rel_buffer.c
lib/libgss/g_rel_cred.c
lib/libgss/g_rel_name.c
lib/libgss/g_rel_oid_set.c
lib/libgss/g_seal.c
lib/libgss/g_sign.c
lib/libgss/g_store_cred.c
lib/libgss/g_unseal.c
lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
uts/common/gssapi/include/gssapi_err_generic.h
uts/common/gssapi/include/mechglueP.h

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If you have Invariant Sections, Front-Cover Texts and Back-Cover Texts, replace the ``` with@dots{} Texts.''@: line with this:

@smallexample
@group
with the Invariant Sections being @var{list their titles}, with the Front-Cover Texts being @var{list}, and with the Back-Cover Texts being @var{list}.
@end group
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@c Local Variables:
c ispell-local-pdict: "ispell-dict"
c End:
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1.370 python-cryptography 2.6.1-3+deb10u2

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1.371 go-uber-org-multierr v1.6.0

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1.372 @progress/pako-esm 1.0.1

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1.373 pam 1.1.8-3.6ubuntu2.18.04.3

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-------------------------------------------------------------------------

Patch for Ubuntu bug #399071

Display the contents of /etc/legal as part of the MOTD, the first time the user logs in, and set a flag in the user's homedir if possible to prevent repeat displays.

Authors: Dustin Kirkland <kirkland@canonical.com>

Upstream status: Ubuntu-specific, maybe submit to Debian

Index: pam.ubuntu/modules/pam_motd/pam_motd.c

--- pam.ubuntu.orig/modules/pam_motd/pam_motd.c
+++ pam.ubuntu/modules/pam_motd/pam_motd.c
@@ -73,6 +73,61 @@
close(fd);
 }

+int display_legal(pam_handle_t *pamh)
+{
+    int retval = PAM_IGNORE, rc;
+    char *user = NULL;
+    char *dir = NULL;
+    char *flag = NULL;
++

struct passwd *pwd = NULL;
struct stat s;
int f;
/* Get the user name to determine if we need to print the disclaimer */
rc = pam_get_item(pamh, PAM_USER, &user);
if (rc == PAM_SUCCESS && user != NULL && *(const char *)user != '\0')
{
    PAM_MODUTIL_DEF_PRIVS(privs);

    /* Get the password entry */
pwd = pam_modutil_getpwnam (pamh, user);
if (pwd != NULL)
{
    if (pam_modutil_drop_priv(pamh, &privs, pwd)) {
        pam_syslog(pamh, LOG_ERR,
                "Unable to change UID to %d temporarily\n",
                pwd->pw_uid);
        retval = PAM_SESSION_ERR;
        goto finished;
    }

    if (asprintf(&dir, "%s/.cache", pwd->pw_dir) == -1 || !dir)
        goto finished;
    if (asprintf(&flag, "%s/motd.legal-displayed", dir) == -1 || !flag)
        goto finished;

    if (stat(flag, &s) != 0)
    {
        display_file(pamh, "/etc/legal");
        mkdir(dir, 0700);
        f = open(flag, O_WRONLY|O_CREAT|O_EXCL,
                S_IRUSR|S_IWUSR|S_IRGRP|S_IROTH);
        if (f>=0) close(f);
    }
}

finished:
if (pam_modutil_regain_priv(pamh, &privs)) {
    pam_syslog(pamh, LOG_ERR,
            "Unable to change UID back to %d\n", privs.old_uid);
    retval = PAM_SESSION_ERR;
}

_pam_drop(flag);
_pam_drop(dir);
}
{ return retval; }
PAM_EXTERN
int pam_sm_open_session(pam_handle_t *pamh, int flags,
int argc, const char **argv)
@@ -116,6 +171,9 @@
    /* Display the updated motd */
    display_file(pamh, motd_path);

+ /* Display the legal disclaimer only if necessary */
+ retval = display_legal(pamh);
+ return retval;
}

This package was debianized by J.H.M. Dassen (Ray) jdassen@debian.org on

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1.374 installation-report 2.71

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1.384 google-uuid v1.1.1

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1.385 azure-storage-queue-go v0.0.0-20181215014128-6ed74e755687

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1.405 es-abstract 1.17.5

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1.408 vpp 20.05.1-5~g09f167997.dirty
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# Sharing Cisco Proprietary VPP Code
This note describes how we share Cisco-proprietary VPP code.

## Basic Rules of the Road

1. Code to be shared _MUST_ be in plugin form
1. Shared proprietary plugins _MUST_ work when loaded by a current, *unmodified* vpp LTS-release core engine
1. Shared plugins _SHOULD_ work with master/latest
1. "make test" functional regression tests _MUST_ be provided
1. CSIT performance tests _SHOULD_ be provided
1. Binary API, debug CLI, code and architecture documentation _MUST_ be provided
1. A MAINTAINERS file _MUST_ be provided
1. The vpp core team _MUST_ conduct an initial code review, and _MUST_ review subsequent significant updates
1. Plugin name and variable namespace collisions _MUST_ be resolved

## Shared plugin directory structure

Each shared plugin occupies a single top-level directory. Please pick a short descriptive directory name. Within the plugin directory, we expect to find the following elements:

- CMakeLists.txt - Build info for the plugin
- MAINTAINERS - Contact info for the plugin
- FEATURE.yaml - Feature description
- doc - Directory which contains feature documentation. Should include enough detail for a vpp-literate engineer to configure the plugin without undue difficulty. Should include a description of the software sufficient for a skillful engineer to modify and/or debug the code.

### MAINTAINERS file description

Please prepend src/plugins to the plugin name, as shown.

Sample MAINTAINERS file:

Sample Cisco Proprietary Plugin
I: prop-sample
M: Joe Cisco <joe@cisco.com>
F: src/plugins/prop-sample/
Y: src/plugins/prop-sample/FEATURE.yaml

Descriptions of section entries:

- M: Maintainer Full name and E-mail address: Full Name <address@domain>
- One maintainer per line. Multiple M: lines acceptable.
- F: Files and directories with wildcard patterns.
- A trailing slash includes all files and subdirectory files.
FEATURE.yaml description

name: Sample Cisco Proprietary Plugin (prop-sample)
maintainer: Joe Cisco <joe@cisco.com>
features:
  - feature 1
  - feature 2
description: "Text description of the feature(s)"
state: production
properties: [API, CLI, MULTITHREAD]

Creating a composite workspace

From the root of the Cisco proprietary plugin workspace, run the setup script:

$ scripts/setup
  VPP Workspace: /scratch/my-vpp-workspace
Include <name-of-plugin1> [Y|n]? <cr>
Adding <name-of-plugin1> to the vpp workspace
Include <name-of-plugin2> [Y|n]? n
Include <name-of-plugin3> [Y|n]? <cr>
Adding <name-of-plugin3> to the vpp workspace

The script creates symbolic links for selected directories in the Cisco proprietary plugin workspace, in the VPP workspace ...
.../src/plugins directory.

You can run the setup script as often as you like. The script unconditionally removes symbolic links prior to deciding whether to create a symbolic link, so the results are _not_ cumulative.

Found in path(s):
* /opt/cola/permits/1130510894_1614697925.57/0/vpp-master-zip/vpp-master/vpp/titan-plugins/README.md
No license file was found, but licenses were detected in source scan.

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1.412 cryptography 3.3.2

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1.424 libnuma 2.0.11 2.1ubuntu0.1

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This package was debianized by Ian Wienand <ianw@gelato.unsw.edu.au> on Wed, 15 Dec 2004.

libnuma sources are available from
https://github.com/numactl/numactl

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# PCG64

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PCG Random Number Generation for C.

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# SPLITMIX64

Written in 2015 by Sebastiano Vigna (vigna@acm.org)

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Adapted from a C++ implementation of Chris Doty-Humphrey's SFC PRNG.

https://gist.github.com/imneme/f1f7821f07cf76504a97f6537c818083

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The rk_random and rk_seed functions algorithms and the original design of the Mersenne Twister RNG:

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Original algorithm for the implementation of rk_interval function from Richard J. Wagner's implementation of the Mersenne Twister RNG, optimised by Magnus Jonsson.

Constants used in the rk_double implementation by Isaku Wada.

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    <signature of Ty Coon>, 1 April 1989
    Ty Coon, President of Vice

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

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1.439 xz 5.2.2 1.3

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1.447 path-parse 1.0.6

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1.448 findutils 4.6.0+git+20190209 2

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A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

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1.459 @babel/parser 7.9.6

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1.461 color-convert 1.9.3

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1.462 react-input-autosize 3.0.0
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1.463 debianutils 4.8.4
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1.465 libusb 1.0.22-2

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Ty Coon, President of Vice

That's all there is to it!

1.466 @emotion/weak-memoize 0.2.5

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1.467 string-natural-compare 3.0.1

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1.468 @emotion/cache 11.1.3

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THE DEVELOPMENT OF SPELL-CHECKING AND THE FIRST ISPELL

The following background information on spelling checkers in general, and ispell in particular, was provided to me by Les Earnest (les@dec-lite.stanford.edu):

> The earliest spelling checker (of sorts) of which I am aware was in a program that attempted to automatically receive human-keyed Morse code, which can be ambiguous because of the variable timing between dots, dashes, intercharacter pauses, and interword pauses. This program didn't use a full dictionary; instead, used a table of digraphs (two-letter sequences) that occur in English and barred improper letter sequences. This program was written by someone at MIT Lincoln Lab around 1959 and, I think, ran on the TX-2 computer there. Unfortunately, I don't remember his name. I might still have the paper he wrote in my files but it would take a major search to find it and I might not succeed.

> A program that I wrote in 1961 to read cursive writing contained a real spelling checker, using the 10,000 most common English words. It is reported in:


> I brought that dictionary to Stanford and got a PhD student to write a spelling checker for text in Lisp running on our PDP-6 computer at the Stanford Artificial Intelligence Lab around 1967. Unfortunately, I do not remember which student it was; it could have been Gil Falk. It was a rather simple program (certainly much simpler than the earlier cursive writing program) and I didn't think of it as a significant development at the time.

> [Later], I got another PhD student, Ralph Gorin, to do a better and
> faster spelling checker sometime in the early '70s, still using my
> old dictionary. Ralph later wrote an article about it in CACM. I
> believe that he later augmented the dictionary.

[note: Ralph has since informed me that he wrote no such article. The
program was called SPELL and was written in 1971. Ralph provided me
with a reference to "Computer Programs for Spelling Correction", by
James L. Peterson, Springer-Verlag, Berlin, 1980, No. 96 in the series
"Lecture Notes in Computer Science." This book states that Ralph's
SPELL program, which was the direct ancestor of ispell, was the first
computer program written for checking the spelling of text documents.
The book is also a good source of references on spelling programs.]

> ...
> [Ispell] was originally written in PDP-10 assembly language and ran
> under the WAITS operating system, which is similar to TOPS-10 but existed
> only on SAIL (a dual processor KA10/PDP-6 system). It was and is called
> SPELL on that machine. It later was modified to run under Tenex and
> TOPS-20.

[Ralph mentions that SPELL was also ported to MIT's ITS and TOPS-10.]

The Tenex version of ispell was later revised by W. E. Matson (1974),
and Bill Ackerman (1978). Bill has provided the following information:

> I came across the SPELL program in 1978 on ITS. It was a port from
> Stanford, and had the names Ralph Gorin (approximately 1971) and
> Wayne Matson (1974) associated with it. I did 3 things to it:
> > Rewrote it as a native program for ITS, and, shortly thereafter,
> > TOPS-20. (I never did anything for TOPS-10, and am not aware
> > that it ever ran on TOPS-10, though it may have.)
> > > Replaced the heuristics for suffix removal, which I found unreliable
> > and unsatisfactory, with an algorithm that was driven by specific
> > suffix flags in the dictionary. This way, the dictionary would have
> > complete control over what words were legal, and there would be no
> > spurious hits.
> > > Apparently most importantly, though I had no idea at time, gave it
> > the name "ISPELL", for "ITS version of spell", since I didn't
> > consider myself authorized to throw away an existing program
> > and overwrite it with a new one under the same name.
> > > I have not followed the history of the program since then, and do not know
> > if it still uses the "suffix flags" in its dictionary. But if it does,
> > I introduced them. The Ispell algorithm that uses those flags to make
accurate decisions about the legality of words was documented in great
detail in James Peterson's Springer-Verlag book. (He spent a semester
at MIT while working on the book, and I provided him with a lot of
information and documentation at that time.)

Bill Ackerman
wba@apollo.hp.com

Michael Adler adds:

I did work on ispell in 1982. Actually, I stole the ispell
dictionary and suffix compression algorithm and wrote a spelling
checker for CP/M in 8080 assembler that I very creatively called "SPELL."
By sorting the dictionary alphabetically and using a difference encoding
I managed to pack the entire dictionary that Bill was using in about
56Kb. The CP/M program read a document, sorted all the words alphabetically
and then checked them. It then reread the document and compared words as
it found them against the in memory, sorted and checked words. SPELL was
around in the public domain on CP/M.

I was in high school at the time and talked to Bill only over email.
We wound up in the same compiler group at Apollo in the late 80's by
coincidence.

DEVELOPMENT OF THE C/UNIX VERSION OF ISPELL

In 1983, Pace Willisson (pace@prep.ai.mit.edu) wrote a C/Unix version
from scratch, based on the ispell documentation.

In 1987, Walt Buehring revised and enhanced ispell, and posted it to the
Usenet along with a dictionary. In addition, Walt wrote the first version
of "ispell.el", the emacs interface.

Geoff Kuenning (geoff@ITcorp.com, that's me, and by the way I
pronounce it "Kenning"; the "u" is silent) picked up this version,
fixed some bugs, and added further enhancements, all of which made me
the de-facto ispell maintainer for the net. I also put quite a bit of
work into improving the quality of the dictionaries. In 1987 I began
work on the "munchlist" script, which I originally intended to be used
to add flags to personal dictionary entries. At the same time I was
studying German, and wanted to use ispell to check the papers I was
writing for that class. After thinking about it for some time, I
realized that the suffix flags could be table-driven, which would both
add flexibility and would get rid of certain difficult-to-find bugs.
In 1988 I rewrote major portions of the code to do this, resulting in
the first multi-lingual version. Ole Bjoern Hessen (obh@ifi.uio.no)
in Norway alpha-tested this version and provided several important
enhancements.
Bob Devine (vianet!devine) provided two larger dictionaries (which became the basis for english.1 and english.2) to me for inclusion with subsequent releases.

Ashwin Ram (ram@cs.yale.edu) made substantial enhancements to Walt Buehring's emacs interface, and provided them to me for inclusion with an earlier release.

The emacs interface was then completely overhauled by Ken Stevens (stevens@hplabs.hp.com), who also beta-tested the software and without whom this posting would not have been possible. If there's a feature in the emacs interface that you like, you probably have Ken to thank for it. His efforts have been tireless for many years.

Martin Boyer made major contributions to the munchlist script, including producing a version that runs under perl (see languages/Where for instructions on how to get that version). Philippe-Andre Prindeville provided xspell (a Motif-based X interface), and Moritz Willers provided a NeXTStep interface.

DEVELOPMENT AND DEATH OF ISPELL 4.0

Meanwhile, and unbeknownst to me, Pace Willisson was working on his own improvements to ispell. He focused primarily on dictionary size and startup time. His solution was a dictionary compression algorithm that detected and encoded frequent letter pairs. This also reduced the time needed to read it in. Pace also changed some internal data structures to improve startup time. Pace and I eventually discovered each other's efforts, and discussed re-merging our changes, but we decided that there would be too much work involved. This was partly because I was close to a release and didn't want to delay it with an extensive and error-prone merge.

In late 1992 (if my memory serves correctly), Richard Stallman contacted me, asking for permission to distribute ispell as part of the GNU suite. I responded that he was welcome to distribute it, but that I was not willing to place my software under the Gnu Public License. Through a misunderstanding, neither of us considered the possibility of finding a compromise license that both could live with. So Richard started a search for an alternate version, and found Pace working right in his back yard.

I have been told that when FSF first learned of Pace's version, they again considered using International Ispell instead because it was both more popular and more capable, but this idea was rejected due to the license misunderstanding. Instead, FSF enhanced Pace's version somewhat and called it ispell 4.0, apparently in the hopes that by
numbering the version higher, it would become the standard.

When ispell 4.0 was released, much confusion ensued. Many ispell users innocently "upgraded" to 4.0 and then screamed when they could not find features to which they had grown accustomed. Europeans in general were angered by the apparent provincialism shown by the "dropping" of international support. I found myself inundated with questions about a version I had never heard of or seen.

One of the earliest and most common suggestions was that FSF should rename their version "gispell". This had a lot of precedent, both in the naming of other FSF utilities and in the then-recent change of the suffix used by gzip from ".z" to ".gz". Unfortunately, the FSF refused to do this. I may have inadvertently contributed to this refusal with a Usenet posting in which I tried to clarify what had happened, pointing out that the FSF version was more recently related to Pace's than my own. This may have been seen as an acknowledgment that FSF should have the rights to the name "ispell," and that I should rename my version.

A flame war arose, and I decided that the only way to solve the problem was to rename my version to eliminate the confusion. However, at about the same time Richard Stallman and I began negotiating via e-mail. We itemized and clarified his objections to my license, and I learned from a third party that FSF is willing to distribute software that falls under the University of California license (also known as the Berkeley license). Richard and I agreed that if I changed my license to be a paraphrase of the UC license, FSF would be willing to distribute my version with no changes. Since then, ispell 4.0 has been dropped by FSF and has pretty well disappeared from the net, leaving International Ispell as the version of choice for nearly everyone.

OTHER CONTRIBUTORS

Numerous other people have contributed enhancements, suggestions, and bug fixes. I used to attempt to keep track of all of them, and list their names here. However, I found that keeping the list of names correct was as time-consuming as fixing bugs, so I finally decided that the list had grown too long, and stopped keeping it current. This is unfortunate, because many people have made significant contributions to ispell and I would like to have a way to acknowledge them. So please remember that hundreds of people have helped in this effort, and that I appreciate each and every one of them.

#ifndef LOCAL_H_INCLUDED
#define LOCAL_H_INCLUDED
/*
 * $Id: local.h.bsd,v 1.1 2005/04/27 00:17:46 geoff Exp $
 */
/*
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 *    such. Binary redistributions based on modified source code
 *    must be clearly marked as modified versions in the documentation
 *    and/or other materials provided with the distribution.
 * 4. The code that causes the 'ispell -v' command to display a prominent
 *    link to the official ispell Web site may not be removed.
 * 5. The name of Geoff Kuenning may not be used to endorse or promote
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 * OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF
 * SUCH DAMAGE.
 */

/*
 * This file is a sample local.h file. It shows what I believe nearly every
 * site will want to include in their local.h. You will probably want to
 * expand this file; see "config.X" to learn of #defines that you might
 * like to add to.
 */

/*
 * WARNING WARNING WARNING
 *
 * This file is *NOT* a normal C header file! Although it uses C
* syntax and is included in C programs, it is also processed by shell
* scripts that are very stupid about format.
*
* Do not try to use #if constructs to configure this file for more
* than one configuration. Do not place whitespace after the "#" in
* "#define". Do not attempt to disable lines by commenting them out.
* Do not use backslashes to reduce the length of long lines.
* None of these things will work the way you expect them to.
*
* WARNING WARNING WARNING
*/

#define MINIMENU /* Display a mini-menu at the bottom of the screen */
#define GENERATE_LIBRARY_PROTOS
#define EGREPCMD "egrep -i"
#define HAS_RENAME

/*
 * Important directory paths. If you change MAN45DIR from man5 to
 * something else, you probably also want to set MAN45SECT and
 * MAN45EXT (but not if you keep the man pages in section 5 and just
 * store them in a different place).
 */
#define BINDIR "/usr/local/bin"
#define LIBDIR "/usr/local/lib"
#define MAN1DIR "/usr/local/man/man1"
#define MAN45DIR "/usr/local/man/man5"
#define MAN45EXT ".5"

/*
 * Place any locally-required #include statements here
 */

#endif /* LOCAL_H_INCLUDED */

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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1.473 argon2 0~20171227-0.2

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1.474 @sinonjs/formatio 3.2.2

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1.475 webencodings 0.5.1

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Found in path(s):
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/x_user_defined.py
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/tests.py
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/labels.py
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/__init__.py

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from setuptools import setup, find_packages
import io
import re
VERSION = re.search("VERSION = '(\[^'\]+)'", io.open(
    path.join(path.dirname(__file__), 'webencodings', '__init__.py'),
    encoding='utf-8').read().strip()).group(1)

LONG_DESCRIPTION = io.open(
    path.join(path.dirname(__file__), 'README.rst'), encoding='utf-8').read()

setup(
    name='webencodings',
    version=VERSION,
    url='https://github.com/SimonSapin/python-webencodings',
    license='BSD',
    author='Simon Sapin',
    author_email='simon.sapin@exyr.org',
    maintainer='Geoffrey Sneddon',
    maintainer_email='me@gsnedders.com',
    description='Character encoding aliases for legacy web content',
    long_description=LONG_DESCRIPTION,
    classifiers=[
        'Development Status :: 4 - Beta',
        'Intended Audience :: Developers',
        'License :: OSI Approved :: BSD License',
        'Programming Language :: Python',
        'Programming Language :: Python :: 2',
        'Programming Language :: Python :: 2.6',
        'Programming Language :: Python :: 2.7',
        'Programming Language :: Python :: 3',
        'Programming Language :: Python :: 3.3',
        'Programming Language :: Python :: 3.4',
        'Programming Language :: Python :: 3.5',
        'Programming Language :: Python :: 3.6',
        'Programming Language :: Python :: Implementation :: CPython',
        'Programming Language :: Python :: Implementation :: PyPy',
        'Topic :: Internet :: WWW/HTTP',
    ],
    packages=find_packages(),
)

Found in path(s):
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1-1.tar.gz/webencodings-0.5.1/setup.py

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import json

try:
    from urllib import urlopen
except ImportError:
    from urllib.request import urlopen

def assert_lower(string):
    assert string == string.lower()
    return string

def generate(url):
    parts = ['''
    """
    webencodings.labels
    """
    Map encoding labels to their name.
    
    :copyright: Copyright 2012 by Simon Sapin
    :license: BSD, see LICENSE for details.
    
    LABELS = {
    """
    labels = [
        (repr(assert_lower(label)).lstrip('u'),
         repr(encoding['name']).lstrip('u'))
        for category in json.loads(urlopen(url).read().decode('ascii'))
        for encoding in category['encodings']
    ...
for label in encoding['labels']
max_len = max(len(label) for label, name in labels)
parts.extend(
'    %s:%s %s,\n' % (label, ' ' * (max_len - len(label)), name)
for label, name in labels)
parts.append('}')
return ''.join(parts)

if __name__ == '__main__':
    print(generate('http://encoding.spec.whatwg.org/encodings.json'))

Found in path(s):
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0-5-1-1-tar-gz/webencodings-0.5.1/webencodings/mklabels.py
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Metadata-Version: 1.1
Name: webencodings
Version: 0.5.1
Summary: Character encoding aliases for legacy web content
Home-page: https://github.com/SimonSapin/python-webencodings
Author: Geoffrey Sneddon
Author-email: me@gsnedders.com
License: BSD
Description: python-webencodings

This is a Python implementation of the `WHATWG Encoding standard <http://encoding.spec.whatwg.org/>`_

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
* PyPI releases: http://pypi.python.org/pypi/webencodings
* License: BSD
* Python 2.6+ and 3.3+

In order to be compatible with legacy web content
when interpreting something like ```Content-Type: text/html; charset=latin1```,
tools need to use a particular set of aliases for encoding labels
as well as some overriding rules.
For example, ```US-ASCII``` and ```iso-8859-1``` on the web are actually
aliases for ```windows-1252```, and an UTF-8 or UTF-16 BOM takes precedence
over any other encoding declaration.
The Encoding standard defines all such details so that implementations do
not have to reverse-engineer each other.
This module has encoding labels and BOM detection, but the actual implementation for encoders and decoders is Python.

Platform: UNKNOWN
Classifier: Development Status :: 4 - Beta
Classifier: Intended Audience :: Developers
Classifier: License :: OSI Approved :: BSD License
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.6
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.3
Classifier: Programming Language :: Python :: 3.4
Classifier: Programming Language :: Python :: 3.5
Classifier: Programming Language :: Python :: 3.6
Classifier: Programming Language :: Python :: Implementation :: CPython
Classifier: Programming Language :: Python :: Implementation :: PyPy
Classifier: Topic :: Internet :: WWW/HTTP

Found in path(s):
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1-1.tar.gz/webencodings-0.5.1/PKG-INFO
* /opt/cola/permits/1110814957_1606854649.94/0/webencodings-0.5.1-1.tar.gz/webencodings-0.5.1/webencodings.egg-info/PKG-INFO

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python-webencodings

This is a Python implementation of the `WHATWG Encoding standard <http://encoding.spec.whatwg.org/>`

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
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* Python 2.6+ and 3.3+

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This module has encoding labels and BOM detection, but the actual implementation for encoders and decoders is Pythons.

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1.476 pexpect 4.6.0-1

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1.477 validate-npm-package-license 3.0.4

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1.478 x-text v0.3.3

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1.479 readline 8.1.0-r0

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1.491 python-googleapi 1.5.5-1

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1.492 kerberos 1.16-2ubuntu0.2

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 cmd/krb5/kadmin/server/propd_svc.c
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 cmd/krb5/slave/kpropd_rpc.c
 lib/gss_mechs/mech_krb5/et/kdb5_err.c
 lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
 lib/gss_mechs/mech_spnego/mech/spnego_mech.c
 lib/krb5/kadm5/kadm_host_srv_names.c
 lib/krb5/kdb/kdb_convert.c
 lib/krb5/kdb/kdb_hdr.h
 lib/krb5/kdb/kdb_log.c
 lib/krb5/kdb/kdb_log.h
 lib/libgss/g_accept_sec_context.c
 lib/libgss/g_acquire_cred.c
 lib/libgss/g_canon_name.c
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```
lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in "src/lib/gssapi", including the following files:

lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
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lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:
include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
kprop/kpropd_rpc.c
kprop/kproplog.c

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1.496 @emotion/css 11.1.3

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1.499 immutable 4.0.0-rc.12

1.500 zlib 1.2.11

1.500.1 Available under license:

/* zlib.h -- interface of the 'zlib' general purpose compression library
 version 1.2.11, January 15th, 2017

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Jean-loup Gailly        Mark Adler
jloup@gzip.org          madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files http://tools.ietf.org/html/rfc1950 (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

*/

1.501 python-cffi 1.12.2-1

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1.502 html5lib 1.0.1-1

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Local-Date: Fri, 06 Jun 2003 13:18:52 -0400
Date: Fri, 6 Jun 2003 10:18:52 -0700
From: Juan Gomez <juang@us.ibm.com>
To: Stephen Frost <sfrost@debian.org>
X-Mailer: Lotus Notes Release 5.0.2a (Intl) 23 November 1999
Subject: Re: Juan C. Gomez license in OpenLDAP Source

Stephen,

"There is no restriction on modifications and derived works" on the work I did for the openldap server as long as this is consistent with the openldap license. Please forward this email to Kurt so he does the appropriate changes to the files to reflect this.

Regards, Juan

MA

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After discussing this license with the OpenLDAP Foundation we received
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---

* To: Stephen Frost <sfrost@snowman.net>
* Subject: Re: OpenLDAP Licenseing issues
* From: "Kurt D. Zeilenga" <Kurt@OpenLDAP.org>
* Date: Wed, 28 May 2003 10:55:44 -0700
* Cc: Steve Langasek <vorlon@netexpress.net>,debian-legal@lists.debian.org, openldap-devel@OpenLDAP.org
* In-reply-to: <20030528162613.GB8524@ns.snowman.net>
* Message-id: <5.2.0.9.0.20030528094229.02924780@127.0.0.1>
* Old-return-path: <Kurt@OpenLDAP.org>

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counsel office (http://www.umich.edu/~vpge/).

Regards, Kurt

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1.518 fdisk 2.31.1

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procfs provides functions to retrieve system, kernel and process metrics from the pseudo-filesystem proc.

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1.532 openssh 7.9p1-10+deb10u2

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* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
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set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
set xtics rotate by -45
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## BSD-specific setup (FreeBSD, OpenBSD, NetBSD, *BSD)
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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)

## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -$l$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcreg)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%:o: $(srcreg)/%.c
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<

%.$(STATIC_O): $(srcreg)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%:o: $(srcreg)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcreg)/%.c
@echo "generating dependency information for $<"
$(SHELL) -ec "$(GEN_DEPS.c) $< \n | sed 's/(^\%\S\S)\S\S[ :]*/\1.o $@ : /g\n' > $@; \n [ -s $@ ] || rm -f $@

%.d: $(srcreg)/%.cpp
@echo "generating dependency information for $<"
$(SHELL) -ec "$(GEN_DEPS.cc) $< \n | sed 's/(^\%\S\S)\S\S[ :]*/\1.o $@ : /g\n' > $@; \n [ -s $@ ] || rm -f $@

## Versioned libraries rules
%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s $< \n
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s $< $@.$(SO).$(SO_TARGET_VERSION)

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup

### 1.558 cracklib 2.9.6-2

#### 1.558.1 Available under license:

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Version 2.1, February 1999

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<signature of Ty Coon>, 1 April 1990
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Discussion thread from mailing list archive, with approval from everyone actively
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[Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:16

Attachments: Message as HTML
looks like 2.8.11 is out and marked as "GPL-2" ... releasing libraries unde=
\r=20
GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists
=2Dmike

Re: [Cracklib-devel] cracklib license
From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:18
I understand that, and you're welcome to bring it up with Alec directly
and see if he wants to relicense his code as LGPL... but at this point,
it was enough to just get it consistent and documented as to what it was
released under. This wasn't actually a license change, just a
clarification of the licensing that was already in place.=20

-- Nathan
=20
------------------------------------------------------------
Nathan Neulinger EMail: nneul@um...
University of Missouri - Rolla Phone: (573) 341-6679
UMR Information Technology Fax: (573) 341-4216

--- Original Message---
From: cracklib-devel-bounces@li...
[mailto:cracklib-devel-bounces@li...] On Behalf Of
Mike Frysinger
Sent: Monday, October 01, 2007 8:15 PM
To: cracklib-devel@li...
Subject: [Cracklib-devel] cracklib license
=20
looks like 2.8.11 is out and marked as "GPL-2" ... releasing
libraries under
GPL-2 is not desirable at all ... this is why the LGPL-2.1 exists
-mike

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2007-10-02 01:33

Attachments: Message as HTML
On Monday 01 October 2007, Neulinger, Nathan wrote:
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> and see if he wants to relicense his code as LGPL... but at this point,
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> clarification of the licensing that was already in place.

the original license (before moving to sourceforge -- aka, 2.7) was not
GPL-2 ... it was a modified artistic license ... i didn't notice the license=
=20
change until it was mentioned in the latest notes.

unlike the old license, GPL-2 prevents people from using cracklib unless th=
eir=20
applications are also GPL-2 which imo is just wrong. it isn't the place of =
a=20
library to dictact to application writes what license they should be using.=
=20
thus LGPL-2.1 enters to fill this void.
=2Dmike

Re: [Cracklib-devel] cracklib license
From: Neulinger, Nathan <nneul@um...> - 2007-10-02 01:46
Seems like the ideal thing here would be for you and the other distro
maintainers to get together with Alec in a conversation and come to a
decision as to what licensing scheme y'all want. I haven't really done
much other than cleaning up the packaging and patches and a small bit of
additional code, so whatever licensing y'all come up with is fine by me.

-- Nathan
=20
------------------------------------------------------------
Nathan Neulinger EMail: nneul@um...
University of Missouri - Rolla Phone: (573) 341-6679
UMR Information Technology Fax: (573) 341-4216

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> From: cracklib-devel-bounces@li...
> [mailto:cracklib-devel-bounces@li...] On Behalf Of
> Mike Frysinger
> Sent: Monday, October 01, 2007 8:33 PM
> To: Neulinger, Nathan
> Cc: cracklib-devel@li...; Alec Muffett
> Subject: Re: [Cracklib-devel] cracklib license
> =20
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> > the original license (before moving to sourceforge -- aka, 2.7) was
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> >
> > unlike the old license, GPL-2 prevents people from using cracklib
> > unless their
> > applications are also GPL-2 which imo is just wrong. it isn't the
> > place of a
> > library to dictate to application writes what license they should
> > be using.
> > thus LGPL-2.1 enters to fill this void.
> > -mike

Re: [Cracklib-devel] cracklib license
From: Alec Muffett <alecm@cr...> - 2007-10-02 08:57
> Seems like the ideal thing here would be for you and the other distro
> maintainers to get together with Alec in a conversation and come to a
> decision as to what licensing scheme y'all want. I haven't really done
> much other than cleaning up the packaging and patches and a small
> bit of
> > additional code, so whatever licensing y'all come up with is fine
> > by me.

I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be
linked with any code, not just GPL....

-a

Re: [Cracklib-devel] cracklib license
From: Devin Reade <gdr@gn...> - 2007-10-02 15:04
I would like to see it under LGPL as well. I think it is in everyone's
best interests to have as secure systems as possible, and I think tainting
it via GPL will just make it less likely that the library gets used, and
will not usually cause companies/developers to GPL the dependent code
(where it is not already GPL).
I like GPL, I use it when I can, but I don't think that it's the correct license in this situation.

Devin

--

If it's sinful, it's more fun.

Re: [Cracklib-devel] cracklib license
From: Nalin Dahyabhai <nalin@re...> - 2008-01-28 16:32
On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:
> > Seems like the ideal thing here would be for you and the other distro
> > maintainers to get together with Alec in a conversation and come to a
> > decision as to what licensing scheme y'all want. I haven't really done
> > much other than cleaning up the packaging and patches and a small
> > bit of
> > additional code, so whatever licensing y'all come up with is fine
> > by me.
> >
> I am sympathetic. Guys, what do you reckon?
> >
> > What I am hearing so far is that LGPL makes sense, since it can be
> > linked with any code, not just GPL...

My apologies for not chiming in in anything resembling a reasonable timeframe.

I'd also suggest the LGPL, for the reason you noted above. Alternately, GPLv2 with the option of using the library under a later version of the GPL would permit applications which were released under version 3 of the GPL to use the library, too, which would be sufficient for the packages which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

Cheers,

Nalin

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-05 21:27

Attachments: Message as HTML
On Monday 28 January 2008, Nalin Dahyabhai wrote:
> On Tue, Oct 02, 2007 at 09:57:31AM +0100, Alec Muffett wrote:
> > > Seems like the ideal thing here would be for you and the other distro
maintainers to get together with Alec in a conversation and come to a
decision as to what licensing scheme y'all want. I haven't really done
much other than cleaning up the packaging and patches and a small
bit of
additional code, so whatever licensing y'all come up with is fine
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I am sympathetic. Guys, what do you reckon?

What I am hearing so far is that LGPL makes sense, since it can be
linked with any code, not just GPL....

My apologies for not chiming in in anything resembling a reasonable
timeframe.

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GPL to use the library, too, which would be sufficient for the packages
which are included in Fedora. FWIW, I'd personally lean toward LGPL.

In any case, I thank you both for working on sorting this out.

looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
the change now ?

-mike

Re: [Cracklib-devel] cracklib license
From: Alec Muffett <alecm@cr...> - 2008-10-05 23:18

In any case, I thank you both for working on sorting this out.

looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we make
the change now ?

yes. go for it. thanks++

-a

Re: [Cracklib-devel] cracklib license
From: Mike Frysinger <vapier@ge...> - 2008-10-25 22:34

Attachments: Message as HTML
On Sunday 05 October 2008, Alec Muffett wrote:

looks like everyone is OK with LGPL-2.1 (GNU Lesser license), so can we
> > make the change now?
> >
> > yes, go for it. thanks++

Nathan Neulinger is the only one who can actually make said change ...

-mike

---------

BELOW IS ORIGINAL LICENSING DISCUSSION RE CHANGING TO GPL from Artistic.
---------

CrackLib was originally licensed with a variant of the Artistic license. In the interests of wider acceptance and more modern licensing, it was switched with the original author's blessing to GPL v2.

This approval was carried out in email discussions in 2005, and has been reconfirmed as of 2007-10-01 with the following email from Alec Muffett.

The below email references nneul@umr.edu address, as that is the address that was used at the time. For any future emails regarding this, please use nneul@neulinger.org.

-------------------------------------
From alecm@crypticide.com Mon Oct  1 12:26:03 2007
Received: from umr-exproto2.cc.umr.edu ([131.151.0.192]) by UMR-CMAIL1.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
   Mon, 1 Oct 2007 12:26:03 -0500
Received: from scansrv2.srv.mst.edu ([131.151.1.114]) by umr-exproto2.cc.umr.edu with Microsoft SMTPSVC(6.0.3790.3959);
   Mon, 1 Oct 2007 12:26:02 -0500
Received: (qmail 8022 invoked from network); 1 Oct 2007 16:59:55 -0000
Received: from smtp1.srv.mst.edu (131.151.1.43)
   by scanin-ipvs.cc.umr.edu with SMTP; 1 Oct 2007 16:59:55 -0000
Received: from spunkymail-mx8.g.dreamhost.com (mx1.spunky.mail.dreamhost.com [208.97.132.47])
   by smtp1.srv.mst.edu (8.13.1/8.13.1) with ESMTP id l91Gxtpr020623
   for <nneul@umr.edu>; Mon, 01 Oct 2007 11:59:55 -0500
Received: from rutherford.zen.co.uk (rutherford.zen.co.uk [212.23.3.142])
   by spunkymail-mx8.g.dreamhost.com (Postfix) with ESMTP id 2C7734D311
   for <nneul@neulinger.org>; Mon, 1 Oct 2007 09:59:50 -0700 (PDT)
Received: from [82.68.43.14] (helo=[192.168.1.3])
   by rutherford.zen.co.uk with esmtp (Exim 4.50)
   id 11cOcX-0004Qt-6L
   for nneul@neulinger.org; Mon, 01 Oct 2007 16:59:49 +0000
Any chance you could write me a self-contained email stating clearly
that the license is being changed to GPL, so I could include that
e-mail
in the repository and clean up the repository/tarballs? I have all the
original discussion, but something succinct and self contained
would be
ideal.

The license for my code in the Cracklib distribution is henceforth GPL.

Happy now? :-)

-a
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1.562 react-fileupload-progress 0.5.0
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1.563 redux-saga-deferred 1.1.2
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1.565 tiny-warning 1.0.3

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1.566 scowl 2018.04.16-1

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Date: Tue, 11 Jul 2000 19:31:34 +0100

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Version: 0.6.21
Summary: AutoRest swagger generator Python client runtime.
Home-page: https://github.com/Azure/msrest-for-python
Author: Microsoft Corporation
License: MIT License
Description: AutoRest: Python Client Runtime

Installation
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To install:

.. code-block:: bash

    $ pip install msrest

Release History
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2021-01-26 Version 0.6.21
**Bug Fixes**

- Fixes `failsafe_deserialize` introduced in `0.6.20` #232

2021-01-25 Version 0.6.20

**Features**

- Add `failsafe_deserialize` method to the `Deserializer` object. #232
- Serialize `datetime`, `date`, `time`, `timedelta` and `Decimal` correctly when serializing `object`. #224

2020-09-08 Version 0.6.19

**Bugfixes**

- Fix serialization of random Model object #220
- Fix serialization of unicode string in Py2 and object mode #221

2020-07-27 Version 0.6.18

**Features**

- Add support for attributes/text in the same XML node #218

2020-06-25 Version 0.6.17

**Bugfixes**

- Fix XML and discriminator #214

2020-06-09 Version 0.6.16

**Bugfixes**

- Fix XML parsing with namespaces and attributes #209

**Features**
- Add py.typed for mypy support

2020-06-04 Version 0.6.15
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**Bugfixes**

- Fix RFC regression introduced in 0.6.14 (RFC parse date are no longer pickable) #208  
- Fix XML parsing with namespaces #206

Thanks to ivanst0 for the contribution

2020-05-18 Version 0.6.14
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**Bugfixes**

- Fix "from_dict" in some complex flattening scenario #204  
- Fix RFC date parsing if machine locale is not English #201

2020-04-07 Version 0.6.13
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**Bugfixes**

- Fix deserializer and flattening if intermediate node is None #198  
- Fix validation exception message for minimum/maximum checks #199

2020-04-06 Version 0.6.12
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**Features**

- Add "time" serializer/deserializer #196

2020-01-30 Version 0.6.11
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**Features**

- XML mode can now be enabled even if the given Model has no XML metadata #184  
- Add Kerberos Authentication #186  
- Improve error message if expected type is dictionary and something else is provided #188
**Bugfixes**

- Fix comma separated serialization of array in query #186
- Fix validation of basic types in some complex scenario #189

Thanks to catatonicprime for the contribution

2019-09-04 Version 0.6.10
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**Features**

- XML mode now supports OpenAPI additional properties #174

2019-07-24 Version 0.6.9
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**Features**

- Accept "is_xml" kwargs to force XML serialization #178
- Disable XML deserialization if received element is not an ElementTree #178
- A "null" enum deserialize as None, and not "None" anymore #173
- Fix some UTF8 encoding issue in Python 2.7 and XML mode #172

2019-06-24 Version 0.6.8
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**Bugfixes**

- Impossible to serialize XML if model contains UTF8 characters on Python 2.7 #165
- Impossible to deserialize a HTTP response as XML if body contains UTF8 characters on Python 2.7 #165
- Loading a serialized configuration fails with NameError on NoOptionError #162

Thanks to cclauss for the contribution

2019-06-12 Version 0.6.7
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**Features**

- Add DomainCredentials credentials for EventGrid

Thanks to kalyanaj for the contribution
2019-03-21 Version 0.6.6
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**Bugfixes**

- Make 0.6.x series compatible with pyinstaller again
- sdist now includes tests

Thanks to dotlambda for the contribution

2019-03-11 Version 0.6.5
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**Bugfixes**

- Fix list of integers serialization if div is provided #151
- Fix parsing of UTF8 with BOM #145

Thanks to eduardomourar for the contribution

2019-01-09 Version 0.6.4
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**Bugfixes**

- Fix regression on credentials configuration if used outside of Autorest scope #135

2019-01-08 Version 0.6.3
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**Features**

- Updated **experimental** async support. Requires Autorest.Python 4.0.64.

2018-11-19 Version 0.6.2
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**Bugfixes**

- Fix circular dependency in TYPE_CHECKING mode #128

2018-10-15 Version 0.6.1
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**Bugfixes**

- Remove unnecessary verbose "warnings" log #126
2018-10-02 Version 0.6.0
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**Features**

- The environment variable AZURE_HTTP_USER_AGENT, if present, is now injected part of the UserAgent
- New **preview** msrest.universal_http module. Provide tools to generic HTTP management (sync/async, requests aiohttp, etc.)
- New **preview** msrest.pipeline implementation:
  - A Pipeline is an ordered list of Policies than can process an HTTP request and response in a generic way.
  - More details in the wiki page about Pipeline: https://github.com/Azure/msrest-for-python/wiki/msrest-0.6.0-Pipeline

- Adding new attributes to Configuration instance:
  - http_logger_policy - Policy to handle HTTP logging
  - user_agent_policy - Policy to handle UserAgent
  - pipeline - The current pipeline used by the SDK client
  - async_pipeline - The current async pipeline used by the async SDK client

- Installing "msrest[async]" now installs the **experimental** async support. Works ONLY for Autorest.Python 4.0.63.

**Breaking changes**

- The HTTPDriver API introduced in 0.5.0 has been replaced by the Pipeline implementation.

- The following classes have been moved from "msrest.pipeline" to "msrest.universal_http":
  - ClientRedirectPolicy
  - ClientProxies
  - ClientConnection

- The following classes have been moved from "msrest.pipeline" to "msrest.universal_http.requests":
  - ClientRetryPolicy

**Bugfixes**

- Fix "long" on Python 2 if used with the "object" type #121

Thanks to robgolding for the contribution

2018-09-04 Version 0.5.5
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**Bugfixes**

- Fix a serialization issue if additional_properties is declared, and "automatic model" syntax is used
  ("automatic model" being the ability to pass a dict to command and have the model auto-created) # 120

2018-07-12 Version 0.5.4
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**Features**

- Support additionalProperties and XML

**BugFixes**

- Better parse empty node and not string types
- Improve "object" XML parsing

2018-07-10 Version 0.5.3
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**BugFixes**

- Fix some XML serialization subtle scenarios

2018-07-09 Version 0.5.2
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**Features**

- deserialize/from_dict now accepts a content-type parameter to parse XML strings

**Bugfixes**

- Fix some complex XML Swagger definitions.

This release likely breaks already generated XML SDKs, that needs to be regenerated with autorest.python
3.0.58

2018-06-21 Version 0.5.1
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**Bugfixes**

- Lower Accept header overwrite logging message #110
- Fix 'object' type and XML format

Thanks to dharmab for the contribution
2018-06-12 Version 0.5.0

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**Disclaimer**

This released is designed to be backward compatible with 0.4.x, but there is too many internal refactoring and new features to continue with 0.4.x versionning

**Features**

- Add XML support
- Add many type hints, and MyPY testing on CI.
- HTTP calls are made through a HTTPDriver API. Only implementation is `requests` for now. This driver API is *not* considered stable
  and you should pin your msrest version if you want to provide a personal implementation.

**Bugfixes**

- Incorrect milliseconds serialization for some datetime object #94

**Deprecation**

That will trigger a DeprecationWarning if an old Autorest generated code is used.

- `_client.add_header` is deprecated, and `config.headers` should be used instead
- `_client.send_formdata` is deprecated, and `_client.put/get/delete/post` should be used instead

2018-04-30 Version 0.4.29

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**Bugfixes**

- Improve `SDKClient.__exit__` to take `exc_details` as optional parameters and not required #93
- `refresh_session` should also use the permanent HTTP session if available #91

2018-04-18 Version 0.4.28

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**Features**

- msrest is now able to keep the ”requests.Session” alive for performance. To activate this behavior:
  - Use the final Client as a context manager (requires generation with Autorest.Python 3.0.50 at least)
  - Use `client.config.keep_alive = True` and `client.close()` (requires generation with Autorest.Python 3.0.50 at least)
  - Use `client.config.keep_alive = True` and `client._client.close()` (not recommended, but available in old releases of SDK)
- All Authentication classes now define `signed_session` and `refresh_session` with an optional `session` parameter.
  
  To take benefits of the session improvement, a subclass of Authentication *MUST* add this optional parameter
  
  and use it if it's not `None`:

```python
def signed_session(self, session=None):
    session = session or requests.Session()
    # As usual from here.
```

2018-03-07 Version 0.4.27
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**Features**

- Disable HTTP log by default (security), add `enable_http_log` to restore it #86

**BugFixes**

- Fix incorrect date parsing if ms precision is over 6 digits #82

2018-01-30 Version 0.4.26
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**Features**

- Add TopicCredentials for EventGrid client

**Bugfixes**

- Fix minimal dependency of isodate
- Fix serialisation from dict if datetime provided

2018-01-08 Version 0.4.25
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**Features**

- Add LROPoller class. This is a customizable LRO engine.
  
  This is the poller engine of Autorest.Python 3.0, and is not used by code generated by previous Autorest version.

2018-01-03 Version 0.4.24
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**Bugfixes**
- Date parsing is now compliant with Autorest / Swagger 2.0 specification (less lenient)

**Internal optimisation**

- Call that does not return a streamable object are now executed in requests stream mode False (was True whatever the type of the call).

  This should reduce the number of leaked opened session and allow urllib3 to manage connection pooling more efficiently.

  Only clients generated with Autorest.Python >= 2.1.31 (not impacted otherwise, fully backward compatible)

2017-12-21 Version 0.4.23
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**Bugfixes**

- Accept to deserialize enum of different type if content string match #75
- Stop failing on deserialization if enum string is unknown. Return the string instead.

**Features**

- Model now accept kwargs in constructor for future kwargs models

2017-12-15 Version 0.4.22
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**Bugfixes**

- Do not validate additional_properties #73
- Improve validation error if expected type is dict, but actual type is not #73

2017-12-14 Version 0.4.21
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**Bugfixes**

- Fix additional_properties if Swagger was flatten #72

2017-12-13 Version 0.4.20
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**Features**

- Add support for additional_properties

  - By default, all additional_properties are kept.
  - Additional properties are sent to the server only if it was specified in the Swagger, or if "enable_additional_properties_sending" is called on the model we want it.

  This is a class method that enables it for all instance of this model.
2017-11-20 Version 0.4.19
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**Features**

- The interpretation of Swagger 2.0 "discriminator" is now lenient. This means for these two scenarios:
  - Discriminator value is missing from the received payload
  - Discriminator value is not defined in the Swagger

Instead of failing with an exception, this now returns the base type for this "discriminator".

Note that this is not a contradiction of the Swagger 2.0 spec, that specifies
"validation SHOULD fail [...] there may exist valid reasons in particular circumstances to ignore a particular
item,
but the full implications must be understood and carefully weighed before choosing a different course."

This cannot be configured for now and is the new default behaviour, but can be in the future if needed.

**Bugfixes**

- Optional formdata parameters were raising an exception (#65)
- "application/x-www-form-urlencoded" form was sent using "multipart/form-data".
  This causes problems if the server does not support "multipart/form-data" (#66)

2017-10-26 Version 0.4.18
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**Features**

- Add ApiKeyCredentials class. This can be used to support OpenAPI ApiKey feature.
- Add CognitiveServicesAuthentication class. Pre-declared ApiKeyCredentials class for Cognitive Services.

2017-10-12 Version 0.4.17
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**Features**

This make Authentication classes more consistent:

- OAuthTokenAuthentication is now a subclass of BasicTokenAuthentication (was Authentication)
- BasicTokenAuthentication has now a "set_token" methods that does nothing.

This allows test like "isinstance(o, BasicTokenAuthentication)" to be guaranteed that the following attributes
exists:

- token
This means for users of "msrestazure", that they are guaranted that all AD classes somehow inherits from "BasicTokenAuthentication"

2017-10-05 Version 0.4.16
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**Bugfixes**

- Fix regression: accept "set<str>" as a valid "[str]" (#60)

2017-09-28 Version 0.4.15
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**Bugfixes**

- Always log response body (#16)
- Improved exception message if error JSON is Odata v4 (#55)
- Refuse "str" as a valid "[str]" type (#41)
- Better exception handling if input from server is not JSON valid

**Features**

- Add Configuration.session_configuration_callback to customize the requests.Session if necessary (#52)
- Add a flag to Serializer to disable client-side-validation (#51)
- Remove "import requests" from "exceptions.py" for apps that require fast loading time (#23)

Thank you to jayden-at-arista for the contribution

2017-08-23 Version 0.4.14
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**Bugfixes**

- Fix regression introduced in msrest 0.4.12 - dict syntax with enum modeled as string and enum used

2017-08-22 Version 0.4.13
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**Bugfixes**

- Fix regression introduced in msrest 0.4.12 - dict syntax using isodate.Duration (#42)

2017-08-21 Version 0.4.12
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**Features**

- Input is now more lenient
- Model have a "validate" method to check content constraints
- Model have now 4 new methods:
  - "serialize" that gives the RestAPI that will be sent
  - "as_dict" that returns a dict version of the Model. Callbacks are available.
  - "deserialize" the parses the RestAPI JSON into a Model
  - "from_dict" that parses several dict syntax into a Model. Callbacks are available.

More details and examples in the Wiki article on Github:
https://github.com/Azure/msrest-for-python/wiki/msrest-0.4.12---Serialization-change

**Bugfixes**

- Better Enum checking (#38)

2017-06-21 Version 0.4.11
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**Bugfixes**

- Fix incorrect dependency to "requests" 2.14.x, instead of 2.x meant in 0.4.8

2017-06-15 Version 0.4.10
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**Features**

- Add requests hooks to configuration

2017-06-08 Version 0.4.9
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**Bugfixes**

- Accept "null" value for paging array as an empty list and do not raise (#30)

2017-05-22 Version 0.4.8
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**Bugfixes**

- Fix random "pool is closed" error (#29)
- Fix requests dependency to version 2.x, since version 3.x is annonced to be breaking.

2017-04-04 Version 0.4.7
**BugFixes**

- Refactor paging #22:
  - "next" is renamed "advance_page" and "next" returns only 1 element (Python 2 expected behavior)
  - paging objects are now real generator and support the "next()" built-in function without need for "iter()"
  - Raise accurate DeserialisationError on incorrect RestAPI discriminator usage #27
  - Fix discriminator usage of the base class name #27
  - Remove default mutable arguments in Clients #20
  - Fix object comparison in some scenarios #24

2017-03-06 Version 0.4.6
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**Bugfixes**

- Allow Model sub-classes to be serialized if type is "object"

2017-02-13 Version 0.4.5
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**Bugfixes**

- Fix polymorphic deserialization #11
- Fix regexp validation if \w is used in Python 2.7 #13
- Fix dict deserialization if keys are unicode in Python 2.7

**Improvements**

- Add polymorphic serialisation from dict objects
- Remove chardet and use HTTP charset declaration (fallback to utf8)

2016-09-14 Version 0.4.4
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**Bugfixes**

- Remove paging URL validation, part of fix https://github.com/Azure/autorest/pull/1420

**Disclaimer**

In order to get paging fixes for impacted clients, you need this package and Autorest > 0.17.0 Nightly 20160913

2016-09-01 Version 0.4.3
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**Bugfixes**

- Better exception message (https://github.com/Azure/autorest/pull/1300)

2016-08-15 Version 0.4.2

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**Bugfixes**

- Fix serialization if "object" type contains None (https://github.com/Azure/autorest/issues/1353)

2016-08-08 Version 0.4.1

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**Bugfixes**

- Fix compatibility issues with requests 2.11.0 (https://github.com/Azure/autorest/issues/1337)
- Allow url of ClientRequest to have parameters (https://github.com/Azure/autorest/issues/1217)

2016-05-25 Version 0.4.0

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This version has no bug fixes, but implements new features of Autorest:
- Base64 url type
- unixtime type
- x-ms-enum modelAsString flag

**Behaviour changes**

- Add Platform information in UserAgent
- Needs Autorest > 0.17.0 Nightly 20160525

2016-04-26 Version 0.3.0

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**Bugfixes**

- Read only values are no longer in __init__ or sent to the server (https://github.com/Azure/autorest/pull/959)
- Useless kwarg removed

**Behaviour changes**

- Needs Autorest > 0.16.0 Nightly 20160426

2016-03-25 Version 0.2.0

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**Bugfixes**

- Manage integer enum values (https://github.com/Azure/autorest/pull/879)
- Add missing application/json Accept HTTP header (https://github.com/Azure/azure-sdk-for-python/issues/553)

**Behavioural Changes**

- Needs Autorest > 0.16.0 Nightly 20160324

2016-03-21 Version 0.1.3

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**Bugfixes**

- Deserialisation of generic resource if null in JSON (https://github.com/Azure/azure-sdk-for-python/issues/544)

2016-03-14 Version 0.1.2

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**Bugfixes**

- urllib3 side effect (https://github.com/Azure/autorest/issues/824)

2016-03-04 Version 0.1.1

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**Bugfixes**

- Source package corrupted in Pypi (https://github.com/Azure/autorest/issues/799)

2016-03-04 Version 0.1.0

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**Behavioural Changes**

- Removed custom logging set up and configuration. All loggers are now children of the root logger 'msrest' with no pre-defined configurations.
- Replaced _required attribute in Model class with more extensive _validation dict.

**Improvement**

- Removed hierarchy scanning for attribute maps from base Model class - relies on generator to populate attribute
maps according to hierarchy.
- Base class Paged now inherits from collections.Iterable.
- Data validation during serialization using custom parameters (e.g. max, min etc).
- Added ValidationError to be raised if invalid data encountered during serialization.

2016-02-29 Version 0.0.3
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**Bugfixes**

- Source package corrupted in Pypi (https://github.com/Azure/autorest/issues/718)

2016-02-19 Version 0.0.2
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**Bugfixes**

- Fixed bug in exception logging before logger configured.

2016-02-19 Version 0.0.1
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- Initial release.

Platform: UNKNOWN
Classifier: Development Status :: 4 - Beta
Classifier: Programming Language :: Python
Classifier: Programming Language :: Python :: 2
Classifier: Programming Language :: Python :: 2.7
Classifier: Programming Language :: Python :: 3
Classifier: Programming Language :: Python :: 3.5
Classifier: Programming Language :: Python :: 3.6
Classifier: Programming Language :: Python :: 3.7
Classifier: Programming Language :: Python :: 3.8
Classifier: Programming Language :: Python :: 3.9
Classifier: License :: OSI Approved :: MIT License
Classifier: Topic :: Software Development
Provides-Extra: async

Found in path(s):
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/msrest.egg-info/PKG-INFO
* /opt/cola/permits/1151950619_1618007319.22/0/msrest-0-6-21-tar-gz/msrest-0.6.21/PKG-INFO

1.604 python-pyasn1-modules 0.2.1-0.2
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1.605 lm-sensors 3.5.0-3

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* Frodo Looijaard
  Original author of libsensors, sensors-detect, sensors and isadump.
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  Configuration file converter.
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Version 2, June 1991

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1.610 libpsl 0.19.1-5build1

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Source: https://github.com/rockdaboot/libpsl

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{ TARGET_FREEBSD_NR___acl_aclcheck_fd, "__acl_aclcheck_fd", "%s(%d, %d, %#x)", NULL, NULL },
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{ TARGET_FREEBSD_NR_acct, "acct", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_adjtime, "adjtime", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_chown, "chown", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_clock_gettime, "clock_gettime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_settime, "clock_settime", NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_connect, "connect", "%s(%d,%#x,%d)" }, NULL, NULL },
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{ TARGET_FREEBSD_NR_dup2, "dup2", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_eaccess, "eaccess", "%s("\%s\",%#x)" }, NULL, NULL },
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{ TARGET_FREEBSD_NR_recvfrom, "recvfrom", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_revcmsg, "recvmsg", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rename,  "rename",  "%s(\"%s\",\"%s\")\", NULL, NULL },
{ TARGET_FREEBSD_NR_revoke,  "revoke",  NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rfork,   "rfork",   NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rmdir,   "rmdir",   NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rt prio_thread, "rt prio_thread", "%s(\%d,\%d,\%p)\", NULL, NULL },
{ TARGET_FREEBSD_NR_sbrk,   "sbrk",   NULL, NULL, NULL },
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{ TARGET_FREEBSD_NR_sendmsg, "sendmsg", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_send to, "sendto", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setcontext, "setcontext", "%s(\%#x)\", NULL, NULL },
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{ TARGET_FREEBSD_NR_seteu id, "seteuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_set get, "setget", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_set ggroups, "set groups", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_set itimer, "set itimer", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_set login, "set login", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setpg id, "setpgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setprior ity, "set priority", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sets regid, "set regid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setres gid, "set res gid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_set resuid, "set res uid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setreuid, "set reuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setr limit, "set rlimit", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setsid, "setsid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setsockopt, "set sockopt", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_settimeofday, "set timeofday", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_setuid, "setuid", NULL, NULL, NULL },
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John R. Hauser
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*/
Index: tdbsa/tdb.c
===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
/*
*/
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
-  
-  Copyright (C) Andrew Tridgell 2005
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Sources were obtained from http://sourceforge.net/projects/e2fsprogs

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This package was added to the e2fsprogs debian source package by Theodore Tso <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

http://sourceforge.net/projects/e2fsprogs

Upstream Author: Theodore Tso <tytso@mit.edu>

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This is the Debian GNU/Linux prepackaged version of the ss command-line interface parsing library. It is currently distributed together with the EXT2 file system utilities, which are otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>, from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

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# This is a Makefile stub which handles the creation of BSD shared libraries.
# In order to use this stub, the following makefile variables must be defined.

# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#

all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PAD_FLAG = -fpic
This is the Debian GNU/Linux prepackaged version of the Common Error Description library. It is currently distributed together with the EXT2 file system utilities, which are otherwise packaged as "e2fsprogs".

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Gadi Oxman, August 1995

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1.616 libbsd 0.9.1 2+deb10u1

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100dpi-lutBS08-IL2.bdf 100dpi-lutBS12-IL2.bdf
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100dpi-lutRS08-IL2.bdf 100dpi-lutRS14-IL2.bdf 100dpi-lutRS14.bdf
100dpi-lutRS19-IL2.bdf 75dpi-lutBS12.bdf 75dpi-lutBS12c.bdf
75dpi-lutBS12-IL2.bdf 75dpi-lutBS14.bdf 75dpi-lutBS14c.bdf
75dpi-lutBS14-IL2.bdf 75dpi-lutRS12.bdf 75dpi-lutRS12c.bdf
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   legacy16c.bdf, legacy16d.bdf, legacy16e.bdf, legacy16f.bdf,
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* configure.ac, Makefile.am: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.

  Author: Julio Merino <jmmv@users.sourceforge.net>

* atf-c/ui.c: The format_paragraph and format_text functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5c6f2bb5b6.
Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp:
These files were derived from the file_handle, systembuf, pipe and pistream
classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* admin/check-style.sh, admin/check-style-common.awk,
admin/check-style-cpp.awk, admin/check-style-shell.awk: These files,
extcept the first one, were first implemented in the Buildtool project.
They were later adapted to be part of Boost.Process and, during that
process, the shell script was created.

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LGPL-3.0
LGPL-3.0+
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Linux-syscall-note
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MIT-CMU
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MPL-2.0-no-copyleft-exception
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copyleft-next-0.3.1
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eCos-exception-2.0
eGenix
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1.631 require-main-filename 2.0.0
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1.632 bzip2 1.0.6 9.2~deb10u1
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Julian Seward, jseward@acm.org
bzip2/libbzip2 version 1.0.8 of 13 July 2019

--------------------------------------------------------------------------

1.633 python 3.7.3 2+deb10u3
1.633.1 Available under license :

This is the Debian GNU/Linux prepackaged version of the Python programming language. Python was written by Guido van Rossum <guido@cwi.nl> and others.

This package was put together by Klee Dienes <klee@debian.org> from sources from ftp.python.org:/pub/python, based on the Debianization by the previous maintainers Bernd S. Brentrup <bsb@uni-muenster.de> and Bruce Perens.

Current maintainer is Matthias Klose <doko@debian.org> until the final 2.3 version is released.

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--------------------------------------------------------------------------

A. HISTORY OF THE SOFTWARE

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a
non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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<td>2.6</td>
<td>2008</td>
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<td>2009</td>
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py3compile, py3clean and debpython module:

=====================================================

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# Contributions to the urllib3 project

## Creator & Maintainer

* Andrey Petrov <andrey.petrov@shazow.net>

## Contributors

In chronological order:

* victor.vde <http://code.google.com/u/victor.vde/>
* HTTPS patch (which inspired HTTPSConnectionPool)

* erikcederstrand <http://code.google.com/u/erikcederstrand/>
* NTLM-authenticated HTTPSConnectionPool
* Basic-authenticated HTTPSConnectionPool (merged into make_headers)

* niphlod <niphlod@gmail.com>
* Client-verified SSL certificates for HTTPSConnectionPool
* Response gzip and deflate encoding support
* Better unicode support for filepost using StringIO buffers

* btoconnor <brian@btoconnor.net>
* Non-multipart encoding for POST requests

* p.dobrogost <http://code.google.com/u/@WBRSRIBZDhBFXQB6/>
* Code review, PEP8 compliance, benchmark fix

* kennethreitz <me@kennethreitz.com>
* Bugfixes, suggestions, Requests integration

* georgemarshall <https://github.com/georgemarshall>
* Bugfixes, Improvements and Test coverage

* Thomas Kluyver <thomas@kluyver.me.uk>
* Python 3 support

* brandon-rhodes <http://rhodesmill.org/brandon>
* Design review, bugfixes, test coverage.

* studer <theo.studer@gmail.com>
* IPv6 url support and test coverage

* Shivaram Lingamneni <slingamn@cs.stanford.edu>
* Support for explicitly closing pooled connections

* hartator <hartator@gmail.com>
* Corrected multipart behavior for params

* Thomas Weischuh <thomas@t-8ch.de>
* Support for TLS SNI
* API unification of ssl_version/cert_reqs
* SSL fingerprint and alternative hostname verification
* Bugfixes in testsuite

* Sune Kirkeby <mig@ibofobi.dk>
* Optional SNI-support for Python 2 via PyOpenSSL.

* Marc Schlaich <marc.schlaich@gmail.com>
* Various bugfixes and test improvements.

* Bryce Boe <bbzbryce@gmail.com>
* Correct six.moves conflict
* Fixed pickle support of some exceptions

* Boris Figovsky <boris.figovsky@ravelosystems.com>
* Allowed to skip SSL hostname verification
* Cory Benfield <https://lukasa.co.uk/about/>
  * Stream method for Response objects.
  * Return native strings in header values.
  * Generate 'Host' header when using proxies.

* Jason Robinson <jaywink@basshero.org>
  * Add missing WrappedSocket.fileno method in PyOpenSSL

* Audrius Butkevicius <audrius.butkevicius@elastichosts.com>
  * Fixed a race condition

* Stanislav Vitkovskiy <stas.vitkovsky@gmail.com>
  * Added HTTPS (CONNECT) proxy support

* Stephen Holsapple <sholsapp@gmail.com>
  * Added abstraction for granular control of request fields

* Martin von Gagern <Martin.vGagern@gmx.net>
  * Support for non-ASCII header parameters

* Kevin Burke <kev@inburke.com> and Pavel Kirichenko <juanych@yandex-team.ru>
  * Support for separate connect and request timeouts

* Peter Waller <p@pwaller.net>
  * HTTPResponse.tell() for determining amount received over the wire

* Nipunn Koorapati <nipunn1313@gmail.com>
  * Ignore default ports when comparing hosts for equality

* Danilo @dbrgn <https://dbrgn.ch/>
  * Disabled TLS compression by default on Python 3.2+
  * Disabled TLS compression in pyopenssl contrib module
  * Configurable cipher suites in pyopenssl contrib module

* Roman Bogorodskiy <roman.bogorodskiy@ericsson.com>
  * Account retries on proxy errors

* Nicolas Delaby <nicolas.delaby@ezeep.com>
  * Use the platform-specific CA certificate locations

* Josh Schneier <https://github.com/jschneier>
  * HTTPHeaderDict and associated tests and docs
  * Bugfixes, docs, test coverage

* Tahia Khan <http://tahia.tk/>
  * Added Timeout examples in docs

* Arthur Grunseid <https://grunseid.com>
* source_address support and tests (with https://github.com/bui)

* Ian Cordasco <graffatcolmingov@gmail.com>
  * PEP8 Compliance and linting
  * Add ability to pass socket options to an HTTP Connection

* Erik Tollerud <erik.tollerud@gmail.com>
  * Support for standard library io module.

* Krishna Prasad <kprasad.iitd@gmail.com>
  * Google App Engine documentation

* Aaron Meurer <asmeurer@gmail.com>
  * Added Url.url, which unparses a Url

* Evgeny Kapun <abacabadabacaba@gmail.com>
  * Bugfixes

* Benjamen Meyer <bm_witness@yahoo.com>
  * Security Warning Documentation update for proper capture

* Shivan Sornarajah <github@sornars.com>
  * Support for using ConnectionPool and PoolManager as context managers.

* Alex Gaynor <alex.gaynor@gmail.com>
  * Updates to the default SSL configuration

* Tomas Tomecek <ttomecek@redhat.com>
  * Implemented generator for getting chunks from chunked responses.

* tlynn <https://github.com/tlynn>
  * Respect the warning preferences at import.

* David D. Riddle <ddriddle@illinois.edu>
  * IPv6 bugfixes in testsuite

* Thea Flowers <magicalgirl@google.com>
  * App Engine environment tests.
  * Documentation re-write.

* John Krauss <https://github.com/talos>
  * Clues to debugging problems with `cryptography` dependency in docs

* Disassem <https://github.com/Disassem>
  * Fix pool-default headers not applying for url-encoded requests like GET.

* James Atherfold <jlatherfold@hotmail.com>
  * Bugfixes relating to cleanup of connections during errors.
* Christian Pedersen <https://github.com/chripede>
  * IPv6 HTTPS proxy bugfix

* Jordan Moldow <https://github.com/jmoldow>
  * Fix low-level exceptions leaking from `HTTPResponse.stream()`.
  * Bugfix for `ConnectionPool.urlopen(release_conn=False)`.
  * Creation of `HTTPConnectionPool.ResponseCls`.

* Predrag Gruevski <https://github.com/obi1kenobi>
  * Made cert digest comparison use a constant-time algorithm.

* Adam Talsma <https://github.com/a-tal>
  * Bugfix to ca_cert file paths.

* Evan Meagher <https://evanmeagher.net>
  * Bugfix related to `memoryview` usage in PyOpenSSL adapter

* John Vandenberg <jayvdb@gmail.com>
  * Python 2.6 fixes; pyflakes and pep8 compliance

* Andy Caldwell <andy.m.caldwell@googlemail.com>
  * Bugfix related to reusing connections in indeterminate states.

* Ville Skytt <ville.skytta@iki.fi>
  * Logging efficiency improvements, spelling fixes, Travis config.

* Shige Takeda <smtakeda@gmail.com>
  * Started Recipes documentation and added a recipe about handling concatenated gzip data in HTTP response

* Jesse Shapiro <jesse@jesseshapiro.net>
  * Various character-encoding fixes/tweaks
  * Disabling IPv6 DNS when IPv6 connections not supported

* David Foster <http://dafoster.net/>
  * Ensure order of request and response headers are preserved.

* Jeremy Cline <jeremy@jcline.org>
  * Added connection pool keys by scheme

* Aviv Palivoda <palaviv@gmail.com>
  * History list to Retry object.
  * HTTPResponse contains the last Retry object.

* Nate Prewitt <nate.prewitt@gmail.com>
  * Ensure timeouts are not booleans and greater than zero.
  * Fixed infinite loop in `stream` when amt=None.
  * Added length_remaining to determine remaining data to be read.
* Added enforce_content_length to raise exception when incorrect content-length received.

* Seth Michael Larson <sethmichaellarson@protonmail.com>
  * Created selectors backport that supports PEP 475.

* Alexandre Dias <alex.dias@smarkets.com>
  * Don't retry on timeout if method not in whitelist

* Moinuddin Quadri <moin18@gmail.com>
  * Lazily load idna package

* Tom White <s6yg1ez3@mail2tor.com>
  * Made SOCKS handler differentiate socks5h from socks5 and socks4a from socks4.

* Tim Burke <tim.burke@gmail.com>
  * Stop buffering entire deflate-encoded responses.

* Tuukka Mustonen <tuukka.mustonen@gmail.com>
  * Add counter for status_forcelist retries.

* Erik Rose <erik@mozilla.com>
  * Bugfix to pyopenssl vending

* Wolfgang Richter <wolfgang.richter@gmail.com>
  * Bugfix related to loading full certificate chains with PyOpenSSL backend.

* Mike Miller <github@mikeage.net>
  * Logging improvements to include the HTTP(S) port when opening a new connection

* Ioannis Tziakos <mail@itziakos.gr>
  * Fix `util.selectors._fileobj_to_fd` to accept `long`.
  * Update appveyor tox setup to use the 64bit python.

* Akamai (through Jesse Shapiro) <jshapiro@akamai.com>
  * Ongoing maintenance

* Dominique Leuenberger <dimstar@opensuse.org>
  * Minor fixes in the test suite

* Will Bond <will@wbond.net>
  * Add Python 2.6 support to `contrib.securetransport`

* Aleksei Alekseev <alekseev.yeskela@gmail.com>
  * using auth info for socks proxy

* Chris Wilcox <git@crwilcox.com>
  * Improve contribution guide
  * Add `HTTPResponse.geturl` method to provide `urllib2.urlopen().geturl()` behavior
* Bruce Merry <https://www.brucemerry.org.za>
  * Fix leaking exceptions when system calls are interrupted with zero timeout

* Hugo van Kemenade <https://github.com/hugovk>
  * Drop support for EOL Python 2.6

* Tim Bell <https://github.com/timb07>
  * Bugfix for responses with Content-Type: message/* logging warnings

* Justin Bramley <https://github.com/jbramleycl>
  * Add ability to handle multiple Content-Encodings

* [Your name or handle] <[email or website]>
  * [Brief summary of your changes]

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### 1.647 ip-cidr 2.1.1

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 */
/**
 * A specialized version of `baseIsEqual` for arrays and objects which performs
 * deep comparisons and tracks traversed objects enabling objects with circular
 * references to be compared.
 *
 * @private
 * @param {Object} object The object to compare.
 * @param {Object} other The other object to compare.
 * @param {Function} equalFunc The function to determine equivalents of values.
 * @param {Function} [customizer] The function to customize comparisons.
 * @param {number} [bitmask] The bitmask of comparison flags. See `baseIsEqual`'s
 * for more details.
 * @param {Object} [stack] Tracks traversed `object` and `other` objects.
 * @returns {boolean} Returns `true` if the objects are equivalent, else `false`.
 */
// for more details.
/**
```
* Creates an array of the own enumerable property names of `object`.
* 
* **Note:** Non-object values are coerced to objects. See the
* [ES spec](http://ecma-international.org/ecma-262/7.0/#sec-object.keys)
* for more details.
* 
* @static
* @since 0.1.0
* @memberOf _
* @category Object
* @param {Object} object The object to query.
* @returns {Array} Returns the array of property names.
* @example
* 
* function Foo() {
*   this.a = 1;
*   this.b = 2;
* }
* 
* Foo.prototype.c = 3;
* 
* _.keys(new Foo);
* // => ['a', 'b'] (iteration order is not guaranteed)
* 
* _.keys('hi');
* // => ['0', '1']
*/

Found in path(s):
*
* /opt/cola/permits/1140310342_1614821877.91/0/ip-cidr-2-1-1-zip/ip-cidr-master/dist/ip-cidr.js

1.648 debianutils 4.8.6.1

1.648.1 Available under license:

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1.652 apt 1.6.13

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1.653 axios 0.19.2

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1.654 semver 5.7.1

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1.655 react-transition-group 4.4.1

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1.668 dkms 2.6.1-4

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1.670 yaml 1.9.2

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1.672 sg3-utils 1.44-1
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1.673 django-fsm 2.7.0
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Metadata-Version: 1.1
Name: django-fsm
Version: 2.7.0
Summary: Django friendly finite state machine support.
Home-page: http://github.com/kmmbvnr/django-fsm
Author: Mikhail Podgurskiy
Author-email: kmmbvnr@gmail.com
License: MIT License
Description: UNKNOWN
Keywords: django
Platform: any
Classifier: Development Status :: 5 - Production/Stable
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try:
    long_description = open('README.rst').read()
except IOError:
    long_description = "

setup(
name='django-fsm',
version='2.7.0',
description='Django friendly finite state machine support.',
author='Mikhail Podgurskiy',
author_email='kmmbvnr@gmail.com',
url='http://github.com/kmmbvnr/django-fsm',
keywords='django',
packages=['django_fsm', 'django_fsm.management', 'django_fsm.management.commands'],
include_package_data=True,
zip_safe=False,
license='MIT License',
platforms=["any"],
classifiers=[
    'Development Status :: 5 - Production/Stable',
    'Environment :: Web Environment',
    'Intended Audience :: Developers',
    'License :: OSI Approved :: MIT License',
    'Operating System :: OS Independent',
    'Framework :: Django',
    'Framework :: Django :: 1.6',
    'Framework :: Django :: 1.8',
    'Framework :: Django :: 1.9',
    'Framework :: Django :: 1.10',
    'Framework :: Django :: 1.11',
    'Framework :: Django :: 2.0',
    'Framework :: Django :: 2.1',
    'Framework :: Django :: 2.2',
    'Framework :: Django :: 3.0',
    'Programming Language :: Python',
    'Programming Language :: Python :: 2.6',
    'Programming Language :: Python :: 2.7',
    'Programming Language :: Python :: 3',
    'Programming Language :: Python :: 3.4',
    'Programming Language :: Python :: 3.5',
    'Programming Language :: Python :: 3.6',
    'Programming Language :: Python :: 3.7',
    'Programming Language :: Python :: 3.8',
    'Framework :: Django',
    'Topic :: Software Development :: Libraries :: Python Modules',
]
)

Found in path(s):
* /opt/cola/permits/1159549930_1620152090.89/0/django-fsm-2-7-0-tar-gz/django-fsm-2.7.0/setup.py
1.674 kmod 24 1ubuntu3.5

1.674.1 Available under license:

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@heading Vincent Rijmen, Antoon Bosselaers, Paulo Barreto

AES in libhcrypto

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rijndael-alg-fst.c

@version 3.0 (December 2000)

Optimised ANSI C code for the Rijndael cipher (now AES)

@author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
@author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
@author Paulo Barreto <paulo.barreto@terra.com.br>

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DES core in libhcrypto

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D3DES (V5.09) -

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lib/hcrypto/test_dh.c
1.684 backoff v2.0.0+incompatible

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1.687 zlib 1.2.3

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1.695 convert-source-map 1.7.0

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1.696 libgpg-error 1.35-1

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## SFC64

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Adapted from a C++ implementation of Chris Doty-Humphrey's SFC PRNG.

https://gist.github.com/imneme/f1f7821f07cf76504a97f6537c818083

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# MT19937

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The rk_random and rk_seed functions algorithms and the original design of 
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Original algorithm for the implementation of rk_interval function from Richard J. Wagner's implementation of the Mersenne Twister RNG, optimised by Magnus Jonsson.

Constants used in the rk_double implementation by Isaku Wada.

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1.699 fs.realpath 1.0.0
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1.700 docker-ce 20.10.7~3-0~debian-buster

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1.706 busybox 1.30.1-4

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//
//
////////////////////////////////////////////////////////////////////////////

#ifndef BOOST_INTRUSIVE_DETAIL_MPL_HPP
#define BOOST_INTRUSIVE_DETAIL_MPL_HPP

#ifndef BOOST_CONFIG_HPP
# include <boost/config.hpp>
#endif

#if defined(BOOST_HAS_PRAGMA_ONCE)
# pragma once
#endif

#include <boost/intrusive/detail/config_begin.hpp>
#include <boost/move/detail/type_traits.hpp>
#include <cstddef>

namespace boost {
namespace intrusive {
namespace detail {

using boost::move_detail::is_same;
using boost::move_detail::add_const;
using boost::move_detail::remove_const;
using boost::move_detail::remove_cv;
using boost::move_detail::remove_reference;
using boost::move_detail::add_reference;

} // namespace detail
} // namespace intrusive
} // namespace boost

#endif // BOOST_INTRUSIVE_DETAIL_MPL_HPP
using boost::move_detail::remove_pointer;
using boost::move_detail::add_pointer;
using boost::move_detail::true_type;
using boost::move_detail::false_type;
using boost::move_detail::enable_if_c;
using boost::move_detail::enable_if;
using boost::move_detail::disable_if_c;
using boost::move_detail::disable_if;
using boost::move_detail::is_convertible;
using boost::move_detail::if_c;
using boost::move_detail::if_
using boost::move_detail::is_const;
using boost::move_detail::identity;
using boost::move_detail::alignment_of;
using boost::move_detail::is_empty;
using boost::move_detail::addressof;
using boost::move_detail::integral_constant;
using boost::move_detail::enable_if_convertible;
using boost::move_detail::disable_if_convertible;
using boost::move_detail::bool_
using boost::move_detail::true_
using boost::move_detail::false_
using boost::move_detail::yes_type;
using boost::move_detail::no_type;
using boost::move_detail::apply;
using boost::move_detail::eval_if_c;
using boost::move_detail::eval_if;
using boost::move_detail::unvoid_ref;
using boost::move_detail::add_const_if_c;

template<typename S>
struct ls_zeros
{
    static const std::size_t value = (S & std::size_t(1)) ? 0 : (1 + ls_zeros<S+1>::value);
};

template<>
struct ls_zeros<0>
{
    static const std::size_t value = 0;
};

template<>
struct ls_zeros<1>
{
    static const std::size_t value = 0;
};
// Infrastructure for providing a default type for T::TNAME if absent.
#define BOOST_INTRUSIVE_INSTANTIATE_DEFAULT_TYPE_TMPLT(TNAME)     \
    template <typename T, typename DefaultType>     \
    struct boost_intrusive_default_type_##TNAME     \
    {                                                      \
        template <typename X>                                 \
        static char test(int, typename X::TNAME*);            \
        template <typename X>                                 \
        static int test(...);                                 \
        struct DefaultWrap { typedef DefaultType TNAME; };     \
    }                                                       \
    static const bool value = (1 == sizeof(test<T>(0, 0)));    \
    typedef typename                                          \
        ::boost::intrusive::detail::if_c                        \
            <value, T, DefaultWrap>::type::TNAME type;       \
};                                                             \
//

#define BOOST_INTRUSIVE_OBTAIN_TYPE_WITH_DEFAULT(INSTANTIATION_NS_PREFIX, T, TNAME, TIMPL)     \
    typename INSTANTIATION_NS_PREFIX                                                       \
        boost_intrusive_default_type_##TNAME<T, TIMPL>::type                            \
//

#define BOOST_INTRUSIVE_INSTANTIATE_EVAL_DEFAULT_TYPE_TMPLT(TNAME)\
    template <typename T, typename DefaultType>     \
    struct boost_intrusive_eval_default_type_##TNAME     \
    {                                                      \
        template <typename X>                                 \
        static char test(int, typename X::TNAME*);            \
        template <typename X>                                 \
        static int test(...);                                 \
        struct DefaultWrap                                          
        { typedef typename DefaultType::type TNAME; };              
        static const bool value = (1 == sizeof(test<T>(0, 0)));    
    typedef typename                                            \
        ::boost::intrusive::detail::eval_if_c                    \
            < value                                               \
            , ::boost::intrusive::detail::identity<T>             \
            , ::boost::intrusive::detail::identity<DefaultWrap>    \
            >::type::TNAME type;                                  \
};
#define BOOST_INTRUSIVE_OBTAIN_TYPE_WITH_EVAL_DEFAULT(INSTANTIATION_NS_PREFIX, T, TNAME, TIMPL) \
    typename INSTANTIATION_NS_PREFIX \
    boost_intrusive_eval_default_type_ ## TNAME< T, TIMPL >::type \

//

#define BOOST_INTRUSIVE_INTERNAL_STATIC_BOOL_IS_TRUE(TRAITS_PREFIX, TYPEDEF_TO_FIND) \
template <class T>\nstruct TRAITS_PREFIX##_bool\n{\n    template<bool Add>\n    struct two_or_three {yes_type _[2 + Add];};\n    template <class U> static yes_type test(...);\n    template <class U> static two_or_three<U::TYPEDEF_TO_FIND> test (int);\n    static const std::size_t value = sizeof(test<T>(0));\n};\n\n//

//

#define BOOST_INTRUSIVE_HAS_STATIC_MEMBER_FUNC_SIGNATURE(TRAITS_NAME, FUNC_NAME) \
template <typename U, typename Signature> \
class TRAITS_NAME \
{\nprivate:\n    template<Signature> struct helper;\n    template<typename T> \
    static ::boost::intrusive::detail::yes_type test(helper<&T::FUNC_NAME>*);\n    template<typename T> static ::boost::intrusive::detail::no_type test(...);\npublic:\n    static const bool value = sizeof(test<U>(0)) == sizeof(::boost::intrusive::detail::yes_type);\n};\n//

#define BOOST_INTRUSIVE_HAS_MEMBER_FUNC_CALLED(TRAITS_NAME, FUNC_NAME) \
template <typename Type> \
struct TRAITS_NAME \
{\n    struct BaseMixin \n
{ \
    void FUNC_NAME(); \
}; \
struct Base : public Type, public BaseMixin { Base(); }; \
template<typename T, T t> class Helper{}; \
template<typename U> \
static ::boost::intrusive::detail::no_type test(U*, Helper<void (BaseMixin::*)(), &U::FUNC_NAME>* = 0); \
static ::boost::intrusive::detail::yes_type test(...); \
static const bool value = sizeof(::boost::intrusive::detail::yes_type) == sizeof(test((Base*)(0)))); \\n}; \
//

#define BOOST_INTRUSIVE_HAS_MEMBER_FUNC_CALLED_IGNORE_SIGNATURE(TRAITS_NAME,
FUNC_NAME) \
BOOST_INTRUSIVE_HAS_MEMBER_FUNC_CALLED(TRAITS_NAME##_ignore_signature, FUNC_NAME) \\n\ 
template<typename Type, class> \
struct TRAITS_NAME \
    : public TRAITS_NAME##_ignore_signature<Type> 
{ } \\
//namespace detail 
} //namespace intrusive
} //namespace boost

#include <boost/intrusive/detail/config_end.hpp>

---

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Adapters for Boost.MPL containers.

- `boost::mpl::integral_c<T, v>`: Adapter for IntegralConstants from the Boost.MPL. [More...](structboost_1_1mpl_1_1integral__c.html)
- `boost::mpl::list<T>`: Adapter for Boost.MPL lists. [More...](structboost_1_1mpl_1_1list.html)
- `boost::mpl::vector<T>`: Adapter for Boost.MPL vectors. [More...](structboost_1_1mpl_1_1vector.html)

---

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#include <boost/mpl/push_back.hpp>
#include <boost/mpl/quote.hpp>
#include <boost/mpl/transform.hpp>
#include <boost/mpl/vector.hpp>

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[section:mpl MPL Interoperability]

All the value based traits in this library conform to MPL's requirements
for an [Integral Constant type].

Please note that these types no longer inherit from `mpl::true_` or `mpl::false_` etc, and the library
will no longer implicitly include any MPL header. However there is an implicit conversion from
`integral_constant` to the corresponding MPL types, therefore tag-dispatching that uses MPL types
in function overloads will still work as before.

[endsect]
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---

```
#include <boost/mpl/push_back.hpp>
#include <boost/mpl/vector.hpp>
```

---
template <int i>
struct t [ ];

using vector = <%= mpl_vector((1..input_size).to_a.map { |n| "t<#{n}>" }) %>

int main() { }

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/////////////////////////////////////////////////////////////////////////////

#ifndef BOOST_INTERPROCESS_DETAIL_MPL_HPP
#define BOOST_INTERPROCESS_DETAIL_MPL_HPP

#ifndef BOOST_CONFIG_HPP
#  include <boost/config.hpp>
#endif
#
#if defined(BOOST_HAS_PRAGMA_ONCE)
#  pragma once
#endif

#include <cstddef>

namespace boost {
    namespace interprocess {
        namespace ipcdetail {

            template <class T, T val>
            struct integral_constant {
                static const T value = val;
                typedef integral_constant<T, val> type;
            };

            template< bool C_ >
            struct bool_ : integral_constant< bool, C_ >
            {
                static const bool value = C_;
            };

        } // namespace ipcdetail
    } // namespace interprocess
} // namespace boost

#endif // BOOST_INTERPROCESS_DETAIL_MPL_HPP

typedef bool_<true>        true_;  
typedef bool_<false>       false_;  

typedef true_  true_type;  
typedef false_ false_type;  

typedef char yes_type;  
struct no_type
{
    char padding[8];
};  

template <bool B, class T = void>
struct enable_if_c {
    typedef T type;
};  

template <class T>
struct enable_if_c<false, T> {};

template <class Cond, class T = void>
struct enable_if : public enable_if_c<Cond::value, T> {};

template <class Cond, class T = void>
struct disable_if : public enable_if_c<!Cond::value, T> {};

template<
    bool C
 , typename T1
 , typename T2
>
struct if_c
{
    typedef T1 type;
};  

template<
    typename T1
 , typename T2
>
struct if_c<false,T1,T2>
{
    typedef T2 type;
};  

template<
    typename T1
 , typename T2
>


struct if_
{
    typedef typename if_<0 != T1::value, T2, T3>::type type;
};

template<std::size_t S>
struct ls_zeros
{
    static const std::size_t value = (S & std::size_t(1)) ? 0 : (1u + ls_zeros<(S >> 1u)>::value);
};

template<>
struct ls_zeros<0>
{
    static const std::size_t value = 0;
};

template<>
struct ls_zeros<1>
{
    static const std::size_t value = 0;
};

#endif  //ifndef BOOST_INTERPROCESS_DETAIL_MPL_HPP

#!/usr/bin/env python
from subprocess import check_output as run
from datetime import datetime
from itertools import groupby
from operator import itemgetter
import re
import magic

def authors(filename):
    log = run(['git', 'log', '--follow',
               '--date=short', '--format=%aN%a%09%a%d', filename],
               universal_newlines=True)
    for line in log.splitlines():
        author, date = line.split('
')
        if author != 'fix-copyright.py':
            yield author, datetime.strptime(date, '%Y-%m-%d')
def new_copyright(filename, previous):
    def f():
        au = list(authors(filename))
        alldates = map(itemgetter(1), au)
        aup = sorted(au + map(lambda a: (a, None), previous), key=itemgetter(0))
        for author, records in groupby(aup, itemgetter(0)):
            dates = filter(None, map(itemgetter(1), records))
            if not dates: dates = alldates
            start = min(dates)
            end = max(dates)
            fmt = '{0}' if start.year == end.year else '{0}-{1}'
            line = 'Copyright ' + fmt.format(start.year, end.year) + ' ' + author
            key = (start, author)
            yield key, line
        return map(itemgetter(1), sorted(f()))

    def fix_copyright(filename):
        # Find copyright block in original file
        prefix = set()
        names = []
        lines = []
        with open(filename, 'r') as f:
            content = list(f)
            for i, line in enumerate(content[:15]):
                m = re.match(r'^\W*?\(c\)? \s*copyright \s*?\(c\)? \s+\d{4}(\s*-\s*\d{4})? \s+(?P<name>.+?) \s*$', line, re.IGNORECASE)
                if m:
                    d = m.groupdict()
                    prefix.add(d['prefix'])
                    lines.append(i)
                    names.append(d['name'].strip())
                if len(prefix) != 1:
                    print 'Not found:', filename
                    return
                prefix = list(prefix)[0]
        print filename
        new = iter(new_copyright(filename, names))
        with open(filename, 'w') as f:
            for i, line in enumerate(content):
                if i in lines:
                    for repl in new:
                        print >>f, prefix + repl
                else:
                    print >>f, line,
            pass

    def all_files():
ls = run(['git', 'ls-files'], universal_newlines=True)
for filename in ls.splitlines():
    if magic.from_file(filename, mime=True).split('/')[0] == 'text':
        yield filename

for f in all_files():
    fix_copyright(f)

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<?xml version="1.0" encoding="utf-8"?>

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<section id="date_time.license">
<title>Boost Date-Time Library License</title>
<bridgehead renderas="sect2">Boost Date-Time Library License</bridgehead>
<!--!
<ulink url="../../../index.htm">
<imagedata align="left" format="GIF"
    fileref="../../../boost.png" alt="C++ Boost" />
</ulink>-->
<!--hr /-->
<para>
The following is the overall license for the boost date_time library. This notice is found in all source files related to the library.
</para>
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</para>
This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser. The Encore Multimix modifications were supplied by Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM.

Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood.

Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code. Al Dossor (dossor@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code.

Jesper Petersen(jep@mtiae.mtia.oz.au), Michel Schinz, and Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port.

Thomas Funke (thf@zelator.in-berlin.de(?)) and Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports.

Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code. Bill Janssen (janssen@parc.xerox.com) supplied the SunOS dynamic loader specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and Sony News specific code. Al Dossor provided Alpha/OSF/1 code. He and Dave Detlefs(detlefs@src.dec.com) also provided several generic bug fixes.

Alistair G. Crooks(agc@uts.amdahl.com) supplied the NetBSD and 386BSD ports.
Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port.
Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to
a Motorola 88K processor running CX/UX (Harris NightHawk).
Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to
nonIBM development environments (a nontrivial task).
Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port.
David Chase, then at Olivetti Research, suggested several improvements.
Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the
code to save and print call stacks for leak detection on a SPARC.
Jesse Hull and John Ellis supplied the C++ interface code.
Zhong Shao performed much of the experimentation that led to the
current typed allocation facility. (His dynamic type inference code hasn't
made it into the released version of the collector, yet.)

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Mathias Koch (mkoch - at - idesis.de 7 : email to boost-owner@lists.boost.org Sep 2007 13:20:09 +0200)
--- end --<?xml version="1.0" encoding="UTF-8"?>
<!DOCTYPE article PUBLIC "-//Boost//DTD BoostBook XML V1.0//EN"
"http://www.boost.org/tools/boostbook/dtd/boostbook.dtd">
<article id="copyright_test" last-revision="DEBUG MODE Date: 2000/12/20 12:00:00 $"
xmlns:xi="http://www.w3.org/2001/XInclude">
<title>Copyright Test</title>
<articleinfo>
<copyright>
<year>1963</year> <year>1964</year> <year>1965</year> <holder>Jane Doe</holder>
</copyright>
<copyright>
<year>2018</year> <holder>Joe Blow, John Coe</holder>
</copyright>
<copyright>
<year>1977</year> <year>1985</year> <holder>Someone else</holder>
</copyright>
</articleinfo>
</article>
//////////////////////////////////////////////////////////////////////////////
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//
//
//////////////////////////////////////////////////////////////////////////////
#ifndef BOOST_CONTAINER_CONTAINER_DETAIL_MPL_HPP
#define BOOST_CONTAINER_CONTAINER_DETAIL_MPL_HPP
#ifndef BOOST_CONFIG_HPP
# include <boost/config.hpp>
#endif
#if defined(BOOST_HAS_PRAGMA_ONCE)
# pragma once

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namespace boost {
namespace container {
namespace container_detail {

using boost::move_detail::integral_constant;
using boost::move_detail::true_type;
using boost::move_detail::false_type;
using boost::move_detail::enable_if_c;
using boost::move_detail::enable_if;
using boost::move_detail::enable_if_convertible;
using boost::move_detail::disable_if_c;
using boost::move_detail::disable_if;
using boost::move_detail::disable_if_convertible;
using boost::move_detail::is_convertible;
using boost::move_detail::if_c;
using boost::move_detail::if_; 
using boost::move_detail::identity;
using boost::move_detail::bool_;
using boost::move_detail::true_; 
using boost::move_detail::false_; 
using boost::move_detail::yes_type;
using boost::move_detail::no_type;
using boost::move_detail::bool_;
using boost::move_detail::true_; 
using boost::move_detail::false_; 
using boost::move_detail::unvoid_ref;
using boost::move_detail::and_; 
using boost::move_detail::or_; 
using boost::move_detail::not_; 
using boost::move_detail::enable_if_and;
using boost::move_detail::disable_if_and;
using boost::move_detail::enable_if_or;
using boost::move_detail::disable_if_or;

template <class FirstType>
struct select1st
{
    typedef FirstType type;
}
template<class T>
const type& operator()(const T& x) const
{  return x.first;   }

template<class T>
type& operator()(T& x)
{  return const_cast<type&>(x.first);   }

}  //namespace container_detail {
}  //namespace container {
}  //namespace boost {

#include <boost/container/detail/config_end.hpp>
#endif   //#ifndef BOOST_CONTAINER_CONTAINER_DETAIL_MPL_HPP

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#include <boost/mpl/fold.hpp>
#include <boost/mpl/push_back.hpp>
#include <boost/mpl/quote.hpp>
#include <boost/mpl/vector.hpp>

template <typename State, typename X>
struct f { using type = X; }; 

struct state { }; 

template <int i>
struct t { }; 

using vector = <%= mpl_vector((1..input_size).to_a.map { |n| "t<#{n}>" }) %>; 

using result = boost::mpl::fold<vector, state, boost::mpl::quote2<f>>::type;

int main() { }

Boost.Geometry (aka GGL, Generic Geometry Library)

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=============================================================================/
/*
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*
*/

#include "licence_info.hpp"
#include "bcp_imp.hpp"
#include "fileview.hpp"
#include <fstream>
#include <iomanip>
#include <cstring>
#include <stdexcept>
#include <boost/lexical_cast.hpp>
#include <boost/filesystem/operations.hpp>
#include <boost/throw_exception.hpp>

// split_path is a small helper for outputting a path name,
// complete with a link to that path:
//
struct split_path
{
    const fs::path& root;
    const fs::path& file;
    split_path(const fs::path& r, const fs::path& f)
        : root(r), file(f){}
private:
    split_path& operator=(const split_path&);
};

std::ostream& operator << (std::ostream& os, const split_path& p)
{
    os << "<a href="" << (p.root / p.file).string() << ">
        " << p.file.string() << "</a>";
    return os;
}

std::string make_link_target(const std::string& s)
{
    // convert an arbitrary string into something suitable
    // for an <a> name:
    std::string result;
for(unsigned i = 0; i < s.size(); ++i)
{
    result.append(1, static_cast<std::string::value_type>(std::isalnum(s[i]) ? s[i] : '_'));
}
return result;

void bcp_implementation::output_license_info()
{
    std::pair<const license_info*, int> licenses = get_licenses();

    std::map<int, license_data>::const_iterator i, j;
i = m_license_data.begin();
j = m_license_data.end();

    std::ofstream os(m_dest_path.string().c_str());
    if(!os)
    {
        std::string msg("Error opening ");
        msg += m_dest_path.string();
        msg += " for output."
        std::runtime_error e(msg);
        boost::throw_exception(e);
    }
    os << "<!DOCTYPE HTML PUBLIC \"-//W3C//DTD HTML 4.0 Transitional//EN\"\n" "<head>\n" "<title>Boost Licence Dependency Information";
    if(m_module_list.size() == 1)
    {
        os << " for " << *(m_module_list.begin());
    }
    os << "</title>\n" "</head>\n" "<body>\n" "<H1>Boost Licence Dependency Information";
    if(m_module_list.size() == 1)
    {
        os << " for " << *(m_module_list.begin());
    }
    os << "</H1>\n" "<H2>Contents</H2>\n" "<pre><a href="#input">Input Information</a>\n";
    if(!m_bsl_summary_mode)
while(i != j)
{
  // title:
  os << "<a href="#" make_link_target(licenses.first[i->first].license_name) "">" << licenses.first[i->first].license_name << "</a><br>
  ++i;
}

os << "<a href="#files" Files with no recognised license</a><br>
" <a href="#authors" Files with no recognised copyright holder</a><br>
if(!m_bsl_summary_mode)
{
  os << "Moving to the Boost Software License...<br>
  " <a href="#bsl-converted" Files that can be automatically converted to the Boost Software License</a><br>
  " <a href="#to-bsl" Files that can be manually converted to the Boost Software License</a><br>
  " <a href="#not-to-bsl" Files that can NOT be moved to the Boost Software License</a><br>
  " <a href="#need-bsl-authors" Authors we need to move to the Boost Software License</a><br>
  " <a href="#copyright" Copyright Holder Information</a><br>
}

os << "<a href="#depend" File Dependency Information</a><br>
"</p>

// // input Information:
// //
// os << "<a name="input"></a><h2>Input Information</h2><br>
if(m_scan_mode)
  os << "<p>The following files were scanned for boost dependencies:<br>
else
  os << "<p>The following Boost modules were checked:<br>

std::list<st::string>::const_iterator si = m_module_list.begin();
std::list<st::string>::const_iterator sj = m_module_list.end();
while(si != sj)
{
  os << *si << "<br>
  ++si;
} os << "</p><p>The Boost path was: <code>" << m_boost_path.string() << "</code></p>"; // // extract the boost version number from the boost directory tree, // not from this app (which may have been built from a previous // version):
//
fileview version_file(m_boost_path / "boost/version.hpp");
static const boost::regex version_regex(
    "^\[[:blank:]*\]*#\[[:blank:]*\]*define\[[:blank:]*\]+BOOST_VERSION\[[:blank:]*\]+(\d+)+\);
boost::cmatch what;
if(boost::regex_search(version_file.begin(), version_file.end(), what, version_regex))
{
    int version = boost::lexical_cast<int>(what.str(1));
    os << "<p The Boost version is: " << version / 100000 << "." << version / 100 % 1000 << "." << version % 100 << "></p>\n";
}

//
// output each license:
//
i = m_license_data.begin();
j = m_license_data.end();
if(!m_bsl_summary_mode)
{
    // start with the summary:
    os << "<a name="summary">\n<h2>Licence Summary</h2>\n";
    while(i != j)
    {
        // title:
        os << "<H3>" << licenses.first[i->first].license_name << "</H3>\n";
        // license text:
        os << "<BLOCKQUOTE>" << licenses.first[i->first].license_text << "</BLOCKQUOTE>";
        // Copyright holders:
        os << "<P>This license is used by " << i->second.authors.size() << " authors and " << i->second.files.size() << " files (see details)\n";
        ++i;
    }
}
//
// and now the details:
//
i = m_license_data.begin();
j = m_license_data.end();
int license_index = 0;
os << "<a name="details">\n<h2>Licence Details</h2>\n";
while(i != j)
{
    // title:
os << "<H3><A name="" << make_link_target(licenses.first[i->first].license_name) << ";" << licenses.first[i->first].license_name << "</H3>
";

// license text:

os << "<BLOCKQUOTE>" << licenses.first[i->first].license_text << "</BLOCKQUOTE>";
if(!m_bsl_summary_mode || (license_index >= 3))
{
    // Copyright holders:
    os << "<P>This license is used by the following " << i->second.authors.size() << " copyright holders:</P>
    <BLOCKQUOTE><P>
    std::set<std::string>::const_iterator x, y;
x = i->second.authors.begin();
y = i->second.authors.end();
while(x != y)
{
    os << *x << "<BR>";
    ++$x;
}
    os << "</P></BLOCKQUOTE>";
    // Files using this license:
    os << "<P>This license applies to the following " << i->second.files.size() << " files:
    <BLOCKQUOTE><P>
    std::set<fs::path, path_less>::const_iterator m, n;
m = i->second.files.begin();
n = i->second.files.end();
while(m != n)
{
    os << split_path(m_boost_path, *m) << "<br>";
    ++$m;
}
    os << "</P></BLOCKQUOTE>";
}
else
{
    os << "<P>This license is used by " << i->second.authors.size() << " authors (list omitted for brevity).</P>";
    os << "<P>This license applies to " << i->second.files.size() << " files (list omitted for brevity).</P>";
}
++license_index;
++i;
}

// Output list of files not found to be under license control:

os << "<h2><a name="" << m_unknown_licenses.size() << " files With No Recognisable Licence</h2>
";
<P>The following " << m_unknown_licenses.size() << " files had no recognisable license information:" << 
<BLOCKQUOTE>";</P>";
std::set<fs::path, path_less>::const_iterator i2, j2;
i2 = m_unknown_licenses.begin();
j2 = m_unknown_licenses.end();
while(i2 != j2)
{
    os << split_path(m_boost_path, *i2) << "<br>
    ++i2;
}
oindent</BLOCKQUOTE>

\begin{quote}
\textbf{Files With No Recognisable Copyright Holder}\n\end{quote}

The following \textbf{Files With No Recognisable Copyright Holder} had no recognisable copyright holder:

\begin{quote}
\begin{center}
\begin{tabular}{l}
\textbf{Files With No Recognisable Copyright Holder}
\end{center}
\end{quote}

\begin{quote}
\begin{center}
\begin{tabular}{l}
\textbf{Files With No Recognisable Copyright Holder}
\end{center}
\end{quote}

if(!m_bsl_summary_mode)
{
\begin{quote}
\textbf{Files that can be automatically converted to the Boost Software License}\n\end{quote}

The following \textbf{Files that can be automatically converted to the Boost Software License} can be automatically converted to the Boost Software License, but require manual verification before they can be committed to CVS:

\begin{quote}
\begin{center}
\begin{tabular}{l}
\textbf{Files that can be automatically converted to the Boost Software License}
\end{center}
\end{quote}

typedef std::map<fs::path, std::pair<std::string, std::string>, path_less>
::const_iterator conv_iterator;
conv_iterator i = m_converted_to_bsl.begin(),
ie = m_converted_to_bsl.end();
int file_num = 1;
while (i != ie)
{
    os << "[\" File: <tt>\" split_path(m_boost_path, i-&gt;first)" << i-&gt;second.first \" </pre>
    << i-&gt;second.second \" </pre>\n    \verb|\n    ++i;
    ++file_num;
}
// Output list of files that could be moved over to the Boost Software License
//
os << "<h2><a name="to-bsl"></a>Files that could be converted to the Boost Software License</h2>
";

i2 = m_can_migrate_to_bsl.begin();
j2 = m_can_migrate_to_bsl.end();
while(i2 != j2)
{
    os << split_path(m_boost_path, *i2) << "<br>
";
    ++i2;
}

os << "</p></BLOCKQUOTE>";

// Output list of files that can not be moved over to the Boost Software License
//

i2 = m_cannot_migrate_to_bsl.begin();
j2 = m_cannot_migrate_to_bsl.end();
while(i2 != j2)
{
    os << split_path(m_boost_path, *i2) << "<br>
";
    ++i2;
}

os << "</p></BLOCKQUOTE>";

// Output list of authors that we need permission for to move to the BSL
//

std::copy(m_authors_for_bsl_migration.begin(), m_authors_for_bsl_migration.end(),
    std::ostream_iterator<std::string>(os, "<br>
"));

os << "</p></BLOCKQUOTE>";

// output a table of copyright information:
//

os << "<H2><a name="copyright"></a>Copyright Holder Information</h2><table border="1">
";

std::map<std::string, std::set<fs::path, path_less> >::const_iterator ad, ead;
ad = m_author_data.begin();
ed = m_author_data.end();
while(ad != ead)


```c++

// output file dependency information:

os << "<H2><a name="depend"></a>File Dependency Information</H2><BLOCKQUOTE><pre>

std::map<fs::path, std::pair<std::string, std::string>, path_less>::const_iterator conv_iterator;
conv_iterator i = m_converted_to_bsl.begin(),
                   ie = m_converted_to_bsl.end();
while(i != ie)
{
    bad_paths.insert(i->first);
    ++i;
}
fi = bad_paths.begin();
efi = bad_paths.end();
while(fi != efi)
{
    os << split_path(m_boost_path, *fi) << " ";
    ++fi;
}
os << " </td></tr><\n";
++ad;
}
```
dep = m_dependencies.find(*fi);
last_dep = m_dependencies.end();
std::set<fs::path, path_less> seen_deps;
if (dep != last_dep)
    while(true)
    {
        os << " -> ";
        if(fs::exists(m_boost_path / dep->second))
            os << split_path(m_boost_path, dep->second);
        else if(fs::exists(dep->second))
            os << split_path(fs::path(), dep->second);
        else
            os << dep->second.string();
        if(seen_deps.find(dep->second) != seen_deps.end())
            { os << " <I>(Circular dependency!)</I>"; break; // circular dependency!!! }
        seen_deps.insert(dep->second);
        last_dep = dep;
        dep = m_dependencies.find(dep->second);
        if((dep == m_dependencies.end()) || (0 == compare_paths(dep->second, last_dep->second)))
            break;
    }
    os << "\n";
    ++fi;
} os << "</pre></BLOCKQUOTE>\n";

os << "</body></html>\n";

if(!os)
{
    std::string msg("Error writing to ");
    msg += m_dest_path.string();
    msg += ".
    std::runtime_error e(msg);
    boost::throw_exception(e);
}
}
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1.713 keyutils 1.6-6

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1.714 libsemanage 2.7 2build2

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Dependent Modules
=================

This code has the following dependencies
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httplib2 - MIT License
# Contribors to oauth2client

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* [Jon Wayne Parrott](https://github.com/jonparrott)
* [Danny Hermes](https://github.com/dhermes)

Previous maintainers:

* [Craig Citro](https://github.com/craigcitro)
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1.720 resolve 1.17.0

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ipv6-normalize
===============

```bash
$ npm i --save ipv6-normalize
```

```
```javascript
var assert = require('assert');
var n = require('./');

assert(n('2001:db8:0:0:0:0:cafe:1111') === '2001:db8::cafe:1111');
assert(n('2001:0DB8:AAAA:0000:0000:0000:0000:000C') === '2001:db8:aaaa::c');
assert(n('2001:db8::1:0:0:0:4') === '2001:db8:0:1::4');
```

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* /opt/cola/permits/1140310845_1614802880.67/0/ipv6-normalize-1-0-1-1-tgz/package/README.md

1.724 dhcp 4.4.1-2+deb10u1

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Format: http://www.debian.org/doc/packaging-manuals/copyright-format/1.0
Source: http://www.isc.org/downloads/dhcp
Files-Excluded:
aclocal.m4
bind
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config.*
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1.732 glibc 2.28-10

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(28) Artem Bityuckiy (--enable-newlib-iconv)

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from a designated place, then offering equivalent access to copy the
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distribute the source code, even though third parties are not
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Library, but is designed to work with the Library by being compiled or
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Section 6 states terms for distribution of such executables.

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that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
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whether or not they are linked directly with the Library itself.

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The "System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that
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@item
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6b.

@item
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@end enumerate

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DRuntime: Runtime Library for the D Programming Language

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Upstream Authors:

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

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* A rewrite of the original Debian's start-stop-daemon Perl script
* in C (faster - it is executed many times during system startup).
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1.745 eslint-module-utils 2.6.0

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1.751 python27-jmespath 0.9.4-1

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1.756 xz 5.2.4 1

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To: Jonathan Nieder <jrnieder@gmail.com>
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1.767 firmware-free 3.4
1.767.1 Available under license:

Driver: keyspan_pda -- USB Keyspan PDA single-port serial device

File: keyspan_pda/keysan_pda.fw
Source: keyspan_pda/keysan_pda.S

File: keyspan_pda/xircom_pgs.fw
Source: keyspan_pda/xircom_pgs.S
Licence: GPLv2+

Compiled from original 8051 source into Intel HEX, used in our binary ihex form.

Driver: dsp56k - Atari DSP56k support

File: dsp56k/bootstrap.bin
Source: dsp56k/bootstrap.asm

Licence: GPLv2 or later

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DSP56001 assembler, possibly buildable with a56 from http://www.zdomain.com/a56.html

Driver: pcnet_cs - NE2000 compatible PCMCIA adapter

File: cis/LA-PCM.cis
File: cis/PCMLM28.cis
File: cis/DP83903.cis
File: cis/NE2K.cis
File: cis/tamarack.cis
File: cis/PE-200.cis
File: cis/PE520.cis
Source: cis/src/LA-PCM.cis
Source: cis/src/PCMLM28.cis
Source: cis/src/DP83903.cis
Source: cis/src/NE2K.cis
Source: cis/src/tamarack.cis
Source: cis/src/PE-200.cis
Source: cis/src/PE520.cis

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Driver: 3c589_cs - 3Com PCMCIA adapter

File: cis/3CXEM556.cis
Source: cis/src/3CXEM556.cis
Driver: 3c574_cs - 3Com PCMCIA adapter

File: cis/3CCFEM556.cis
Source: cis/src/3CCFEM556.cis

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Driver: serial_cs - Serial PCMCIA adapter

File: cis/MT5634ZLX.cis
File: cis/RS-COM-2P.cis
File: cis/COMpad2.cis
File: cis/COMpad4.cis
Source: cis/src/MT5634ZLX.cis
Source: cis/src/RS-COM-2P.cis
Source: cis/src/COMpad2.cis
Source: cis/src/COMpad4.cis

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Driver: serial_cs - Serial PCMCIA adapter

File: cis/SW_555_SER.cis
File: cis/SW_7xx_SER.cis
File: cis/SW_8xx_SER.cis

Licence: GPLv3

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These binary files are understood to be the preferred form for modification. They follow the PCMCIA Card Information Structure (CIS) format. The pcmcia-cs project <http://pcmcia-cs.sourceforge.net/> distributes tools for converting between CIS and an unofficial text format.

Driver: dvb-tpci -- AV7110 cards

File: av7110/bootcode.bin
Source: av7110/Boot.S

Licence: GPLv2 or later

Driver: usbdux/usbduxfast/usbduxsigma - usbdux data acquisition cards

File: usbdux_firmware.bin
File: usbduxfast_firmware.bin
File: usbduxsigma_firmware.bin
Source: usbdux/fx2-include.asm
Source: usbdux/usbduxfast_firmware.asm
Source: usbdux/usbdux_firmware.asm
Source: usbdux/usbduxsigma_firmware.asm

Licence: GPLv2. See source code in usbdux/ subdirectory.

Provided from the author, Bernd Porr <BerndPorr@f2s.com>

Driver: isci -- Intel C600 SAS controller driver

File: isci/isci_firmware.bin
Source: isci/create_fw.c
Source: isci/create_fw.h
Source: isci/probe_roms.h
Source: isci/Makefile

Licence: GPLv2

Driver: carl9170 -- Atheros AR9170 802.11 draft-n USB driver

File: carl9170-1.fw
Version: 1.9.6
Source: carl9170fw/

Downloaded from http://linuxwireless.org/en/users/Drivers/carl9170

Licence: GPLv2. Some build scripts use the New BSD (3-clause) licence.

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Driver: keyspan_pda -- USB Keyspan PDA single-port serial device

File: keyspan_pda/keyspan_pda.fw
Source: keyspan_pda/keyspan_pda.S

File: keyspan_pda/xircom_pgs.fw
Source: keyspan_pda/xircom_pgs.S

Licence: GPLv2+

Compiled from original 8051 source into Intel HEX, used in our binary ihex form.

Driver: dsp56k - Atari DSP56k support

File: dsp56k/bootstrap.bin
Source: dsp56k/bootstrap.asm

Licence: GPLv2 or later

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DSP56001 assembler, possibly buildable with a56 from http://www.zdomain.com/a56.html

Driver: pcnet_cs - NE2000 compatible PCMCIA adapter

File: cis/LA-PCM.cis
File: cis/PCMLM28.cis
File: cis/DP83903.cis
File: cis/NE2K.cis
File: cis/tamarack.cis
File: cis/PE-200.cis
File: cis/PE520.cis
Source: cis/src/LA-PCM.cis
Source: cis/src/PCMLM28.cis
Source: cis/src/DP83903.cis
Source: cis/src/NE2K.cis
Source: cis/src/tamarack.cis
Source: cis/src/PE-200.cis
Source: cis/src/PE520.cis

Licence: Dual GPLv2/MPL

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-------------------------------------------------------------------------

Driver: 3c589_cs - 3Com PCMCIA adapter

File: cis/3CXEM556.cis
Source: cis/src/3CXEM556.cis

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-------------------------------------------------------------------------

Driver: 3c574_cs - 3Com PCMCIA adapter

File: cis/3CCFEM556.cis
Source: cis/src/3CCFEM556.cis

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-------------------------------------------------------------------------

Driver: serial_cs - Serial PCMCIA adapter

File: cis/MT5634ZLX.cis
File: cis/RS-COM-2P.cis
File: cis/COMpad2.cis
File: cis/COMpad4.cis
Source: cis/src/MT5634ZLX.cis
Source: cis/src/RS-COM-2P.cis
Source: cis/src/COMpad2.cis
Source: cis/src/COMpad4.cis

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------------------------------------------------------------------
Driver: serial_cs - Serial PCMCIA adapter

File: cis/SW_555_SER.cis
File: cis/SW_7xx_SER.cis
File: cis/SW_8xx_SER.cis

Licence: GPLv3

Copyright Sierra Wireless

These binary files are understood to be the preferred form for modification. They follow the PCMCIA Card Information Structure (CIS) format. The pcmcia-cs project <http://pcmcia-cs.sourceforge.net/> distributes tools for converting between CIS and an unofficial text format.

------------------------------------------------------------------
Driver: dvb-tpci -- AV7110 cards

File: av7110/bootcode.bin
Source: av7110/Boot.S

Licence: GPLv2 or later

------------------------------------------------------------------
Driver: usbdux/usbduxfast/usbduxsigma - usbdux data acquisition cards

File: usbdux_firmware.bin
File: usbduxfast_firmware.bin
File: usbduxsigma_firmware.bin
Source: usbdux/fix2-include.asm
Source: usbdux/usbduxfast_firmware.asm
Source: usbdux/usbdux_firmware.asm
Source: usbdux/usbduxsigma_firmware.asm

Licence: GPLv2. See source code in usbdux/ subdirectory.

Provided from the author, Bernd Porr <BerndPorr@f2s.com>

Driver: isci -- Intel C600 SAS controller driver

File: isci/isci_firmware.bin
Source: isci/create_fw.c
Source: isci/create_fw.h
Source: isci/probe_roms.h
Source: isci/Makefile

Licence: GPLv2

Driver: carl9170 -- Atheros AR9170 802.11 draft-n USB driver

File: carl9170-1.fw
Version: 1.9.6
Source: carl9170fw/

Downloaded from http://linuxwireless.org/en/users/Drivers/carl9170

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 can be found in /usr/share/common-licenses/GPL-3.
Atheros carl9170 firmware - used by the ar9170 wireless device

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1.768 dpdk 18.11.11-1~deb10u1

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^L
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1.785 vim 8.1.0875-5

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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Also add information on how to contact you by electronic and paper mail.

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Yoyodyne, Inc., hereby disclaims all copyright interest in the library ‘Frob’ (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!
*uganda.txt* For Vim version 8.1. Last change: 2018 May 17

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VIM REFERENCE MANUAL by Bram Moolenaar

---

*uganda* *Uganda* *copying* *copyright* *license*

SUMMARY

*iccf* *ICCF*

Vim is Charityware. You can use and copy it as much as you like, but you are encouraged to make a donation for needy children in Uganda. Please see [kcc] below or visit the ICCF web site, available at these URLs:

http://iccf-holland.org/
http://www.vim.org/iccf/
http://www.iccf.nl/

You can also sponsor the development of Vim. Vim sponsors can vote for features. See [sponsor]. The money goes to Uganda anyway.

The Open Publication License applies to the Vim documentation, see [manual-copyright].

---

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I) There are no restrictions on distributing unmodified copies of Vim except that they must include this license text. You can also distribute unmodified parts of Vim, likewise unrestricted except that they must include this license text. You are also allowed to include executables that you made from the unmodified Vim sources, plus your own usage examples and Vim scripts.

II) It is allowed to distribute a modified (or extended) version of Vim, including executables and/or source code, when the following four conditions are met:

1) This license text must be included unmodified.
2) The modified Vim must be distributed in one of the following five ways:
a) If you make changes to Vim yourself, you must clearly describe in the distribution how to contact you. When the maintainer asks you (in any way) for a copy of the modified Vim you distributed, you must make your changes, including source code, available to the maintainer without fee. The maintainer reserves the right to include your changes in the official version of Vim. What the maintainer will do with your changes and under what license they will be distributed is negotiable. If there has been no negotiation then this license, or a later version, also applies to your changes.

The current maintainer is Bram Moolenaar <Bram@vim.org>. If this changes it will be announced in appropriate places (most likely vim.sf.net, www.vim.org and/or comp.editors). When it is completely impossible to contact the maintainer, the obligation to send him your changes ceases. Once the maintainer has confirmed that he has received your changes they will not have to be sent again.

b) If you have received a modified Vim that was distributed as mentioned under a) you are allowed to further distribute it unmodified, as mentioned at I). If you make additional changes the text under a) applies to those changes.

c) Provide all the changes, including source code, with every copy of the modified Vim you distribute. This may be done in the form of a context diff. You can choose what license to use for new code you add. The changes and their license must not restrict others from making their own changes to the official version of Vim.

d) When you have a modified Vim which includes changes as mentioned under c), you can distribute it without the source code for the changes if the following three conditions are met:

- The license that applies to the changes permits you to distribute the changes to the Vim maintainer without fee or restriction, and permits the Vim maintainer to include the changes in the official version of Vim without fee or restriction.
- You keep the changes for at least three years after last distributing the corresponding modified Vim. When the maintainer or someone who you distributed the modified Vim to asks you (in any way) for the changes within this period, you must make them available to him.
- You clearly describe in the distribution how to contact you. This contact information must remain valid for at least three years after last distributing the corresponding modified Vim, or as long as possible.

e) When the GNU General Public License (GPL) applies to the changes, you can distribute the modified Vim under the GNU GPL version 2 or any later version.

3) A message must be added, at least in the output of the ":version" command and in the intro screen, such that the user of the modified Vim is able to see that it was modified. When distributing as mentioned under 2)e) adding the message is only required for as far as this does not conflict with the license used for the changes.
4) The contact information as required under 2)a) and 2)d) must not be removed or changed, except that the person himself can make corrections.

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IV) It is not allowed to remove this license from the distribution of the Vim sources, parts of it or from a modified version. You may use this license for previous Vim releases instead of the license that they came with, at your option.

=== end of license ===

Note:

- If you are happy with Vim, please express that by reading the rest of this file and consider helping needy children in Uganda.

- If you want to support further Vim development consider becoming a [sponsor]. The money goes to Uganda anyway.

- According to Richard Stallman the Vim license is GNU GPL compatible. A few minor changes have been made since he checked it, but that should not make a difference.

- If you link Vim with a library that goes under the GNU GPL, this limits further distribution to the GNU GPL. Also when you didn't actually change anything in Vim.

- Once a change is included that goes under the GNU GPL, this forces all further changes to also be made under the GNU GPL or a compatible license.

- If you distribute a modified version of Vim, you can include your name and contact information with the "--with-modified-by" configure argument or the MODIFIED_BY define.

==============================================================================
Kibaale Children's Centre*KCC* *Kibaale* *charity*

Kibaale Children's Centre (KCC) is located in Kibaale, a small town in the south of Uganda, near Tanzania, in East Africa. The area is known as Rakai District. The population is mostly farmers. Although people are poor, there
is enough food. But this district is suffering from AIDS more than any other part of the world. Some say that it started there. Estimations are that 10 to 30% of the Ugandans are infected with HIV. Because parents die, there are many orphans. In this district about 60,000 children have lost one or both parents, out of a population of 350,000. And this is still continuing.

The children need a lot of help. The KCC is working hard to provide the needy with food, medical care and education. Food and medical care to keep them healthy now, and education so that they can take care of themselves in the future. KCC works on a Christian base, but help is given to children of any religion.

The key to solving the problems in this area is education. This has been neglected in the past years with president Idi Amin and the following civil wars. Now that the government is stable again, the children and parents have to learn how to take care of themselves and how to avoid infections. There is also help for people who are ill and hungry, but the primary goal is to prevent people from getting ill and to teach them how to grow healthy food.

Most of the orphans are living in an extended family. An uncle or older sister is taking care of them. Because these families are big and the income (if any) is low, a child is lucky if it gets healthy food. Clothes, medical care and schooling is beyond its reach. To help these needy children, a sponsorship program was put into place. A child can be financially adopted. For a few dollars a month KCC sees to it that the child gets indispensable items, is healthy, goes to school and KCC takes care of anything else that needs to be done for the child and the family that supports it.

Besides helping the child directly, the environment where the child grows up needs to be improved. KCC helps schools to improve their teaching methods. There is a demonstration school at the centre and teacher trainings are given. Health workers are being trained, hygiene education is carried out and households are stimulated to build a proper latrine. I helped setting up a production site for cement slabs. These are used to build a good latrine. They are sold below cost price.

There is a small clinic at the project, which provides children and their family with medical help. When needed, transport to a hospital is offered. Immunization programs are carried out and help is provided when an epidemic is breaking out (measles and cholera have been a problem).

*donate*

Summer 1994 to summer 1995 I spent a whole year at the centre, working as a volunteer. I have helped to expand the centre and worked in the area of water and sanitation. I learned that the help that the KCC provides really helps.

When I came back to Holland, I wanted to continue supporting KCC. To do this I'm raising funds and organizing the sponsorship program. Please consider one of these possibilities:
1. Sponsor a child in primary school: 17 euro a month (or more).
2. Sponsor a child in secondary school: 25 euro a month (or more).
3. Sponsor the clinic: Any amount a month or quarter
4. A one-time donation

Compared with other organizations that do child sponsorship the amounts are very low. This is because the money goes directly to the centre. Less than 5% is used for administration. This is possible because this is a small organization that works with volunteers. If you would like to sponsor a child, you should have the intention to do this for at least one year.

How do you know that the money will be spent right? First of all you have my personal guarantee as the author of Vim. I trust the people that are working at the centre, I know them personally. Furthermore, the centre has been co-sponsored and inspected by World Vision, Save the Children Fund and is now under the supervision of Pacific Academy Outreach Society. The centre is visited about once a year to check the progress (at our own cost). I have visited the centre myself many times, starting in 1993. The visit reports are on the ICCF web site.

If you have any further questions, send me e-mail: <Bram@vim.org>.

The address of the centre is:
Kibaale Children's Centre
p.o. box 1658
Masaka, Uganda, East Africa

Sending money:*iccf-donations*

Check the ICCF web site for the latest information! See [iccf] for the URL.

USA: The methods mentioned below can be used.
Sending a check to the Nehemiah Group Outreach Society (NGOS) is no longer possible, unfortunately. We are looking for another way to get you an IRS tax receipt.
For sponsoring a child contact KCF in Canada (see below). US checks can be sent to them to lower banking costs.

Canada: Contact Kibaale Children's Fund (KCF) in Surrey, Canada. They take care of the Canadian sponsors for the children in Kibaale. KCF forwards 100% of the money to the project in Uganda. You can send them a one time donation directly. Please send me a note so that I know what has been donated because of Vim. Ask KCF for information about sponsorship.
Kibaale Children's Fund c/o Pacific Academy
10238-168 Street
Surrey, B.C. V4N 1Z4
Canada
Phone: 604-581-5353
If you make a donation to Kibaale Children's Fund (KCF) you will receive a tax receipt which can be submitted with your tax return.

Holland: Transfer to the account of "Stichting ICCF Holland" in Lisse. This will allow for tax deduction if you live in Holland.
Postbank, nr. 4548774
IBAN: NL95 INGB 0004 5487 74

Germany: It is possible to make donations that allow for a tax return. Check the ICCF web site for the latest information:
http://iccf-holland.org/germany.html

World: Use a postal money order. That should be possible from any country, mostly from the post office. Use this name (which is in my passport): "Abraham Moolenaar". Use Euro for the currency if possible.

Europe: Use a bank transfer if possible. Your bank should have a form that you can use for this. See "Others" below for the swift code and IBAN number.
Any other method should work. Ask for information about sponsorship.

Credit Card: You can use PayPal to send money with a Credit card. This is the most widely used Internet based payment system. It's really simple to use. Use this link to find more info:
The e-mail address for sending the money to is:
Bram@iccf-holland.org
For amounts above 400 Euro ($500) sending a check is preferred.

Others: Transfer to one of these accounts if possible:
Postbank, account 4548774
Swift code: INGB NL 2A
IBAN: NL95 INGB 0004 5487 74
under the name "stichting ICCF Holland", Lisse
If that doesn't work:
Rabobank Lisse, account 3765.05.117
Swift code: RABO NL 2U
under the name "Bram Moolenaar", Lisse
Otherwise, send a check in euro or US dollars to the address below. Minimal amount: $70 (my bank does not accept smaller amounts for foreign check, sorry)
Address to send checks to:
Bram Moolenaar
Finsterrueithof 1
8134 Adliswil
Switzerland

This address is expected to be valid for a long time.

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Version 2.1, February 1999

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(For example, a function in a library to compute square roots has
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" Vim syntax file
" Language:Software Distributor product specification file
"(POSIX 1387.2-1995).
" Maintainer:Rex Barzee <rex_barzee@hp.com>
" Last change:25 Apr 2001

" quit when a syntax file was already loaded
if exists("b:current_syntax")
    finish
endif

" Product specification files are case sensitive
syn case match

syn keyword psfObject bundle category control_file depot distribution
syn keyword psfObject end_file fileset host installed_software media
syn keyword psfObject product root subproduct vendor

syn match psfUnquotString +[^# ][^#]+* contained
syn region psfQuotString start=+"" skip=+"" end=+"" contained

syn match psfObjTag "\<-\(+A-Z0-9a-z]+\\\-[_-\(+A-Z0-9a-z]+\)>" contained
syn match psfAttAbbrev ",\(<fa|fr|\[aclqrv\]<\|>|<=|>=|==\)>[^,]\)+ contained
syn match psfObjTags "\<-\(+A-Z0-9a-z]+\\\-[_-\(+A-Z0-9a-z]+\)>\(<\|>|<=|>=|==\)>\<\|>|<=|>=|==\)" contained

syn match psfNumber "\<\d+\>" contained
syn match psfFloat "\<\d+\>\<\d+\>\<\d+\>" contained

syn match psfLongDate "\<\d+\d+\d+\d+\d+\d+\d+\d+\d+\d+\d+\>" contained

syn keyword psfState available configured corrupt installed transient contained
syn keyword psfPState applied committed superseded contained

syn keyword psfBoolean false true contained

"Some of the attributes covered by attUnquotString and attQuotString:
" architecture category_tag control_directory copyright
" create_date description directory file_permissions install_source
" install_type location machine_type mod_date number os_name os_release
" os_version pose_as_os_name pose_as_os_release readme revision
" share_link title vendor_tag
syn region psfAttUnquotString matchgroup=psfAttrib start=--[^\s]*[^\# ][^\s]\<[^# ][^\# ]>--rs=e-1 contains=psfUnquotString,psfComment end=-$~ keepend oneline
These regions are defined in attempt to do syntax checking for some of the attributes.

```
syn region psfAttQuotString matchgroup=psfAttrib start=~\^s*[^# ]+/\|+/\^+-rs=e-1 contains=psfQuotString.psfComment skip=~\^"~ matchgroup=psfQuotString end=~"~ keepend

" These regions are defined in attempt to do syntax checking for some of the attributes.

syn region psfAttTag matchgroup=psfAttrib start="^s*tag\|\+" contains=psfObjTag.psfComment end="$" keepend oneline

syn region psfAttSpec matchgroup=psfAttrib start="^s*(ancestor\|applied_patches\|applied_to\|contents\|corequisites\|exrequisites\|prerequisites\|software_spec\|supersedes\|superseded_by\)\|\+" contains=psfObjTag,psfAttAbbrev,psfComment end="$" keepend oneline

syn region psfAttTags matchgroup=psfAttrib start="^s*all_filesets\|\+" contains=psfObjTags,psfComment end="$" keepend oneline

syn region psfAttNumber matchgroup=psfAttrib start="^s*(compressed_size\|instance_id\|media_sequence_number\|sequence_number\|size)\|\+" contains=psfNumber,psfComment end="$" keepend oneline

syn region psfAttTime matchgroup=psfAttrib start="^s*(create_time\|ctime\|mod_time\|mtime\|timestamp)\|\+" contains=psfNumber,psfComment end="$" keepend oneline

syn region psfAttFloat matchgroup=psfAttrib start="^s*\(data_model_revision\|layout_version)\|\+" contains=psfFloat,psfComment end="$" keepend oneline

syn region psfAttLongDate matchgroup=psfAttrib start="^s*(install_date)\|\+" contains=psfLongDate,psfComment end="$" keepend oneline

syn region psfAttState matchgroup=psfAttrib start="^s*(state)\|\+" contains=psfState,psfComment end="$" keepend oneline

syn region psfAttPState matchgroup=psfAttrib start="^s*(patch_state)\|\+" contains=psfPState,psfComment end="$" keepend oneline

syn region psfAttBoolean matchgroup=psfAttrib start="^s*\(is_kernel\|is_locatable\|is_patch\|is_protected\|is_reboot\|is_reference\|is_secure\|is_sparse)\|\+" contains=psfBoolean,psfComment end="$" keepend oneline

syn match psfComment ".*$"

" Define the default highlighting.
" Only when an item doesn't have highlighting yet

hi def link psfObject Statement
hi def link psfAttrib Type
hi def link psfQuotString String
hi def link psfObjTag Identifier
hi def link psfAttAbbrev PreProc
hi def link psfObjTags Identifier

hi def link psfComment Comment

" Long descriptions and copyrights confuse the syntax highlighting, so
" force vim to backup at least 100 lines before the top visible line
" looking for a sync location.
syn sync lines=100

let b:current_syntax = "psf"

1.786 loose-envify 1.4.0

1.787 @babel/runtime 7.13.9

1.787.1 Available under license :
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1.788 base-files 10.1ubuntu2.10

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1.790 certifi 2021.5.30

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Certificate data from Mozilla as of: Thu Nov 3 19:04:19 2011#
This is a bundle of X.509 certificates of public Certificate Authorities
(CA). These were automatically extracted from Mozilla's root certificates
file (certdata.txt). This file can be found in the mozilla source tree:
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1.791 make 4.2.1 1.2

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1.794 libdnet 1.12-8
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1.795 libyaml 0.2.1 1

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1.796 errors v0.9.1

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1.797 pgdg-keyring 2018.2

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1.799 python-webencodings 0.5.1-1
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from setuptools import setup, find_packages
import io
import re
from os import path

VERSION = re.search("VERSION = "\([^"\]+"\)", io.open(path.join(path.dirname(__file__), 'webencodings', '__init__.py'), encoding='utf-8')
).read().strip()).group(1)

LONG_DESCRIPTION = io.open(
    path.join(path.dirname(__file__), 'README.rst'),
    encoding='utf-8'
).read()

setup(
    name='webencodings',
    version=VERSION,
    url='https://github.com/SimonSapin/python-webencodings',
    license='BSD',
    author='Simon Sapin',
    author_email='simon.sapin@exyr.org',
    maintainer='Geoffrey Sneddon',
    maintainer_email='me@gsnedders.com',
    description='Character encoding aliases for legacy web content',
    long_description=LONG_DESCRIPTION,
    classifiers=[
        'Development Status :: 4 - Beta',
        'Intended Audience :: Developers',
        'License :: OSI Approved :: BSD License',
        'Programming Language :: Python',
        'Programming Language :: Python :: 2',
        'Programming Language :: Python :: 2.6',
        'Programming Language :: Python :: 2.7',
        'Programming Language :: Python :: 3',
        'Programming Language :: Python :: 3.3',
        'Programming Language :: Python :: 3.4',
        'Programming Language :: Python :: 3.5',
        'Programming Language :: Python :: 3.6',
        'Programming Language :: Python :: Implementation :: CPython',
        'Programming Language :: Python :: Implementation :: PyPy',
        'Topic :: Internet :: WWW/HTTP',
    ],
    packages=find_packages(),
)

Found in path(s):
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-0.5.1/setup.py
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Found in path(s):
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0-5-1-orig-1-tar-gz/webencodings-
This is a Python implementation of the `WHATWG Encoding standard`_.

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
* PyPI releases: http://pypi.python.org/pypi/webencodings
* License: BSD
* Python 2.6+ and 3.3+

In order to be compatible with legacy web content
when interpreting something like `Content-Type: text/html; charset=latin1`,
tools need to use a particular set of aliases for encoding labels
as well as some overriding rules.
For example, ``US-ASCII`` and ``iso-8859-1`` on the web are actually
aliases for ``windows-1252``, and an UTF-8 or UTF-16 BOM takes precedence
over any other encoding declaration.
The Encoding standard defines all such details so that implementations do
not have to reverse-engineer each other.

This module has encoding labels and BOM detection,
but the actual implementation for encoders and decoders is Pythons.

---

``webencodings.mklabels``

---------------

Regenerate the `webencodings.labels` module.

:copyright: Copyright 2012 by Simon Sapin
import json
try:
    from urllib import urlopen
except ImportError:
    from urllib.request import urlopen

def assert_lower(string):
    assert string == string.lower()
    return string

def generate(url):
    parts = [""
    LABELS = {
        ""
    labels = [
        (repr(assert_lower(label)).lstrip('u'),
        repr(encoding['name']).lstrip('u'))
        for category in json.loads(urlopen(url).read().decode('ascii'))
        for encoding in category['encodings']
        for label in encoding['labels']
    max_len = max(len(label) for label, name in labels)
    parts.extend(
        '    %s:%s\n' % (label, ' ' * (max_len - len(label)), name)
        for label, name in labels)
    parts.append('}
    return ''.join(parts)
if __name__ == '__main__':
    print(generate('http://encoding.spec.whatwg.org/encodings.json'))

Found in path(s):
* /opt/cola/permits/1102484124_1603771709.92/0/python-webencodings-0.5.1-orig-1-tar-gz/webencodings-0.5.1/webencodings/mklabels.py
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Metadata-Version: 1.1
Name: webencodings
Version: 0.5.1
Summary: Character encoding aliases for legacy web content
Home-page: https://github.com/SimonSapin/python-webencodings
Author: Geoffrey Sneddon
Author-email: me@gsnedders.com
License: BSD
Description: python-webencodings

This is a Python implementation of the `WHATWG Encoding standard <http://encoding.spec.whatwg.org/>`

* Latest documentation: http://packages.python.org/webencodings/
* Source code and issue tracker: https://github.com/gsnedders/python-webencodings
* PyPI releases: http://pypi.python.org/pypi/webencodings
* License: BSD
* Python 2.6+ and 3.3+

In order to be compatible with legacy web content
when interpreting something like `Content-Type: text/html; charset=latin1`,
tools need to use a particular set of aliases for encoding labels
as well as some overriding rules.
For example, `US-ASCII` and `iso-8859-1` on the web are actually
aliases for `windows-1252`, and an UTF-8 or UTF-16 BOM takes precedence
over any other encoding declaration.
The Encoding standard defines all such details so that implementations do
not have to reverse-engineer each other.

This module has encoding labels and BOM detection,
but the actual implementation for encoders and decoders is Pythons.

Platform: UNKNOWN
Classifier: Development Status :: 4 - Beta
Classifier: Intended Audience :: Developers
Classifier: License :: OSI Approved :: BSD License
Classifier: Programming Language :: Python
1.800 iputils 20161105 1ubuntu3

1.800.1 Available under license:

This package was debianized by Anthony Towns <ajt@debian.org> on Sat, 5 Aug 2000 19:51:36 +1000.
It has been maintained for Debian by Noah Meyerhans <noahm@debian.org> since 6 Feb 2001.

It was downloaded from https://github.com/iputils/iputils

Upstream Author: Alexey Kuznetsov <kuznet@ms2.inr.ac.ru>

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1.802 base-passwd 3.5.46

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Maintainer: Colin Watson <cjwatson@debian.org>

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Files: passwd.master, group.master
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands
as a successor of a language called ABC. Guido remains Python's
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us)
in Reston, Virginia where he released several versions of the
software.

In May 2000, Guido and the Python core development team moved to
BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope
Corporation, see http://www.zope.com). In 2001, the Python Software
Foundation (PSF, see http://www.python.org/psf/) was formed, a
non-profit organization created specifically to own Python-related
Intellectual Property. Zope Corporation is a sponsoring member of
the PSF.

All Python releases are Open Source (see http://www.opensource.org
for the Open Source Definition). Historically, most, but not all, Python
releases have also been GPL-compatible; the table below summarizes
the various releases.

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This is a bundle of X.509 certificates of public Certificate Authorities
(CA). These were automatically extracted from Mozilla's root certificates
file (certdata.txt). This file can be found in the mozilla source tree:
It contains the certificates in PEM format and therefore
can be directly used with curl / libcurl / php_curl, or with
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Just configure this file as the SSLCACertificateFile.#

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@(#) SRCsfile: certdata.txt,v $ $Revision: 1.80 $ $Date: 2011/11/03 15:11:58 $ License

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1.811 intel-ipsec-mb 0.52-2

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1.812 python-requests 2.25.1

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1.813 go-homedir v1.1.0

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1.815 python-setuptools 56.0.0

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1.816 selinux 2.7-2build2

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1.817 libqb 1.0.5-1

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ASN.1 library for Python
------------------------
[![Python Versions](https://img.shields.io/pypi/pyversions/pyasn1.svg)](https://pypi.python.org/pypi/pyasn1/)
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This is a free and open source implementation of ASN.1 types and codecs as a Python package. It has been first written to support particular protocol (SNMP) but then generalized to be suitable for a wide range of protocols based on [ASN.1 specification](https://www.itu.int/rec/dologin_pub.asp?lang=e&id=T-REC-X.208-198811-W!!PDF-E&type=items).

**Features**
--------

* Generic implementation of ASN.1 types (X.208)
* Standards compliant BER/CER/DER codecs
* Dumps/loads ASN.1 structures from Python types
* 100% Python, works with Python 2.4 up to Python 3.6
* MT-safe
* Contributed ASN.1 compiler [Asn1ate](https://github.com/kimgr/asn1ate)

**Why using pyasn1**
---------------------

ASN.1 solves the data serialisation problem. This solution was designed long ago by the wise Ancients. Back then, they did not have the luxury of wasting bits. That is why ASN.1 is designed to serialise data structures of unbounded complexity into something compact and efficient when it comes to processing the data.

That probably explains why many network protocols and file formats still rely on the 30+ years old technology. Including a number of high-profile Internet protocols and file formats.

Quite a number of books cover the topic of ASN.1.
Open Source Used In Cisco Telemetry Broker On Prem 1.1  5436

Communication between heterogeneous systems[http://www.oss.com/asn1/dubuisson.html] by Olivier Dubuisson is one of those high quality books freely available on the Internet.

The pyasn1 package is designed to help Python programmers tackling network protocols and file formats at the comfort of their Python prompt. The tool struggles to capture all aspects of a rather complicated ASN.1 system and to represent it on the Python terms.

How to use pyasn1

-------------

With pyasn1 you can build Python objects from ASN.1 data structures. For example, the following ASN.1 data structure:

```
```bash
Record ::= SEQUENCE {
  id        INTEGER,
  room  [0] INTEGER OPTIONAL,
  house [1] INTEGER DEFAULT 0
}
```bash

Could be expressed in pyasn1 like this:

```
```python
class Record(Sequence):
  componentType = NamedTypes(
    NamedType('id', Integer()),
    OptionalNamedType('room', Integer().subtype(implicitTag=Tag(tagClassContext, tagFormatSimple, 0))
  ),
  DefaultedNamedType(
    'house', Integer(0).subtype(implicitTag=Tag(tagClassContext, tagFormatSimple, 1))
  ),
)
```python

It is in the spirit of ASN.1 to take abstract data description and turn it into a programming language specific form. Once you have your ASN.1 data structure expressed in Python, you can use it along the lines of similar Python type (e.g. ASN.1 'SET' is similar to Python 'dict', 'SET OF' to 'list'): 
Part of the power of ASN.1 comes from its serialisation features. You can serialise your data structure and send it over the network.

Conversely, you can turn serialised ASN.1 content, as received from network or read from a file, into a Python object which you can introspect, modify, encode and send back.

The pyasn1 classes struggle to emulate their Python prototypes (e.g. int, list, dict etc.). But ASN.1 types exhibit more complicated behaviour. To make life easier for a Pythonista, they can turn their pyasn1 classes into Python built-ins:
```python
>>> from pyasn1.codec.native.encoder import encode
>>> encode(record)
{'id': 123, 'room': 321, 'house': 0}
``` 

Or vice-versa -- you can initialize an ASN.1 structure from a tree of Python objects:

```python
>>> from pyasn1.codec.native.decoder import decode
>>> record = decode({'id': 123, 'room': 321, 'house': 0}, asn1Spec=Record())
>>> str(record)
Record:
id=123
room=321
``` 

With ASN.1 design, serialisation codecs are decoupled from data objects, so you could turn every single ASN.1 object into many different serialised forms. As of this moment, pyasn1 supports BER, DER, CER and Python built-ins codecs. The extremely compact PER encoding is expected to be introduced in the upcoming pyasn1 release.

More information on pyasn1 APIs can be found in the documentation, compiled ASN.1 modules for different protocols and file formats could be found in the pyasn1-modules repository.

How to get pyasn1
-----------------

The pyasn1 package is distributed under terms and conditions of 2-clause BSD license. Source code is freely available as a GitHub repository. You could `pip install pyasn1` or download it from PyPI.

If something does not work as expected, open an issue at GitHub or post your question on Stack Overflow or try browsing the mailing list archives.

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ASN.1 library for Python
========================

.. toctree::

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Abstract Syntax Notation One (ASN.1) is a technology for exchanging structured data in a universally understood, hardware agnostic way. Many industrial, security and telephony applications heavily rely on ASN.1.

The `pyasn1 <https://pypi.python.org/pypi/pyasn1/>`_ library implements ASN.1 support in pure-Python.

What is ASN.1
--------------

ASN.1 is a large, arguably over-engineered and extremely old data modelling and serialisation tool. It is probably among the first serialisation protocols in the history of computer science and technology.

ASN.1 started its life over 30 years ago as a serialisation mechanism for the first electronic mail (known as X.400). Later on it was split off the e-mail application and become a stand-alone tech still being actively supported by its designers and widely used in industry and technology.

Since then ASN.1 is sort of haunted by its relations with the OSI model -- the first, unsuccessful, version of the Internet. You can read many interesting `discussions <https://news.ycombinator.com/item?id=8871453>`_ on that topic.
In the following years, generations of software engineers tackled the serialisation problem many times. We can see that in Google’s `ProtoBuffers <https://developers.google.com/protocol-buffers/>`_ or `FlatBuffers <https://google.github.io/flatbuffers/>`_, for example. Interestingly, many new takes on binary protocol design do not depart far from ASN.1 from technical perspective. It’s more of a matter of striking a balance between processing overhead, wire format overhead and human readability.

Looking at what ASN.1 has to offer, it has three loosely coupled parts:

* Data types: the standard introduces a collection of basic data types (integers, bits, strings, arrays and records) that can be used for describing arbitrarily complex, nested data structures.

* Serialisation protocols: the above data structures could be converted into a series of octets for storage or transmission over the wire as well as recovered back into their structured form. The system is fully agnostic to hardware architectures differences.

* Schema language: ASN.1 data structures could be described in terms of a schema language for ASN.1 compiler to turn it into platform-specific implementation.

ASN.1 applications

Being an old and generally successful standard, ASN.1 is widely adopted for many uses. To give you an example, these technologies use ASN.1 for their data exchange needs:

* Signaling standards for the public switched telephone network (SS7 family)
* Network management standards (SNMP, CMIP)
* Directory standards (X.500 family, LDAP)
* Public Key Infrastructure standards (X.509, etc.)
* PBX control (CSTA)
* IP-based Videoconferencing (H.323 family)
* Biometrics (BIP, CBEFF, ACBio)
* Intelligent transportation (SAE J2735)
* Cellular telephony (GSM, GPRS/EDGE, UMTS, LTE)

ASN.1 gotchas

Apparently, ASN.1 is hard to implement properly. Quality open-source ASN.1 tools are rare, but ad-hoc implementations are numerous. Judging from the `statistics <http://cve.mitre.org/cgi-bin/cvekey.cgi?keyword=ASN.1>`_ on discovered
security vulnerabilities, many people have implemented ASN.1 parsers and oftentimes fell victim to its edge cases.

On the bright side, ASN.1 has been around for a long time, it is well understood and security reviewed.

Documentation

Use case

Download & Install

Changes

License

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Getting help
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Books on ASN.1
--------------

The pyasn1 implementation is largely based on reading up the following awesome books:

* "ASN.1 - Communication between heterogeneous systems <http://www.oss.com/asn1/dubuisson.html>" by Olivier Dubuisson

Here you can get the official standards which is hard to read:


On the other end of the readability spectrum, here is a quick and sweet write up:


If you are working with ASN.1, we'd highly recommend reading a proper book on the subject.

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1.830 e2fsprogs 1.44.5 1+deb10u3

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--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
/*
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
-
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Theodore Ts'o
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That's all there is to it!
This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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Upstream Author: Theodore Ts'o <tytso@mit.edu>

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This is the Debian GNU/Linux prepackaged version of the ss
command-line interface parsing library. It is currently
distributed together with the EXT2 file system utilities, which are
otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>,
from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

From the original distribution:

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#
# This is a Makefile stub which handles the creation of BSD shared
# libraries.
#
# In order to use this stub, the following makefile variables must be defined.
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:$(BSD_LIB)

$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS_SHLIB) $(OBJS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) `echo $(my_dir) | sed -e 's;lib/;;'`/$(BSD_LIB) $(BSD_LIB))

install-shlibInstall-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)

install-strip: install

install-shlibstrip install-shlibs-strip: install-shlibs

uninstall-shlib uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)

This is the Debian GNU/Linux prepackaged version of the Common Error Description library. It is currently distributed together with the EXT2 file system utilities, which are otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>,
from sources obtained from a mirror of:
tsx-11.mit.edu:/pub/linux/packages/ext2fs/

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Gadi Oxman, August 1995

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

---
abstract: 'Build and install Perl modules'
author:
- 'Ken Williams <kwilliams@cpan.org>'
- "Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-build@perl.org>.'
build_requires:
  File::Temp: 0.15
  Test::Harness: 3.16
  Test::More: 0.49
generated_by: 'Module::Build version 0.3608'
license: gpl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
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<one line to give the program's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

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<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

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The End

#!/perl
NAME
copyright.t

DESCRIPTION
Tests that the latest copyright years in the top-level README file and the C<perl -v> output match each other.

If the test fails, update at least one of README and perl.c so that they match reality.

Optionally you can pass the C<--now> option to check they are at the current year. This isn't checked by default, so that it doesn't fail for people working on older releases. It should be run before making a new release.

=cut

use strict;
use Config;
BEGIN { require './test.pl' }

if ( $Config{usecrosscompile} ) {
    skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so that tests still pass for people intentionally working on older versions:
# $opt eq '--now'
if ($opt eq '--now')
{
    my $current_year = (gmtime)[5] + 1900;
    is $v_year, $current_year, 'perl -v copyright includes current year';
    is $readme_year, $current_year, 'README copyright includes current year';
}

# Otherwise simply check that the two copyright dates match each other:
else
{
    is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;
sub readme_year
# returns the latest copyright year from the top-level README file
{

    open my $readme, '<', '../README' or die "Opening README failed: $!";

    # The copyright message is the first paragraph:
    local $/ = '';
    my $copyright_msg = <$readme>;

    my ($year) = $copyright_msg =~ /.*(\d{4,})/s
        or die "Year not found in README copyright message '$copyright_msg'';

    $year;
}

sub v_year
# returns the latest copyright year shown in perl -v
{

    my $output = runperl switches => ['-v'];
    my ($year) = $output =~ /copyright 1987.*\b(\d{4,})/i
        or die "Copyright statement not found in perl -v output '$output'';

    $year;
}

1.853 follow-redirects 1.5.10
1.853.1 Available under license :

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1.854 python-defaults 3.7.3-1

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This is the Debian GNU/Linux prepackaged version of the Python programming language. Python was written by Guido van Rossum <guido@cwi.nl> and others.

This package was put together by Klee Dienes <klee@debian.org> from sources from ftp.python.org:/pub/python, based on the Debianization by the previous maintainers Bernd S. Brentrup <bsb@uni-muenster.de> and Bruce Perens.

Current maintainer is Matthias Klose <doko@debian.org> until the final 2.3 version is released.

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--------------------------------------------------------------

A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python
releases have also been GPL-compatible; the table below summarizes the various releases.

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1.857 redux-saga-delay-p 1.1.2

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1.858 icu 67.1-r2

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## Commands to generate dependency files
GEN_DEPS.c=$(CC) -E -MM $(DEFS) $(CPPFLAGS)
GEN_DEPS.cc=$(CXX) -E -MM $(DEFS) $(CPPFLAGS)
## Flags for position independent code
SHAREDLIBCFLAGS = -fPIC
SHAREDLIBCXXFLAGS = -fPIC
SHAREDLIBCPPFLAGS = -DPIC

## Additional flags when building libraries and with threads
THREADSCPPFLAGS = -D_REENTRANT
LIBCPPFLAGS =

## Compiler switch to embed a runtime search path
LD_RPATH=
LD_RPATH_PRE=-Wl,-rpath,

## Compiler switch to embed a library name
LD_SONAME = -Wl,-soname -Wl,$(notdir $(MIDDLE_SO_TARGET))

## Shared library options
LD_SOOPTIONS= -Wl,-Bsymbolic

## Shared object suffix
SO = so

## Non-shared intermediate object suffix
STATIC_O = ao

## Compilation rules
%.$(STATIC_O): $(srcdir)/%.c
$(COMPILE.c) $(STATICCPPFLAGS) $(STATICCFLAGS) -o $@ $<
%:o: $(srcdir)/%.c
$(COMPILE.c) $(DYNAMICCPPFLAGS) $(DYNAMICCFLAGS) -o $@ $<

%.$(STATIC_O): $(srcdir)/%.cpp
$(COMPILE.cc) $(STATICCPPFLAGS) $(STATICCXXFLAGS) -o $@ $<
%:o: $(srcdir)/%.cpp
$(COMPILE.cc) $(DYNAMICCPPFLAGS) $(DYNAMICCXXFLAGS) -o $@ $<

## Dependency rules
%.d: $(srcdir)/%.c
@echo "generating dependency information for $<" 
@$(SHELL) -ec "$ (GEN_DEPS.c) $< \ 
| sed \'s/\(^.*\)\.o[ :]*/\1.o $@ : /g\' \ > $@; \ 
[ -s $@ ] || rm -f $@"

%.d: $(srcdir)/%.cpp
@echo "generating dependency information for $<" 
@$ (SHELL) -ec "$ (GEN_DEPS.cc) $< \ 
| sed \'s/\(^.*\)\.o[ :]*/\1.o $@ : /g\' \ > $@; \ 

## Versioned libraries rules

%.$(SO).$(SO_TARGET_VERSION_MAJOR): %.$(SO).$(SO_TARGET_VERSION)
$(RM) $@ && ln -s ${<F} $@
%.$(SO): %.$(SO).$(SO_TARGET_VERSION_MAJOR)
$(RM) $@ && ln -s {*F}.$(SO).$(SO_TARGET_VERSION) $@

## Bind internal references

# LDflags that pkgdata will use
BIR_LDFLAGS= -Wl,-Bsymbolic

# Dependencies [i.e. map files] for the final library
BIR_DEPS=

## Remove shared library 's'
STATIC_PREFIX_WHEN_USED =
STATIC_PREFIX =

## End BSD-specific setup

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1.873 django 3.2.4
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===========================
Advice for new contributors
===========================

New contributor and not sure what to do? Want to help but just don't know how
to get started? This is the section for you.

.. admonition:: Get up and running!

    If you are new to contributing to Django, the :doc:`/intro/contributing`
tutorial will give you an introduction to the tools and the workflow.

This page contains more general advice on ways you can contribute to Django,
and how to approach that.

If you are looking for a reference on the details of making code contributions,
see the :doc:`/internals/contributing/writing-code/index` documentation.

First steps

Start with these steps to discover Django's development process.

* **Triage tickets**

    If an `unreviewed ticket` reports a bug, try and reproduce it. If you
can reproduce it and it seems valid, make a note that you confirmed the bug
and accept the ticket. Make sure the ticket is filed under the correct
component area. Consider writing a patch that adds a test for the bug's
behavior, even if you don't fix the bug itself. See more at
:ref:`how-can-i-help-with-triaging`

* **Look for tickets that are accepted and review patches to build familiarity"
with the codebase and the process

Mark the appropriate flags if a patch needs docs or tests. Look through the changes a patch makes, and keep an eye out for syntax that is incompatible with older but still supported versions of Python. :doc:`Run the tests </internals/contributing/writing-code/unit-tests>` and make sure they pass. Where possible and relevant, try them out on a database other than SQLite. Leave comments and feedback!

* **Keep old patches up to date**

Oftentimes the codebase will change between a patch being submitted and the time it gets reviewed. Make sure it still applies cleanly and functions as expected. Updating a patch is both useful and important! See more on :doc:`writing-code/submitting-patches`.

* **Write some documentation**

Django's documentation is great but it can always be improved. Did you find a typo? Do you think that something should be clarified? Go ahead and suggest a documentation patch! See also the guide on :doc:`writing-documentation`.

.. note::

   The `reports page`_ contains links to many useful Trac queries, including several that are useful for triaging tickets and reviewing patches as suggested above.

   .. _reports page: https://code.djangoproject.com/wiki/Reports

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Guidelines

As a newcomer on a large project, it’s easy to experience frustration. Here’s some advice to make your work on Django more useful and rewarding.
* **Pick a subject area that you care about, that you are familiar with, or that you want to learn about**

You don't already have to be an expert on the area you want to work on; you become an expert through your ongoing contributions to the code.

* **Analyze tickets' context and history**

Trac isn't an absolute; the context is just as important as the words. When reading Trac, you need to take into account who says things, and when they were said. Support for an idea two years ago doesn't necessarily mean that the idea will still have support. You also need to pay attention to who *hasn't* spoken -- for example, if an experienced contributor hasn't been recently involved in a discussion, then a ticket may not have the support required to get into Django.

* **Start small**

It's easier to get feedback on a little issue than on a big one. See the `easy pickings`.

* **If you're going to engage in a big task, make sure that your idea has support first**

This means getting someone else to confirm that a bug is real before you fix the issue, and ensuring that there's consensus on a proposed feature before you go implementing it.

* **Be bold! Leave feedback!**

Sometimes it can be scary to put your opinion out to the world and say "this ticket is correct" or "this patch needs work", but it's the only way the project moves forward. The contributions of the broad Django community ultimately have a much greater impact than that of any one person. We can't do it without **you**!

* **Err on the side of caution when marking things Ready For Check-in**

If you're really not certain if a ticket is ready, don't mark it as such. Leave a comment instead, letting others know your thoughts. If you're mostly certain, but not completely certain, you might also try asking on IRC to see if someone else can confirm your suspicions.

* **Wait for feedback, and respond to feedback that you receive**

Focus on one or two tickets, see them through from start to finish, and repeat. The shotgun approach of taking on lots of tickets and letting some
fall by the wayside ends up doing more harm than good.

**Be rigorous**

When we say ":pep:`8`, and must have docs and tests", we mean it. If a patch doesn't have docs and tests, there had better be a good reason. Arguments like "I couldn't find any existing tests of this feature" don't carry much weight--while it may be true, that means you have the extra-important job of writing the very first tests for that feature, not that you get a pass from writing tests altogether.

**Be patient**

It's not always easy for your ticket or your patch to be reviewed quickly. This isn't personal. There are a lot of tickets and pull requests to get through.

Keeping your patch up to date is important. Review the ticket on Trac to ensure that the *Needs tests*, *Needs documentation*, and *Patch needs improvement* flags are unchecked once you've addressed all review comments.

Remember that Django has an 8 month release cycle, so there's plenty of time for your patch to be reviewed.

Finally, a well-timed reminder can help. See :ref:`contributing code FAQ <new-contributors-faq>` for ideas here.

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In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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1.883 binutils 2.31.1 16
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Dean Elsner wrote the original gas for vax.  [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.

Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of
Buffalo University and Torbjorn Granlund of the Swedish Institute of
Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS
back end (tc-mips.c, tc-mips.h), and contributed Rose format support
that hasn't been merged in yet.  Ralph Campbell worked with the MIPS
code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors
(tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format
(obj-ieee), was written by Steve Chamberlain of Cygnus Solutions.
Steve also modified the COFF back end (obj-coffbfd) to use BFD for
some low-level operations, for use with the Hitachi, 29k and Zilog
targets.
John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g., jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.

The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.
Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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1.893 libjson-perl 4.02000-1

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative
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Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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Version 3, 29 June 2007

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Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those domains in future versions of the GPL, as needed to protect the freedom of users.

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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1.911 websocketpp 0.7.0

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Bundled Libraries:

****** Base 64 Library (base64/base64.hpp) ******

base64.hpp is a repackaging of the base64.cpp and base64.h files into a single header suitable for use as a header only library. This conversion was done by Peter Thorson (webmaster@zaphoyd.com) in 2012. All modifications to the code are redistributed under the same license as the original, which is listed below.
base64.cpp and base64.h

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Ren Nyffenegger rene.nyffenegger@adp-gmbh.ch

******* SHA1 Library (sha1/sha1.hpp) ******
sha1.hpp is a repackaging of the sha1.cpp and sha1.h files from the shallsha1 library (http://code.google.com/p/smallsha1/) into a single header suitable for use as a header only library. This conversion was done by Peter Thorson (webmaster@zaphoyd.com) in 2013. All modifications to the code are redistributed under the same license as the original, which is listed below.

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md5.hpp is a reformulation of the md5.h and md5.c code from
http://www.opensource.apple.com/source/cups/cups-59/cups/md5.c to allow it to
function as a component of a header only library. This conversion was done by
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L. Peter Deutsch
ghost@aladdin.com

****** UTF8 Validation logic (utf8_validation.hpp) *****

utf8_validation.hpp is adapted from code originally written by Bjoern Hoehrmann
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1.912 lodash-es 4.17.20

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1.913 redux-thunk 2.3.0

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1.920 libestr 0.1.10 2.1

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1.922 mawk 1.3.3 17+b3

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lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
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lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
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slave/kpropd_rpc.c
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* Being respectful of differing viewpoints and experiences
* Gracefully accepting constructive criticism
* Focusing on what is best for the community
* Showing empathy towards other community members

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* The use of sexualized language or imagery and unwelcome sexual attention or advances
* Trolling, insulting/derogatory comments, and personal or political attacks
* Public or private harassment
* Publishing others' private information, such as a physical or electronic address, without explicit permission
* Other conduct which could reasonably be considered inappropriate in a professional setting

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.. _contrib:

=============

click-contrib

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As the userbase of Click grows, more and more major feature requests pop up in Click's bugtracker. As reasonable as it may be for those features to be bundled with Click instead of being a standalone project, many of those requested features are either highly experimental or have unproven practical use, while potentially being a burden to maintain.

This is why click-contrib exists. The GitHub organization is a collection of possibly experimental third-party packages whose featureset does not belong into Click, but also a playground for major features that may be added to Click in the future. It is also meant to coordinate and concentrate effort on writing third-party extensions for Click, and to ease the effort of searching for such extensions. In that sense it could be described as a low-maintenance alternative to extension repositories of other frameworks.

Please note that the quality and stability of those packages may be different than what you expect from Click itself. While published under a common organization, they are still projects separate from Click.

.. _click-contrib: https://github.com/click-contrib/

1.938 ms 2.0.0
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1.939 mbed-tls 2.8.0-1

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libcurl can be built to use a fair amount of various third party libraries, libraries that are written and provided by other parties that are distributed using their own licenses. Even libcurl itself contains code that may cause problems to some. This document attempts to describe what licenses libcurl and the other libraries use and what possible dilemmas linking and mixing them all can lead to for end users.

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One common dilemma is that [GPL](https://www.gnu.org/licenses/gpl.html)
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1.964 isodate 0.6.0

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Metadata-Version: 1.1
Name: isodate
Version: 0.6.0
Summary: An ISO 8601 date/time/duration parser and formatter
Home-page: https://github.com/gweis/isodate/
Author: Gerhard Weis
Author-email: gerhard.weis@proclos.com
License: BSD
Description:
ISO 8601 date/time parser

.. image:: https://travis-ci.org/gweis/isodate.svg?branch=master
   :target: https://travis-ci.org/gweis/isodate
   :alt: Travis-CI
This module implements ISO 8601 date, time and duration parsing. The implementation follows ISO8601:2004 standard, and implements only date/time representations mentioned in the standard. If something is not mentioned there, then it is treated as non-existent, and not as an allowed option.

For instance, ISO8601:2004 never mentions 2 digit years. So, it is not intended by this module to support 2 digit years. (while it may still be valid as ISO date, because it is not explicitly forbidden.)

Another example is, when no time zone information is given for a time, then it should be interpreted as local time, and not UTC.

As this module maps ISO 8601 dates/times to standard Python data types, like *date*, *time*, *datetime* and *timedelta*, it is not possible to convert all possible ISO 8601 dates/times. For instance, dates before 0001-01-01 are not allowed by the Python *date* and *datetime* classes. Additionally fractional seconds are limited to microseconds. That means if the parser finds for instance nanoseconds it will round it to microseconds.

Documentation
--------------

Currently there are four parsing methods available.

* parse_time:
  parses an ISO 8601 time string into a *time* object
* parse_date:
  parses an ISO 8601 date string into a *date* object
* parse_datetime:
  parses an ISO 8601 date-time string into a *datetime* object
* parse_duration:
  parses an ISO 8601 duration string into a *timedelta* or *Duration* object.
* parse_tzinfo:
  parses the time zone info part of an ISO 8601 string into a *tzinfo* object.

As ISO 8601 allows to define durations in years and months, and *timedelta*
does not handle years and months, this module provides a *Duration* class, which can be used almost like a *timedelta* object (with some limitations). However, a *Duration* object can be converted into a *timedelta* object.

There are also ISO formatting methods for all supported data types. Each *xxx_isoformat* method accepts a format parameter. The default format is always the ISO 8601 expanded format. This is the same format used by *datetime.isoformat*:

* time_isoformat:
  Intended to create ISO time strings with default format
  *hh:mm:ssZ*.

* date_isoformat:
  Intended to create ISO date strings with default format
  *yyyy-mm-dd*.

* datetime_isoformat:
  Intended to create ISO date-time strings with default format
  *yyyy-mm-ddThh:mm:ssZ*.

* duration_isoformat:
  Intended to create ISO duration strings with default format
  *PnnYnnMnnDTnnHnnMnnS*.

* tz_isoformat:
  Intended to create ISO time zone strings with default format
  *hh:mm*.

* strftime:
  A re-implementation mostly compatible with Python's *strftime*, but supports only those format strings, which can also be used for dates prior 1900. This method also understands how to format *datetime* and *Duration* instances.

Installation:
-------------

This module can easily be installed with Python standard installation methods.

Either use *python setup.py install* or in case you have *setuptools* or *distribute* available, you can also use *easy_install*.

Limitations:
------------

* The parser accepts several date/time representation which should be invalid according to ISO 8601 standard.

1. for date and time together, this parser accepts a mixture of basic and extended format. e.g. the date could be in basic format, while the time is accepted in extended format.
   It also allows short dates and times in date-time strings.
2. For incomplete dates, the first day is chosen. e.g. 19th century results in a date of
1901-01-01.
3. negative *Duration* and *timedelta* value are not fully supported yet.

Further information:
---------------------

The doc strings and unit tests should provide rather detailed information about the methods and their limitations.

The source release provides a *setup.py* script, which can be used to run the unit tests included.

Source code is available at `<http://github.com/gweis/isodate>`.

CHANGES
=======

0.6.0 (2017-10-13)
------------------

- support incomplete month date (Fabien Loffredo)
- rely on duck typing when doing duration maths
- support `:` as separator in fractional time zones (usrenmae)

0.5.4 (2015-08-06)
------------------

- Fix parsing of Periods (Fabien Bochu)
- Make Duration objects hashable (Geoffrey Fairchild)
- Add multiplication to duration (Reinoud Elhorst)

0.5.1 (2014-11-07)
------------------

- fixed pickling of Duration objects
- raise ISO8601Error when there is no 'T' separator in datetime strings (Adrian Coveney)

0.5.0 (2014-02-23)
------------------

- ISO8601Error are subclasses of ValueError now (Michael Hrivnak)
- improve compatibility across various python variants and versions
- raise exceptions when using fractional years and months in date maths with durations
- renamed method todatetime on Duration objects to totimedelta
0.4.9 (2012-10-30)
-------------

- support pickling FixedOffset instances
- make sure parsed fractional seconds are in microseconds
- add leading zeros when formatting microseconds (Jarom Loveridge)

0.4.8 (2012-05-04)
-------------

- fixed incompatibility of unittests with python 2.5 and 2.6 (runs fine on 2.7 and 3.2)

0.4.7 (2012-01-26)
-------------

- fixed tzinfo formatting (never pass None into tzinfo.utcoffset())

0.4.6 (2012-01-06)
-------------

- added Python 3 compatibility via 2to3

0.4.5 (2012-01-06)
-------------

- made setuptools dependency optional

0.4.4 (2011-04-16)
-------------

- Fixed formatting of microseconds for datetime objects

0.4.3 (2010-10-29)
-------------

- Fixed problem with %P formatting and fractions (supplied by David Brooks)

0.4.2 (2010-10-28)
-------------

- Implemented unary - for Duration (supplied by David Brooks)
- Output fractional seconds with '%P' format. (partly supplied by David Brooks)
0.4.1 (2010-10-13)
-------------------
- fixed bug in comparison between timedelta and Duration.
- fixed precision problem with microseconds (reported by Tommi Virtanen)

0.4.0 (2009-02-09)
-------------------
- added method to parse ISO 8601 time zone strings
- added methods to create ISO 8601 conforming strings

0.3.0 (2009-1-05)
-------------------
- Initial release

TODOs
=====

This to do list contains some thoughts and ideas about missing features, and parts to think about, whether to implement them or not. This list is probably not complete.

Missing features:
-------------------

* time formatting does not allow to create fractional representations.
* parser for ISO intervals.
* currently microseconds are always padded to a length of 6 characters.
  trailing 0s should be optional

Documentation:
--------------

* parse_datetime:
  - complete documentation to show what this function allows, but ISO forbids.
  and vice verse.
  - support other separators between date and time than 'T'

* parse_date:
  - yeardigits should be always greater than 4
  - dates before 0001-01-01 are not supported

* parse_duration:
  - alternative formats are not fully supported due to parse_date restrictions
  - standard duration format is fully supported but not very restrictive.
* Duration:
  - support fractional years and month in calculations
  - implement w3c order relation? (`\<http://www.w3.org/TR/xmlschema-2/#duration-order>`_)
  - refactor to have duration mathematics only at one place.
  - localize \_\_str\_ method (does timedelta do this?)
  - when is a Duration negative?
  - normalize Durations. months [00-12] and years [-inf,+inf]

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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=aarch64-- | FileCheck %s

; A shuffle mask with all undef elements is always legal.

define <4 x i32> @PR41535(<2 x i32> %p1, <2 x i32> %p2) {
    ; CHECK-LABEL: PR41535:
    ; CHECK:       // %bb.0:
    ; CHECK-NEXT:    ext v0.8b, v0.8b, v1.8b, #4
    ; CHECK-NEXT:    mov v0.d[1], v0.d[0]
    ; CHECK-NEXT:    ret
    %cat1 = shufflevector <2 x i32> %p1, <2 x i32> undef, <4 x i32> <i32 undef, i32 1, i32 undef, i32 undef>
    %cat2 = shufflevector <2 x i32> %p2, <2 x i32> undef, <4 x i32> <i32 0, i32 undef, i32 undef, i32 undef>
    %r = shufflevector <4 x i32> %cat1, <4 x i32> %cat2, <4 x i32> <i32 undef, i32 undef, i32 1, i32 4>
    ret <4 x i32> %r
}
define void @autogen_SD4739(i8*) { 
 ; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
    %L34 = load i8, i8* %0
    %Cmp56 = icmp sgt i8 undef, %L34
    br label %CF246

CF246: ; preds = %CF246, %BB
    %S1163 = select i1 %Cmp56, i8 %L34, i8 undef
    br i1 undef, label %CF246, label %CF248

CF248: ; preds = %CF248, %CF246
    store i8 %S1163, i8* %0
    br label %CF248
}
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define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
  ; CHECK-LABEL: test_sitofp_fixed:

  ; First, extend each i32 to i64
  ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
  ; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0
; Next, convert each to double.
; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]

; CHECK: ret
%flt = sitofp <16 x i32> %in to <16 x double>
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret
%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res
}

; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
; 
; D31946
; Check that we dont end up with the ""LLVM ERROR: Cannot select"" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
; CHECK-LABEL: TestSelect:
; CHECK:
; CHECK-NEXT: pushq %rbx
; CHECK-NEXT: .cfi_def_cfa Offset 16
; CHECK-NEXT: subq $32, %rsp
; CHECK-NEXT: .cfi_def_cfa Offset 48
; CHECK-NEXT: .cfi_offset %rbx, -16
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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
   ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
   ret void
}

define internal void @innerNoAttribute() {
   ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
   call void @innerLarge()
   ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
   call void @innerSmall()
   ret void
}
define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
  call void @innerSmall()
  ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
  call void @innerNoAttribute()
  ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
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; RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none-musleabi"
; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]?]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]?]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]?]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]?]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %2 = inttoptr i32 %[[M1:const_mat[0-9]?]] = add i32 %[[C2]], 4
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %4 = inttoptr i32 %[[M2:const_mat[0-9]?]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %8 = inttoptr i32 %[[M3:const_mat[0-9]?]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %11 = add i32 %[[M4:const_mat[0-9]?]], 31
; CHECK-NEXT: %12 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %13 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
; CHECK-NEXT: %14 = add i32 %[[M5:const_mat[0-9]?]], 7
; CHECK-NEXT: %15 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %16 = add i32 %[[M6:const_mat[0-9]?]], 80
; CHECK-NEXT: %17 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8 %14, i8** @goo
@goo = global i8* undef

define void @foo_i8() {
entry:
%0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
%1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
%2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
%3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
%4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
store i8 %4, i8* inttoptr(i32 805873688 to i8*)
store i8 %3, i8* inttoptr(i32 805873719 to i8*)
store i8 %2, i8* inttoptr(i32 805873720 to i8*)
store i8 %1, i8* inttoptr(i32 805874688 to i8*)
store i8 %0, i8* inttoptr(i32 805874692 to i8*)
store i8 %2, i8* inttoptr(i32 805874719 to i8*)
store i8 %3, i8* inttoptr(i32 805874720 to i8*)
store i8 %4, i8* inttoptr(i32 805874727 to i8*)
store i8 %0, i8* inttoptr(i32 805874688 to i8*)
store i8 %1, i8* inttoptr(i32 805874692 to i8*)
store i8 %2, i8* inttoptr(i32 805874719 to i8*)
store i8 %3, i8* inttoptr(i32 805874720 to i8*)
store i8 %4, i8* inttoptr(i32 805874727 to i8*)
}

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store i8 %1, i8* inttoptr(i32 805873727 to i8*)
store i8* inttoptr(i32 805874800 to i8*), i8** @goo
ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i16*
; CHECK-NEXT: %5 = load volatile i16, i16* %4, align 2
; CHECK-NEXT: %6 = inttoptr i32 %[[M3]] to i16*
; CHECK-NEXT: %7 = load volatile i16, i16* %6, align 2
; CHECK-NEXT: %8 = inttoptr i32 %[[C1]] to i16*
; CHECK-NEXT: %9 = load volatile i16, i16* %8, align 2
; CHECK-NEXT: %10 = inttoptr i32 %[[M4]] to i16*
; CHECK-NEXT: %11 = load volatile i16, i16* %10, align 2
define void @foo_i16() {
entry:
%0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
%1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
%2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
%3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
%4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
%5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
ret void
}

; Check that for i32 type, the maximum legal offset is 124.
; CHECK: foo_i32
; CHECK-DAG: %0 = inttoptr i32 %[[C2]] to i32*
; CHECK-NEXT: %1 = load volatile i32, i32* %0, align 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i32*
; CHECK-NEXT: %3 = load volatile i32, i32* %2, align 4
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i32*

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define void @foo_i32() {
  entry:
    %0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
    %1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
    %2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
    %3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
    %4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
    %5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
    ret void
  }

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; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --scrub-attributes
; RUN: opt -S -argpromotion < %s | FileCheck %s
; RUN: opt -S -passes=argpromotion < %s | FileCheck %s
; Test that we only promote arguments when the caller/callee have compatible function attributes.

target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
    ; CHECK-LABEL: define {{\[^\@\]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
    ; CHECK-SAME: (<8 x i64>* \[\[ARG:%.*\]\], <8 x i64> \[\[ARG1_VAL:%.*\]\])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: store <8 x i64> \[\[ARG1_VAL\]\], <8 x i64>* \[\[ARG\]\]
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
  ; CHECK-LABEL: define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512
  ; CHECK-SAME: ("<8 x i64>* [[ARG:]]")
  ; CHECK-NEXT: bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ; CHECK-LABEL: define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
  ; CHECK-SAME: (<"<8 x i64>* [[ARG:]], <8 x i64>* [[ARG1_VAL:]]")
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT: ret void
  
  ; This should promote
  define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
    ; CHECK-LABEL: define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (<"<8 x i64>* [[ARG:]], <8 x i64>* [[ARG1_VAL:]]")
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
    ; CHECK-NEXT: ret void
    
    ; bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {  
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])  
; CHECK-NEXT: bb:  
  bb:  
%tmp = load <8 x i64>, <8 x i64>* %arg  
store <8 x i64> %tmp, <8 x i64>* %arg  
ret void  
};  
}  

; This should promote  
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg,  
<8 x i64>* readonly %arg1) #1 {  
; CHECK-LABEL: define {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64> [[ARG1_VAL:%.*]])  
; CHECK-NEXT: bb:  
  bb:  
%tmp = load <8 x i64>, <8 x i64>* %arg1  
store <8 x i64> %tmp, <8 x i64>* %arg  
ret void  
}  

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {  
; CHECK-LABEL: define {{[^@]+}}@avx512_legal512_prefer512_call_avx512_legal512_prefer256  
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])  
; CHECK-NEXT: bb:  
  bb:  
%tmp = alloca <8 x i64>, align 32  
%tmp2 = alloca <8 x i64>, align 32  
%tmp3 = bitcast <8 x i64>* %tmp to i8*  
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)  
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)  
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32  
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2  
ret void  
}  


i64* [[TMP2]], <8 x i64>* [[TMP_VAL]]
; CHECK-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
; CHECK-LABEL: define {{\[^@]+}}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* readonly [[ARG1:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:  [[TMP:%.*]] = load <8 x i64>, <8 x i64>* %arg1
; CHECK-NEXT:  store <8 x i64> %tmp, <8 x i64>* %arg
; CHECK-NEXT:  ret void
;
bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
; CHECK-LABEL: define {{\[^@]+}}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
; CHECK-NEXT:  bb:
; CHECK-NEXT:  [[TMP:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:  [[TMP2:%.*]] = alloca <8 x i64>, align 32
; CHECK-NEXT:  [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; CHECK-NEXT:  call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; CHECK-NEXT:  call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
; CHECK-NEXT:  [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
; CHECK-NEXT:  store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; CHECK-NEXT:  ret void
;
bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should not promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {  
  define {{[^@]+}}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
  define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #1 {  
  define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal512_prefer256

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
  ; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]], <8 x i64>* [[ARG1_VAL:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
  ; CHECK-NEXT: ret void
  
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {
  ; CHECK-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
  ; CHECK-SAME: (<8 x i64>* [[ARG:%.*]])
  ; CHECK-NEXT: bb:
  ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
  ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
  ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
  ; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
  ; CHECK-NEXT: call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* [[TMP2]], <8 x i64>* [[TMP_VAL]])
  ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
  ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
  ; CHECK-NEXT: ret void
  
  bb:
  %tmp = alloca <8 x i64>, align 32
  %tmp2 = alloca <8 x i64>, align 32
  %tmp3 = bitcast <8 x i64>* %tmp to i8*
  call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
  call fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
  %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
  store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
  ret void
}

; This should promote

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define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(8 x i64)* %arg, 8 x i64>* %arg1) #4 {
    ; CHECK-LABEL: define {{[^@]+}}@callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
    ; CHECK-SAME: (8 x i64)* [[ARG:%.*]], 8 x i64>* [[ARG1_VAL:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: store <8 x i64> [[ARG1_VAL]], <8 x i64>* [[ARG]]
    ; CHECK-NEXT: ret void
    
    bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg1
    store <8 x i64> %tmp, <8 x i64>* %arg
    ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(8 x i64)* %arg) #3 {
    ; CHECK-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal256_prefer256
    ; CHECK-SAME: (8 x i64)* [[ARG:%.*]])
    ; CHECK-NEXT: bb:
    ; CHECK-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
    ; CHECK-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    ; CHECK-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
    ; CHECK-NEXT: [[TMP_VAL:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]]
    ; CHECK-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(8 x i64)*
                  [[TMP2]], <8 x i64> [[TMP_VAL]])
    ; CHECK-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 32
    ; CHECK-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
    ; CHECK-NEXT: ret void
    
    bb:
    %tmp = load <8 x i64>, <8 x i64>* %arg
    %tmp2 = alloca <8 x i64>, align 32
    %tmp3 = bitcast <8 x i64>* %tmp to i8*
    call void @llvm.memset.p0i8.i64(i8* %tmp3, i8 0, i64 32, i1 false)
    call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(8 x i64)* %tmp2, <8 x i64>* %tmp
    %tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
    store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
    ret void
}

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32* %Y) #2 {
    ; CHECK-LABEL: define {{[^@]+}}@scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
    ; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
    ; CHECK-NEXT: [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
define i32 @scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal256_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32* [[B:%.*]]
; CHECK-NEXT: [[A:%.*]] = alloca i32
; CHECK-NEXT: store i32 1, i32* [[A]]
; CHECK-NEXT: [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT: [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT: [[C:%.*]] = call i32 @scalar_callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT: ret i32 [[C]]

; If the arguments are scalar, its ok to promote.
define internal i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256_prefer256(i32* %X, i32* %Y) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32 [[X_VAL:%.*]], i32 [[Y_VAL:%.*]])
; CHECK-NEXT: [[C:%.*]] = add i32 [[X_VAL]], [[Y_VAL]]
; CHECK-NEXT: ret i32 [[C]]

; %A = alloca i32
store i32 1, i32* %A
%C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32* %A, i32* %B)
ret i32 %C
}

define i32 @scalar_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32* %B) #2 {
; CHECK-LABEL: define {{[^@]+}}@scalar_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; CHECK-SAME: (i32* [[B:%.*]])
; CHECK-NEXT: [[A:%.*]] = alloca i32
; CHECK-NEXT: store i32 1, i32* [[A]]
; CHECK-NEXT: [[A_VAL:%.*]] = load i32, i32* [[A]]
; CHECK-NEXT: [[B_VAL:%.*]] = load i32, i32* [[B]]
; CHECK-NEXT: [[C:%.*]] = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT: ret i32 [[C]]

; %A = load i32, i32* %X
%B = load i32, i32* %Y
%C = add i32 %A, %B
ret i32 %C
}

@scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32 [[A_VAL]], i32 [[B_VAL]])
; CHECK-NEXT:    ret i32 [[C]]
;
%A = alloca i32
store i32 1, i32* %A

%C = call i32 @scalar_callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(i32* %A, i32* %B)
ret i32 %C

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit | FileCheck %s
; --check-prefixes=CHECK,CHECK-AVX512
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mcpu=skylake-avx512 -mattr=prefer-256-bit,avx512vbmi | FileCheck %s
; --check-prefixes=CHECK,CHECK-VBMI
; Make sure CPUs default to prefer-256-bit. avx512vnni isn't interesting as it just adds an isel peephole for
vpmaddwd+vpaddd

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This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {  
; CHECK-LABEL: add256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT:  vpaddd 32(%rsi), %ymm0, %ymm1
; CHECK-NEXT:  vpaddd (%rsi), %ymm0, %ymm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}

define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {  
; CHECK-LABEL: add512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vpaddd (%rsi), %zmm0, %zmm0
; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}
define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
; CHECK-LABEL: avg_v64i8_256:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpavgb (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpavgb 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vmovdqu %ymm1, (%rax)
; CHECK-NEXT: vmovdqu %ymm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:
# %bb.0:
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,
i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32
1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1,

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define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: pmaddwd_32_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:    vpmaddwd 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT:    vpmaddwd (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT:    vmovdqa %ymm0, 32(%rdx)
    ; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    %odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
    %even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: pmaddwd_32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT:    vmovdwd64 (%rsi), %zmm1, %zmm1
    ; CHECK-NEXT:    vpmaddwd64 (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT:    vmovdqa64 %zmm0, %zmm1, 32(%rdx)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    ret void
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: psubus_64i8_max_256:
 ; CHECK:      # %bb.0:
 ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
 ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
 ; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1
 ; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
 ; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %x = load <64 x i8>, <64 x i8>* %xptr
 %y = load <64 x i8>, <64 x i8>* %yptr
 %cmp = icmp ult <64 x i8> %x, %y
 %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
 %res = sub <64 x i8> %max, %y
 store <64 x i8> %res, <64 x i8>* %zptr
 ret void
}

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
 ; CHECK-LABEL: psubus_64i8_max_512:
 ; CHECK:      # %bb.0:
 ; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
 ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
 ; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
 ; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
 %x = load <64 x i8>, <64 x i8>* %xptr
 %y = load <64 x i8>, <64 x i8>* %yptr
 %cmp = icmp ult <64 x i8> %x, %y
 %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
 %res = sub <64 x i8> %max, %y
 store <64 x i8> %res, <64 x i8>* %zptr
define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: _Z9test_charPcS_i_256:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:    movl %edx, %eax
    ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT:    xorl %ecx, %ecx
    ; CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT:    vpxor %xmm2, %xmm2, %xmm2
    ; CHECK-NEXT:    .p2align 4, 0x90
    ; CHECK-NEXT:  .LBB8_1: # %vector.body
    ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT:    vpmovsxbw 16(%rdi,%rcx), %ymm3
    ; CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %ymm4
    ; CHECK-NEXT:    vpmovsxbw 16(%rsi,%rcx), %ymm5
    ; CHECK-NEXT:    vpmaddwd %ymm3, %ymm5, %ymm3
    ; CHECK-NEXT:    vpmaddwd %ymm2, %ymm3, %ymm2
    ; CHECK-NEXT:    vpmaddwd %ymm1, %ymm3, %ymm1
    ; CHECK-NEXT:    addq $32, %rcx
    ; CHECK-NEXT:    cmpq %rcx, %rax
    ; CHECK-NEXT:    jne .LBB8_1
    ; CHECK-NEXT:  # %bb.2: # %middle.block
    ; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm1
    ; CHECK-NEXT:    vpaddd %ymm0, %ymm2, %ymm0
    ; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
    ; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
    ; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
    ; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
    ; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
    ; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
    ; CHECK-NEXT:    vmovd %xmm0, %eax
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq

    entry:
    %3 = zext i32 %2 to i64
    br label %vector.body

    %vector.body:
    %index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
    %vec.phi = phi <32 x i32> [ %index.next, %vector.body ], [ zeroinitializer, %entry ]
    %4 = getelementptr inbounds i8, i8* %0, i64 %index
    %5 = bitcast i8* %4 to <32 x i8>*

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define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: _Z9test_charPcS_i_512:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:    movl %edx, %eax
    ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
    ;
    ; middle:block:
    %rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
    %bin.rdx1 = add <32 x i32> %11, <32 x i32> undef
    %rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
    %bin.rdx = add <32 x i32> %bin.rdx1, <32 x i32> undef
    %rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
    %bin.rdx32 = add <32 x i32> %bin.rdx, <32 x i32> undef
    %rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
    %bin.rdx18 = add <32 x i32> %bin.rdx32, <32 x i32> undef
    %rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
    %bin.rdx20 = add <32 x i32> %bin.rdx18, <32 x i32> undef
    %13 = extractelement <32 x i32> %bin.rdx20, i32 0
    ret i32 %13
}

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entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%index.next = add i64 %index, 32
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%wide.load12 = load <32 x i32>, <32 x i32>* %11, align 1
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20,
i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
; CHECK-NEXT:  vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpshufd [{.*#+}] %xmm1 = %xmm0[2,3,0,1]
; CHECK-NEXT:  vpadd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:  vpshufd [{.*#+}] %xmm1 = %xmm0[1,1,2,3]
; CHECK-NEXT:  vmovd %xmm0, %eax
; CHECK-NEXT:  vzeroupper
entry:
  br label %vector.body

type.vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitialize, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
  %2 = zext <16 x i8> %wide.load to <16 x i32>
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = zext <16 x i8> %wide.load1 to <16 x i32>
  %6 = sub nsw <16 x i32> %2, %5
  %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
  %8 = sub nsw <16 x i32> zeroinitialize, %6
  %9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
  %10 = add nsw <16 x i32> %9, %vec.phi
  %index.next = add i64 %index, 1024
  br i1 %11, label %middle.block, label %vector.body

middle.block:
  %rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx = add <16 x i32> %10, %rdx.shuf
  %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
  %rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
  %rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}

define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
; CHECK-LABEL: sad_16i8_512:
; CHECK:    # %bb.0: # %entry
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: movq $-1024, %rax # imm = 0FC00
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT:  .LBB11_1: # %vector.body
; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm1
; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm1, %xmm1
; CHECK-NEXT:    vpadd %zmm0, %zmm1, %zmm0
; CHECK-NEXT:    addq $4, %rax
; CHECK-NEXT:    jne .LBB11_1
; CHECK-NEXT:  # %bb.2: # %middle.block
; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm1
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT:    vpaddd %xmm1, %xmm0, %xmm0
; CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
entry:
br label %vector.body

vector.body:
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, %i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%rdx.shuf = shufflevector <16 x i32> %10, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %10, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12

define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: sbto16f32_256:
; CHECK:    # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT:    kshiftrw $8, %k0, %k1
; CHECK-NEXT:    vpmovm2d %k1, %ymm0
; CHECK-NEXT:    vcvtqd2ps %ymm0, %ymm0
; CHECK-NEXT:    vpmovm2d %k0, %ymm1
; CHECK-NEXT:    vcvtqd2ps %ymm1, %ymm1
; CHECK-NEXT:    vmovaps %ymm1, (%rdi)
; CHECK-NEXT:    vmovaps %ymm0, 32(%rdi)
; CHECK-NEXT:    vzzeroupper
; CHECK-NEXT:    retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: sbto16f32_512:
; CHECK:    # %bb.0:
; CHECK-NEXT:    vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vcvtq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}
define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res)  "min-legal-vector-width"="256" { ; CHECK-LABEL: sbto16f64_256:
; CHECK:  # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vcvtq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vcvtq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, (%rdi)
; CHECK-NEXT: vmovaps %ymm0, 96(%rdi)
; CHECK-NEXT: vmovaps %ymm1, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}
define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res)  "min-legal-vector-width"="512" { ; CHECK-LABEL: sbto16f64_512:
; CHECK:  # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vcvtq2pd %ymm0, %zmm1
; CHECK-NEXT: vextracti64x4 $1, %zmm0, %ymm0
; CHECK-NEXT: vcvtq2pd %ymm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, 64(%rdi)
; CHECK-NEXT: vmovaps %zmm1, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = sitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: ubto16f32_256:
 ; CHECK:       # %bb.0:
 ; CHECK-NEXT: vpmovw2m %ymm0, %k0
 ; CHECK-NEXT: kshiftrw $8, %k0, %k1
 ; CHECK-NEXT: vpmovmd %k1, %ymm0
 ; CHECK-NEXT: vpmovmd %k0, %ymm0
 ; CHECK-NEXT: vpmovmd %k1, %ymm1
 ; CHECK-NEXT: vpmovmd %k0, %ymm1
 ; CHECK-NEXT: vmovaps %ymm1, (%rdi)
 ; CHECK-NEXT: vmovaps %ymm0, 32(%rdi)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
 ; CHECK-LABEL: ubto16f32_512:
 ; CHECK:       # %bb.0:
 ; CHECK-NEXT: vpmovw2m %zmm0, %k0
 ; CHECK-NEXT: vpmovmd %k0, %zmm0
 ; CHECK-NEXT: vpmovmd %k1, %zmm0
 ; CHECK-NEXT: kshiftrw $8, %k0, %k1
 ; CHECK-NEXT: vpmovmd %k1, %zmm1
 ; CHECK-NEXT: vpmovmd %k0, %zmm1
 ; CHECK-NEXT: vpmovaps %zmm1, (%rdi)
 ; CHECK-NEXT: vpmovaps %zmm0, 32(%rdi)
 ; CHECK-NEXT: vzeroupper
 ; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
 ; CHECK-LABEL: ubto16f64_256:
 ; CHECK:       # %bb.0:
 ; CHECK-NEXT: vpmovw2m %ymm0, %k0
 ; CHECK-NEXT: kshiftrw $8, %k0, %k1

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {  
; CHECK-LABEL: test_16f32tosb_256:  
; CHECK:       # %bb.0:  
; CHECK-NEXT:   vcvtps2dq (%rdi), %ymm1  
; CHECK-NEXT:   vpmovd2m %ymm0, %k0  
; CHECK-NEXT:   vcvtps2dq 32(%rdi), %ymm1  
; CHECK-NEXT:   vpmovd2m %ymm1, %k1  
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1  
; CHECK-NEXT:   vpmovd2m %zmm1, %k1  
; CHECK-NEXT:   vmovdqu16 %ymm1, %ymm0 [%k1] {z}  
; CHECK-NEXT:   retq  
%a = load <16 x float>, <16 x float>* %ptr  
%mask = fptosi <16 x float> %a to <16 x i1>  
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
ret <16 x i16> %select  
}  
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
; CHECK-LABEL: test_16f32tosb_512:  
; CHECK:       # %bb.0:  
; CHECK-NEXT:   vcvtps2dq (%rdi), %zmm1  
; CHECK-NEXT:   vpmovd2m %zmm1, %k1  
; CHECK-NEXT:   vcvtps2dq 32(%rdi), %ymm1  
; CHECK-NEXT:   vpmovd2m %zmm1, %k1  
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1  
; CHECK-NEXT:   vpmovdqu16 %ymm1, %ymm0 [%k1] {z}  
; CHECK-NEXT:   retq  
%a = load <16 x float>, <16 x float>* %ptr  
%mask = fptoui <16 x float> %a to <16 x i1>  
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
ret <16 x i16> %select  
}  
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
; CHECK-LABEL: test_16f32tosb_512:  
; CHECK:       # %bb.0:  
; CHECK-NEXT:   vcvtps2dq 32(%rdi), %ymm1  
; CHECK-NEXT:   vpmovd2m %ymm1, %k1  
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1  
; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 [%k1] {z}  
; CHECK-NEXT:   retq  
%a = load <16 x float>, <16 x float>* %ptr  
%mask = fptoui <16 x float> %a to <16 x i1>  
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
ret <16 x i16> %select  
}  
define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
; CHECK-LABEL: test_16f32tosb_512:  
; CHECK:       # %bb.0:  
; CHECK-NEXT:   vcvtps2dq 32(%rdi), %ymm1  
; CHECK-NEXT:   vpmovd2m %ymm1, %k1  
; CHECK-NEXT:   kunpckbw %k0, %k1, %k1  
; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 [%k1] {z}  
; CHECK-NEXT:   retq  
%a = load <16 x float>, <16 x float>* %ptr  
%mask = fptoui <16 x float> %a to <16 x i1>  
%select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
ret <16 x i16> %select  
}
; CHECK-NEXT:  vcvtps2dq (%rdi), %zmm1
; CHECK-NEXT:  vmovd2m %zmm1, %k1
; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 [%k1] {z}
; CHECK-NEXT:  retq

%a = load<16 x float>, <16 x float>* %ptr
%mask = fptosi<16 x float> %a to <16 x i1>
%select = select<16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
ret<16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL: mul256:
; CHECK-AVX512:  # %bb.0:
; CHECK-AVX512-NEXT:  vmovdqa (%rdi), %ymm0
; CHECK-AVX512-NEXT:  vmovdqa (%rsi), %ymm1
; CHECK-AVX512-NEXT:  vmovdqa (%rdx), %ymm2
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm4 =
    ymm3\[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31\]
; CHECK-AVX512-NEXT:  vpmullw %ymm4, %ymm4, %ymm4
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm5 =
    ymm1\[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31\]
; CHECK-AVX512-NEXT:  vpmullw %ymm5, %ymm5, %ymm5
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm4 =
    ymm3\[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23\]
; CHECK-AVX512-NEXT:  vpmullw %ymm4, %ymm4, %ymm4
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm3 =
    ymm1\[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23\]
; CHECK-AVX512-NEXT:  vpmullw %ymm3, %ymm3, %ymm3
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm1 =
    ymm0\[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23\]
; CHECK-AVX512-NEXT:  vpmullw %ymm0, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm2 =
    ymm2\[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23\]
; CHECK-AVX512-NEXT:  vpmullw %ymm2, %ymm2, %ymm2
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} ymm0 =
    ymm0\[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23\]
; CHECK-AVX512-NEXT:  vpmullw %ymm0, %ymm0, %ymm0
; CHECK-AVX512-NEXT:  vpunpckhww {{.*#+}} %rdx
; CHECK-AVX512-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-AVX512-NEXT:  vzeroupper
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" {
    %d = load <64 x i8>, <64 x i8>* %a
    %e = load <64 x i8>, <64 x i8>* %b
    %f = mul <64 x i8> %d, %e
    store <64 x i8> %f, <64 x i8>* %c
    retq
}

define void @mul256 {
    %d = load <64 x i8>, <64 x i8>* %a
    %e = load <64 x i8>, <64 x i8>* %b
    %f = mul <64 x i8> %d, %e
    store <64 x i8> %f, <64 x i8>* %c
    retq
}

define void @mul128 {
    %d = load <64 x i8>, <64 x i8>* %a
    %e = load <64 x i8>, <64 x i8>* %b
    %f = mul <64 x i8> %d, %e
    store <64 x i8> %f, <64 x i8>* %c
    retq
}

define void @mul64 {
    %d = load <64 x i8>, <64 x i8>* %a
    %e = load <64 x i8>, <64 x i8>* %b
    %f = mul <64 x i8> %d, %e
    store <64 x i8> %f, <64 x i8>* %c
    retq
}
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} zmm2 =
  zmm1[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
  2,42,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-AVX512-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
  zmm0[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
  2,42,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-AVX512-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-AVX512-NEXT:  vmovdqa64 {{.*#+}} zmm3 =
; CHECK-AVX512-NEXT:  vpandq %zmm3, %zmm2, %zmm2
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} zmm1 =
  zmm1[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35
 ,36,36,37,37,38,38,39,39,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61
 ,62,62,63,63]
; CHECK-AVX512-NEXT:  vpunpcklbw {{.*#+}} zmm0 =
  zmm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35
 ,36,36,37,37,38,38,39,39,40,40,41,41,42,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61
 ,61,62,62,63,63]
; CHECK-AVX512-NEXT:  vpmullw %zmm1, %zmm0, %zmm0
; CHECK-AVX512-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-AVX512-NEXT:  vzeroupper
; CHECK-AVX512-NEXT:  retq

; CHECK-VBMI-LABEL: mul512:
; CHECK-VBMI:     # bb.0:
; CHECK-VBMI-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-VBMI-NEXT:  vmovdqa64 (%rsi), %zmm1
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} zmm2 =
  zmm1[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
  2,42,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} zmm3 =
  zmm0[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4
  2,42,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
; CHECK-VBMI-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-VBMI-NEXT:  vpackuswb %zmm2, %zmm0, %zmm0
; CHECK-VBMI-NEXT:  vmovdqa64 %zmm0, (%rdx)
; CHECK-VBMI-NEXT:  vzeroupper
; This threw an assertion at one point.
define <4 x i32> @mload_v4i32(<4 x i32> %trigger, <4 x i32>* %addr, <4 x i32> %dst) "min-legal-vector-width"="256" {  
; CHECK-LABEL: mload_v4i32:  
; CHECK:    # %bb.0:  
; CHECK-NEXT:    vptestnmd %xmm0, %xmm0, %k1  
; CHECK-NEXT:    vpblendmd (%rdi), %xmm1, %xmm0 {%k1}  
; CHECK-NEXT:    retq  
%mask = icmp eq <4 x i32> %trigger, zeroinitializer  
%res = call <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>* %addr, i32 4, <4 x i31> %mask, <4 x i32> %dst)  
ret <4 x i32> %res  
}  
declare <4 x i32> @llvm.masked.load.v4i32.p0v4i32(<4 x i32>*, i32, <4 x i1>, <4 x i32>)
define <16 x i32> @trunc_v16i64_v16i32(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
; CHECK-LABEL: trunc_v16i64_v16i32:  
; CHECK:    # %bb.0:  
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0  
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1  
; CHECK-NEXT:    vmovdqa 64(%rdi), %ymm2  
; CHECK-NEXT:    vmovdqa 96(%rdi), %ymm3  
; CHECK-NEXT:    vpmovqd %ymm0, %xmm0  
; CHECK-NEXT:    vpmovqd %ymm1, %xmm1  
; CHECK-NEXT:    vpmovqd %ymm2, %xmm2  
; CHECK-NEXT:    vpmovqd %ymm3, %xmm3  
; CHECK-NEXT:    vinserti128 $1, %xmm1, %ymm0, %ymm0  
; CHECK-NEXT:    vpmovqd %ymm2, %xmm1  
; CHECK-NEXT:    vinserti128 $1, %xmm2, %ymm1, %ymm1  
; CHECK-NEXT:    vpmovqd %ymm3, %xmm2  
; CHECK-NEXT:    vinserti128 $1, %xmm2, %ymm1, %ymm1  
; CHECK-NEXT:    retq  
%a = load <16 x i64>, <16 x i64>* %x  
%b = trunc <16 x i64> %a to <16 x i32>  
ret <16 x i32> %b  
}  
define <16 x i8> @trunc_v16i64_v16i8(<16 x i64>* %x) nounwind "min-legal-vector-width"="256" {  
; CHECK-LABEL: trunc_v16i64_v16i8:  
; CHECK:    # %bb.0:  
; CHECK-NEXT:    vmovdqa (%rdi), %ymm0  
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1  
; CHECK-NEXT:    vmovdqa 64(%rdi), %ymm2
define <8 x i32> @trunc_v8i64_v8i32_zeroes(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v8i64_v8i32_zeroes:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vpsrlq $48, 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpsrlq $48, (%rdi), %ymm2
    ; CHECK-NEXT:  vmovdqa [%.*#+], ymm0
    ; CHECK-NEXT:  vpermi2w 32(%rdi), %ymm1, %ymm0
    ; CHECK-NEXT:  retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = trunc <8 x i64> %a to <8 x i16>
    ret <8 x i16> %b
}

define <16 x i16> @trunc_v16i32_v16i16_zeroes(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_v16i32_v16i16_zeroes:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vpmovqw %ymm1, %xmm1
    ; CHECK-NEXT:  vpmovqw %ymm0, %xmm0
    ; CHECK-NEXT:  vunpcklqdq [%.*#+] ymm0 = xmm0[0],ymm1[0]
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %a = load <8 x i64>, <8 x i64>* %x
    %b = trunc <8 x i64> %a to <8 x i16>
    ret <8 x i16> %b
}

define <32 x i8> @trunc_v32i16_v32i8_zeroes(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
    ; CHECK-AVX512-LABEL: trunc_v32i16_v32i8_zeroes:
    ; CHECK-AVX512:       # %bb.0:
    ; CHECK-AVX512-NEXT:  vpsrlw $8, 32(%rdi), %ymm0
    ; CHECK-AVX512-NEXT:  vpsrlw $8, (%rdi), %ymm1
    ; CHECK-AVX512-NEXT:  vpackuswb %ymm0, %ymm1, %ymm0
    ; CHECK-AVX512-NEXT:  vpermq [%.*#+] ymm0 = ymm0[0,2,1,3]
    ; CHECK-AVX512-NEXT:  retq
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; CHECK-VBMI-LABEL:  trunc_v32i16_v32i8_zeroes:
; CHECK-VBMI:       # %bb.0:
; CHECK-VBMI-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-VBMI-NEXT:  vmovdqa { [...16] } ymm0 =
   [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63]
; CHECK-VBMI-NEXT:  vperm2b 32(%rdi), %ymm1, %ymm0
; CHECK-VBMI-NEXT:  retq

%a = load <32 x i16>, <32 x i16>* %x
%b = lshr <32 x i16> %a, <i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8, i16 8>
%c = trunc <32 x i16> %b to <32 x i8>
ret <32 x i8> %c
}

define <8 x i32> @trunc_v8i64_v8i32_sign(<8 x i64>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL:  trunc_v8i64_v8i32_sign:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpsraq $48, 32(%rdi), %ymm0
; CHECK-NEXT:  vpsraq $48, (%rdi), %ymm1
; CHECK-NEXT:  vpmovqd %ymm1, %xmm1
; CHECK-NEXT:  vpmovqd %ymm0, %xmm0
; CHECK-NEXT:  vinsertd $1, %xmm0, %ymm1, %ymm0
; CHECK-NEXT:  retq
%a = load <8 x i64>, <8 x i64>* %x
%b = ashr <8 x i64> %a, <i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48, i64 48>
%c = trunc <8 x i64> %b to <8 x i32>
ret <8 x i32> %c
}

define <16 x i16> @trunc_v16i32_v16i16_sign(<16 x i32>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-LABEL:  trunc_v16i32_v16i16_sign:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa (%rdi), %ymm1
; CHECK-NEXT:  vmovdqa { [...16] } ymm0 = [1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31]
; CHECK-NEXT:  vperm2w 32(%rdi), %ymm1, %ymm0
; CHECK-NEXT:  retq
%a = load <16 x i32>, <16 x i32>* %x
%b = ashr <16 x i32> %a, <i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16, i32 16>
%c = trunc <16 x i32> %b to <16 x i16>
ret <16 x i16> %c
}

define <32 x i8> @trunc_v32i16_v32i8_sign(<32 x i16>* %x) nounwind "min-legal-vector-width"="256" {
; CHECK-AVX512-LABEL:  trunc_v32i16_v32i8_sign:
; CHECK-AVX512:       # %bb.0:
; CHECK-AVX512-NEXT:  vpsraw $8, 32(%rdi), %ymm0
define void @zext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: zext_v16i8_v16i64:
    ; CHECK:    # %bb.0:
    ; CHECK-NEXT:    vpmovzxbw {{.*#+}} ymm1 =
        xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,
        xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
    ; CHECK-NEXT:    vpsrldq {{.*#+}} ymm2 =
        xmm2[0],zero,zero,xmm2[1],zero,zero,zero,xmm2[2],zero,zero,zero,xmm2[3],zero,zero,zero
    ; CHECK-NEXT:    vextracti128 $1, %ymmm1, %xmm1
    ; CHECK-NEXT:    vpsrldq {{.*#+}} ymm3 =
        xmm3[0],zero,zero,xmm3[1],zero,zero,zero,xmm3[2],zero,zero,zero,xmm3[3],zero,zero,zero
    ; CHECK-NEXT:    vpmovzxwq {{.*#+}} ymm1 =
        xmm1[0],zero,zero,xmm1[1],zero,zero,zero,xmm1[2],zero,zero,zero,xmm1[3],zero,zero,zero
    ; CHECK-NEXT:    vpmovzxbq {{.*#+}} ymm0 =
        xmm0[0],zero,zero,zero,xmm0[1],zero,zero,zero,xmm0[2],zero,zero,zero,xmm0[3],zero,zero,zero
    ; CHECK-NEXT:    vpmovdqa %ymmm0, (%rdi)
    ; CHECK-NEXT:    vpmovdqa %ymmm1, 64(%rdi)
    ; CHECK-NEXT:    vpmovdqa %ymmm3, 96(%rdi)
    ; CHECK-NEXT:    vpmovdqa %ymmm2, 32(%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %a = zext <16 x i8> %x to <16 x i64>
    store <16 x i64> %a, <16 x i64>* %y
    ret void
}
define void @sext_v16i8_v16i64(<16 x i8> %x, <16 x i64>* %y) nounwind "min-legal-vector-width"="256" {    ; CHECK-LABEL: sext_v16i8_v16i64:
; CHECK:     % %bb.0:
; CHECK-NEXT: vpmovsxbw %xmm0, %ymm0
; CHECK-NEXT: vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
; CHECK-NEXT: vpmovsxwq %ymm1, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm2
; CHECK-NEXT: vpshufd {{.*#+}} xmm3 = xmm2[2,3,0,1]
; CHECK-NEXT: vpmovsxwq %xmm3, %ymm3
; CHECK-NEXT: vpmovsxwq %xmm0, %ymm0
; CHECK-NEXT: vpmovsxwq %xmm2, %ymm2
; CHECK-NEXT: vmovdqa %ymm2, 64(%rdi)
; CHECK-NEXT: vmovdqa %ymm0, (%rdi)
; CHECK-NEXT: vmovdqa %ymm3, 96(%rdi)
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = sext <16 x i8> %x to <16 x i64>
store <16 x i64> %a, <16 x i64>* %y
ret void
}

define void @vselect_split_v8i16_setcc(<8 x i16> %s, <8 x i16> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {    ; CHECK-LABEL: vselect_split_v8i16_setcc:
; CHECK:     % %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm2
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
; CHECK-NEXT: vpcmpeqw %xmm1, %xmm0, %k1
; CHECK-NEXT: kshiftrb $4, %k1, %k2
; CHECK-NEXT: vmovdqa64 32(%rdi), %ymm3 [%k2]
; CHECK-NEXT: vmovdqa64 (%rdi), %ymm2 [%k1]
; CHECK-NEXT: vmovdqa %ymm2, (%rdx)
; CHECK-NEXT: vmovdqa %ymm3, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%a = load <8 x i16> %s, <8 x i16>* %p
%y = load <8 x i64>, <8 x i64>* %q
%a = icmp eq <8 x i16> %s, %t
%b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
store <8 x i64> %b, <8 x i64>* %r
ret void
}

define void @vselect_split_v8i32_setcc(<8 x i32> %s, <8 x i32> %t, <8 x i64>* %p, <8 x i64>* %q, <8 x i64>* %r) "min-legal-vector-width"="256" {    ; CHECK-LABEL: vselect_split_v8i32_setcc:
define void @vselect_split_v16i8_setcc(<16 x i8> %s, <16 x i8> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: vselect_split_v16i8_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:  vpcmpeqb %xmm1, %xmm0, %k1
    ; CHECK-NEXT:  kshiftrw $8, %k1, %k2
    ; CHECK-NEXT:  vmovdqa32 32(%rdi), %ymm3 {%k2}
    ; CHECK-NEXT:  vmovdqa32 (%rdi), %ymm2 {%k1}
    ; CHECK-NEXT:  vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm3, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <8 x i64>, <8 x i64>* %p
    %y = load <8 x i64>, <8 x i64>* %q
    %a = icmp eq <16 x i8> %s, %t
    %b = select <8 x i1> %a, <8 x i64> %x, <8 x i64> %y
    store <8 x i64> %b, <8 x i64>* %r
    ret void
}

define void @vselect_split_v16i16_setcc(<16 x i16> %s, <16 x i16> %t, <16 x i32>* %p, <16 x i32>* %q, <16 x i32>* %r) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: vselect_split_v16i16_setcc:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:  vpcmpeqw %ymm1, %ymm0, %k1
    ; CHECK-NEXT:  kshiftrb $4, %k1, %k2
    ; CHECK-NEXT:  vmovdqa64 32(%rdi), %ymm3 {%k2}
    ; CHECK-NEXT:  vmovdqa64 (%rdi), %ymm2 {%k1}
    ; CHECK-NEXT:  vmovdqa %ymm2, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm3, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <16 x i32>, <16 x i32>* %p
    %y = load <16 x i32>, <16 x i32>* %q
    %a = icmp eq <16 x i16> %s, %t
    %b = select <16 x i1> %a, <16 x i32> %x, <16 x i32> %y
    store <16 x i32> %b, <16 x i32>* %r
    ret void
}
define <16 x i8> @trunc_packus_v16i32_v16i8(<16 x i32>* %p) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: trunc_packus_v16i32_v16i8:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vpackusdw 32(%rdi), %ymm0, %ymm0
    ; CHECK-NEXT: vpermq {{.*#+}} ymm0 = ymm0[0,2,1,3]
    ; CHECK-NEXT: vpmovuswb %ymm0, %xmm0
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq
    %a = load <16 x i32>, <16 x i32>* %p
    %b = icmp slt <16 x i32> %a, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255>
    %c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> %x, <16 x i32> %y
    store <16 x i32> %b, <16 x i32>* %r
    ret void
}
%c = select <16 x i1> %b, <16 x i32> %a, <16 x i32> %d, <i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255, i32 255> %d = icmp sgt <16 x i32> %c, zeronizlizer
%e = select <16 x i1> %d, <16 x i32> %c, <16 x i32> zeronizlizer
%f = trunc <16 x i32> %e to <16 x i8>
store <16 x i8> %f, <16 x i8>* %q
ret void
}

define <64 x i1> @v64i1_argument_return(<64 x i1> %x) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_argument_return:
; CHECK:       # %bb.0:
; CHECK-NEXT:    retq
; CHECK-NEXT: ret <64 x i1> %x
}

define void @v64i1_shuffle(<64 x i8>* %x, <64 x i8>* %y) "min-legal-vector-width"="256" {
; CHECK-LABEL: v64i1_shuffle:
; CHECK:       # %bb.0: # %entry
; CHECK-NEXT:    vmovdqa (%rdi), %ymm1
; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm0
; CHECK-NEXT:    vptestnmb %ymm1, %ymm1, %k0
; CHECK-NEXT:    kshiftrd $1, %k0, %k1
; CHECK-NEXT:    movq $-3, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $62, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-5, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $61, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-9, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftlq $63, %k2, %k2
; CHECK-NEXT:    kshiftrq $60, %k2, %k2
; CHECK-NEXT:    korq %k2, %k1, %k1
; CHECK-NEXT:    movq $-17, %rax
; CHECK-NEXT:    kmovq %rax, %k2
; CHECK-NEXT:    kandq %k2, %k1, %k1
; CHECK-NEXT:    kshiftlq $5, %k0, %k2
; CHECK-NEXT:    kshiftrq $63, %k2, %k2

; CHECK-NEXT:   kshiftrq $59, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-33, %rax
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $4, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $58, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-65, %rax
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $7, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $57, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-129, %rax
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $6, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $56, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-257, %rax # imm = 0xFEFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $9, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $55, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-513, %rax # imm = 0xFDFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $8, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $54, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-1025, %rax # imm = 0xFBFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $11, %k0, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $53, %k2, %k2
; CHECK-NEXT:   korq %k2, %k1, %k1
; CHECK-NEXT:   movq $-2049, %rax # imm = 0xF7FF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k1, %k1
; CHECK-NEXT:   kshiftrd $10, %k0, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $24, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $38, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-67108865, %rax # imm = 0xFBFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $27, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $37, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-134217729, %rax # imm = 0xF7FFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $26, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $36, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-268435457, %rax # imm = 0xEFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $29, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $35, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-536870913, %rax # imm = 0xDFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $28, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $34, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movq $-1073741825, %rax # imm = 0xBFFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k1
; CHECK-NEXT:  kshiftrd $31, %k0, %k2
; CHECK-NEXT:  kshiftlq $63, %k2, %k2
; CHECK-NEXT:  kshiftrq $33, %k2, %k2
; CHECK-NEXT:  korq %k2, %k1, %k1
; CHECK-NEXT:  movabsq $-2147483649, %rax # imm = 0xFFFFFFFF7FFFFFFF
; CHECK-NEXT:  kmovq %rax, %k2
; CHECK-NEXT:  kandq %k2, %k1, %k2
; CHECK-NEXT:  vptestnmb %ymm0, %ymm0, %k1
; CHECK-NEXT:  kshiftrd $30, %k0, %k0
; CHECK-NEXT:  kshiftlq $63, %k0, %k0
; CHECK-NEXT:  kshiftrq $32, %k0, %k0
; CHECK-NEXT:  korq %k0, %k2, %k0
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-70368744177665, %rax # imm = 0xFFFFBFFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $15, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $17, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-140737488355329, %rax # imm = 0xFFFF7FFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $14, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $16, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-281474976710657, %rax # imm = 0xFEFFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $17, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $15, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-562949953421313, %rax # imm = 0xFFDFFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $16, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $14, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-1125899906842625, %rax # imm = 0xFFFFFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $19, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $13, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-2251799813685249, %rax # imm = 0xFFEFFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $21, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $12, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-4503599627370497, %rax # imm = 0xFFF7FFFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $18, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $18, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-9007199254740993, %rax # imm = 0xFFFF7FFFFFFFFFFF
; CHECK-NEXT:   kmovq %rax, %k2
; CHECK-NEXT:   kandq %k2, %k0, %k0
; CHECK-NEXT:   kshiftrd $20, %k1, %k2
; CHECK-NEXT:   kshiftlq $63, %k2, %k2
; CHECK-NEXT:   kshiftrq $17, %k2, %k2
; CHECK-NEXT:   korq %k2, %k0, %k0
; CHECK-NEXT:   movabsq $-1801439851626997, %rax # imm = 0xFEFFFFFFFFFFFF
: CHECK-NEXT:  kshiftrq $11, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-9007199254740993, %rax # imm = 0xFFDFFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $20, %k1, %k2  
: CHECK-NEXT:  kshiftlq $63, %k2, %k2  
: CHECK-NEXT:  kshiftrq $10, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-18014398509481985, %rax # imm = 0xFFBFFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $23, %k1, %k2  
: CHECK-NEXT:  kshiftlq $63, %k2, %k2  
: CHECK-NEXT:  kshiftrq $9, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-36028797018963969, %rax # imm = 0xFF7FFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $22, %k1, %k2  
: CHECK-NEXT:  kshiftlq $63, %k2, %k2  
: CHECK-NEXT:  kshiftrq $8, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-72057594037927937, %rax # imm = 0xFEFFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $25, %k1, %k2  
: CHECK-NEXT:  kshiftlq $63, %k2, %k2  
: CHECK-NEXT:  kshiftrq $7, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-144115188075855873, %rax # imm = 0xFDFFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $24, %k1, %k2  
: CHECK-NEXT:  kshiftlq $63, %k2, %k2  
: CHECK-NEXT:  kshiftrq $6, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-288230376151711745, %rax # imm = 0xFDFFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $27, %k1, %k2  
: CHECK-NEXT:  kshiftlq $63, %k2, %k2  
: CHECK-NEXT:  kshiftrq $5, %k2, %k2  
: CHECK-NEXT:  korq %k2, %k0, %k0  
: CHECK-NEXT:  movabsq $-576460752303423489, %rax # imm = 0xF7FFFFFFFFFFFFF  
: CHECK-NEXT:  kmovq %rax, %k2  
: CHECK-NEXT:  kandq %k2, %k0, %k0  
: CHECK-NEXT:  kshiftrd $26, %k1, %k2
entry:
\%
a = load <64 x i8>, <64 x i8>* %a
\%
b = icmp eq <64 x i8> %a, zeroinitialize
\%
%shuf = shufflevector <64 x i1> %b, <64 x i1> undef, <64 x i32> %i32 1, i32 0, i32 3, i32 2, i32 5, i32 4, i32 7, i32 6, i32 9, i32 8, i32 11, i32 10, i32 13, i32 12, i32 15, i32 14, i32 17, i32 16, i32 19, i32 18, i32 21, i32 20, i32 23, i32 22, i32 25, i32 24, i32 27, i32 26, i32 29, i32 28, i32 31, i32 30, i32 33, i32 32, i32 35, i32 34, i32 37, i32 36, i32 39, i32 38, i32 41, i32 40, i32 43, i32 42, i32 45, i32 44, i32 47, i32 46, i32 49, i32 48, i32 51, i32 50, i32 53, i32 52, i32 55, i32 54, i32 57, i32 56, i32 59, i32 58, i32 61, i32 60, i32 63, i32 62>
call void @llvm.masked.store.v64i8.p0v64i8(<64 x i8> %a, <64 x i8>* %y, <64 x i1> %shuf)
ret void
}
declare void @llvm.masked.store.v64i8.p0v64i8(<64 x i8>, <64 x i8>* i32, <64 x i1>)

@mem64_dst = global i64 0, align 8
@mem64_src = global i64 0, align 8
define void @cmp_v8i64_slext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: cmp_v8i64_slext:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm1
    ; CHECK-NEXT:  vpcmpgtq 32(%rdi), %ymm1, %ymm1
    ; CHECK-NEXT:  vpcmpgtq (%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:  vpsllq $63, %ymm1, %ymm1
    ; CHECK-NEXT:  vpsllq $63, %ymm0, %ymm0
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <8 x i64>, <8 x i64>* %xptr
    %y = load <8 x i64>, <8 x i64>* %yptr
    %cmp = icmp slt <8 x i1>, %x, %y
    %ext = sext <8 x i1> %cmp to <8 x i64>
    store <8 x i64> %ext, <8 x i64>* %zptr
    ret void
}

define void @cmp_v8i64_zext(<8 x i64>* %xptr, <8 x i64>* %yptr, <8 x i64>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: cmp_v8i64_zext:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm1
    ; CHECK-NEXT:  vpcmpgtq 32(%rdi), %ymm1, %ymm1
    ; CHECK-NEXT:  vpcmpgtq (%rdi), %ymm0, %ymm0
    ; CHECK-NEXT:  vpsrlq $63, %ymm1, %ymm1
    ; CHECK-NEXT:  vpsrlq $63, %ymm0, %ymm0
    ; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
    ; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
    ; CHECK-NEXT:  vzeroupper
    ; CHECK-NEXT:  retq
    %x = load <8 x i64>, <8 x i64>* %xptr
    %y = load <8 x i64>, <8 x i64>* %yptr
    %cmp = icmp slt <8 x i1>, %x, %y
    %ext = sext <8 x i1> %cmp to <8 x i64>
    store <8 x i64> %ext, <8 x i64>* %zptr
    ret void
}
define <16 x i8> @var_rotate_v16i8(<16 x i8> %a, <16 x i8> %b) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: var_rotate_v16i8:
    ; CHECK:       # %bb.0:
    CHECK-NEXT:   vmovdqa {{.*#+}} xmm2 = [8,8,8,8,8,8,8,8,8,8,8,8,8,8,8,8]
    CHECK-NEXT:   vpsubb %xmm1, %xmm2, %xmm2
    CHECK-NEXT:   vpmovzxbw {{.*#+}} ymm1 =
      xmm1[0],zero,xmm1[1],zero,xmm1[2],zero,xmm1[3],zero,xmm1[4],zero,xmm1[5],zero,xmm1[6],zero,xmm1[7],zero,xmm1[8],zero,xmm1[9],zero,xmm1[10],zero,xmm1[11],zero,xmm1[12],zero,xmm1[13],zero,xmm1[14],zero,xmm1[15],zero
    CHECK-NEXT:   vpmovzxbw {{.*#+}} ymm0 =
      xmm0[0],zero,xmm0[1],zero,xmm0[2],zero,xmm0[3],zero,xmm0[4],zero,xmm0[5],zero,xmm0[6],zero,xmm0[7],zero,xmm0[8],zero,xmm0[9],zero,xmm0[10],zero,xmm0[11],zero,xmm0[12],zero,xmm0[13],zero,xmm0[14],zero,xmm0[15],zero
    CHECK-NEXT:   vpsllvw %ymm1, %ymm2, %ymmm2
    CHECK-NEXT:   vpsrlvw $4, %ymm3, %ymm3
    CHECK-NEXT:   vpternlogq $216, {{.*}}(%rip), %ymm2, %ymm3
    CHECK-NEXT:   vpsllw $5, %ymm0, %ymmm0
    CHECK-NEXT:   vpdivb %ymmm0, %ymmm1, %ymmm1
    CHECK-NEXT:   vpor %ymmm0, %ymmm1, %ymmm1
    CHECK-NEXT:   vzeroupper
    CHECK-NEXT:   retq
%
%b8 = sub <16 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
%shl = shl <16 x i8> %a, %b
%ishr = ishr <16 x i8> %a, %b8
%or = or <16 x i8> %shl, %ishr
ret <16 x i8> %or
}

define <32 x i8> @var_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: var_rotate_v32i8:
    ; CHECK:       # %bb.0:
    CHECK-NEXT:   vpsllw $4, %ymmm0, %ymmm2
    CHECK-NEXT:   vpsrlw $4, %ymmm0, %ymmm3
    CHECK-NEXT:   vpternlogq $216, {{.*}}(%rip), %ymmm2, %ymmm3
    CHECK-NEXT:   vpsllw $5, %ymmm1, %ymmm1
define <32 x i8> @splatvar_rotate_v32i8(<32 x i8> %a, <32 x i8> %b) nounwind "min-legal-vector-width"="256" {
    ; CHECK-LABEL: splatvar_rotate_v32i8:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vpbroadcastb %xmm1, %xmm1
    ; CHECK-NEXT: vpand {{.*}}(%rip), %xmm1, %xmm1
    ; CHECK-NEXT: vpmovzxbq {{.*#+}} xmm2 =
    ; xmm1[0],zero,zero,zero,zero,zero,xmm1[1],zero,zero,zero,zero,zero,zero,zero,zero,zero,zero
    ; CHECK-NEXT: vpsllw %xmm2, %ymm0, %ymm3
    ; CHECK-NEXT: vpcmpeqd %xmm4, %xmm4, %xmm4
    ; CHECK-NEXT: vbroadcastb %xmm2, %ymm2
    ; CHECK-NEXT: vpmovzxbq {{.*#+}} xmm1 =
    ; xmm1[0],zero,zero,zero,zero,zero,xmm1[1],zero,zero,zero,zero,zero,zero,zero,zero,zero
    ; CHECK-NEXT: vpsllw %xmm0, %ymm0, %ymm5
    ; CHECK-NEXT: vpsllw %xmm1, %xmm4, %xmm0
    ; CHECK-NEXT: vpsllw $8, %xmm0, %xmm0
    ; CHECK-NEXT: vpbroadcastb %xmm0, %ymm0
    ; CHECK-NEXT: vpmovzxbq {{.*#+}} %ymm0
    ; CHECK-NEXT: vpternlogq $236, %ymm5, %ymm2, %ymm0
    ; CHECK-NEXT: retq
    %splat = shufflevector <32 x i8> %b, <32 x i8> undef, <32 x i32> zeroinitializer
    %splat8 = sub <32 x i8> <i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8, i8 8>, %b
    %shl = shl <32 x i8> %a, %b
    %lshr = lshr <32 x i8> %a, %b
    %or = or <32 x i8> %shl, %lshr
    ret <32 x i8> %or
}
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or

}
; CHECK-VBMI-NEXT:  vpaddb %ymm2, %ymm2, %ymm2
; CHECK-VBMI-NEXT:  vpbldnb %ymm2, %ymm2, %ymm1
; CHECK-VBMI-NEXT:  vpaddb %ymm1, %ymm1, %ymm3
; CHECK-VBMI-NEXT:  vpaddb %ymm2, %ymm2, %ymm2
; CHECK-VBMI-NEXT:  vpbldnb %ymm2, %ymm3, %ymm1, %ymm1
; CHECK-VBMI-NEXT:  vxor %xmm2, %xmm2, %xmm2
; CHECK-VBMI-NEXT:  vpunpckhbw {{.*#+}} ymm3 =
|  ymm0[8], ymm2[8], ymm0[9], ymm2[9], ymm0[10], ymm2[10], ymm0[11], ymm2[11], ymm0[12], ymm2[12], ymm0[13], ymm2[13], ymm0[14], ymm2[14], ymm0[15], ymm2[15], ymm0[24], ymm2[24], ymm0[25], ymm2[25], ymm0[26], ymm2[26], ymm0[27], ymm2[27], ymm0[28], ymm2[28], ymm0[29], ymm2[29], ymm0[30], ymm2[30], ymm0[31], ymm2[31]
; CHECK-VBMI-NEXT:  vpsllw {{.*}}(%rip), %ymm3, %ymm3
; CHECK-VBMI-NEXT:  vpunpcklbw {{.*#+}} ymm0 =
|  ymm0[0], ymm2[0], ymm0[1], ymm2[1], ymm0[2], ymm2[2], ymm0[3], ymm2[3], ymm0[4], ymm2[4], ymm0[5], ymm2[5], ymm0[6], ymm2[6], ymm0[7], ymm2[7], ymm0[16], ymm2[16], ymm0[17], ymm2[17], ymm0[18], ymm2[18], ymm0[19], ymm2[19], ymm0[20], ymm2[20], ymm0[21], ymm2[21], ymm0[22], ymm2[22], ymm0[23], ymm2[23]
; CHECK-VBMI-NEXT:  vpsllw {{.*}}(%rip), %ymm0, %ymm0
; CHECK-VBMI-NEXT:  vmovdqa {{.*#+}} ymm2 =
|  [1, 3, 5, 7, 9, 11, 13, 15, 33, 35, 37, 39, 41, 43, 45, 47, 17, 19, 21, 23, 25, 27, 29, 31, 49, 51, 53, 55, 57, 59, 61, 63]
; CHECK-VBMI-NEXT:  vperm2b %ymm3, %ymm0, %ymm2
; CHECK-VBMI-NEXT:  vpor %ymm2, %ymm1, %ymm0
; CHECK-VBMI-NEXT:  retq
%shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31, i8 32>
%lshr = lshr <32 x i8> %a, <i8 32, i8 31, i8 30, i8 29, i8 28, i8 27, i8 26, i8 25, i8 24, i8 23, i8 22, i8 21, i8 20, i8 19, i8 18, i8 17, i8 16, i8 15, i8 14, i8 13, i8 12, i8 11, i8 10, i8 9, i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31, i8 32>
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or
}
define <32 x i8> @splatconstant_rotate_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
 ; CHECK-LABEL: splatconstant_rotate_v32i8:
 ; CHECK:       # %bb.0:
 ; CHECK-NEXT:  vpsllw $4, %ymm0, %ymm1
 ; CHECK-NEXT:  vpsrlw $4, %ymm0, %ymm0
 ; CHECK-NEXT:  vpternlogq $216, {{.*}}(%rip), %ymm1, %ymm0
 ; CHECK-NEXT:  retq
%shl = shl <32 x i8> %a, <i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31, i8 32>
%lshr = lshr <32 x i8> %a, <i8 32, i8 31, i8 30, i8 29, i8 28, i8 27, i8 26, i8 25, i8 24, i8 23, i8 22, i8 21, i8 20, i8 19, i8 18, i8 17, i8 16, i8 15, i8 14, i8 13, i8 12, i8 11, i8 10, i8 9, i8 8, i8 7, i8 6, i8 5, i8 4, i8 3, i8 2, i8 1, i8 0, i8 1, i8 2, i8 3, i8 4, i8 5, i8 6, i8 7, i8 8, i8 9, i8 10, i8 11, i8 12, i8 13, i8 14, i8 15, i8 16, i8 17, i8 18, i8 19, i8 20, i8 21, i8 22, i8 23, i8 24, i8 25, i8 26, i8 27, i8 28, i8 29, i8 30, i8 31, i8 32>
%or = or <32 x i8> %shl, %lshr
ret <32 x i8> %or
}
define <32 x i8> @splatconstant_rotate_mask_v32i8(<32 x i8> %a) nounwind "min-legal-vector-width"="256" {
 ; CHECK-LABEL: splatconstant_rotate_mask_v32i8:
 ; CHECK:       # %bb.0:
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add_lldb_library(lldbPluginObjectContainerBSDArchive PLUGIN
ObjectContainerBSDArchive.cpp

LINK_LIBS
lldbCore
lldbHost
lldbSymbol

LINK_COMPONENTS
Support

# People who have agreed to one of the CLAs and can contribute patches.
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# https://developers.google.com/open-source/clia/individual
# https://developers.google.com/open-source/clia/corporate
#
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#
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#
# Please keep the list sorted.

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Chris Kennelly <ckennelly@google.com> <ckennelly@ckennelly.com>
Christopher Seymour <chris.j.seymour@hotmail.com>
Cyrille Faucheux <cyrille.faucheux@gmail.com>
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Dominic Hamon <dma@stripysock.com> <dominic@google.com>
Dominik Czarnota <dominik.b.czarnota@gmail.com>
Eric Fiselier <eric@efcs.ca>
Eugene Zhuk <eugene.zhuk@gmail.com>
target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
    br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
%lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
switch i32 undef, label %exit [
  i32 1, label %sw.bb.i
  i32 2, label %sw.bb3.i
]

sw.bb.i: ; preds = %while.body
unreachable

sw.bb3.i: ; preds = %while.body
unreachable

exit: ; preds = %while.body
switch i32 undef, label %exit.2 [
  i32 1, label %sw.bb.i17
  i32 2, label %sw.bb3.i20
]

sw.bb.i17: ; preds = %exit
%0 = bitcast %struct.0* %lsr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lsr.iv to i8**
%3 = load i8*, i38** %2, align 4
unreachable

exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }
; NOTE: Assertions have been autogenerated by utils/update_test_checks.py UTC_ARGS: --function-signature --
scrub-attributes
; RUN: opt -attributor -attributor-manifest-internal -attributor-max-iterations-verify -attributor-annotate-decl-cs -
attributor-max-iterations=4 -S < %s | FileCheck %s --check-
prefixes=CHECK,NOT_CGSCC_NPM,NOT_CGSCC_OPM,NOT_TUNIT_NPM,IS__TUNIT____,IS________OP
M,IS__TUNIT_OPM
; RUN: opt -aa-pipeline=basic-aa -passes=attributor -attributor-manifest-internal -attributor-max-iterations-verify -
target triple = "x86_64-unknown-linux-gnu"

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #0 {
  
  NOT_TUNIT_NPM-LABEL: define
  {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
  NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
  NOT_TUNIT_NPM-NEXT: bb:
  NOT_TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
  NOT_TUNIT_NPM-NEXT:    store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
  NOT_TUNIT_NPM-NEXT:    [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
  NOT_TUNIT_NPM-NEXT:    store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
  NOT_TUNIT_NPM-NEXT:    ret void
  
  IS__TUNIT_NPM-LABEL: define
  {{[^@]+}}@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512
  IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
  IS__TUNIT_NPM-NEXT: bb:
  IS__TUNIT_NPM-NEXT:  [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
  IS__TUNIT_NPM-NEXT:    store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
  IS__TUNIT_NPM-NEXT:    [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
  IS__TUNIT_NPM-NEXT:    load <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
  IS__TUNIT_NPM-NEXT:    ret void
  
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* %arg) #0 {
  

; IS__TUNIT_OPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
    [[TMP:%.*]] = alloca <8 x i64>, align 32
    [[TMP2:%.*]] = alloca <8 x i64>, align 32
    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: ret void
;
; IS__TUNIT_NPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
    [[TMP:%.*]] = alloca <8 x i64>, align 32
    [[TMP2:%.*]] = alloca <8 x i64>, align 32
    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void
;
; IS__CGSCC_OPM-LABEL: define {{[^@]+}} @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
    [[TMP:%.*]] = alloca <8 x i64>, align 32
    [[TMP2:%.*]] = alloca <8 x i64>, align 32
    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
@callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void
;

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; IS__CGSCC_NPM-LABEL: define @avx512_legal512_prefer512_call_avx512_legal512_prefer512
; IS__CGSCC_NPM-SAME: (<8 x i64> nocapturenonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64> noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer512(<8 x i64> noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

}; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64> %arg, <8 x i64> readonly %arg1) #1 {

; NOT_TUNIT_NPM-LABEL: define
{[^@]+}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64> nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define
{[^@]+}@callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64> noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64

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; IS__CGSCC_OPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP3]])
; IS__CGSCC_OPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT:    ret void
;
; IS__CGSCC_NPM-LABEL: define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 2 dereferenceable(64) [[ARG:*]])
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 2 dereferenceable(64) [[ARG:*]])
; IS__CGSCC_NPM-NEXT:    bb:
; IS__CGSCC_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT:    call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP3]])
; IS__CGSCC_NPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:    ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

}; This should promote
define internal fastcc void @callee_avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {

};

; NOT_TUNIT_NPM-LABEL: define
| {{[@]+}} | callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (8 x i64)* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:*]], (8 x i64)* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:*]]

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}

define void @avx512_legal512_prefer512_call_avx512_legal512_prefer256(<8 x i64>* %arg) #0 {
  bb:
  %tmp = load <8 x i64>, <8 x i64>* %arg1
  store <8 x i64> %tmp, <8 x i64>* %arg
  ret void
}
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; This should promote
define internal fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg,
<8 x i64>* readonly %arg1) #0 {
 ;
 ; NOT_TUNIT_NPM-LABEL: define
 ; {{[@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
 ; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofreenonnull writeonlyalign 64 dereferenceable(64)
 ![ARG:.%.]], <8 x i64>* nocapture nofreenonnullreadonlyalign 64 dereferenceable(64) ![ARG1:.%.]])
 ; NOT_TUNIT_NPM-NEXT: bb:
 ; NOT_TUNIT_NPM-NEXT: [[TMP:.%.*]] = load <8 x i64>, <8 x i64>* ![ARG1], align 64
 ; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* ![ARG], align 64
 ; NOT_TUNIT_NPM-NEXT: ret void
 ;
 ; IS_TUNIT_NPM-LABEL: define
 ; {{[@]+}} @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512
 ; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofreenonnullwriteonlyalign 64 dereferenceable(64)
 ![ARG:.%.]], <8 x i64> ![TMP0:.%.]])
 ; IS_TUNIT_NPM-NEXT: bb:
 ; IS_TUNIT_NPM-NEXT: [[ARG1_PRIV:.%.*]] = alloca <8 x i64>, align 64
 ; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
 ; IS_TUNIT_NPM-NEXT: [[TMP:.%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
 ; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
 ; IS_TUNIT_NPM-NEXT: ret void
 ;
 bb:
 %tmp = load <8 x i64>, <8 x i64>* %arg1
 store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %arg) #1 {
 ;
 ; IS_TUNIT_OPM-LABEL: define {{[@]+}} @avx512_legal512_prefer256_call_avx512_legal512_prefer512
 ; IS_TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:.%.]])
 ; IS_TUNIT_OPM-NEXT: bb:
 ; IS_TUNIT_OPM-NEXT: [[ARG1_PRIV:.%.*]] = alloca <8 x i64>, align 32
 ; IS_TUNIT_OPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 32
 ; IS_TUNIT_OPM-NEXT: [[TMP:.%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 32
 ; IS_TUNIT_OPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
 ; IS_TUNIT_OPM-NEXT: call fastcc void
 @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* nocapture nofree nonnull
writeonlyalign 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnullreadonlyalign 64
dereferenceable(64) [[TMP]])
: IS__UNIT_OPM-NEXT:    [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
: IS__UNIT_OPM-NEXT:    store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
: IS__UNIT_OPM-NEXT:    ret void
:
: IS__UNIT_NPM-LABEL: define { [[^@]+]} @avx512_legal512_prefer256_call_avx512_legal512_prefer512
: IS__UNIT_NPM-SAME: ( <8 x i64>* nocapture writeonly [[ARG:%.*]])
: IS__UNIT_NPM-NEXT:    bb:
: IS__UNIT_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
: IS__UNIT_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
: IS__UNIT_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
: IS__UNIT_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
: IS__UNIT_NPM-NEXT:    [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
: IS__UNIT_NPM-NEXT:    ret void
:
: IS__CGSCC_OPM-LABEL: define { [[^@]+]} @avx512_legal512_prefer256_call_avx512_legal512_prefer512
: IS__CGSCC_OPM-SAME: ( <8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
: IS__CGSCC_OPM-NEXT:    bb:
: IS__CGSCC_OPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_OPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_OPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
: IS__CGSCC_OPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
: IS__CGSCC_OPM-NEXT:    [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
: IS__CGSCC_OPM-NEXT:    ret void
:
: IS__CGSCC_NPM-LABEL: define { [[^@]+]} @avx512_legal512_prefer256_call_avx512_legal512_prefer512
: IS__CGSCC_NPM-SAME: ( <8 x i64>* nocapture writeonly align 2 dereferenceable(64) [[ARG:%.*]])
: IS__CGSCC_NPM-NEXT:    bb:
: IS__CGSCC_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
: IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
: IS__CGSCC_NPM-NEXT:    [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
: IS__CGSCC_NPM-NEXT:    ret void
:
: IS__CGSCC_NPM-NEXT:    [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP0]], align 64
dereferenceable(64) [[TMP0]])
: IS__CGSCC_NPM-NEXT:    bb:
: IS__CGSCC_NPM-NEXT:    [[TMP:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_NPM-NEXT:    [[TMP2:%.*]] = alloca <8 x i64>, align 32
: IS__CGSCC_NPM-NEXT:    [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
: IS__CGSCC_NPM-NEXT:    call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64
dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
: IS__CGSCC_NPM-NEXT:    [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP0]], align 64
dereferenceable(64) [[TMP0]])
: IS__CGSCC_NPM-NEXT:    ret void
; IS__CGSCC_NPM-NEXT:   [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT:   store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT:   ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer512(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should not promote
define internal fastcc void @callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #1 {
  ;
  ; NOT_TUNIT_NPM-LABEL: define
  {[^@]+}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
   ; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64)
          [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
   ; NOT_TUNIT_NPM-NEXT: bb:
   ; NOT_TUNIT_NPM-NEXT:   [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
   ; NOT_TUNIT_NPM-NEXT:   store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
   ; NOT_TUNIT_NPM-NEXT:   ret void
    
   ; IS__TUNIT_NPM-LABEL: define
   {[^@]+}@callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256
   ; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64)
          [[ARG:%.*]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
   ; IS__TUNIT_NPM-NEXT: bb:
   ; IS__TUNIT_NPM-NEXT:   [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
   ; IS__TUNIT_NPM-NEXT:   store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
   ; IS__TUNIT_NPM-NEXT:   ret void
    
   bb:
   %tmp = load <8 x i64>, <8 x i64>* %arg1
   store <8 x i64> %tmp, <8 x i64>* %arg
   ret void

define void @avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg) #2 {
  ;
  ; IS__TUNIT_OPM-LABEL: define {[^@]+}@avx512_legal256_prefer256_call_avx512_legal512_prefer256
  ; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [TMP:%.%] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [TMP2:%.%] = alloca <8 x i64>, align 32
; IS__TUNIT_OPM-NEXT: [TMP3:%.%] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void
callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [TMP4:%.%] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void

; IS__TUNIT_NPM-LABEL: define {{[^@]+}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.%%]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [TMP:%.%] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [TMP2:%.%] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [TMP3:%.%] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: call fastcc void
callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_NPM-NEXT: [TMP4:%.%] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* nocapture nonnull writeonly 2 dereferenceable(64) [[ARG:%.%%]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [TMP:%.%] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [TMP2:%.%] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [TMP3:%.%] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void
callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [TMP4:%.%] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}} @avx512_legal256_prefer256_call_avx512_legal512_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly 2 dereferenceable(64) [[ARG:%.%%]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* noalias nocapture nofree nullnonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nullnonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void callee_avx512_legal256_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should not promote
define internal fastcc void callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #2 {

; NOT_TUNIT_NPM-LABEL: define
({[@]+}) @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nullnonly align 64 dereferenceable(64)
[[ARG1:%.*]], <8 x i64>* nocapture nofree nullnonly align 64 dereferenceable(64) [[ARG1:.%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void

; IS_TUNIT_NPM-LABEL: define
({[@]+}) @callee_avx512_legal512_prefer256_call_avx512_legal512_prefer256
; IS_TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nullnonly align 64 dereferenceable(64)
[[ARG1:%.*]], <8 x i64>* noalias nocapture nofree nullnonly align 64 dereferenceable(64) [[ARG1:.%.*]])
; IS_TUNIT_NPM-NEXT: bb:
; IS_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; IS_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS_TUNIT_NPM-NEXT: ret void


bb:
%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
   @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
}

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)

#define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
   @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
}

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)

#define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
   @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
}

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)

#define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
   @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
}

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)

#define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
   @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
}

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)

#define void @avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg) #1 {
   @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
}

@callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %arg)
; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx512_legal512_prefer256_call_avx512_legal256_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* nocapture nonnull writeonly align 2 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* align 32 [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx512_legal512_prefer256_call_avx512_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void

; This should promote
define internal fastcc void @callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg, <8 x i64>* readonly %arg1) #3 {
;
; NOT_TUNIT_NPM-LABEL: define {{[^@]+}}@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
define void @avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg) #4 {

bb:
%tmp = load <8 x i64>, <8 x i64>* %arg
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

; IS__TUNIT_OPM-LABEL: define {{[^@]+}}@avx2_legal256_prefer256_call_avx2_legal512_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture noreturn writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_OPM-NEXT: ret void

@callee_avx2_legal256_prefer256_call_avx2_legal512_prefer256(<8 x i64>* %arg)

定义的函数在Cisco Telemetry Broker On Prem 1.1中使用了开源代码。
This should promote
define internal fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg, <8 x i64>* %arg1) #4 {

; NOT_TUNIT_NPM-LABEL: define
{ [[^@]+] } @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; NOT_TUNIT_NPM-SAME: (<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[ARG1:%.*]])
; NOT_TUNIT_NPM-NEXT: bb:
; NOT_TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1]], align 64
; NOT_TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; NOT_TUNIT_NPM-NEXT: ret void
;
: IS__TUNIT_NPM-LABEL: define { [[^@]+] } @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]], <8 x i64> [[TMP0:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_NPM-NEXT: ret void
;
bb:
	%tmp = load <8 x i64>, <8 x i64>* %arg1
store <8 x i64> %tmp, <8 x i64>* %arg
ret void
}

define void @avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %arg) #3 {

; IS__TUNIT_OPM-LABEL: define { [[^@]+] } @avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_OPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_OPM-NEXT: bb:
; IS__TUNIT_OPM-NEXT: [[ARG1_PRIV:%.*]] = alloca <8 x i64>, align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP0]], <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_OPM-NEXT: [[TMP:%.*]] = load <8 x i64>, <8 x i64>* [[ARG1_PRIV]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP]], <8 x i64>* [[ARG]], align 64
; IS__TUNIT_OPM-NEXT: ret void

bb:
	call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_OPM-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* nocapture nofree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__TUNIT_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_OPM-NEXT: ret void
;
: IS__TUNIT_NPM-LABEL: define { [[^@]+] } @avx2_legal512_prefer256_call_avx2_legal256_prefer256
; IS__TUNIT_NPM-SAME: (<8 x i64>* nocapture writeonly [[ARG:%.*]])
; IS__TUNIT_NPM-NEXT: bb:
; IS__TUNIT_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__TUNIT_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__TUNIT_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 32 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__TUNIT_NPM-NEXT: [[TMP0:%.*]] = load <8 x i64>, <8 x i64>* [[TMP]], align 64
; IS__TUNIT_NPM-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal56_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64> [[TMP0]])
; IS__TUNIT_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__TUNIT_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__TUNIT_NPM-NEXT: ret void

; IS__CGSCC_OPM-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal56_prefer256
; IS__CGSCC_OPM-SAME: (<8 x i64>* noalias nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_OPM-NEXT: bb:
; IS__CGSCC_OPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_OPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_OPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_OPM-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal56_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_OPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_OPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_OPM-NEXT: ret void

; IS__CGSCC_NPM-LABEL: define {{[^@]+}}@avx2_legal512_prefer256_call_avx2_legal56_prefer256
; IS__CGSCC_NPM-SAME: (<8 x i64>* noalias nocapture nonfree nonnull writeonly align 64 dereferenceable(64) [[ARG:%.*]])
; IS__CGSCC_NPM-NEXT: bb:
; IS__CGSCC_NPM-NEXT: [[TMP:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP2:%.*]] = alloca <8 x i64>, align 32
; IS__CGSCC_NPM-NEXT: [[TMP3:%.*]] = bitcast <8 x i64>* [[TMP]] to i8*
; IS__CGSCC_NPM-NEXT: call void @llvm.memset.p0i8.i64(i8* nocapture nonnull writeonly align 64 dereferenceable(64) [[TMP3]], i8 0, i64 32, i1 false)
; IS__CGSCC_NPM-NEXT: call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal56_prefer256(<8 x i64>* noalias nocapture nofree nonnull writeonly align 64 dereferenceable(64) [[TMP2]], <8 x i64>* noalias nocapture nonfree nonnull readonly align 64 dereferenceable(64) [[TMP]])
; IS__CGSCC_NPM-NEXT: [[TMP4:%.*]] = load <8 x i64>, <8 x i64>* [[TMP2]], align 64
; IS__CGSCC_NPM-NEXT: store <8 x i64> [[TMP4]], <8 x i64>* [[ARG]], align 2
; IS__CGSCC_NPM-NEXT: ret void

bb:
%tmp = alloca <8 x i64>, align 32
%tmp2 = alloca <8 x i64>, align 32
%tmp3 = bitcast <8 x i64>* %tmp to i8*
call void @llvm.memset.p0i8.i64(i8* align 32 %tmp3, i8 0, i64 32, i1 false)
call fastcc void @callee_avx2_legal512_prefer256_call_avx2_legal256_prefer256(<8 x i64>* %tmp2, <8 x i64>* %tmp)
%tmp4 = load <8 x i64>, <8 x i64>* %tmp2, align 32
store <8 x i64> %tmp4, <8 x i64>* %arg, align 2
ret void
}

; Function Attrs: argmemonly nounwind
declare void @llvm.memset.p0i8.i64(i8* nocapture writeonly, i8, i64, i1) #5

attributes #0 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="512" }
attributes #1 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #2 = { inlinehint norecurse nounwind uwtable "target-features"="+avx512vl" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #3 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="512" "prefer-vector-width"="256" }
attributes #4 = { inlinehint norecurse nounwind uwtable "target-features"="+avx2" "min-legal-vector-width"="256" "prefer-vector-width"="256" }
attributes #5 = { argmemonly nounwind }

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; RUN: llc -O3 -mtriple=powerpc-unknown-linux-gnu -mcpu=e500 -mattr=spe < %s | FileCheck %s

; PowerPC SPE is a rare in-tree target that has the FP_TO_SINT node marked
; Legal.

; Verify that fptosi(42.1) isn't simplified when the rounding mode is
; unknown.
; Verify that no gross errors happen.
; CHECK-LABEL: @f20
; COMMON: cfdctsiz
define i32 @f20(double %a) strictfp {
entry:
%result = call i32 @llvm.experimental.constrained.fptosi.i32.f64(double 42.1,
    metadata !"fpexcept.strict")
    strictfp
ret i32 %result
}

@llvm.fp.env = thread_local global i8 zeroinitializer, section "llvm.metadata"
declare i32 @llvm.experimental.constrained.fptosi.i32.f64(double, metadata)

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==============================================================================
; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,AVX512VL512
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,AVX512VL512,SKX256
; RUN: opt < %s -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512,SKX512
define void @zext256() "min-legal-vector-width"="256" {
    ; VEC256-LABEL: 'zext256'
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; AVX512VL512-LABEL: 'zext256'
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    ; SKX512-LABEL: 'zext256'
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    ;
    %A = zext <8 x i16> undef to <8 x i64>
    %B = zext <8 x i32> undef to <8 x i64>
    %C = zext <16 x i8> undef to <16 x i32>
    %D = zext <16 x i16> undef to <16 x i32>
    %E = zext <32 x i8> undef to <32 x i16>
    ret void
}
define void @zext512() "min-legal-vector-width"="512" {
 ; AVX-LABEL: 'zext512'
 ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = zext <8 x i16> undef to <8 x i64>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %D = zext <16 x i16> undef to <16 x i32>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
 ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
 ;
 ; AVX512VL256-LABEL: 'zext512'
 ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
 ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
 ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
 ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
 ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
 ; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
 ;
 ; AVX512VL512-LABEL: 'zext512'
 ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
 ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
 ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
 ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
 ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
 ; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
 ;
 ; SKX256-LABEL: 'zext512'
 ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
 ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
 ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
 ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
 ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
 ; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
define void @sext256() "min-legal-vector-width"="256" {
;
VEC256-LABEL: 'sext256'
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
;
; AVX512VL512-LABEL: 'sext256'
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

SKX512-LABEL: 'sext256'
SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

%A = sext <8 x i8> undef to <8 x i64>
%B = sext <8 x i16> undef to <8 x i64>
%C = sext <8 x i32> undef to <8 x i64>
%D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>
ret void

define void @sext512() "min-legal-vector-width"="512" {
AVX-LABEL: 'sext512'
AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %A = sext <8 x i8> undef to <8 x i64>
AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = sext <8 x i16> undef to <8 x i64>
AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = sext <16 x i16> undef to <16 x i32>
AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void

AVX512VL256-LABEL: 'sext512'
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = sext <8 x i8> undef to <8 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
define void @trunc256() {
    return;
    %A = sext <8 x i64> undef to <8 x i32>
    %B = sext <8 x i64> undef to <8 x i16>
    %C = sext <8 x i64> undef to <8 x i8>
    %D = sext <16 x i32> undef to <16 x i16>
    %E = sext <16 x i32> undef to <16 x i8>
    %F = sext <32 x i16> undef to <32 x i8>
    return;
    %A = trunc <8 x i64> undef to <8 x i32>
    %B = trunc <8 x i64> undef to <8 x i16>
    %C = trunc <8 x i64> undef to <8 x i8>
    %D = trunc <16 x i32> undef to <16 x i16>
    %E = trunc <16 x i32> undef to <16 x i8>
    %F = trunc <32 x i16> undef to <32 x i8>
    return;
    %A = trunc <8 x i64> undef to <8 x i32>
    %B = trunc <8 x i64> undef to <8 x i16>
    %C = trunc <8 x i64> undef to <8 x i8>
    %D = trunc <16 x i32> undef to <16 x i16>
    %E = trunc <16 x i32> undef to <16 x i8>
    %F = trunc <32 x i16> undef to <32 x i8>
    return;
    %A = trunc <8 x i64> undef to <8 x i32>
    %B = trunc <8 x i64> undef to <8 x i16>
    %C = trunc <8 x i64> undef to <8 x i8>
    %D = trunc <16 x i32> undefined to <16 x i16>
    %E = trunc <16 x i32> undefined to <16 x i8>
    %F = trunc <32 x i16> undefined to <32 x i8>
    return;
    %A = trunc <8 x i64> undefined to <8 x i32>
    %B = trunc <8 x i64> undefined to <8 x i16>
    %C = trunc <8 x i64> undefined to <8 x i8>
    %D = trunc <16 x i32> undefined to <16 x i16>
    %E = trunc <16 x i32> undefined to <16 x i8>
    %F = trunc <32 x i16> undefined to <32 x i8>
    return;
}

@trunc256()"min-legal-vector-width"="256"
define i32 @zext256_vXi1() "min-legal-vector-width"="256" {
  ; AVX-LABEL: 'zext256_vXi1'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
  %V2i64 = zext <2 x i1> undef to <2 x i64>
  %V4i64 = zext <4 x i1> undef to <4 x i64>
  %V8i64 = zext <8 x i1> undef to <8 x i64>
  ret void
}
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; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 19 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX256-LABEL: 'zext256_vXi1'
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; SKX512-LABEL: 'zext256_vXi1'
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = zext <2 x i1> undef to <2 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = zext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i64 = zext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = zext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = zext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = zext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = zext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = zext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = zext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = zext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = zext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = zext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = zext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = zext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = zext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = zext <16 x i1> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = zext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = zext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
%V2i64 = zext <2 x i1> undef to <2 x i64>
%V4i64 = zext <4 x i1> undef to <4 x i64>
%V8i64 = zext <8 x i1> undef to <8 x i64>

%V2i32 = zext <2 x i1> undef to <2 x i32>
%V4i32 = zext <4 x i1> undef to <4 x i32>
%V8i32 = zext <8 x i1> undef to <8 x i32>
%V16i32 = zext <16 x i1> undef to <16 x i32>

%V2i16 = zext <2 x i1> undef to <2 x i16>
%V4i16 = zext <4 x i1> undef to <4 x i16>
%V8i16 = zext <8 x i1> undef to <8 x i16>
%V16i16 = zext <16 x i1> undef to <16 x i16>
%V32i16 = zext <32 x i1> undef to <32 x i16>

%V2i8 = zext <2 x i1> undef to <2 x i8>
%V4i8 = zext <4 x i1> undef to <4 x i8>
%V8i8 = zext <8 x i1> undef to <8 x i8>
%V16i8 = zext <16 x i1> undef to <16 x i8>
%V32i8 = zext <32 x i1> undef to <32 x i8>
%V64i8 = zext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @sext256_vXi1() "min-legal-vector-width"="256" { 
  ; AVX-LABEL: "sext256_vXi1"
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
  ; AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

AVX512VL256-LABEL: 'sext256_vXi1'
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i64 = sext <16 x i1> undef to <16 x i64>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 21 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 42 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
;
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I64 = sext i1 undef to i64
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i64 = sext <2 x i1> undef to <2 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i64 = sext <16 x i1> undef to <16 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i64 = sext <32 x i1> undef to <32 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i64 = sext <64 x i1> undef to <64 x i64>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %V64i16 = sext <64 x i1> undef to <64 x i16>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 15 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; AVX512VL512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i64 = sext <4 x i1> undef to <4 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i64 = sext <8 x i1> undef to <8 x i64>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I32 = sext i1 undef to i32
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = sext <2 x i1> undef to <2 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i32 = sext <4 x i1> undef to <4 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i32 = sext <8 x i1> undef to <8 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I16 = sext i1 undef to i16
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = sext <2 x i1> undef to <2 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = sext <4 x i1> undef to <4 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i16 = sext <8 x i1> undef to <8 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i32 = sext <16 x i1> undef to <16 x i32>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i16 = sext <16 x i1> undef to <16 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i16 = sext <32 x i1> undef to <32 x i16>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %I8 = sext i1 undef to i8
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = sext <2 x i1> undef to <2 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = sext <4 x i1> undef to <4 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = sext <8 x i1> undef to <8 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V16i8 = sext <16 x i1> undef to <16 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V32i8 = sext <32 x i1> undef to <32 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V64i8 = sext <64 x i1> undef to <64 x i8>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

%I64 = sext i1 undef to i64
%V2i64 = sext <2 x i1> undef to <2 x i64>
%V4i64 = sext <4 x i1> undef to <4 x i64>
%V8i64 = sext <8 x i1> undef to <8 x i64>

%I32 = sext i1 undef to i32
%V2i32 = sext <2 x i1> undef to <2 x i32>
%V4i32 = sext <4 x i1> undef to <4 x i32>
%V8i32 = sext <8 x i1> undef to <8 x i32>
%V16i32 = sext <16 x i1> undef to <16 x i32>

%I16 = sext i1 undef to i16
%V2i16 = sext <2 x i1> undef to <2 x i16>
%V4i16 = sext <4 x i1> undef to <4 x i16>
%V8i16 = sext <8 x i1> undef to <8 x i16>
%V16i16 = sext <16 x i1> undef to <16 x i16>
%V32i16 = sext <32 x i1> undef to <32 x i16>

%I8 = sext i1 undef to i8
%V2i8 = sext <2 x i1> undef to <2 x i8>
%V4i8 = sext <4 x i1> undef to <4 x i8>
%V8i8 = sext <8 x i1> undef to <8 x i8>
%V16i8 = sext <16 x i1> undef to <16 x i8>
%V32i8 = sext <32 x i1> undef to <32 x i8>
%V64i8 = sext <64 x i1> undef to <64 x i8>

ret i32 undef
}

define i32 @trunc_vXi1() "min-legal-vector-width"="256" {
  : AVX-LABEL: 'trunc_vXi1'
  : AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 23 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 46 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
  : AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 18 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
: AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

: AVX512VL256-LABEL: 'trunc_vXi1'
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i64 = trunc <2 x i64> undef to <2 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i64 = trunc <4 x i64> undef to <4 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 9 for instruction: %V8i64 = trunc <8 x i64> undef to <8 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V16i64 = trunc <16 x i64> undef to <16 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 22 for instruction: %V32i64 = trunc <32 x i64> undef to <32 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 44 for instruction: %V64i64 = trunc <64 x i64> undef to <64 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i32 = trunc <2 x i32> undef to <2 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i32 = trunc <4 x i32> undef to <4 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i32 = trunc <8 x i32> undef to <8 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 10 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 20 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
: AVX512VL256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i32 = trunc <16 x i32> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V32i32 = trunc <32 x i32> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 11 for instruction: %V64i32 = trunc <64 x i32> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i16 = trunc <2 x i16> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i16 = trunc <4 x i16> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i16 = trunc <8 x i16> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i16 = trunc <16 x i16> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i16 = trunc <32 x i16> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 5 for instruction: %V64i16 = trunc <64 x i16> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V2i8 = trunc <2 x i8> undef to <2 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V4i8 = trunc <4 x i8> undef to <4 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V8i8 = trunc <8 x i8> undef to <8 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V16i8 = trunc <16 x i8> undef to <16 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V32i8 = trunc <32 x i8> undef to <32 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 2 for instruction: %V64i8 = trunc <64 x i8> undef to <64 x i1>
; SKX512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret i32 undef

%V2i64 = trunc <2 x i64> undef to <2 x i1>
%V4i64 = trunc <4 x i64> undef to <4 x i1>
%V8i64 = trunc <8 x i64> undef to <8 x i1>
%V16i64 = trunc <16 x i64> undef to <16 x i1>
%V32i64 = trunc <32 x i64> undef to <32 x i1>
%V64i64 = trunc <64 x i64> undef to <64 x i1>

%V2i32 = trunc <2 x i32> undef to <2 x i1>
%V4i32 = trunc <4 x i32> undef to <4 x i1>
%V8i32 = trunc <8 x i32> undef to <8 x i1>
%V16i32 = trunc <16 x i32> undef to <16 x i1>
%V32i32 = trunc <32 x i32> undef to <32 x i1>
%V64i32 = trunc <64 x i32> undef to <64 x i1>

%V2i16 = trunc <2 x i16> undef to <2 x i1>
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%%V4i16 = trunc <4 x i16> undef to <4 x i1>
%%V8i16 = trunc <8 x i16> undef to <8 x i1>
%%V16i16 = trunc <16 x i16> undef to <16 x i1>
%%V32i16 = trunc <32 x i16> undef to <32 x i1>
%%V64i16 = trunc <64 x i16> undef to <64 x i1>

%%V2i8 = trunc <2 x i8> undef to <2 x i1>
%%V4i8 = trunc <4 x i8> undef to <4 x i1>
%%V8i8 = trunc <8 x i8> undef to <8 x i1>
%%V16i8 = trunc <16 x i8> undef to <16 x i1>
%%V32i8 = trunc <32 x i8> undef to <32 x i1>
%%V64i8 = trunc <64 x i8> undef to <64 x i1>

ret i32 undef
}

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1.980 gorilla v1.6.2

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1.981 netcat 1.10-41.1

1.982 suds-jurko 0.7.dev0

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1.983 parse-json 2.2.0

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1.984 safe-buffer 5.1.2

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1.985 ssl-cert 1.0.39

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1.986 gcc-defaults 8.3.0-1

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1.987 emacsen-common 3.0.4

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1.988 olefile 0.46-1
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This is a non-exhaustive list of all the people who helped me improve the olefile project (formerly OleFileIO_PL), in approximative chronological order. Please contact me if I forgot to mention your name.

A big thank you to all of them:

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import re
import sys
import textwrap
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# The standard short names in Debian are defined here:
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license_information = [
    {'shortname': 'BSD-3-clause'},
'filename': 'BSD-3-clause-UCB',
'upstream_names': ['BSD_3_CLAUSE_UCB'],
},
{
'shortname': 'BSD-4-clause',
'filename': 'BSD-4-clause-UCB',
'upstream_names': ['BSD_4_CLAUSE_UCB', 'BSD_ONELINE_CDROM'],
},
{
'shortname': 'Expat',
'filename': 'Expat',
'upstream_names': ['PERMISSIVE_MISC', 'MIT'],
},
{
'shortname': 'freely-redistributable',
'filename': 'freely-redistributable',
'upstream_names': ['FREELY_REDISTRIBUTABLE'],
},
{
'shortname': 'GPL-2',
'filename': 'GPL-2',
'upstream_names': ['GPLv2_MISC', 'GPLv2_ONELINE'],
},
{
'shortname': 'GPL-2+',
'filename': 'GPL-2+',
'upstream_names': ['GPLv2_NOVERSION_ONELINE', 'GPLv2+',
'GPLv2+_DOC_FULL', 'GPLv2+_DOC_MISC',
'GPLv2+_DOC_ONEPARE', 'GPLv2+_SW_3_PARA',
'GPLv2+_SW_ONEPARE'],
},
{
'shortname': 'henry-spencer-regex',
'filename': 'henry-spencer-regex',
'upstream_names': ['MISC'],
},
{
'shortname': 'LDPv1',
'filename': 'LDPv1',
'upstream_names': ['LDPv1'],
},
{
'shortname': 'public-domain',
'filename': 'public-domain',
'upstream_names': ['PUBLIC_DOMAIN'],
}
licenses_with_manpages = {}
symlinks = {}

def get_license_shortname(name):
    """Gets the Debian shortname for the name supplied by upstream.
    Includes a check that upstream's names are only assigned
to one shortname.
    """
    shortname = ""
    already_found = False
    for info in license_information:
        if name in info['upstream_names']:
            if not already_found:
                shortname = info['shortname']
                already_found = True
            else:
                sys.exit("Fatal error: Upstream license name defined multiple times: " + name)
    return shortname

def add_manpage_to_shortname(manpage, copyright_holders, licenses):
    # Ensure a string for the filename
    filename = str(manpage)
    # Strip the leading "./"
    filename = filename[3:len(filename)]
    # Common case: only one license for the manpage, so
    # the shortname is just e.g. "GPL-2+"
    shortname = " and ".join(sorted(licenses))
    if shortname not in licenses_with_manpages:
        licenses_with_manpages[shortname] = {'files': [filename], 'copyright': copyright_holders}
    else:
        licenses_with_manpages[shortname]['files'].append(filename)
        # Do not add same lines twice
        existing_copyright_holders = licenses_with_manpages[shortname]['copyright']
        joined_copyright_holders = list(set(existing_copyright_holders + copyright_holders))
        licenses_with_manpages[shortname]['copyright'] = joined_copyright_holders
stanza = ""
# Collect files and symlinks into a common list
all_files = file_info['files']
for file in file_info['files']:
    if file in symlinks:
        all_files = all_files + symlinks[file]
# Join the files into a whitespace separated list,
# at most 76 characters long
files = " ".join(sorted(all_files))
# The wrap is 69 + 7 (length of "Files: ") = 76
files = textwrap.wrap(files, width=69, break_long_words=False, break_on_hyphens=False)
files = "\n  ".join(files)
# Now format the copyright holders
copyright = "\n  ".join(sorted(file_info['copyright']))
# An empty field is an error, so ensure a value
if len(copyright) == 0:
    copyright = "(could not be detected automatically)"
# Finally, create the stanza
stanza += "Files: " + files
stanza += "\nCopyright: " + copyright
stanza += "\nLicense: " + shortname + "\n\n"
return stanza

def get_license_text(shortname):
    """Gets the text for the Debian license shortname."""
    text = ""
    for info in license_information:
        if info['shortname'] == shortname:
            text += "License: " + shortname + "\n"
    with open("licenses/" + info['filename']) as licensefile:
        for line in licensefile:
            text += " " + line
    return text

p = Path("..")
for manpage in p.glob("man?/*/"):
    with manpage.open() as file:
        licenses = []
copyright_holders = []
manpage_is_symlink = False
for line in file:
    # Do not create copyright stanzas for symlink files
    # but add them to a symlink list
    symlink = re.search(r"^\s+\.(.*)", line)
    if symlink:
        manpage_is_symlink = True
        # Ensure a string for the filename
        linkname = str(manpage)
# Strip the leading "../"
linkname = linkname[3:len(linkname)]
filename = symlink.group(1)
if filename in symlinks:
symlinks[filename].append(linkname)
else:
symlinks[filename] = [linkname]
break
# Only parse the header, so stop after seeing ".TH"
if re.search(r"^\"\%.TH\(%\)\"", line):
    break
# Extract all copyright holders
copyright = re.search(r"^\"\%.\%Copyright (.*)", line)
if copyright:
copyright_holders.append(copyright.group(1))
# Match the beginning of the license
license_start = re.search(r"^\" \%\%LICENSE_START\((.*)\)\"", line)
if license_start:
    license_name = license_start.group(1)
    license_short_name = get_license_shortname(license_name)
    if not license_short_name:
sy.exit("Fatal error: Upstream license name not known: " + license_name)
    licenses.append(license_short_name)
    if not manpage_is_symlink:
        add_manpage_to_shortname(manpage, copyright_holders, licenses)
    # Flatten the symlinks by detecting and removing
    # a symlink which points to another symlink.
    for link_to_test in symlinks:
        # Now cycle through all symlink entries
        for link in symlinks:
            if link_to_test in symlinks[link]:
                symlinks[link] = symlinks[link] + symlinks[link_to_test]
    # Make sorting of licenses deterministic
    stanzas ="
    license_texts = []
    for shortname in sorted(licenses_with_manpages):
        stanzas += get_copyright_stanza(shortname, licenses_with_manpages[shortname])
        text = get_license_text(shortname)
        if text:
            license_texts.append(text)
    # Read in the first lines of copyright, without
    # the automatically generated parts. Stop after
    # the third occurrence of "License".
    manual_lines ="
    license_line_count = 0
with open("copyright") as copyright_file:
for line in copyright_file:
    manual_lines += line
if re.search(r"License", line):
    license_line_count += 1
if license_line_count == 3:
    # Add a final newline for separation
    manual_lines += "\n"
    break
# Open the file for output
with open("copyright", "w") as copyright_file:
copyright_file.write(manual_lines)
copyright_file.write(stanzas)
copyright_file.write("\n".join(license_texts))
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       man3/CIRCLEQ_INSERT_HEAD.3 man3/CIRCLEQ_INSERT_TAIL.3
       man3/CIRCLEQ_REMOVE.3 man3/LIST_EMPTY.3 man3/LIST_ENTRY.3
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1.996 grpc-go v1.23.1

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The following list of people, sorted by last name, have contributed
code or patches to this implementation of sudo since I began
maintaining it in 1993. This list is known to be incomplete--if
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Ackeret, Matt
Adler, Mark
Allbery, Russ
Anderson, Jamie
Andrew, Nick
Andric, Dimitry
Barron, Danny
Bates, Tom
Behan, Zdenk
Bellis, Ray
Benali, Elias
Beverly, Jamie
Boardman, Spider
Bostley, P.J.
Bowes, Keith
Boyce, Keith Garry
Brantley, Michael
Braun, Rob
Bezina, Pavel
Brooks, Piete
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Burton, Ross
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Calvin, Gary
Campbell, Aaron
Chazelas, Stephane
Cheloha, Scott
ek, Vtzslav
Coleman, Chris
Corzine, Deven T.
Cusack, Frank
Dai, Wei
Dill, David
Earickson, Jeff
Eckhardt, Drew
Schoenfeld, Patrick
Schuring, Arno
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Scott, Dougal
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Smith, Andy
Sobrado, Igor
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Spradling, Cloyce D.
Stier, Matthew
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Tucker, Darren
Uhl, Robert
Uzel, Petr
Valery, Reznic
Van Dinter, Theo
Venckus, Martynas
de Vries, Maarten
Wagner, Klaus
Walsh, Dan
Warburton, John
Webb, Kirk
Wetzel, Timm
Wieringen, Marco van
Wilk, Jakub
Winiger, Gary
Wood, David
Zacarias, Gustavo
Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see https://translationproject.org for more details.

Albuquerque, Pedro
Blittermann, Mario
Bogusz, Jakub
1.1006 redux-saga 1.1.3

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1.1007 object-keys 1.1.1

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