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History and License
********************

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl/) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us/) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see http://www.zope.com/). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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Mersenne Twister
----------------

The `mod`:`_random` module includes code based on a download from http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/MT2002/emt19937ar.html. The following are the verbatim comments from the original code::

A C-program for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.
Before using, initialize the state by using init_genrand(seed)
or init_by_array(init_key, key_length).

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http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/emt.html
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Sockets
------

The :mod:`socket` module uses the functions, :func:`getaddrinfo`, and
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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).
This code implements the MD5 Algorithm defined in RFC 1321, whose
text is available at

http://www.ietf.org/rfc/rfc1321.txt

The code is derived from the text of the RFC, including the test suite
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The original and principal author of md5.h is L. Peter Deutsch
<ghost@aladdin.com>. Other authors are noted in the change history
that follows (in reverse chronological order):

2002-04-13 lpd Removed support for non-ANSI compilers; removed
references to Ghostscript; clarified derivation from RFC 1321;
now handles byte order either statically or dynamically.
1999-11-04 lpd Edited comments slightly for automatic TOC extraction.
1999-10-18 lpd Fixed typo in header comment (ansi2knr rather than md5);
added conditionalization for C++ compilation from Martin
Purschke <purschke@bnl.gov>.
1999-05-03 lpd Original version.

Asynchronous socket services
-------------------------------

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UUencode and UUdecode functions
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Modified by Jack Jansen, CWI, July 1995:
- Use binascii module to do the actual line-by-line conversion
  between ascii and binary. This results in a 1000-fold speedup. The C
  version is still 5 times faster, though.
- Arguments more compliant with Python standard

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--------------------------
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--------------

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zlib
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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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1.3 django 1.5.1

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Advice for new contributors

New contributor and not sure what to do? Want to help but just don't know how
to get started? This is the section for you.

First steps

Start with these easy tasks to discover Django's development process.

* **Triage tickets**

If an `unreviewed ticket` reports a bug, try and reproduce it. If you
can reproduce it and it seems valid, make a note that you confirmed the bug
and accept the ticket. Make sure the ticket is filed under the correct
component area. Consider writing a patch that adds a test for the bug's
behavior, even if you don't fix the bug itself. See more at
Look for tickets that are accepted and review patches to build familiarity with the codebase and the process.

Mark the appropriate flags if a patch needs docs or tests. Look through the changes a patch makes, and keep an eye out for syntax that is incompatible with older but still supported versions of Python. Run the tests and make sure they pass on your system. Where possible and relevant, try them out on a database other than SQLite. Leave comments and feedback!

Keep old patches up to date.

Oftentimes the codebase will change between a patch being submitted and the time it gets reviewed. Make sure it still applies cleanly and functions as expected. Simply updating a patch is both useful and important! See more on writing-code/submitting-patches.

Write some documentation.

Django's documentation is great but it can always be improved. Did you find a typo? Do you think that something should be clarified? Go ahead and suggest a documentation patch! See also the guide on improving-the-documentation, in particular the tips for improving-the-documentation.

Guidelines
---------

As a newcomer on a large project, it's easy to experience frustration. Here's some advice to make your work on Django more useful and rewarding.

Pick a subject area that you care about, that you are familiar with, or that you want to learn about.

You don't already have to be an expert on the area you want to work on; you become an expert through your ongoing contributions to the code.
* **Analyze tickets' context and history**

Trac isn't an absolute; the context is just as important as the words. When reading Trac, you need to take into account who says things, and when they were said. Support for an idea two years ago doesn't necessarily mean that the idea will still have support. You also need to pay attention to who *hasn't* spoken -- for example, if a core team member hasn't been recently involved in a discussion, then a ticket may not have the support required to get into trunk.

* **Start small**

It's easier to get feedback on a little issue than on a big one. See the 'easy pickings'_.

* **If you're going to engage in a big task, make sure that your idea has support first**

This means getting someone else to confirm that a bug is real before you fix the issue, and ensuring that the core team supports a proposed feature before you go implementing it.

* **Be bold! Leave feedback!**

Sometimes it can be scary to put your opinion out to the world and say "this ticket is correct" or "this patch needs work", but it's the only way the project moves forward. The contributions of the broad Django community ultimately have a much greater impact than that of the core developers. We can't do it without YOU!

* **Err on the side of caution when marking things Ready For Check-in**

If you're really not certain if a ticket is ready, don't mark it as such. Leave a comment instead, letting others know your thoughts. If you're mostly certain, but not completely certain, you might also try asking on IRC to see if someone else can confirm your suspicions.

* **Wait for feedback, and respond to feedback that you receive**

Focus on one or two tickets, see them through from start to finish, and repeat. The shotgun approach of taking on lots of tickets and letting some fall by the wayside ends up doing more harm than good.

* **Be rigorous**

When we say "`.pep:`8", and must have docs and tests", we mean it. If a patch doesn't have docs and tests, there had better be a good reason. Arguments
like "I couldn't find any existing tests of this feature" don't carry much weight—while it may be true, that means you have the extra-important job of writing the very first tests for that feature, not that you get a pass from writing tests altogether.

.. _easy pickings: https://code.djangoproject.com/query?status=!closed&easy=1

.. _new-contributors-faq:

FAQ
---

1. **This ticket I care about has been ignored for days/weeks/months! What can I do to get it committed?**

First off, it's not personal. Django is entirely developed by volunteers (even the core developers), and sometimes folks just don't have time. The best thing to do is to send a gentle reminder to the django-developers mailing list asking for review on the ticket, or to bring it up in the #django-dev IRC channel.

2. **I'm sure my ticket is absolutely 100% perfect, can I mark it as RFC myself?**

Short answer: No. It's always better to get another set of eyes on a ticket. If you're having trouble getting that second set of eyes, see question 1, above.

3. **My ticket has been in DDN forever! What should I do?**

Design Decision Needed requires consensus about the right solution. At the very least it needs consensus among the core developers, and ideally it has consensus from the community as well. The best way to accomplish this is to start a thread on the django-developers mailing list, and for very complex issues to start a wiki page summarizing the problem and the possible solutions.

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1.4 pycairo 1.8.6 2.1.el6

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1.6 ruby 2.2.4

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1.7 curl 7.44.0

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1.8 ciscosafec 3.0.0

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1.9 geo-ip 1.6.9 1

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*/

ELF(4(O-MhbP@P`04P
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HEte?@h$0
ZZ
$100
(J1@pP00.0S)
0Y@000D0H0L
0P000
0P
pP
WARNING: BSD label detected on unknown (0x%x) primary partition
WARNING: BSD partition (%d) overflow detected, ignore

WEVGCC: (Linaro GCC 4.5.2012.02) 4.5.4 20120201 (prerelease)A.acabiS-A
A_.symtab.strtab.shstrtab.rel.text.data.bss.rel.data.rel.ro.local.rodata.str1.1.comment.note.GNU-
stack.ARM.attributes4
%

(h
x$s).3$8

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/* tailf.c -- tail a log file and then follow it
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*
* less -F and tail -f cause a disk access every five seconds.  This
* program avoids this problem by waiting for the file size to change.
* Hence, the file is not accessed, and the access time does not need to be
* flushed back to disk.  This is sort of a "stealth" tail.
*/
#
# bsd.lo - a libtool object file
# Generated by libtool (GNU libtool) 2.4
#
# Please DO NOT delete this file!
# It is necessary for linking the library.

# Name of the PIC object.
pic_object='.libs/bsd.o'

# Name of the non-PIC object
non_pic_object='bsd.o'

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*/

* setpwnam.c -- edit an entry in a password database.
*
1.14 libxml 2.9.1 6.1.el7.centos.3

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1.15 xar 1.6.1

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/*
* 03-Apr-2005
* DRI: Rob Braun <bbraun@synack.net>
*/

1.16 proxy-connect 0.0.1

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  regenc.[ch]:
  regerror.c:
  regexec.c:
  regint.h:
  regparse.[ch]:
  enc/ascii.c
  enc/big5.c
  enc/cp949.c
  enc/emacs_mule.c
  enc/encdb.c
  enc/euc].[c
  enc/euc.kr.c
  enc/euc_tw.c
  enc/gb18030.c
  enc/gb2312.c
  enc/gbk.c
  enc/iso_8859_1.c
  enc/iso_8859_10.c
  enc/iso_8859_11.c
  enc/iso_8859_13.c
  enc/iso_8859_14.c
  enc/iso_8859_15.c
  enc/iso_8859_16.c
  enc/iso_8859_2.c
  enc/iso_8859_3.c
  enc/iso_8859_4.c
  enc/iso_8859_5.c
  enc/iso_8859_6.c
  enc/iso_8859_7.c
  enc/iso_8859_8.c
  enc/iso_8859_9.c
  enc/koi8_r.c
  enc/koi8_u.c
  enc/shiftjis.c
  enc/unicode.c
  enc/us_ascii.c
  enc/utf_16be.c
  enc/utf_16le.c
  enc/utf_32be.c
  enc/utf_32le.c
  enc/utf_8.c
  enc/windows_1251.c

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random.c

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A C-program for MT19937, with initialization improved 2002/2/10.
Coded by Takuji Nishimura and Makoto Matsumoto.
This is a faster version by taking Shawn Cokus's optimization,
Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using init_genrand(seed)
or init_by_array(init_key, key_length).
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http://www.math.keio.ac.jp/matumoto/emt.html
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st.[ch]:
missing/alloca.c:
missing/dup2.c:
missing/erf.c:
missing/finite.c:
missing/hypot.c:
missing/isinf.c:
missing/isnan.c:
missing/lgamma_r.c:
missing/memcmp.c:
missing/memmove.c:
missing/strchr.c:
missing/strstr.c:
missing/strtol.c:
missing/tgamma.c:
ext/digest/sha1/sha1.[ch]:

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missing/strlcpy.c

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Major authors:
-------------

Brad Fitzpatrick <brad@danga.com> -- maintainer, original implementations

Anatoly Vorobey <mellon@pobox.com> -- lots of the modern server code

Steven Grimm <sgrimm@facebook.com> -- iov writing (less CPU), UDP mode, non-2.0 slab mantissas, multithread, ...

Other Contributors
------------------

Evan Martin <evan@danga.com>
Nathan Neulinger <nneul@umr.edu>
Eric Hodel <drbrain@segment7.net>
Michael Johnson <ahze@ahze.net>
Paul Querna <chip@corelands.com>
Jamie McCarthy <jamie@mccarthy.vg>
Philip Neustrom <philipn@gmail.com>
Andrew O'Brien <andrewo@oriel.com.au>
Josh Rotenberg <joshrotenberg@gmail.com>
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Tim Yardley <liquid@haveheart.com>
Paolo Borelli <paolo.borelli@gmail.com>
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*/dbg/example3.c
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*/dbg/main.c
*/dbg/my_main.c
*/dbg/remove_function_from_trace.pl
*/dbg/tests.c
*/dbg/tests-t.pl
*/extra/yassl/src/dummy.cpp
*/include/probes_mysql_nodtrace.h
*/libmysqld/resource.h
*/mysql-test/*
*/regex/*
*/sql-bench/graph-compare-results.sh
*/storage/ndb/bin/*
*/storage/ndb/demos/*
*/support-files/binary-configure.sh
*/support-files/my-huge.cnf.sh
*/support-files/my-innodb-heavy-4G.cnf.sh
*/support-files/my-large.cnf.sh
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*/scripts/mysql_fix_extensions.sh
*/scripts/mysql_setpermission.sh
*/sql-bench/*
*/storage/myisam/ftbench/ft-test-run.sh
*/storage/myisam/mi_test_all.sh
*/storage/ndb/test/run-test/atrt-analyze-result.sh
*/storage/ndb/test/run-test/atrt-clear-result.sh
*/storage/ndb/test/run-test/atrt-gather-result.sh
*/storage/ndb/test/run-test/atrt-setup.sh
*/storage/ndb/test/run-test/make-config.sh
*/storage/ndb/test/run-test/make-html-reports.sh
*/storage/ndb/test/run-test/make-index.sh
*/storage/ndb/test/run-test/ndb-autotest.sh
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# Variable Name: license #
# Scope: Global #
# Access Type: Static #
# Data Type: string #
#
# Creation Date: 2008-02-07 #
# Author: Sharique Abdullah #
#
# Description: Test Cases of Dynamic System Variable license that checks the behavior of this variable in the following ways #
#  * Value Check #
#  * Scope Check #
#

--echo '#---------------------BS_STVARS_040_01----------------------#'

# Displaying default value #

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_02----------------------#'

# Check if Value can set #

--error ER_INCORRECT_GLOBAL_LOCAL_VAR
SET @@GLOBAL.license=1;
--echo Expected error 'Read only variable'

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_03----------------------#'

# Check if the value in GLOBAL Table matches value in variable #

--error ER_INCORRECT_GLOBAL_LOCAL_VAR
SET @@GLOBAL.license=1;
--echo Expected error 'Read only variable'

SELECT COUNT(@@GLOBAL.license);
--echo 1 Expected
--disable_warnings
SELECT @GLOBAL.license = VARIABLE_VALUE
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
--enable_warnings
--echo 1 Expected

SELECT COUNT(@GLOBAL.license);
--echo 1 Expected

--disable_warnings
SELECT COUNT(VARIABLE_VALUE)
FROM INFORMATION_SCHEMA.GLOBAL_VARIABLES
WHERE VARIABLE_NAME='license';
--enable_warnings
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_04----------------------#'
#########################################################################
#  Check if accessing variable with and without GLOBAL point to same variable  #
#########################################################################
SELECT @license = @GLOBAL.license;
--echo 1 Expected

--echo '#---------------------BS_STVARS_040_05----------------------#'
#########################################################################
#   Check if license can be accessed with and without @@ sign                  #
#########################################################################
SELECT COUNT(@license);
--echo 1 Expected

--Error ER_INCORRECT_GLOBAL_LOCAL_VAR
SELECT COUNT(@local.license);
--echo Expected error 'Variable is a GLOBAL variable'

--Error ER_INCORRECT_GLOBAL_LOCAL_VAR
SELECT COUNT(@SESSION.license);
--echo Expected error 'Variable is a GLOBAL variable'

SELECT COUNT(@GLOBAL.license);
--echo 1 Expected
--Error ER_BAD_FIELD_ERROR
SELECT license = @@SESSION/license;
--echo Expected error 'Readonly variable'
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SELECT @@license = @@GLOBAL.license;
@@license = @@GLOBAL.license
1
1 Expected

SELECT COUNT(@@license);
COUNT(@@license)
1
1 Expected

SELECT COUNT(@@local.license);
ERROR HY000: Variable 'license' is a GLOBAL variable
Expected error 'Variable is a GLOBAL variable'
SELECT COUNT(@@SESSION.license);
ERROR HY000: Variable 'license' is a GLOBAL variable
Expected error 'Variable is a GLOBAL variable'
SELECT COUNT(@@GLOBAL.license);
COUNT(@@GLOBAL.license)
1
1 Expected
SELECT license = @@SESSION.license;
ERROR 42S22: Unknown column 'license' in 'field list'
Expected error 'Readonly variable'

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Major authors:

************
Brad Fitzpatrick <brad@danga.com> -- maintainer, original implementations

Anatoly Vorobey <mellon@pobox.com> -- lots of the modern server code

Steven Grimm <sgrimmm@facebook.com> -- iov writing (less CPU), UDP mode, non-2.0 slab mantissas, multithread, ...

Other Contributors
------------------
Evan Martin <evan@danga.com>
Nathan Neulinger <nneul@umr.edu>
Eric Hodel <drbrain@segment7.net>
Michael Johnson <ahze@ahze.net>
Paul Querna <chip@corelands.com>
Jamie McCarthy <jamie@mcCarthy.vg>
Philip Neustrom <philipn@gmail.com>
Andrew O'Brien <andrewo@oriel.com.au>
Josh Rotenberg <joshrotenberg@gmail.com>
Robin H. Johnson <robbat2@gentoo.org>
Tim Yardley <liquid@haveheart.com>
Paolo Borelli <paolo.borelli@gmail.com>
Eli Bingham <eli@pandora.com>
Jean-Francois Bustarret <jfbustarret@wat.tv>
Paul G <paul-lists@perforge.com>
Paul Lindner <lindner@inuus.com>
Dormando <dormando@rydia.net>
Dustin Sallings <dustin@spy.net>
Chris Goffinet <goffinet@yahoo-inc.com>
Tomash Brechko <tomash.brechko@gmail.com>
Brian Aker <brian@tangent.org>
Trond Norbye <trond.norbye@sun.com>

1.24 utf-bom-utils 1.0
1.24.1 Available under license :
-----------------------------------------
utf8_bom_tools
by Jose Luis Blanco Claraco
-----------------------------------------

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See the Google code website:
http://code.google.com/p/utf-bom-utils/
1.25 jansson 2.7 1+deb8u1

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Upstream-Contact: Petri Lehtinen <petri@digip.org>
Source: https://github.com/akheron/jansson

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1.26 xmlsec 1.2.20 7.el7_4

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References

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* AOL
http://www.aleksey.com/pipermail/xmlsec/attachments/20030729/0e25648e/attachment.htm

* Cordys R&D BV

* Cryptocom LTD

1.27 graphite-whisper 0.9.12
1.27.1 Available under license:

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1.28 python-setuptools 0.6c11

1.28.1 Available under license:

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A. HISTORY OF THE SOFTWARE

Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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regcomp.c:
regenenc.[ch]:
regerror.c:
regexec.c:
regint.h:
regparse.[ch]:
enc/ascii.c
enc/big5.c
enc/cp949.c
enc/emacs_mule.c
enc/encdb.c
enc/euc_jp.c
enc/euc_kr.c
enc/euc_tw.c
enc/gb18030.c
enc/gb2312.c
enc/gbk.c
enc/iso_8859_1.c
enc/iso_8859_10.c
enc/iso_8859_11.c
enc/iso_8859_13.c
enc/iso_8859_14.c
enc/iso_8859_15.c
enc/iso_8859_16.c
enc/iso_8859_2.c
enc/iso_8859_3.c
enc/iso_8859_4.c
enc/iso_8859_5.c
enc/iso_8859_6.c
enc/iso_8859_7.c
enc/iso_8859_8.c
enc/iso_8859_9.c
enc/koi8_r.c
enc/koi8_u.c
enc/shift_jis.c
enc/unicode.c
enc/us_ascii.c
enc/utf_16be.c
enc/utf_16le.c
enc/utf_32be.c
enc/utf_32le.c
enc/utf_8.c
enc/windows_1251.c

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http://www.ruby-lang.org/cgi-bin/cvsweb.cgi/oniguruma/
http://www.freebsd.org/cgi/cvsweb.cgi/ports/devel/oniguruma/

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random.c

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A C-program for MT19937, with initialization improved 2002/2/10.
Coded by Takuji Nishimura and Makoto Matsumoto.
This is a faster version by taking Shawn Cokus's optimization,
Matthe Bellew's simplification, Isaku Wada's real version.

Before using, initialize the state by using init_genrand(seed)
or init_by_array(init_key, key_length).

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http://www.math.keio.ac.jp/matumoto/emt.html
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st.[ch]:
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  missing/dup2.c:
  missing/erf.c:
  missing/finite.c:
  missing/hypot.c:
  missing/isinf.c:
  missing/isnan.c:
  missing/lgamma_r.c:
  missing/memcmp.c:
  missing/memmove.c:
  missing/strchr.c:
  missing/strstr.c:
  missing/strtol.c:
  missing/tgamma.c:
  ext/digest/sha1/sha1.[ch]:

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missing/strlcpy.c

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ext/nkf/nkf-utf8/nkf.c:
ext/nkf/nkf-utf8/utf8tbl.c:

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1.30 libssh 1.4.3

1.31 fcgi 2.4.0

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doc/sitemap_gen.py

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util/unlink.py

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1.35 free-type 2.4.11

1.36 pixman 0.32.6
1.36.1 Available under license:

/*

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*/

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*
* Author: Siarhei Siamashka (siarhei.siamashka@nokia.com)
*/

/*

* This file contains a macro ('generate_composite_function') which can
* construct 2D image processing functions, based on a common template.
* Any combinations of source, destination and mask images with 8bpp,
* 16bpp, 24bpp, 32bpp color formats are supported.
*
* This macro takes care of:
* - handling of leading and trailing unaligned pixels
* - doing most of the work related to L2 cache preload
* - encourages the use of software pipelining for better instructions
* - scheduling
*
* The user of this macro has to provide some configuration parameters
* (bit depths for the images, prefetch distance, etc.) and a set of
* macros, which should implement basic code chunks responsible for
* pixels processing. See 'pixman-arm-neon-asm.S' file for the usage
* examples.
*
* TODO:
* - try overlapped pixel method (from Ian Rickards) when processing
* - exactly two blocks of pixels
* - maybe add an option to do reverse scanline processing
/*
 * Bit flags for 'generate_composite_function' macro which are used
 * to tune generated functions behavior.
 */
.set FLAG_DST_WRITEONLY,       0
.set FLAG_DST_READWRITE,       1
.set FLAG_DEINTERLEAVE_32BPP,  2

/*
 * Offset in stack where mask and source pointer/stride can be accessed
 * from 'init' macro. This is useful for doing special handling for solid mask.
 */
.set ARGS_STACK_OFFSET,        40

/*
 * Constants for selecting preferable prefetch type.
 */
.set PREFETCH_TYPE_NONE,       0 /* No prefetch at all */
.set PREFETCH_TYPE_SIMPLE,     1 /* A simple, fixed-distance-ahead prefetch */
.set PREFETCH_TYPE_ADVANCED,   2 /* Advanced fine-grained prefetch */

/*
 * Definitions of supplementary pixld/pixst macros (for partial load/store of
 * pixel data).
 */

.macro pixldst1 op, elem_size, reg1, mem_operand, abits
 .if abits > 0
   op&.&elem_size {d&reg1}, [&mem_operand&, :&abits&]!
 .else
   op&.&elem_size {d&reg1}, [&mem_operand&]!
 .endif
.endm

.macro pixldst2 op, elem_size, reg1, reg2, mem_operand, abits
 .if abits > 0
   op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&, :&abits&]!
 .else
   op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&]!
 .endif
.endm

.macro pixldst4 op, elem_size, reg1, reg2, reg3, reg4, mem_operand, abits
 .if abits > 0
   op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&, :&abits&]!
 .else
   op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&]!
 .endif
.endm
.macro pixldst0 op, elem_size, reg1, idx, mem_operand, abits
    op&.&elem_size {d&reg1[idx]}, [mem_operand]!
.endm

.macro pixldst3 op, elem_size, reg1, reg2, reg3, mem_operand
    op&.&elem_size {d&reg1, d&reg2, d&reg3, [mem_operand]}!
.endm

.macro pixldst30 op, elem_size, reg1, reg2, reg3, idx, mem_operand
    op&.&elem_size {d&reg1[idx], d&reg2[idx], d&reg3[idx]}, [mem_operand]!
.endm

.macro pixldst numbytes, op, elem_size, basereg, mem_operand, abits
    .if numbytes == 32
        pixldst4 op, elem_size, %((basereg+4), %((basereg+5), \
            %((basereg+6), %((basereg+7), mem_operand, abits
        .elseif numbytes == 16
            pixldst2 op, elem_size, %((basereg+2), %((basereg+3), mem_operand, abits
        .elseif numbytes == 8
            pixldst1 op, elem_size, %((basereg+1), mem_operand, abits
        .else numbytes == 4
            .if !RESPECT STRICT_ALIGNMENT || (elem_size == 32)
                pixldst0 op, 32, %((basereg+0), 1, mem_operand, abits
            .elseif elem_size == 16
                pixldst0 op, 16, %((basereg+0), 2, mem_operand, abits
                pixldst0 op, 16, %((basereg+0), 3, mem_operand, abits
            .else
                pixldst0 op, 8, %((basereg+0), 4, mem_operand, abits
                pixldst0 op, 8, %((basereg+0), 5, mem_operand, abits
                pixldst0 op, 8, %((basereg+0), 6, mem_operand, abits
                pixldst0 op, 8, %((basereg+0), 7, mem_operand, abits
            .endif
        .elseif numbytes == 2
            .if !RESPECT STRICT_ALIGNMENT || (elem_size == 16)
                pixldst0 op, 16, %((basereg+0), 1, mem_operand, abits
            .else
                pixldst0 op, 8, %((basereg+0), 2, mem_operand, abits
                pixldst0 op, 8, %((basereg+0), 3, mem_operand, abits
            .endif
        .elseif numbytes == 1
            pixldst0 op, 8, %((basereg+0), 1, mem_operand, abits
        .else
            .error "unsupported size: numbytes"
        .endif
    .endif

.endp
.macro pixld numpix, bpp, basereg, mem_operand, abits=0
    .if bpp > 0
        .if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE_32BPP_ENABLED != 0)
            pixldst4 vld4, 8, (%basereg+4), (%basereg+5),
                (%basereg+6), (%basereg+7), mem_operand, abits
        .elseif (bpp == 24) && (numpix == 8)
            pixldst3 vld3, 8, (%basereg+3), (%basereg+4), (%basereg+5), mem_operand
        .else
            pixldst3 vld3, 8, (%basereg+0), (%basereg+1), (%basereg+2), mem_operand
        .endif
    .endif
.endm

.mcro pixst numpix, bpp, basereg, mem_operand, abits=0
    .if bpp > 0
        .if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE_32BPP_ENABLED != 0)
            pixldst4 vst4, 8, (%basereg+4), (%basereg+5),
                (%basereg+6), (%basereg+7), mem_operand, abits
        .elseif (bpp == 24) && (numpix == 8)
            pixldst3 vst3, 8, (%basereg+3), (%basereg+4), (%basereg+5), mem_operand
        .else
            pixldst3 vst3, 8, (%basereg+0), (%basereg+1), (%basereg+2), mem_operand
        .endif
    .endif
.endm
.macro pixld_a numpix, bpp, basereg, mem_operand
  .if (bpp * numpix) <= 128
    pixld numpix, bpp, basereg, mem_operand, %(bpp * numpix)
  .else
    pixld numpix, bpp, basereg, mem_operand, 128
  .endif
.endm

.endm

.macro pixst_a numpix, bpp, basereg, mem_operand
  .if (bpp * numpix) <= 128
    pixst numpix, bpp, basereg, mem_operand, %(bpp * numpix)
  .else
    pixst numpix, bpp, basereg, mem_operand, 128
  .endif
.endm

/*
 * Pixel fetcher for nearest scaling (needs TMP1, TMP2, VX, UNIT_X register
 * aliases to be defined)
*/

.macro pixld1_s elem_size, reg1, mem_operand
  .if elem_size == 16
    mov     TMP1, VX, asr #16
    adds    VX, VX, UNIT_X
    5:  subpls  VX, VX, SRC_WIDTH_FIXED
        bpl     5b
        add    TMP1, mem_operand, TMP1, asl #1
        mov    TMP2, VX, asr #16
        adds    VX, VX, UNIT_X
    5:  subpls  VX, VX, SRC_WIDTH_FIXED
        bpl     5b
        add    TMP2, mem_operand, TMP2, asl #1
        vld1.16 {d&reg1&[0]}, [TMP1, :16]
        mov    TMP1, VX, asr #16
        adds    VX, VX, UNIT_X
    5:  subpls  VX, VX, SRC_WIDTH_FIXED
        bpl     5b
        add    TMP1, mem_operand, TMP1, asl #1
        vld1.16 {d&reg1&[1]}, [TMP1, :16]
        mov    TMP2, VX, asr #16
        adds    VX, VX, UNIT_X
    5:  subpls  VX, VX, SRC_WIDTH_FIXED
        bpl     5b
        add    TMP1, mem_operand, TMP1, asl #1
        vld1.16 {d&reg1&[2]}, [TMP1, :16]
        vld1.16 {d&reg1&[3]}, [TMP2, :16]
    .elseif elem_size == 32
        mov    TMP1, VX, asr #16
        mov    TMP2, VX, asr #16
adds VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
   bpl 5b
   add TMP1, mem_operand, TMP1, asl #2
   mov TMP2, VX, asr #16
   adds VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
   bpl 5b
   add TMP2, mem_operand, TMP2, asl #2
   vld1.32 {d&reg1&[0]}, [TMP1, :32]
   vld1.32 {d&reg1&[1]}, [TMP2, :32]
.else
   .error "unsupported"
   .endif
.endm

.macro pixld2_s elem_size, reg1, reg2, mem_operand
   .if 0 /* elem_size == 32 */
      mov TMP1, VX, asr #16
      add VX, VX, UNIT_X, asl #1
      add TMP1, mem_operand, TMP1, asl #2
      mov TMP2, VX, asr #16
      sub VX, VX, UNIT_X
      add TMP2, mem_operand, TMP2, asl #2
      vld1.32 {d&reg1&[0]}, [TMP1, :32]
      vld1.32 {d&reg1&[1]}, [TMP2, :32]
   .else
      pixld1_s elem_size, reg1, mem_operand
      pixld1_s elem_size, reg2, mem_operand
   .endif
.endm

.macro pixld0_s elem_size, reg1, idx, mem_operand
   .if elem_size == 16
      mov TMP1, VX, asr #16
      adds VX, VX, UNIT_X
      5: subpls VX, VX, SRC_WIDTH_FIXED
         bpl 5b
         add TMP1, mem_operand, TMP1, asl #1
         vld1.16 {d&reg1&[idx]}, [TMP1, :16]
.if elem_size == 32
  mov   TMP1, VX, asr #16
  adds  VX, VX, UNIT_X
5: subpls VX, VX, SRC_WIDTH_FIXED
  bpl   5b
  add   TMP1, mem_operand, TMP1, asl #2
  vld1.32 {d&reg1&[idx]}, [TMP1, :32]
.endif
.endm

.macro pixld_s_internal numbytes, elem_size, basereg, mem_operand
  .if numbytes == 32
    pixld2_s elem_size, %(basereg+4), %(basereg+5), mem_operand
    pixld2_s elem_size, %(basereg+6), %(basereg+7), mem_operand
    pixdeinterleave elem_size, %(basereg+4)
  .elseif numbytes == 16
    pixld2_s elem_size, %(basereg+2), %(basereg+3), mem_operand
  .elseif numbytes == 8
    pixld1_s elem_size, %(basereg+1), mem_operand
  .elseif numbytes == 4
    .if elem_size == 32
      pixld0_s elem_size, %(basereg+0), 1, mem_operand
    .elseif elem_size == 16
      pixld0_s elem_size, %(basereg+0), 2, mem_operand
      pixld0_s elem_size, %(basereg+0), 3, mem_operand
    .else
      pixld0_s elem_size, %(basereg+0), 4, mem_operand
      pixld0_s elem_size, %(basereg+0), 5, mem_operand
      pixld0_s elem_size, %(basereg+0), 6, mem_operand
      pixld0_s elem_size, %(basereg+0), 7, mem_operand
    .endif
  .elseif numbytes == 2
    .if elem_size == 16
      pixld0_s elem_size, %(basereg+0), 1, mem_operand
    .else
      pixld0_s elem_size, %(basereg+0), 2, mem_operand
      pixld0_s elem_size, %(basereg+0), 3, mem_operand
    .endif
  .elseif numbytes == 1
    pixld0_s elem_size, %(basereg+0), 1, mem_operand
  .else
    .error "unsupported size: numbytes"
  .endif
.endm

.macro pixld_s numpix, bpp, basereg, mem_operand
  .if bpp > 0
    pixld_s_internal % (numpix * bpp / 8), %(bpp), basereg, mem_operand
  .endif
.endm
/* deinterleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixdeinterleave bpp, basereg
.if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
vuzp8 %(basereg+0), %(basereg+1)
vuzp8 %(basereg+2), %(basereg+3)
vuzp8 %(basereg+1), %(basereg+3)
vuzp8 %(basereg+0), %(basereg+2)
.endif
.endm

/* interleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixinterleave bpp, basereg
.if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
vzip8 %(basereg+0), %(basereg+2)
vzip8 %(basereg+1), %(basereg+3)
vzip8 %(basereg+2), %(basereg+3)
vzip8 %(basereg+0), %(basereg+1)
.endif
.endm

/*
* This is a macro for implementing cache preload. The main idea is that
* cache preload logic is mostly independent from the rest of pixels
* processing code. It starts at the top left pixel and moves forward
* across pixels and can jump across scanlines. Prefetch distance is
* handled in an 'incremental' way: it starts from 0 and advances to the
* optimal distance over time. After reaching optimal prefetch distance,
* it is kept constant. There are some checks which prevent prefetching
* unneeded pixel lines below the image (but it still can prefetch a bit
* more data on the right side of the image - not a big issue and may
* be actually helpful when rendering text glyphs). Additional trick is
* the use of LDR instruction for prefetch instead of PLD when moving to
* the next line, the point is that we have a high chance of getting TLB
* miss in this case, and PLD would be useless.
*
* This sounds like it may introduce a noticeable overhead (when working with
* fully cached data). But in reality, due to having a separate pipeline and
* instruction queue for NEON unit in ARM Cortex-A8, normal ARM code can
* execute simultaneously with NEON and be completely shadowed by it. Thus
* we get no performance overhead at all (*). This looks like a very nice
* feature of Cortex-A8, if used wisely. We don't have a hardware prefetcher,
* but still can implement some rather advanced prefetch logic in software
* for almost zero cost!
*
* (*) The overhead of the prefetcher is visible when running some trivial
* pixels processing like simple copy. Anyway, having prefetch is a must
* when working with the graphics data.
*/

.macro PF a, x:vararg
  .if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_ADVANCED)
    a x
  .endif
.endm

.macro cache_preload std_increment, boost_increment
  .if (src_bpp_shift >= 0) || (dst_r_bpp ! 0) || (mask_bpp_shift >= 0)
    .if regs_shortage
      PF ldr ORIG_W, [sp] /* If we are short on regs, ORIG_W is kept on stack */
    .endif
    .if std_increment != 0
      PF add PF_X, PF_X, #std_increment
    .endif
    PF tst PF_CTL, #0xF
    PF addne PF_X, PF_X, #boost_increment
    PF subne PF_CTL, PF_CTL, #1
    PF cmp PF_X, ORIG_W
    .if src_bpp_shift >= 0
      PF pld, [PF_SRC, PF_X, lsl #src_bpp_shift]
    .endif
    .if dst_r_bpp != 0
      PF pld, [PF_DST, PF_X, lsl #dst_bpp_shift]
    .endif
    .if mask_bpp_shift >= 0
      PF pld, [PF_MASK, PF_X, lsl #mask_bpp_shift]
    .endif
    PF subge PF_X, PF_X, ORIG_W
    PF subges PF_CTL, PF_CTL, #0x10
    .if src_bpp_shift >= 0
      PF ldrgeb DUMMY, [PF_SRC, SRC_STRIDE, lsl #src_bpp_shift]!
    .endif
    .if dst_r_bpp != 0
      PF ldrgeb DUMMY, [PF_DST, DST_STRIDE, lsl #dst_bpp_shift]!
    .endif
    .if mask_bpp_shift >= 0
      PF ldrgeb DUMMY, [PF_MASK, MASK_STRIDE, lsl #mask_bpp_shift]!
.macro cache_preload_simple
   .if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_SIMPLE)
      .if src_bpp > 0
         pld [SRC, #(PREFETCH_DISTANCE_SIMPLE * src_bpp / 8)]
      .endif
      .if dst_r_bpp > 0
         pld [DST_R, #(PREFETCH_DISTANCE_SIMPLE * dst_r_bpp / 8)]
      .endif
      .if mask_bpp > 0
         pld [MASK, #(PREFETCH_DISTANCE_SIMPLE * mask_bpp / 8)]
      .endif
   .endif
.endm

.macros fetch_mask_pixblock
   pixld pixblock_size, mask_bpp, 
      (mask_basereg - pixblock_size * mask_bpp / 64), MASK
.endm

/*
 * Macro which is used to process leading pixels until destination
 * pointer is properly aligned (at 16 bytes boundary). When destination
 * buffer uses 16bpp format, this is unnecessary, or even pointless.
 */

.macro ensure_destination_ptr_alignment
   .if dst_w_bpp != 24
      tst DST_R, #0xF
      beq 2f
   .irp lowbit, 1, 2, 4, 8, 16
   local skip1
   .if (dst_w_bpp <= (lowbit * 8)) & (lowbit * 8) < (pixblock_size * dst_w_bpp))
      .if lowbit < 16 /* we don’t need more than 16-byte alignment */
         tst DST_R, #lowbit
         beq 1f
      .endif
      pixld_src (lowbit * 8 / dst_w_bpp), src_bpp, src_basereg, SRC
      pixld (lowbit * 8 / dst_w_bpp), mask_bpp, mask_basereg, MASK
      .if dst_r_bpp > 0
         pixld_a (lowbit * 8 / dst_r_bpp), dst_r_bpp, dst_r_basereg, DST_R
      .else
         add DST_R, DST_R, #lowbit
   .endif
.endm
PF add PF_X, PF_X, #(lowbit * 8 / dst_w_bpp)
sub W, W, #(lowbit * 8 / dst_w_bpp)
1:
.endif
.endr
pixdeinterleave src_bpp, src_basereg
pixdeinterleave mask_bpp, mask_basereg
pixdeinterleave dst_r_bpp, dst_r_basereg

process_pixblock_head
cache_preload 0, pixblock_size
cache_preload_simple
process_pixblock_tail
.pixinterleave dst_w_bpp, dst_w_basereg
.irp lowbit, 1, 2, 4, 8, 16
.if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
.if lowbit < 16 /* we don't need more than 16-byte alignment */
tst DST_W, #lowbit
beq 1f
.endif
.pixst_a (lowbit * 8 / dst_w_bpp), dst_w_bpp, dst_w_basereg, DST_W
1:
.endif
.endr
.endif
2:
.endm

/*
 * Special code for processing up to (pixblock_size - 1) remaining
 * trailing pixels. As SIMD processing performs operation on
 * pixblock_size pixels, anything smaller than this has to be loaded
 * and stored in a special way. Loading and storing of pixel data is
 * performed in such a way that we fill some 'slots' in the NEON
 * registers (some slots naturally are unused), then perform compositing
 * operation as usual. In the end, the data is taken from these 'slots'
 * and saved to memory.
 *
 * cache_preload_flag - allows to suppress prefetch if
 * set to 0
 * dst_aligned_flag - selects whether destination buffer
 * is aligned
 */
.macro process_trailing_pixels cache_preload_flag, \
 dst_aligned_flag, \
 process_pixblock_head, \n  cache_preload_flag, \
  dst_aligned_flag, \
  process_pixblock_head, \
  cache_preload_simple, \
  process_pixblock_tail, \
  pixdeinterleave dst_r_bpp, dst_r_basereg, \
  pixdeinterleave mask_bpp, mask_basereg, \
  pixdeinterleave src_bpp, src_basereg, \
  process_pixblock_head, \
  cache_preload 0, pixblock_size, \
  cache_preload_simple, \
  process_pixblock_tail, \
  pixinterleave dst_w_bpp, dst_w_basereg
  .irp lowbit, 1, 2, 4, 8, 16, \
  .if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
  .if lowbit < 16 /* we don't need more than 16-byte alignment */
  tst DST_W, #lowbit
  beq 1f
  .endif
  .endif
  .endr
  .endif
  2:
  .endm
process_pixblock_tail, \
process_pixblock_tail_head

tst W, #(pixblock_size - 1)
beq 2f
.irp chunk_size, 16, 8, 4, 2, 1
.if pixblock_size > chunk_size
  tst W, #chunk_size
  beq 1f
  pixld_src chunk_size, src_bpp, src_basereg, SRC
  pixld chunk_size, mask_bpp, mask_basereg, MASK
.if dst_aligned_flag != 0
  pixld_a chunk_size, dst_r_bpp, dst_r_basereg, DST_R
.else
  pixld chunk_size, dst_r_bpp, dst_r_basereg, DST_R
.endif
.if cache_preload_flag != 0
  PF add PF_X, PF_X, #chunk_size
.endif
1:
.endif
.endr
endr

pixdeinterleave src_bpp, src_basereg
pixdeinterleave mask_bpp, mask_basereg
pixdeinterleave dst_r_bpp, dst_r_basereg

process_pixblock_head
.if cache_preload_flag != 0
  cache_preload 0, pixblock_size
  cache_preload_simple
.endif

process_pixblock_tail
  pixinterleave dst_w_bpp, dst_w_basereg
.irp chunk_size, 16, 8, 4, 2, 1
.if pixblock_size > chunk_size
  tst W, #chunk_size
  beq 1f
.if dst_aligned_flag != 0
  pixst_a chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.else
  pixst chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.endif
1:
.endif
.endr

2:
.endm

/*
* Macro, which performs all the needed operations to switch to the next
* scanline and start the next loop iteration unless all the scanlines
* are already processed.
*
.. macro advance_to_next_scanline start_of_loop_label
.. if regs_shortage
   ldrd        W, [sp] /* load W and H (width and height) from stack */
.. else
   mov         W, ORIG_W
.. endif
   add         DST_W, DST_W, DST_STRIDE, lsl #dst_bpp_shift
.. if src_bpp != 0
   add         SRC, SRC, SRC_STRIDE, lsl #src_bpp_shift
.. endif
.. if mask_bpp != 0
   add         MASK, MASK, MASK_STRIDE, lsl #mask_bpp_shift
.. endif
.. if (dst_w_bpp != 24)
   sub         DST_W, DST_W, W, lsl #dst_bpp_shift
.. endif
.. if (src_bpp != 24) && (src_bpp != 0)
   sub         SRC, SRC, W, lsl #src_bpp_shift
.. endif
.. if (mask_bpp != 24) && (mask_bpp != 0)
   sub         MASK, MASK, W, lsl #mask_bpp_shift
.. endif
   subs        H, H, #1
   mov         DST_R, DST_W
.. if regs_shortage
   str         H, [sp, #4] /* save updated height to stack */
.. endif
   bge         start_of_loop_label
.. endm

/*
* Registers are allocated in the following way by default:
* d0, d1, d2, d3   - reserved for loading source pixel data
* d4, d5, d6, d7   - reserved for loading destination pixel data
* d24, d25, d26, d27 - reserved for loading mask pixel data
* d28, d29, d30, d31 - final destination pixel data for writeback to memory
*/
.. macro generate_composite_function fname, \
   src_bpp, \n   mask_bpp, \n   dst_w_bpp, \n   flags, \n   pixblock_size, \n   prefetch_distance, \n
init, \n cleanup, \n process_pixblock_head, \n process_pixblock_tail, \n process_pixblock_tail_head, \n dst_w_basereg_ = 28, \n dst_r_basereg_ = 4, \n src_basereg_ = 0, \n mask_basereg_ = 24

\.func fname
.\global fname
/* For ELF format also set function visibility to hidden */
 ifndef __ELF__
 .\hidden fname
 .\type fname, %function
 endif
 fname:
 push {r4-r12, lr} /* save all registers */

 /*
 * Select prefetch type for this function. If prefetch distance is
 * set to 0 or one of the color formats is 24bpp, SIMPLE prefetch
 * has to be used instead of ADVANCED.
 */
 .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_DEFAULT
 .if prefetch_distance == 0
 .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
 .elseif (PREFETCH_TYPE_CURRENT > PREFETCH_TYPE_SIMPLE) && \n (src_bpp_ == 24) || (mask_bpp_ == 24) || (dst_w_bpp_ == 24))
 .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_SIMPLE
 .endif

 /*
 * Make some macro arguments globally visible and accessible
 * from other macros
 */
 .set src_bpp, src_bpp_
 .set mask_bpp, mask_bpp_
 .set dst_w_bpp, dst_w_bpp_
 .set pixblock_size, pixblock_size_
 .set dst_w_basereg, dst_w_basereg_
 .set dst_r_basereg, dst_r_basereg_
 .set src_basereg, src_basereg_
 .set mask_basereg, mask_basereg_

 .macro pixld_src x:vararg
 pixld x
.endm
.macro fetch_src_pixblock
    pixld_src pixblock_size, src_bpp, \n    (src_basereg - pixblock_size * src_bpp / 64), SRC
.endm
/*
* Assign symbolic names to registers
*/
W .req r0 /* width (is updated during processing) */
H .req r1 /* height (is updated during processing) */
DST_W .req r2 /* destination buffer pointer for writes */
DST_STRIDE .req r3 /* destination image stride */
SRC .req r4 /* source buffer pointer */
SRC_STRIDE .req r5 /* source image stride */
DST_R .req r6 /* destination buffer pointer for reads */
MASK .req r7 /* mask pointer */
MASK_STRIDE .req r8 /* mask stride */
PF_CTL .req r9 /* combined lines counter and prefetch */
    /* distance increment counter */
PF_X .req r10 /* pixel index in a scanline for current */
    /* pretetch position */
PF_SRC .req r11 /* pointer to source scanline start */
    /* for prefetch purposes */
PF_DST .req r12 /* pointer to destination scanline start */
    /* for prefetch purposes */
PF_MASK .req r14 /* pointer to mask scanline start */
    /* for prefetch purposes */
/*
* Check whether we have enough registers for all the local variables.
* If we don't have enough registers, original width and height are
* kept on top of stack (and 'regs_shortage' variable is set to indicate
* this for the rest of code). Even if there are enough registers, the
* allocation scheme may be a bit different depending on whether source
* or mask is not used.
*/
.if (PREFETCH_TYPE_CURRENT < PREFETCH_TYPE_ADVANCED)
    ORIG_W .req r10 /* saved original width */
    DUMMY .req r12 /* temporary register */
    .set regs_shortage, 0
.elseif mask_bpp == 0
    ORIG_W .req r7 /* saved original width */
    DUMMY .req r8 /* temporary register */
    .set regs_shortage, 0
.elseif src_bpp == 0
    ORIG_W .req r4 /* saved original width */
    DUMMY .req r5 /* temporary register */
.set  regs_shortage, 0

else

  ORIG_W .req r1 /* saved original width */
  DUMMY .req r1 /* temporary register */
  .set  regs_shortage, 1

.endif

.set mask_bpp_shift, -1

.if src_bpp == 32
  .set src_bpp_shift, 2
elseif src_bpp == 24
  .set src_bpp_shift, 0
elseif src_bpp == 16
  .set src_bpp_shift, 1
elseif src_bpp == 8
  .set src_bpp_shift, 0
elseif src_bpp == 0
  .set src_bpp_shift, -1
else
  .error "requested src bpp (src_bpp) is not supported"
.endif

.if mask_bpp == 32
  .set mask_bpp_shift, 2
elseif mask_bpp == 24
  .set mask_bpp_shift, 0
elseif mask_bpp == 8
  .set mask_bpp_shift, 0
elseif mask_bpp == 0
  .set mask_bpp_shift, -1
else
  .error "requested mask bpp (mask_bpp) is not supported"
.endif

.if dst_w_bpp == 32
  .set dst_bpp_shift, 2
elseif dst_w_bpp == 24
  .set dst_bpp_shift, 0
elseif dst_w_bpp == 16
  .set dst_bpp_shift, 1
elseif dst_w_bpp == 8
  .set dst_bpp_shift, 0
else
  .error "requested dst bpp (dst_w_bpp) is not supported"
.endif

.if (((flags) & FLAG_DST_READWRITE) != 0)
  .set dst_r_bpp, dst_w_bpp
else
  .set dst_r_bpp, 0
.endif
.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
.set DEINTERLEAVE_32BPP_ENABLED, 1
.else
.set DEINTERLEAVE_32BPP_ENABLED, 0
.endif

.if prefetch_distance < 0 || prefetch_distance > 15
.error "invalid prefetch distance (prefetch_distance)"
.endif

.if src_bpp > 0
   ldr SRC, [sp, #40]
.endif
.if mask_bpp > 0
   ldr MASK, [sp, #48]
.PF mov PF_X, #0
.if src_bpp > 0
   ldr SRC_STRIDE, [sp, #44]
.endif
.if mask_bpp > 0
   ldr MASK_STRIDE, [sp, #52]
.endif
   mov DST_R, DST_W

.if src_bpp == 24
   sub SRC_STRIDE, SRC_STRIDE, W
   sub SRC_STRIDE, SRC_STRIDE, W, lsl #1
.endif
.if mask_bpp == 24
   sub MASK_STRIDE, MASK_STRIDE, W
   sub MASK_STRIDE, MASK_STRIDE, W, lsl #1
.endif
.if dst_w_bpp == 24
   sub DST_STRIDE, DST_STRIDE, W
   sub DST_STRIDE, DST_STRIDE, W, lsl #1
.endif

/*
* Setup advanced prefetcher initial state
*/
PF mov PF_SRC, SRC
PF mov PF_DST, DST_R
PF mov PF_MASK, MASK
/* PF_CTL = prefetch_distance | ((h - 1) << 4) */
PF mov PF_CTL, H, lsl #4
PF add PF_CTL, #((prefetch_distance - 0x10)
init
.if regs_shortage
    push   {r0, r1}
.endif
.subs   H, H, #1
.if regs_shortage
    str    H, [sp, #4] /* save updated height to stack */
.else
    mov    ORIG_W, W
.endif
.blt    9f
.cmp    W, #(pixblock_size * 2)
.blt    8f
/*
* This is the start of the pipelined loop, which if optimized for
* long scanlines
*/
0:
    ensure_destination_ptr_alignment process_pixblock_head, \
        process_pixblock_tail, \
        process_pixblock_tail_head
/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
.pixld_a pixblock_size, dst_r_bpp, \n        (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
.fetch_src_pixblock
.pixld pixblock_size, mask_bpp, \n        (mask_basereg - pixblock_size * mask_bpp / 64), MASK
.PF add PF_X, PF_X, #pixblock_size
.process_pixblock_head
.cache_preload 0, pixblock_size
.cache_preload_simple
.subs    W, W, #(pixblock_size * 2)
.blt     2f
1:
    process_pixblock_tail_head
.cache_preload_simple
.subs    W, W, #pixblock_size
.bge     1b
2:
    process_pixblock_tail
.pixst_a pixblock_size, dst_w_bpp, \n        (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
/* Process the remaining trailing pixels in the scanline */
.process_trailing_pixels 1, 1, \n        process_pixblock_head, \
        process_pixblock_tail, \
        process_pixblock_tail_head
process_pixblock_tail, \nprocess_pixblock_tail_head

advance_to_next_scanline 0b

.if regs_shortage
    pop {r0, r1}
.endif

cleanup
pop {r4-r12, pc} /* exit */

/*
* This is the start of the loop, designed to process images with small width
* (less than pixblock_size * 2 pixels). In this case neither pipelining
* nor prefetch are used.
*/

8:
/* Process exactly pixblock_size pixels if needed */
tst   W, #pixblock_size
beq   1f
pixld pixblock_size, dst_r_bpp, \n      (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld pixblock_size, mask_bpp, \n      (mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
process_pixblock_tail
pixst pixblock_size, dst_w_bpp, \n      (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
1:
/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 0, 0, \n    process_pixblock_head, \n    process_pixblock_tail, \n    process_pixblock_tail_head

advance_to_next_scanline 8b
9:
.if regs_shortage
    pop {r0, r1}
.endif

cleanup
pop {r4-r12, pc} /* exit */

.purgem fetch_src_pixblock
.purgem pixld_src

.unreq SRC
.unreq MASK
.unreq DST_R
.unreq DST_W
/*
 * A simplified variant of function generation template for a single
 * scanline processing (for implementing pixman combine functions)
 */

.macro generate_composite_function_scanline        use_nearest_scaling, \
   fname, \
   src_bpp_, \
   mask_bpp_, \
   dst_w_bpp_, \
   flags, \
   pixblock_size_, \
   init, \
   cleanup, \
   process_pixblock_head, \
   process_pixblock_tail, \
   process_pixblock_tail_head, \
   dst_w_basereg_ = 28, \
   dst_r_basereg_ = 4, \
   src_basereg_  = 0, \
   mask_basereg_ = 24

   .func fname
   .global fname
   /* For ELF format also set function visibility to hidden */
   #ifdef __ELF__
   .hidden fname
   .type fname, %function
   #endif
   fname:
   .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
   /*
   * Make some macro arguments globally visible and accessible
   * from other macros
   */
/*
 .set src_bpp, src_bpp_
 .set mask_bpp, mask_bpp_
 .set dst_w_bpp, dst_w_bpp_
 .set pixblock_size, pixblock_size_
 .set dst_w_basereg, dst_w_basereg_
 .set dst_r_basereg, dst_r_basereg_
 .set src_basereg, src_basereg_
 .set mask_basereg, mask_basereg_

 .if use_nearest_scaling != 0
 */
 *
 */
* Assign symbolic names to registers for nearest scaling
*/
W .req  r0
DST_W .req  r1
SRC .req  r2
VX .req  r3
UNIT_X .req  ip
MASK .req  lr
TMP1 .req  r4
TMP2 .req  r5
DST_R .req  r6
SRC_WIDTH_FIXED .req  r7

.macro pixld_src x:vararg
  pixld_s x
.endm

ldr  UNIT_X, [sp]
push  {r4-r8, lr}
ldr  SRC_WIDTH_FIXED, [sp, #(24 + 4)]
.if mask_bpp != 0
  ldr  MASK, [sp, #(24 + 8)]
.endif
.else
 */
* Assign symbolic names to registers
*/
W .req  r0  /* width (is updated during processing) */
DST_W .req  r1  /* destination buffer pointer for writes */
SRC .req  r2  /* source buffer pointer */
DST_R .req  ip  /* destination buffer pointer for reads */
MASK .req  r3  /* mask pointer */

.macro pixld_src x:vararg
  pixld x
.endm
.endif

.if (((flags) & FLAG_DST_READWRITE) != 0)
   .set dst_r_bpp, dst_w_bpp
.else
   .set dst_r_bpp, 0
.endif
.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
   .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
   .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif

.macro fetch_src_pixblock
   pixld_src   pixblock_size, src_bpp, \(src_basereg - pixblock_size * src_bpp / 64), SRC
.endm

.init
mov         DST_R, DST_W
cmp         W, #pixblock_size
blt         8f
ensure_destination_ptr_alignment process_pixblock_head, \ process_pixblock_tail, \ process_pixblock_tail_head
subs        W, W, #pixblock_size
blt         7f
/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
pixld_a     pixblock_size, dst_r_bpp, \(dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld       pixblock_size, mask_bpp, \(mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
subs        W, W, #pixblock_size
blt         2f
1:
   process_pixblock_tail_head
subs        W, W, #pixblock_size
bge         1b
2:
   process_pixblock_tail
pixst_a     pixblock_size, dst_w_bpp, \(dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
/* Process the remaining trailing pixels in the scanline (dst aligned) */
process_trailing_pixels 0, 1, \
    process_pixblock_head, \
    process_pixblock_tail, \
    process_pixblock_tail_head

cleanup
.if use_nearest_scaling != 0
    pop         {r4-r8, pc}  /* exit */
.else
    bx          lr  /* exit */
.endif
/* Process the remaining trailing pixels in the scanline (dst unaligned) */
process_trailing_pixels 0, 0, \
    process_pixblock_head, \
    process_pixblock_tail, \
    process_pixblock_tail_head

cleanup
.if use_nearest_scaling != 0
    pop         {r4-r8, pc}  /* exit */
.unreq      DST_R
.unreq      SRC
.unreq      W
.unreq      VX
.unreq      UNIT_X
.unreq      TMP1
.unreq      TMP2
.unreq      DST_W
.unreq      MASK
.unreq      SRC_WIDTH_FIXED
.else
    bx          lr  /* exit */
.unreq      SRC
.unreq      MASK
.unreq      DST_R
.unreq      DST_W
.unreq      W
.endif
.purgem     fetch_src_pixblock
.purgem     pixld_src
/* Default prologue/epilogue, nothing special needs to be done */

.macro default_init
  .endm

.macro default_cleanup
  .endm

/*
 * Prologue/epilogue variant which additionally saves/restores d8-d15
 * registers (they need to be saved/restored by callee according to ABI).
 * This is required if the code needs to use all the NEON registers.
 */

.macro default_init_need_all_regs
  vpush    {d8-d15}
  .endm

.macro default_cleanup_need_all_regs
  vpop     {d8-d15}
  .endm

/******************************************************************************/

/*
 * Conversion of 8 r5g6b6 pixels packed in 128-bit register (in)
 * into a planar a8r8g8b8 format (with a, r, g, b color components
 * stored into 64-bit registers out_a, out_r, out_g, out_b respectively).
 * Warning: the conversion is destructive and the original
 * value (in) is lost.
 */

.macro convert_0565_to_8888 in, out_a, out_r, out_g, out_b
  vshrn.u16   out_r, in,    #8
  vshrn.u16   out_g, in,    #3
  vsli.u16    in,    in,    #5
  .endm
vmov.u8    out_a, #255
vsri.u8    out_r, out_r, #5
vsri.u8    out_g, out_g, #6
vshrn.u16  out_b, in,  #2
.endm

.macro convert_0565_to_x888 in, out_r, out_g, out_b
vshrn.u16  out_r, in,  #8
vshrn.u16  out_g, in,  #3
vsli.u16   in,    in,    #5
vsri.u8    out_r, out_r, #5
vsri.u8    out_g, out_g, #6
vshrn.u16  out_b, in,  #2
.endm

/******************************************************************
* Conversion from planar a8r8g8b8 format (with a, r, g, b color components
* in 64-bit registers in_a, in_r, in_g, in_b respectively) into 8 r5g6b6
* pixels packed in 128-bit register (out). Requires two temporary 128-bit
* registers (tmp1, tmp2)
*/
.macro convert_8888_to_0565 in_r, in_g, in_b, out, tmp1, tmp2
vshll.u8   tmp1, in_g, #8
vshll.u8   out, in_r, #8
vshll.u8   tmp2, in_b, #8
vsri.u16   out, tmp1, #5
vsri.u16   tmp,  tmp2, #11
.endm

/******************************************************************
* Conversion of four r5g6b5 pixels (in) to four x8r8g8b8 pixels
* returned in (out0, out1) registers pair. Requires one temporary
* 64-bit register (tmp). 'out1' and 'in' may overlap, the original
* value from 'in' is lost
*/
.macro convert_four_0565_to_x888_packed in, out0, out1, tmp
vshl.u16   out0, in,   #5 /* G top 6 bits */
vshl.u16   tmp,  in,   #11/* B top 5 bits */
vsri.u16   in,    in,   #5 /* R is ready in top bits */
vsri.u16   out0, out0, #6 /* G is ready in top bits */
vsri.u16   tmp,  tmp,  #5 /* B is ready in top bits */
vshr.u16   out1, in,   #8 /* R is in place */
vsri.u16   out0, tmp,  #8 /* G & B is in place */
vzip.u16   out0, out1 /* everything is in place */
.endm

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 */

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b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.
If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.
d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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Ty Coon, President of Vice

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Some devices are designed to deny users access to install or run
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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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The "System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

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You may convey a covered work in object code form under the terms of sections 4 and 5, provided that you also convey the machine-readable Corresponding Source under the terms of this License, in one of these ways:

a) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.

b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

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 *
 * These routines may need modification for non-Unix environments or
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 */
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[PCRE]

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THE BASIC LIBRARY FUNCTIONS

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Written by: Philip Hazel
Email local part: ph10
THE C++ WRAPPER FUNCTIONS
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---------------------------------------------------------------------------
[zlib]

/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.3, July 18th, 2005

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jloup@gzip.org          madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for
Comments) 1950 to 1952 in the files http://www.ietf.org/rfc/rfc1950.txt
(zlib format), rfc1951.txt (deflate format) and rfc1952.txt (gzip format).
/*

---------------------------------------------------------------------------
[edoc, syntax_tools]

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[eldap]

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Version 2.1, February 1999

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*/    </code>
</section>
</chapter>
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Version 1.1

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Upstream Authors:

  Eliot Horowitz
  Dwight Merriman
  Aaron Staple
  Michael Dirolf
  Kristina Chodorow

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Email local part: ph10
Email domain: cam.ac.uk

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1.47 django-tagging 0.3.1 3.el6
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1.49 asn1c 0.9.24

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1.52 perl 5.20.0

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<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

1.53 gir1.2-gdkpixbuf-2.0 2.27.0

1.53.1 Available under license:

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1.55 libpng 1.5.22

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```c
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```

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Glenn Randers-Pehrson
glenrp at users.sourceforge.net
March 26, 2015

1.56 haproxy 1.7.2

1.56.1 Available under license:

HAPROXY's license - 2006/06/15

Historically, haproxy has been covered by GPL version 2. However, an issue appeared in GPL which will prevent external non-GPL code from being built using the headers provided with haproxy. My long-term goal is to build a core system able to load external modules to support specific application protocols.

Since some protocols are found in rare environments (finance, industry, ...), some of them might be accessible only after signing an NDA. Enforcing GPL on such modules would only prevent them from ever being implemented, while not providing anything useful to ordinary users.
For this reason, I *want* to be able to support binary only external modules when needed, with a GPL core and GPL modules for standard protocols, so that people fixing bugs don't keep them secretly to try to stay over competition.

The solution was then to apply the LGPL license to the exportable include files, while keeping the GPL for all the rest. This way, it still is mandatory to redistribute modified code under customer request, but at the same time, it is expressly permitted to write, compile, link and load non-GPL code using the LGPL header files and not to distribute them if it causes a legal problem.

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Willy Tarreau - w@1wt.eu

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Version 2.1, February 1999

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1.59 log4cxx 0.10.0 13ubuntu2

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  @import url("./css/maven-theme.css");
  @import url("./css/site.css");
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    log4cxx

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    <li class="none">
      <a href="building/ant.html">Apache Ant</a>
    </li>
  </ul>
</li>
</ul>
<li class="none">
    <a href="building/maven.html">Apache Maven 2</a>
</li>

<li class="none">
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    </li>
    <li class="none">
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    </li>
    <li class="none">
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    </li>
</ul>

<a href="http://maven.apache.org/" title="Built by Maven" class="poweredBy">
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</a>
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      <p>Typically the licenses listed for the project are that of the project itself, and not of dependencies.</p>
    </div>
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      <h2>Project License</h2>
      <div class="section">
        <h3>Apache License, Version 2.0</h3>
        <div class="source">
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This code is derived from section 17.1 of Applied Cryptography, second edition, which describes a stream cipher allegedly compatible with RSA Labs "RC4" cipher (the actual description of which is a trade secret). The same algorithm is used as a stream cipher called "arcfour" in Tatu Ylonen's ssh package.

Here the stream cipher has been modified always to include the time when initializing the state. That makes it impossible to regenerate the same random sequence twice, so this can't be used for encryption, but will generate good random numbers.
RC4 is a registered trademark of RSA Laboratories.

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This code implements the MD5 message-digest algorithm. The algorithm is due to Ron Rivest. This code was written by Colin Plumb in 1993, no copyright is claimed. This code is in the public domain; do with it what you wish.

Equivalent code is available from RSA Data Security, Inc. This code has been tested against that, and is equivalent, except that you don't need to include two pages of legalese with every copy.

To compute the message digest of a chunk of bytes, declare an MD5Context structure, pass it to MD5Init, call MD5Update as needed on buffers full of bytes, and then call MD5Final, which will fill a supplied 16-byte array with the digest.

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compatible with RSA Labs "RC4" cipher (the actual description of which is a trade secret). The same algorithm is used as a stream cipher called "arcfour" in Tatu Ylonen's ssh package.

Here the stream cipher has been modified always to include the time when initializing the state. That makes it impossible to regenerate the same random sequence twice, so this can't be used for encryption, but will generate good random numbers.

RC4 is a registered trademark of RSA Laboratories.

This code implements the MD5 message-digest algorithm. The algorithm is due to Ron Rivest. This code was written by Colin Plumb in 1993, no copyright is claimed. This code is in the public domain; do with it what you wish.

Equivalent code is available from RSA Data Security, Inc. This code has been tested against that, and is equivalent, except that you don't need to include two pages of legalese with every copy.

To compute the message digest of a chunk of bytes, declare an MD5Context structure, pass it to MD5Init, call MD5Update as needed on buffers full of bytes, and then call MD5Final, which will fill a supplied 16-byte array with the digest.

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Contributors (in approximate order of appearance)

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Ben Elliston bje at cygnus dot com
 Initiated the project;
 setup the project infrastructure (CVS, web page, etc.);
 early prototype routines.

Ross Johnson rpj at callisto dot canberra dot edu dot au
 early prototype routines;
 ongoing project coordination/maintenance;
 implementation of spin locks and barriers;
 various enhancements;
 bug fixes;
 documentation;
 testsuite.

Robert Colquhoun rjc at trump dot net dot au
 Early bug fixes.
John E. Bossom
John dot Bossom at cognos dot com
Contributed substantial original working implementation;
bug fixes;
goinging guidance and standards interpretation.
Anders Norlander
Anders Norlander at hem2 dot passagen dot se
Early enhancements and runtime checking for supported
Win32 routines.
Tor Lilqvist
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General enhancements;
early bug fixes to condition variables.
Scott Lightner
Scott Lightner at curriculum dot com
Bug fix.
Kevin Ruland
Kevin dot Ruland at anheuser-busch dot com
Various bug fixes.
Mike Russomiker
Mike Russomiker at eai dot com
Bug fix.
Mark E. Armstrong
Mark E. Armstrong at pacbell dot net
Bug fixes.
Lorin Hochstein
Lorin Hochstein at xiphos dot ca
general bug fixes; bug fixes to condition variables.
Peter Slacik
Peter dot Slacik at tatramed dot sk
Bug fixes.
Mumit Khan
Mumit Khan at xraylith dot wisc dot edu
Fixes to work with Mingw32.
Milan Gardian
Milan Gardian at tatramed dot sk
Bug fixes and reports/analyses of obscure problems.
Aurelio Medina
Aurelio Medina at crt dot com
First implementation of read-write locks.
Graham Dumpleton
Graham dot Dumpleton at ra dot pad dot otc dot telstra dot com dot au
Bug fix in condition variables.
Tristan Savatier
Tristan Savatier at mpegtv dot com
WinCE port.
Erik Hensema
Erik Hensema at hensema dot xs4all dot nl
Bug fixes.
Rich Peters
Rich Peters at micro-magic dot com
Todd Owen
Todd Owen at lucidcalm dot dropbear dot id dot au
Bug fixes to dll loading.
Jason Nye
Jason Nye at nbnet dot nb dot ca
Implementation of async cancelation.
Fred Forest
Fred Forest at eticomm dot net
Kevin D. Clark
Kevin D. Clark at cabletron dot com
David Baggett
David Baggett at itasoftware dot com
Bug fixes.
Paul Redondo
Paul Redondo at matchvision dot com
Scott McCaskill
Scott McCaskill at 3dfx dot com
Bug fixes.
Jef Gearhart
Jef Gearhart at tpssys dot com
Bug fix.
Arthur Kantor
at bexusa dot com
Mutex enhancements.

Steven Reddie
at esemer dot com dot au
Bug fix.

Alexander TerekhoVTEREKHOV
at de dot ibm dot com
Re-implemented and improved read-write locks;
(with Louis Thomas) re-implemented and improved
condition variables;
enhancements to semaphores;
enhancements to mutexes;
new mutex implementation in 'futex' style;
suggested a robust implementation of pthread_once
similar to that implemented by V.Kliathcko;
system clock change handling re CV timeouts;
bug fixes.

Thomas Pfaff
at gmx dot net
Changes to make C version usable with C++ applications;
re-implemented mutex routines to avoid Win32 mutexes
and TryEnterCriticalSection;
procedure to fix Mingw32 thread-safety issues.

Franco Bez
at bez at gmx dot de
procedure to fix Mingw32 thread-safety issues.

Louis Thomas
at arbitrade dot com
(with Alexander Terekhov) re-implemented and improved
condition variables.

David Korndgk
at research dot att dot com
Ported to UWIN.

Phil Frisbie, Jr.
at hawksoft dot com
Bug fix.

Ralf Brese
at pdb4 dot siemens dot de
Bug fix.

prionx
at juno dot com
Bug fixes.

Max Woodbury
at cds dot duke dot edu
POSIX versioning conditionals;
reduced namespace pollution;
idea to separate routines to reduce statically
linked image sizes.

Rob Fanner
at stonethree dot com
Bug fix.

Michael Johnson
at maine dot rr dot com
Bug fix.

Nicolas Barry
at yahoo dot com
Bug fixes.

Piet van Bruggen
at newbridges dot nl
Bug fix.

Makoto Katoraven
at oldskool dot jp
AMD64 port.
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Agreement version 2.4

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The obligations of the Receiving Party under this Section shall continue during the Initial Term and for a period of five (5) years after expiration or termination of this Agreement. To the extent that the terms of the Non-Disclosure Agreement between Digia and Licensee conflict with the terms of this Section 9, this Section 9 shall be controlling over the terms of the Non-Disclosure Agreement.

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"Confidential Information" shall not include information that (a) is or becomes generally known to the public through no act or omission of the Receiving Party; (b) was in the Receiving Party's lawful possession prior to the disclosure hereunder and was not subject to limitations on disclosure or use; (c) is developed by the Receiving Party without access to the Confidential Information of the Disclosing Party or by persons who have not had access to the Confidential Information of the
Disclosing Party as proven by the written records of the Receiving Party; (d) is lawfully disclosed to the Receiving Party without restrictions, by a third party not under an obligation of confidentiality; or (e) the Receiving Party is legally compelled to disclose the information, in which case the Receiving Party shall assert the privileged and confidential nature of the information and cooperate fully with the Disclosing Party to protect against and prevent disclosure of any Confidential Information and to limit the scope of disclosure and the dissemination of disclosed Confidential Information by all legally available means.

The obligations of the Receiving Party under this Section shall continue during the Initial Term and for a period of five (5) years after expiration or termination of this Agreement. To the extent that the terms of the Non-Disclosure Agreement between Digia and Licensee conflict with the terms of this Section 9, this Section 9 shall be controlling over the terms of the Non-Disclosure Agreement.

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Paul Schmidt
Tim Wegner

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Glenn Randers-Pehrson
glennrp at users.sourceforge.net
March 29, 2012
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Balzs Dan <balazs.dan@gmail.com>
Bharat Mediratta <bharat@menalto.com>
Chandler Carruth <chandlerc@google.com>
Chris Prince <cprince@google.com>
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Keir Mierle <mierle@gmail.com>
Keith Ray <keith.ray@gmail.com>
Kenton Varda <kenton@google.com>
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Markus Heule <markus.heule@gmail.com>
Mika Raento <mikie@iki.fi>
Mikl’s Fazekas <mfazekas@szemafor.com>
Patrick Hanna <phanna@gmail.com>
Patrick Riley <spr@google.com>
Peter Kaminski <piotrk@google.com>
Preston Jackson <preston.a.jackson@gmail.com>
Rainer Klaffenboeck <rainer.klaffenboeck@dynatrace.com>
Russ Cox <rsc@google.com>
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Sigurur sgeirsson <siggi@google.com>
Tracy Bialik <tracy@pentad.com>
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