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**Contents**

1.1 Angular Bootstrap 0.12.0  
   1.1.1 Available under license  

1.2 Angular UI Sortable 1.1.1  
   1.2.1 Available under license

1.3 angular-chart.js 0.7.2  
   1.3.1 Available under license

1.4 Angular-dashboard-frmework 0.8.0  
   1.4.1 Available under license

1.5 AngularJS-1.3.16 1.3.16  
   1.5.1 Available under license

1.6 Arctext.js 1  
   1.6.1 Available under license

1.7 arp-scan 1.9-40-g69b2f70-1  
   1.7.1 Available under license

1.8 avahi 0.6.32  
   1.8.1 Available under license

1.9 BOOST C++ Library 1.58.0  
   1.9.1 Available under license
1.10 bootstrap 3.3.4
   1.10.1 Available under license
1.11 Chart.js 1.0.2
   1.11.1 Available under license
1.12 classnames 2.1.5
   1.12.1 Available under license
1.13 curl 7.61.1
   1.13.1 Available under license
1.14 d3 3.5.5
1.15 datepicker_ui 1.11.1 :2014/8/27
   1.15.1 Available under license
1.16 ejdb 1.2.12
   1.16.1 Available under license
1.17 es6-promise 3.0.2
   1.17.1 Available under license
1.18 event-emitter 0.3.3
   1.18.1 Available under license
1.19 FileSaver.js 1.1.0
   1.19.1 Available under license
1.20 Font Awesome 4.3.0
   1.20.1 Available under license
1.21 heirloom-mailx(nail) 1.2.5
   1.21.1 Available under license
1.22 httpclient 1.1
1.23 jquery 2.1.1
   1.23.1 Available under license
1.24 jquery 2.1.4
   1.24.1 Available under license
1.25 jquery-ui 1.11.2 :2014/11/17
   1.25.1 Available under license
1.26 jquery.flot.Stack 1.2
   1.26.1 Available under license
1.27 jquery.flot.tooltip 0.6.2
   1.27.1 Available under license
1.28 jquery.flot.valuelabels 2
   1.28.1 Available under license
1.29 json-c 0.12.1
   1.29.1 Available under license
1.30 jszip.js 2.4.0
1.30.1 Available under license
1.31 libcgroupl 0.41
1.31.1 Available under license
1.32 libmicrohttpd 0.9.42
1.32.1 Available under license
1.33 libnetconf 0.10
1.33.1 Available under license
1.34 libssh 0.7.5
1.34.1 Available under license
1.35 libuuid 2.25.2
1.35.1 Available under license
1.36 libwebsockets 2.2.1
1.36.1 Available under license
1.37 libxml2 2.9.8
1.37.1 Available under license
1.38 linux 3.10.70 :2015_T1.1p10
1.38.1 Available under license
1.39 lodash 3.7.0
1.39.1 Available under license
1.40 MiniZip 1.01E
1.40.1 Available under license
1.41 moment 2.10.6
1.41.1 Available under license
1.42 net-snmp 5.7.3-1ubuntu4.2
1.42.1 Available under license
1.43 Next.js 0.9.0
1.43.1 Available under license
1.44 ngDialog 0.4.0
1.44.1 Available under license
1.45 nginx 1.10.1
1.45.1 Available under license
1.46 nmap 6.47
1.46.1 Available under license
1.47 OpenSSH 7.3p1
1.47.1 Available under license
1.48 OpenSSL 1.0.2m
1.48.1 Notifications
1.48.2 Available under license
1.49 openssl 1.1.0b
1.49.1 Available under license

1.50 p2Linux 1.0
1.50.1 Available under license

1.51 Polar-SSL 0.10
1.51.1 Available under license

1.52 react 16.0.0
1.52.1 Available under license

1.53 react-select 1.0.0-rc.4
1.53.1 Available under license

1.54 react-tabs 2.1.0
1.54.1 Available under license

1.55 reflux 0.2.7
1.55.1 Available under license

1.56 RequireJS 2.1.18
1.56.1 Available under license

1.57 sarissa 0.9.9.6
1.57.1 Available under license

1.58 tcpdump 4.9.2 :0ubuntu0.16.04.1
1.58.1 Available under license

1.59 tinyxml 2.6.2
1.59.1 Available under license

1.60 u-boot 2013_Q3_eng_drop_2
1.60.1 Available under license

1.61 zlib 1.2.8
1.61.1 Available under license

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This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:
More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser. The Encore Multimax modifications were supplied by Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM. Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood. Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code. Al Dosser (dosser@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk) subsequently provided updates and information on variation between ULTRIX systems. Parag Patel (parag@netcom.com) supplied the A/UX code. Jesper Peterson (jep@mtiame.mtia.oz.au), Michel Schinz, and Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port. Thomas Funke (thf@zelator.in-berlin.de(?)) and Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports. Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code. Bill Janssen (janssen@parc.xerox.com) supplied the SunOS dynamic loader specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and Dave Detlefs (detlefs@src.dec.com) also provided several generic bug fixes. Alistair G. Crooks (age@uts.amdahl.com) supplied the NetBSD and 386BSD ports. Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port. Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to a Motorola 88K processor running CX/UX (Harris NightHawk). Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to nonIBM development environments (a nontrivial task). Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port. David Chase, then at Olivetti Research, suggested several improvements. Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the code to save and print call stacks for leak detection on a SPARC. Jesse Hull and John Ellis supplied the C++ interface code. Zhong Shao performed much of the experimentation that led to the current typed allocation facility. (His dynamic type inference code hasn't made it into the released version of the collector, yet.)
This package was debianized by Vladimir Prus <ghost@cs.msu.su> on Wed, 17 July 2002, 19:27:00 +0400.

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Aleksey Gurtovoy (agurtovoy@meta-comm.com)
Andrei Alexandrescu (andrewalex - at - hotmail.com) (See Boost list message of August 12, 2004 11:06:58 AM
EST)
Andrew Lumsdaine ()
Anthony Williams (anthony -at- justsoftwaresolutions.co.uk)
Beman Dawes (bdawes@acm.org)
Brad King (brad.king -at- kitware.com) (See Boost list message of Wed, 21 Jul 2004 11:15:46 -0400)
Brian Osman (osman -at- vvisions.com) (See CVS log)
Bruce Barr (schmoost -at- yahoo.com) (See Boost list of Mon, 16 Aug 2004 15:06:43 -0500)
Bruno da Silva de Oliveira (bruno - at - esss.com.br)
Christian Engstrom (christian.engstrom -at- glindra.org) (See Boost list message of Mon, 30 Aug 2004 14:31:49
+0200)
Cromwell D Enage (sponage -at- yahoo.com) (See Boost list message of August 12, 2004 11:49:13 AM EST)
Dan Gohman (djg -at- cray.com) (See Boost list messsage of Sat, 21 Aug 2004 10:54:59 +0100)
Dan Nuffer (dan -at- nuffer.name)
Daniel Frey (d.frey -at- gmx.de, daniel.frey -at- aixigo.de)
Daniel Nuffer (dan -at- nuffer.name)
Darin Adler (darin -at- bentspoon.com) (Email to Andreas Huber, see change log)
Daryle Walker (darylew - at - hotmail.com)
Dave Abrahams (dave@boost-consulting.com)
Dave Moore (dmoore -at- viefinancial.com) (See Boost list message of 18 Dec 2003 15:35:50 -0500)
David Abrahams (dave@boost-consulting.com)
Dietmar Kuehl (dietmar_kuehl -at- yahoo.com) (Email to Andreas Huber, see change log)
Douglas Gregor (gregod -at- cs.rpi.edu, dggregor -at- cs.indiana.edu, doug.gregor -at- gmail.com)
Dr John Maddock (john - at - johnmaddock.co.uk)
Edward D. Brey (brey -at- ductape.net) (Email to Andreas Huber, see change log)
Eric Ford (un506n902 -at- sneakemail.com) (See Boost list message of Sun, 15 Aug 2004 10:29:13 +0100)
Eric Friedman (ebf@users.sourceforge.net)
Eric Niebler (eric@boost-consulting.com)
Fernando Cacciola (fernando_cacciola@ciudad.com.ar)
Fernando Luis Cacciola Carballal (fernando_cacciola@ciudad.com.ar)
Francois Faure (Francois.Faure -at- imag.fr) (See CVS log)
Gary Powell (powellg - at - amazon.com) (See Boost list message of 10 Feb 2004 14:22:46 -0800)
Gennadiy Rozental (rogeeoff -at- mail.com) (Email to Andreas Huber, see change log)
Gottfried Ganssauge (Gottfried.Ganssauge -at- HAUFE.DE) (See Boost List message of Mon, 16 Aug 2004 10:09:19 +0200)
Gottfried Ganssauge (Gottfried.Ganssauge -at- HAUFE.DE) (Alternative spelling of Gottfried Ganssauge)
Greg Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)
Gregory Colvin (gregory.colvin -at- oracle.com) (See Boost list message of Sat, 14 Aug 2004 10:57:00 +0100)
Gunter Winkler (gunter.winkler -at- unibw-muenchen.de) (See Boost List message of Mon, 16 Aug 2004 10:24:17 +0200)
Hartmut Kaiser (hartmut.kaiser -at- gmail.com)
Herve Bronnimann (hbr -at- poly.edu)
Herv Brnnimann (hbr -at- poly.edu)
Housemarque Oy (Ilari Kuittinen ilari.kuittinen -at- housemarque.fi)
Howard Hinnant (hinnant -at- twcny.rr.com) (See Boost list message of July 25, 2004 3:44:49 PM EST)
Hubert Holin (hubert_holin -at- users.sourceforge.net)
Indiana University (
Itay Maman (imaman -at- users.sourceforge.net)
Jaakko Jrv (jajarvi -at- osl.iu.edu)
Jaap Suter (j.suter -at- student.utwente.nl) (See Boost list message of Thu, 16 Sep 2004 09:32:43 -0700)
Jeff Garland (jeff - at - crystallearsoftware.com) (see Boost list post of July 25, 2004 19:31:09 -0700)
Jens Maurer (Jens.Maurer@gmx.net)
Jeremy G Siek (jsiek@osl.iu.edu)
Jeremy Siek (jsiek@osl.iu.edu)
Joel de Guzman (joel -at- boost-consulting.com) (See Boost list message of July 25, 2004 8:32:00 PM EST)
John Bandela (jbandela-at-ufl.edu)
John Maddock (john - at - johnmaddock.co.uk)
John R Bandela (jbandela-at-ufl.edu)
Jonathan Turkanis (turkanis -at- coderage dot com)
Juergen Hunold (hunold -at- i-ive.uni-hannover.de) (See Boost List Message of Fri, 13 Aug 2004 19:39:55 +0200)
Kevlin Henney (kevin -at- curbralan.com) (See Boost list message of Wed, 15 Sep 2004 18:15:17 +0200)
Kresimir Fresl (fresl -at- master.grad.hr) (See Boost List message of August 16, 2004 8:23:35 AM EST)
Lars Gullik Bjnnes (larshj -at- lyx.org) (See Boost list message of Tue, 17 Aug 2004 15:49:02 +0100)
Lie-Quan Lee (liequan - at - slac.stanford.edu, llee - at - cs.indiana.edu)
Maarten Keijzer (mkeijzer -at- cs.vu.nl) (See Boost list message of Wed, 18 Aug 2004 21:43:18 +0100)
Mac Murrett (mmurrett -at- mac.com)
Marc Wintemantel (wintemantel -at- imes.mav.ethz.ch, wintemantel -at- even-ag.ch) (See CVS log)
Michael Glassford (glassfordm - at - hotmail.com)
Michael Stevens (Michael.Stevens - at - epost.de)
Multi Media Ltd. (pdimov@mmltd.net)
Nicolai M Josuttis (solutions -at- josuttis.com) (See Boost list message of Mon, 30 Aug 2004 10:52:00 +0100)
Nikolay Mladenov (nickm -at- sitius.com) (See Boost list message of Tue, 17 Aug 2004 15:45:33 +0100)
Paul Mensonides (pmenso57 -at- comcast.net) (See Boost list message of July 21, 2004 1:12:21 AM EST)
Pavol Droba (droba -at- topmail.sk)
Peter Dimov (pdimov@mmltd.net)
R W Grosse-Kunstleve (RWGrosse-Kunstleve@lbl.gov)
Ralf W. Grosse-Kunstleve (RWGrosse-Kunstleve@lbl.gov)
Rational Discovery LLC (Greg Landrum Landrum -at- RationalDiscovery.com) (See Boost list post of Tue, 17 Aug
2004 10:35:36 +0100)
Rene Rivera (grafik/redshift-software.com, rrivera/acm.org)
Robert Ramey (ramey@www.rrsd.com)
Roland Richter (roland -at- flll.jku.at) (See Boost list post of Mon, 16 Aug 2004 22:16:55 +0200)
Roland Schwarz (roland.schwarz -at- chello.at)
Ronald Garcia (garcia -at- cs.indiana.edu) (Email to Andreas Huber, see change log)
Samuel Krempp (krempp -at- crans.ens-cachan.fr) (See Boost list message of Mon, 27 Sep 2004 13:18:36 +0200)
Stefan Seefeld (seefeld -at- sympatico.ca)
Stephen Cleary (scleary -at- jerviswebb.com) (See Boost list message of Tue, 28 Sep 2004 13:11:46 +0100)
Steve Cleary (Variant of Stephen Cleary)
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Mathias Koch (mkoch - at - idesis.de 7 : email to boost-owner@lists.boost.org Sep 2007 13:20:09 +0200)

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1.13 curl 7.61.1

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1.14 d3 3.5.5

1.15 datepicker_ui 1.11.1 :2014/8/27

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```
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1.16 ejdb 1.2.12

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**1.17 es6-promise 3.0.2**

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1.18 event-emitter 0.3.3

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1.19 FileSaver.js 1.1.0

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[1]: http://eligrey.com

1.20 Font Awesome 4.3.0

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1.21 heirloom-mailx(nail) 1.2.5

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==========================================================================
/*
 * imap_gssapi.c is partially derived from sample code in:
 * GSS-API Programming Guide
 * Part No: 816-1331-11
 * Sun Microsystems, Inc. 4150 Network Circle Santa Clara, CA 95054 U.S.A.
 *
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 */
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/*
* hmac.c is derived from:

Network Working Group  H. Krawczyk
Request for Comments: 2104  IBM
Category: Informational  M. Bellare
         UCSD
         R. Canetti
         IBM
February 1997

HMAC: Keyed-Hashing for Message Authentication

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Appendix -- Sample Code
==========================================================================
Parts of nss.c are derived from the Mozilla NSS 3.9.2 source, mozilla/security/nss/cmd/smimetools/cmsutil.c. Therefore:

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Version 1.1

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1.22 httpclient 1.1
1.23 jQuery 2.1.1

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1.24 jQuery 2.1.4

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1.25 jquery-ui 1.11.2 :2014/11/17
1.25.1 Available under license :

/*! jQuery UI - v1.11.2 - 2014-10-16
 * http://jqueryui.com
 * Includes: core.js, widget.js, mouse.js, position.js, accordion.js, autocomplete.js, button.js, datepicker.js, dialog.js,
draggable.js, droppable.js, effect.js, effect-blind.js, effect-bounce.js, effect-clip.js, effect-drop.js, effect-explode.js,
effect-fade.js, effect-fold.js, effect-highlight.js, effect-puff.js, effect-pulsate.js, effect-scale.js, effect-shake.js, effect-size.js, effect-slide.js, effect-transfer.js, menu.js, progressbar.js, resizable.js, selectable.js, selectmenu.js, slider.js,
sortable.js, spinner.js, tabs.js, tooltip.js
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1.26 jquery.flot.Stack 1.2
1.26.1 Available under license :

/* Flot plugin for stacking data sets rather than overlying them.

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The plugin assumes the data is sorted on x (or y if stacking horizontally). For line charts, it is assumed that if a line has an undefined gap (from a null point), then the line above it should have the same gap - insert zeros instead of "null" if you want another behaviour. This also holds for the start and end of the chart. Note that stacking a mix of positive and negative values in most instances doesn't make sense (so it looks weird).

Two or more series are stacked when their "stack" attribute is set to the same key (which can be any number or string or just "true"). To specify the default stack, you can set the stack option like this:

```javascript
series: {
    stack: null/false, true, or a key (number/string)
}
```

You can also specify it for a single series, like this:

```javascript
$.plot( $('#placeholder'), [{
    data: [ ... ],
    stack: true
}])
```

The stacking order is determined by the order of the data series in the array (later series end up on top of the previous).

Internally, the plugin modifies the datapoints in each series, adding an offset to the y value. For line series, extra data points are inserted through interpolation. If there's a second y value, it's also adjusted (e.g for bar charts or filled areas).

perm

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1.27 jQuery Flot Tooltip 0.6.2

1.27.1 Available under license:

/*
 * jQuery Flot Tooltip
 *
 * Description: easy-to-use tooltips for Flot charts
 * Version: 0.6.2
 * Author: Krzysztof Urbas @krzysu [myviews.pl]
 *
 * Build on 2013-09-30
 * Released under MIT License, 2012
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1.28 jQuery Flot ValueLabels 2
1.28.1 Available under license:

/* Value Labels Plugin for flot.
 * Homepage:
 * http://sites.google.com/site/petrsstuff/projects/flotvallab
 *
 * Released under the MIT license by Petr Blahos, December 2009.
 *
 */

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1.29 json-c 0.12.1

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1.30 jszip.js 2.4.0

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.
When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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Linking with OpenSSL

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1.35 libuuid 2.25.2

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   d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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b) Use a suitable shared library mechanism for linking with the
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Initialize empty image
f1c9645dbc14efddc7d8a322685f26eb bsd.img
Create new DOS partition table
57e721e38d1266c2df055067c18f2cf9 bsd.img

---layout----------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

-------------------
Create 1st primary partition
ada64ace122978d00d1d1c0e5ee45d26 bsd.img

---layout----------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type
<removed>1 2048 4095 2048 1M 83 Linux

-------------------
Create 2st primary partition
1beb87248e05d6e4e62b749da65d023 bsd.img
Set 2nd partition type
2d8e8dff51a88a045db233418dd73fbe bsd.img

---layout--------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type
<removed>1 2048 4095 2048 1M 83 Linux
<removed>2 4096 20479 16384 8M a5 FreeBSD

-------------------
Create default BSD
2e1cee529cb59c9341afe0443f196a1 bsd.img

---layout--------
Welcome to fdisk <removed>.
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors
Geometry: 255 heads, 63 sectors/track, 1 cylinders
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: bsd
partitions: 4

Slice Start   End Sectors  Size Type   Fsize Bsize Cpg
  c   4096 20479    16384   8M unused   0    0    0
  d   0 16064     16065    7.9M unused   0    0    0

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):
-------------------
b5c121c2091b2ff26b880551feac7112 bsd.img
---layout--------
Welcome to fdisk <removed>.
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors
Geometry: 255 heads, 63 sectors/track, 1 cylinders
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: bsd
partitions: 4

Slice Start   End Sectors  Size Type   Fsize Bsize Cpg
  a   4096 6144     2049   1M 4.2BSD   0    0    0
  c   4096 20479    16384   8M unused   0    0    0
  d   0 16064     16065    7.9M unused   0    0    0

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):
-------------------
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.
Command (m for help): Entering nested BSD disklabel.

Command (m for help):
0 unused 5 4.1BSD 9 4.4LFS d boot
1 swap 6 Eighth Edition a unknown e ADOS
2 Version 6 7 4.2BSD b HPFS f HFS
3 Version 7 8 MS-DOS c ISO-9660 10 AdvFS
4 System V

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1.37 libxml2 2.9.8

1.37.1 Available under license:

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/*
 * ahci_mv.c - Marvell AHCI SATA platform support
 *
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 *
 * based on the AHCI SATA platform driver by Jeff Garzik and Anton Vorontsov
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 *
 */

#include <linux/kernel.h>
#include <linux/gfp.h>
#include <linux/module.h>
#include <linux/pm.h>
#include <linux/init.h>
#include <linux/interrupt.h>
#include <linux/device.h>
#include <linux/platform_device.h>
#include <linux/libata.h>
#include <linux/ahci_platform.h>
#include <linux/ata_platform.h>
#include <linux/mbus.h>
#include "ahci.h"

#define AHCI_WINDOW_CTRL(win)(0x60 + ((win) << 4))
#define AHCI_WINDOW_BASE(win)(0x64 + ((win) << 4))
#define AHCI_WINDOW_SIZE(win)(0x68 + ((win) << 4))

#define VENDOR_SPECIFIC_0_ADDR 0xa0
#define VENDOR_SPECIFIC_0_DATA 0xa4

static void ahci_mv_windows_config(struct ahci_host_priv *hpriv,
    const struct mbus_dram_target_info *dram)
{
    int i;

    for (i = 0; i < 4; i++) {
        writel(0, hpriv->mmio + AHCI_WINDOW_CTRL(i));
        writel(0, hpriv->mmio + AHCI_WINDOW_BASE(i));
        writel(0, hpriv->mmio + AHCI_WINDOW_SIZE(i));
    }

    for (i = 0; i < dram->num_cs; i++) {
        const struct mbus_dram_window *cs = dram->cs + i;

        writel((cs->mbus_attr << 8) | (dram->mbus_dram_target_id << 4) | 1,
                hpriv->mmio + AHCI_WINDOW_CTRL(i));
        writel(cs->base >> 16, hpriv->mmio + AHCI_WINDOW_BASE(i));
        writel(((cs->size - 1) & 0xffff0000),
                hpriv->mmio + AHCI_WINDOW_SIZE(i));
    }
}

static void ahci_mv_host_stop(struct ata_host *host);

static struct ata_port_operations ahci_mv_ops = {
    .inherits = &ahci_ops,
    .host_stop = ahci_mv_host_stop,
};

static const struct ata_port_info ahci_mv_port_info = {
    .flags = AHCI_FLAG_COMMON,
    .pio_mask = ATA_PIO4,
    .udma_mask = ATA_UDMA6,
    .port_ops = &ahci_mv_ops,
};

static struct scsi_host_template ahci_mv_platform_sht = {
    AHCI_SHT("ahci_mv_platform"),
};

static const struct of_device_id ahci_mv_of_match[] = {
    { .compatible = "marvell,ahci-sata", },
    {};

MODULE_DEVICE_TABLE(of, ahci_of_match);
static int ahci_mv_probe(struct platform_device *pdev)
{
    struct device *dev = &pdev->dev;
    const struct mbus_dram_target_info *dram;
    struct ata_port_info pi = ahci_mv_port_info;
    const struct ata_port_info *ppi[] = { &pi, NULL };
    struct ahci_host_priv *hpriv;
    struct ata_host *host;
    struct resource *mem;
    int irq;
    int n_ports;
    int i;
    int rc;

    mem = platform_get_resource(pdev, IORESOURCE_MEM, 0);
    if (!mem) {
        dev_err(dev, "no mmio space\n");
        return -EINVAL;
    }

    irq = platform_get_irq(pdev, 0);
    if (irq <= 0) {
        dev_err(dev, "no irq\n");
        return -EINVAL;
    }

    hpriv = devm_kzalloc(dev, sizeof(*hpriv), GFP_KERNEL);
    if (!hpriv) {
        dev_err(dev, "can't alloc ahci_host_priv\n");
        return -ENOMEM;
    }

    hpriv->clk = devm_clk_get(&pdev->dev, NULL);
    if (IS_ERR(hpriv->clk)) {
        dev_err(dev, "can't get clock\n");
        return PTR_ERR(hpriv->clk);
    }

    rc = clk_prepare_enable(hpriv->clk);
    if (rc < 0) {
        dev_err(dev, "can't enable clock\n");
        return rc;
    }

    hpriv->flags |= (unsigned long)pi.private_data;

    hpriv->mmio = devm_request_and_ioremap(dev, mem);
if (!hpriv->mmio) {
  dev_err(dev, "can't map %pR
", mem);
  clk_disable_unprepare(hpriv->clk);
  return -ENOMEM;
}

/*
 * (Re-)program MBUS remapping windows if we are asked to.
 */
dram = mv_mbus_dram_info();
if (dram)
  ahci_mv_windows_config(hpriv, dram);

ahci_save_initial_config(dev, hpriv, 0, 0);

/* prepare host */
if (hpriv->cap & HOST_CAP_NCQ)
  pi.flags |= ATA_FLAG_NCQ;

if (hpriv->cap & HOST_CAP_PMP)
  pi.flags |= ATA_FLAG_PMP;

ahci_set_em_messages(hpriv, &pi);

/*
 * CAP.NP sometimes indicate the index of the last enabled
 * port, at other times, that of the last possible port, so
 * determining the maximum port number requires looking at
 * both CAP.NP and port_map.
 */
n_ports = max(ahci_nr_ports(hpriv->cap), fls(hpriv->port_map));

host = ata_host_alloc_pinfo(dev, ppi, n_ports);
if (!host) {
  clk_disable_unprepare(hpriv->clk);
  return -ENOMEM;
}

host->private_data = hpriv;

if (!hpriv->cap & HOST_CAP_SSS || ahci_ignore_sss)
  host->flags |= ATA_HOST_PARALLEL_SCAN;
else
  printk(KERN_INFO "ahci: SSS flag set, parallel bus scan disabled\n");

if (pi.flags & ATA_FLAG_EM)
  ahci_reset_em(host);
for (i = 0; i < host->n_ports; i++) {
    struct ata_port *ap = host->ports[i];

    ata_port_desc(ap, "mmio %pR", mem);
    ata_port_desc(ap, "port 0x%x", 0x100 + ap->port_no * 0x80);

    /* set enclosure management message type */
    if (ap->flags & ATA_FLAG_EM)
        ap->em_message_type = hpriv->em_msg_type;

    /* disabled/not-implemented port */
    if (!(hpriv->port_map & (1 << i)))
        ap->ops = &ata_dummy_port_ops;
    }

    /* Enabling regret bit to enables the SATA unit to regret
     * a request that didn't receive an acknowledge and avoid a deadlock */

    writel(0x4, hpriv->mmio + VENDOR_SPECIFIC_0_ADDR);
    writel(0x80, hpriv->mmio + VENDOR_SPECIFIC_0_DATA);

    rc = ahci_reset_controller(host);
    if (rc) {
        clk_disable_unprepare(hpriv->clk);
        return rc;
    }

    ahci_init_controller(host);
    ahci_print_info(host, "platform");

    rc = ata_host_activate(host, irq, ahci_interrupt, IRQF_SHARED,
        &ahci_mv_platform_sht);
    if (rc) {
        clk_disable_unprepare(hpriv->clk);
        return rc;
    }

    return 0;
}

static void ahci_mv_host_stop(struct ata_host *host) {
    struct ahci_host_priv *hpriv = host->private_data;
    clk_disable_unprepare(hpriv->clk);
}

#ifndef CONFIG_PM_SLEEP
static int ahci_mv_suspend(struct device *dev)
struct ata_host *host = dev_get_drvdata(dev);
struct ahci_host_priv *hpriv = host->private_data;
void __iomem *mmio = hpriv->mmio;
u32 ctl;
int rc;

if (hpriv->flags & AHCI_HFLAG_NO_SUSPEND) {
    dev_err(dev, "firmware update required for suspend/resume\n");
    return -EIO;
}

/*
 * AHCI spec rev1.1 section 8.3.3:
 * Software must disable interrupts prior to requesting a
 * transition of the HBA to D3 state.
 */
ctl = readl(mmio + HOST_CTL);
ctl &= ~HOST_IRQ_EN;
writel(ctl, mmio + HOST_CTL); /* flush */

rc = ata_host_suspend(host, PMSG_SUSPEND);
if (rc)
    return rc;

return 0;
}

static int ahci_mv_resume(struct device *dev)
{
    struct ata_host *host = dev_get_drvdata(dev);
    struct ahci_host_priv *hpriv = host->private_data;
    const struct mbus_dram_target_info *dram;
    int rc;

    dram = mv_mbus_dram_info();
    if (dram)
        ahci_mv_windows_config(hpriv, dram);

    if (dev->power.power_state.event == PM_EVENT_SUSPEND) {
        rc = ahci_reset_controller(host);
        if (rc)
            return rc;

        ahci_init_controller(host);
    }
}
ata_host_resume(host);

return 0;
}
#endif

static SIMPLE_DEV_PM_OPS(ahci_mv_pm_ops, ahci_mv_suspend, ahci_mv_resume);

static struct platform_driver ahci_mv_driver = {
    .probe = ahci_mv_probe,
    .remove = ata_platform_remove_one,
    .driver = {
        .name = "ahci_mv",
        .owner = THIS_MODULE,
        .of_match_table = ahci_mv_of_match,
        .pm = &ahci_mv_pm_ops,
    },
};

module_platform_driver(ahci_mv_driver);

MODULE_DESCRIPTION("Marvell AHCI SATA platform driver");
MODULE_AUTHOR("Marcin Wojtas <mw@semihalf.com>");
MODULE_LICENSE("GPL");
MODULE_ALIAS("platform:ahci_mv");

1.39 lodash 3.7.0

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1.40 MiniZip 1.01E

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1.41 moment 2.10.6
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Written by: Philip Hazel
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On all platforms, Nmap is normally linked with:
- libpcap: Network packet capture library. Distributed with Nmap in the libpcap subdirectory.
http://www.tcpdump.org/

- libdnet: Networking library, used for low-level tasks such as sending ethernet frames. A modified version is distributed with Nmap in the libdnet-stripped subdirectory. A summary of Nmap-local modifications is in the file NMAP_MODIFICATIONS.
  http://code.google.com/p/libdnet/

- PCRE: Perl-compatible regular expressions. PCRE is part of Nmap's version detection and is also made available as an NSE library. Distributed with Nmap in the libpcre subdirectory.
  http://www.pcre.org/

- liblua: Lua programming language. Lua is the implementation language of NSE, the Nmap Scripting Engine. Nmap links with liblua to allow running Lua programs inside Nmap. Distributed with Nmap in the liblua subdirectory. liblua can be omitted by configuring with the --without-liblua configuration directive.
  http://www.lua.org/

- OpenSSL: Cryptographic library. OpenSSL is used by service detection and by NSE to connect to SSL services. NSE also provides access to OpenSSL functions such as encryption and digest calculation. OpenSSL can be disabled by configuring with the --without-openssl configuration directive.
  http://www.openssl.org/

- LIBLINEAR. Used for IPv6 OS classification.
  http://www.csie.ntu.edu.tw/~cjlin/liblinear/

- libsvn. The Subversion library, used by the updater program


On Windows only, Nmap uses:

- WinPcap: libpcap for Windows. The libpcap license applies to WinPcap, and it also has its own license. A binary copy of the library is distributed with Nmap in the subdirectory mswin32/winpcap.
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Certain Nmap Scripting Engine scripts use the simplified BSD license in licenses/BSD-simplified.

Zenmap and Ndiff require:

- Python. The binary distributions of Nmap include a Python interpreter and various libraries, built using either py2exe or py2app.
  http://www.python.org/

The Windows and Mac OS X binary packages include bundled versions of:

- GLib, GTK+, ATK, Pango: These libraries are licensed under the GNU LGPL 2, a copy of which is in the file licenses/LGPL-2.
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- Cairo: graphics library. Dual-licensed under the GNU LGPL 2.1 and the MPL 1.1. See the files licenses/LGPL-2.1 and licenses/MPL-1.1.
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o zlib and bzip2: compression libraries.
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o SQLite.
http://www.sqlite.org/
o PyGTK, PyGObject: bindings for Python. Licensed under the GNU LGPL
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o PyCairo: bindings for Python. Dual-licensed under the GNU LGPL 2.1
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The Mac OS binary packages also include:
o Various X.org libraries. These were built using MacPorts.
http://www.x.org/ http://www.macports.org/

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*
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-----------------------------------------------

Table of Contents

* GNU GENERAL PUBLIC LICENSE
  o Preamble
  o TERMS AND CONDITIONS FOR COPYING, DISTRIBUTION AND MODIFICATION
  o How to Apply These Terms to Your New Programs
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Written by: Philip Hazel
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If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or
link a “work that uses the Library” with the Library to produce a
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modification of the work for the customer's own use and reverse
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directing the user to the copy of this License. Also, you must do one
of these things:

a) Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable “work that
uses the Library”, as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

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1.52 react 16.0.0

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1.53 react-select 1.0.0-rc.4

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1.54 react-tabs 2.1.0

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1.55 reflux 0.2.7

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1.57 sarissa 0.9.9.6

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Current upstream maintainers:
Bill Fenner<fenner@research.att.com>
Fulvio Risso<risso@polito.it>
Guy Harris <guy@alum.mit.edu>
Hannes Gredler<hannes@juniper.net>
Jun-ichiro itojun Hagino<itojun@iijlab.net>
Michael Richardson <mcr@sandelman.ottawa.on.ca>

Additional people who have contributed patches:

Alan Bawden<Alan@LCS.MIT.EDU>
Alexey Kuznetsov<kuznet@ms2.inr.ac.ru>
Albert Chin<china@thewrittenword.com>
Andrew Brown<atatat@atatdot.net>
Antti Kantee<pooka@netbsd.org>
Arkadiusz Miskiewicz<misiek@pld.org.pl>
Armando L. Caro Jr.<acaro@mail.eecis.udel.edu>
Assar Westerlund <assar@sics.se>
Brian Ginsbach <ginsbach@cray.com>
Charles M. Hannum <mycroft@netbsd.org>
Chris G. Demetriou <cgd@netbsd.org>
Chris Pepper <pepper@mail.repppe.com>
Darren Reed <darrenr@reed.wattle.id.au>
David Kaebbling <drk@sgi.com>
David Young <dyoung@ojctech.com>
Don Ebright <Don.Ebright@compuware.com>
Eric Anderson <anderse@hpl.hp.com>
Franz Schaefer <schaefer@mond.at>
Gianluca Varenni <varenni@netgroup-serv.polito.it>
Gisle Vanem <giva@bgnett.no>
Graeme Hewson <ghewson@cix.compulink.co.uk>
Greg Stark <gsstark@mit.edu>
Greg Troxel <gdt@ir.bbn.com>
Guillaume Pelat <endymion_@users.sourceforge.net>
Hyung Sik Yoon <hsyn@kr.ibm.com>
Igor Khristophorov <igor@atdot.org>
Jan-Philip Velders <jpv@veldersjes.net>
Jason R. Thorpe <thorpej@netbsd.org>
Javier Achirica <achirica@ttdd.net>
Jean Tourrilhes <jt@hpl.hp.com>
Jefferson Ogata <jogata@nodc.noaa.gov>
Jesper Peterson <jesper@endace.com>
John Bankier <jbankier@rainfinity.com>
Jon Lindgren <joni@yubyub.net>
Juergen Schoenwaelder <schoenw@ibr.cs.tu-bs.de>
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Klaus Klein <kleink@netbsd.org>
Koryn Grant <koryn@endace.com>
Krzysztod Halasa <khc@pm.waw.pl>
Lorenzo Cavallaro <sullivan@sikurezza.org>
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Love Hrnquist-strand <lha@stacken.kth.se>
Maciej W. Rozycki <mac@ds2.pg.gda.pl>
Marcus Felipe Pereira <marcus@task.com.br>
Martin Husemann <martin@netbsd.org>
Mike Wiacek <mike@iroot.net>
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Onno van der Linden <onno@simplex.nl>
Paul Mundt <lethal@linux-sh.org>
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Peter Fales <peter@fales-lorenz.net>
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Scott Gifford<sgifford@tir.com>
Sebastian Krahmer<krahmer@cs.uni-potsdam.de>
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1.59 tinyxml 2.6.2
1.59.1 Available under license:

/*
 * www.sourceforge.net/projects/tinyxml
 * Original code (2.0 and earlier) copyright (c) 2000-2006 Lee Thomason (www.grinninglizard.com)
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 */

1.60 u-boot 2013_Q3_eng_drop_2

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From d223f6b3f1be839ea23c5b21aa4a266f84a75acec Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 09:07:29 +0300
Subject: [PATCH 0631/2009] fix: common: update copyright header

use include to mvCopyright.h file instead of an old copyright header

Change-Id: I0e25072e7e163c750bbfecd50958a194c78e6c68
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3035
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>

---

board/mv_ebu/common/common/mvList.c | 17 +++++--------
board/mv_ebu/common/common/mvList.h | 17 +++++--------
board/mv_ebu/common/common/mvStack.c | 17 +++++--------
board/mv_ebu/common/common/mvStack.h | 17 +++++--------
4 files changed, 12 insertions(+), 56 deletions(-)
# Header File for

* FILENAME: $Workfile: mvStack.h$

---

1.7.9.5

From 0adbe3af131850060daaf644d1742beb9af Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 10:28:42 +0300
Subject: [PATCH 0636/2009] fix: cpu: update copyright header

use include to mvCopyright.h file instead of GPL header

Change-Id: Idc0e1117289627feb86e93f9911d9599d87b8116
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3036
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>

---

.../common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c | 16 +---------------
board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c | 16 +---------------
board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c | 16 +---------------
board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c | 16 +---------------
4 files changed, 4 insertions(+), 60 deletions(-)

diff --git a/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c b/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c
index 5b9a091..db2268f 100644
--- a/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c
@@ -1,18 +1,4 @@
-/*
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- * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA  02111-1307  USA
- */
+#include "mvCopyright.h"
```c
#include "mvOs.h"
#include ".\mvCpuCntrs.h"
```

diff --git a/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c b/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c
index 33f8c06..f78bc0b 100644
--- a/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c
@@ -1,18 +1,4 @@
-/*
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-* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
-*/
+#include "mvCopyright.h"

#include "mvOs.h"
#include "mvCpuCntrs.h"

diff --git a/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c b/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c
index 18120be..19460af 100644
--- a/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c
@@ -1,18 +1,4 @@
-/*
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-*/
```
+#include "mvCopyright.h"

#include "mvOs.h"
#include "mvCpuL2Cntrs.h"

diff --git a/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
b/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
index 0b38381..836f267 100644
--- a/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
@@ -1,18 +1,4 @@
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- * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
- */
+#include "mvCopyright.h"

#include "mvOs.h"
#include "../mvCpuCntrs.h"
--

1.7.9.5
From 61c56e2b35786fa69e425f3e73d4cb8e54e8e18 Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Tue, 9 Jul 2013 16:16:38 +0300
Subject: [PATCH 0531/2009] Marvell copyright header

This copyright file is replacing the current Marvell Triple license header. Each file with Marvell triple copyright, should add include to this h file in the beginning of the file.
This is relevant to files that are common to more than one OSs.
This license header include Marvell Quad header, include the current license headers - GPL, BSD & Marvell Commercial License, and addition of a new license header - General Public License FreeRTOS Exception.

Change-Id: Ie8b6f03bfab41273575b451e7c741507b8cca17d
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/2463
Reviewed-by: Maen Suleiman <maen@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
Tested-by: Nadav Haklai <nadavh@marvell.com>
--board/mv_ebu/common/common/mvCopyright.h | 97 ++++++++++++++++++++++++++++++
1 file changed, 97 insertions(+)
create mode 100644 board/mv_ebu/common/common/mvCopyright.h
diff --git a/board/mv_ebu/common/common/mvCopyright.h b/board/mv_ebu/common/common/mvCopyright.h
new file mode 100644
index 0000000..458562b
--- /dev/null
+++ b/board/mv_ebu/common/common/mvCopyright.h
@@ -0,0 +1,97 @@
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+
+*******************************************************************************/
--
1.7.9.5
From 3119d3409bd752808772589578e525a1294935dc Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Tue, 1 Oct 2013 11:01:52 +0200
Subject: [PATCH 0671/2009] fix: qd-dsdt: update copyright header

use qd-dsdt copyright header file instead of proprietary header

Change-Id: Id745d6cb389de29a86b5762e9d15316bf7d08dcc
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3619
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
---
../mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h     |   15 ++-------------
../common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h |   11 +----------
../common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h  |   15 ++-------------
../common/mv_hal/qd-dsdt/Include/msApiTypes.h     |   11 +----------
4 files changed, 6 insertions(+), 46 deletions(-)

diff --git a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
index 4fd3f94..ca5b8e7 100644
--- a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
+++ b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
@@ -1,17 +1,16 @@
+#include <Copyright.h>
+
+*******************************************************************************/
-* Copyright 2002, GALILEO TECHNOLOGY, LTD.
-*
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--- a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h
+++ b/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h
@@ -1,13 +1,4 @@
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- */
+#include <Copyright.h>

/#

* FILENAME:  $Workfile: mv_types.h $

--

1.7.9.5
From d94585ce826b7e523b124e24ab8887517f159640 Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 10:31:30 +0300
Subject: [PATCH 0638/2009] fix: neta: update copyright header

use Marvell triple license header instead of GPL header

Change-Id: Ibf7ffa3abf58354f7c9cdc705258ff04af69c930
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitin04.il.marvell.com:8080/3037
Reviewed-by: Dmitri Epshtein <dima@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
Tested-by: Nadav Haklai <nadavh@marvell.com>
---
diff --git a/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c b/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
index 232bb6f..8260963 100644
--- a/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
+++ b/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
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+
+*******************************************************************************/

#include "mvOs.h"
--
1.7.9.5
From b607bda07a2440b3778130b0c6bb5a9ba91b04c0 Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Mon, 1 Jul 2013 15:01:52 +0300
Subject: [PATCH 0497/2009] Add Marvell copyright to VANIR driver

Add Marvell copyright and Marvell GPL license to the VANIR driver files.
This is Marvell driver that was originated from Freescale driver.

Change-Id: I1dc2893307d608591bfa9e64b18ed9e84969ef59
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/2376
Reviewed-by: Star_Automation <star@marvell.com>
Tested-by: Omri Itach <omrii@marvell.com>
Reviewed-by: Omri Itach <omrii@marvell.com>
---
drivers/block/mv94xx_scsi.c |   18 ++++++++++++++++++
drivers/block/mv94xx_scsi.c |   18 ++++++++++++++++++
2 files changed, 36 insertions(+)
diff --git a/drivers/block/mv94xx_scsi.c b/drivers/block/mv94xx_scsi.c
index 8090c04..f3f0d89 100644
--- a/drivers/block/mv94xx_scsi.c
+++ b/drivers/block/mv94xx_scsi.c
@@ -1,3 +1,21 @@
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+*******************************************************************************
+/*
+ * Copyright (C) Freescale Semiconductor, Inc. 2006.
+ * Author: Jason Jin<Jason.jin@freescale.com>
+diff --git a/include/mv94xx_scsi.h b/include/mv94xx_scsi.h
index 16fcb34..6f1c058 100644
--- a/include/mv94xx_scsi.h
+++ b/include/mv94xx_scsi.h
@@ -1,3 +1,21 @@
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+/*
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+ * Author: Jason Jin<Jason.jin@freescale.com>
+--
+1.7.9.5

1.61 zlib 1.2.8

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/* zlib.h -- interface of the 'zlib' general purpose compression library
   version 1.2.8, April 28th, 2013

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Jean-loup Gailly        Mark Adler
jloup@gzip.org          madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for
(zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).
*/