Open Source Used In Cisco Web Security Appliances AsyncOS 12.5

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1.4 jackson-jaxrs-json-provider 2.9.5

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1.13 lua 5.3.4

1.14 bcel 2.7.2
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1.16 mitchellh-mapstructure 1.1.2

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1.19 jersey-media-jaxb 2.22

1.20 libtool 2.4.6
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1.22 jetty-io 9.2.13.v20150730

1.23 lua-cjson 2.1.0
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1.24 tomcat-jsp-api 8.5.32

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1.26 binutils 2.28

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If the program is interactive, make it output a short notice like this when it starts in an interactive mode:
Gnomovision version 69. Copyright (C) year name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it
under certain conditions; type `show c' for details.

The hypothetical commands `show w' and `show c' should show the appropriate
parts of the General Public License. Of course, the commands you use may
be called something other than `show w' and `show c'; they could even be
mouse-clicks or menu items--whatever suits your program.

You should also get your employer (if you work as a programmer) or your
school, if any, to sign a "copyright disclaimer" for the program, if
necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the program
`Gnomovision' (which makes passes at compilers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

This General Public License does not permit incorporating your program into
proprietary programs. If your program is a subroutine library, you may
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library. If this is what you want to do, use the GNU Library General
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Version 2, June 1991

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[This is the first released version of the library GPL. It is
numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

The licenses for most software are designed to take away your
freedom to share and change it. By contrast, the GNU General Public
Licenses are intended to guarantee your freedom to share and change
free software--to make sure the software is free for all its users.

This license, the Library General Public License, applies to some
specially designated Free Software Foundation software, and to any
other libraries whose authors decide to use it. You can use it for
your libraries, too.
When we speak of free software, we are referring to freedom, not price. Our General Public Licenses are designed to make sure that you have the freedom to distribute copies of free software (and charge for this service if you wish), that you receive source code or can get it if you want it, that you can change the software or use pieces of it in new free programs; and that you know you can do these things.

To protect your rights, we need to make restrictions that forbid anyone to deny you these rights or to ask you to surrender the rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library, or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

Our method of protecting your rights has two steps: (1) copyright the library, and (2) offer you this license which gives you legal permission to copy, distribute and/or modify the library.

Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without
changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated
"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

   a) The modified work must itself be a software library.

   b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

   c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

   d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form
under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.
You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.
7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Libraries

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library 'Frob' (a library for tweaking knobs) written by James Random Hacker.

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Ty Coon, President of Vice

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[This is the first released version of the Lesser GPL. It also counts as the successor of the GNU Library Public License, version 2, hence the version number 2.1.]

Preamble

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This license, the Lesser General Public License, applies to some specially designated software packages--typically libraries--of the
Free Software Foundation and other authors who decide to use it. You

can use it too, but we suggest you first think carefully about whether

this license or the ordinary General Public License is the better

strategy to use in any particular case, based on the explanations below.

When we speak of free software, we are referring to freedom of use,

not price. Our General Public Licenses are designed to make sure that

you have the freedom to distribute copies of free software (and charge

for this service if you wish); that you receive source code or can get

it if you want it; that you can change the software and use pieces of

it in new free programs; and that you are informed that you can do

these things.

To protect your rights, we need to make restrictions that forbid
distributors to deny you these rights or to ask you to surrender these

rights. These restrictions translate to certain responsibilities for

you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis
or for a fee, you must give the recipients all the rights that we gave

you. You must make sure that they, too, receive or can get the source

code. If you link other code with the library, you must provide

complete object files to the recipients, so that they can relink them

with the library after making changes to the library and recompiling

it. And you must show them these terms so they know their rights.

We protect your rights with a two-step method: (1) we copyright the

library, and (2) we offer you this license, which gives you legal

permission to copy, distribute and/or modify the library.

To protect each distributor, we want to make it very clear that

there is no warranty for the free library. Also, if the library is

modified by someone else and passed on, the recipients should know

that what they have is not the original version, so that the original

author's reputation will not be affected by problems that might be

introduced by others.

Finally, software patents pose a constant threat to the existence of

any free program. We wish to make sure that a company cannot
effectively restrict the users of a free program by obtaining a

restrictive license from a patent holder. Therefore, we insist that

any patent license obtained for a version of the library must be

consistent with the full freedom of use specified in this license.

Most GNU software, including some libraries, is covered by the

ordinary GNU General Public License. This license, the GNU Lesser

General Public License, applies to certain designated libraries, and

is quite different from the ordinary General Public License. We use
this license for certain libraries in order to permit linking those libraries into non-free programs.

When a program is linked with a library, whether statically or using a shared library, the combination of the two is legally speaking a combined work, a derivative of the original library. The ordinary General Public License therefore permits such linking only if the entire combination fits its criteria of freedom. The Lesser General Public License permits more lax criteria for linking other code with the library.

We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification").

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:
a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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d) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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`Frob' (a library for tweaking knobs) written by James Random Hacker.

@var{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice
@end smallexample

That's all there is to it!
(This file is under construction.)

If you've contributed to gas and your name isn't listed here, it is
not meant as a slight. I just don't know about it. Email me,
nickc@redhat.com and I'll correct the situation.

This file will eventually be deleted: The general info will go into
the documentation, and info on specific files will go into an AUTHORS
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++++++++++++++++

Dean Elsner wrote the original gas for vax. [more details?]

Jay Fenlason maintained gas for a while, adding support for
gdb-specific debug information and the 68k series machines, most of
the preprocessing pass, and extensive changes in messages.c,
input-file.c, write.c.

K. Richard Pixley maintained gas for a while, adding various
enhancements and many bug fixes, including merging support for several
processors, breaking gas up to handle multiple object file format
backends (including heavy rewrite, testing, an integration of the coff
and b.out backends), adding configuration including heavy testing and
verification of cross assemblers and file splits and renaming,
converted gas to strictly ansi C including full prototypes, added
support for m680[34]0 & cpu32, considerable work on i960 including a
coff port (including considerable amounts of reverse engineering), a
sparc opcode file rewrite, decstation, rs6000, and hp300hpux host
ports, updated "know" assertions and made them work, much other
reorganization, cleanup, and lint.

Ken Raeburn wrote the high-level BFD interface code to replace most of
the code in format-specific I/O modules.

The original Vax-VMS support was contributed by David L. Kashtan.
Eric Youngdale and Pat Rankin have done much work with it since.

The Intel 80386 machine description was written by Eliot Dresselhaus.
Minh Tran-Le at IntelliCorp contributed some AIX 386 support.

The Motorola 88k machine description was contributed by Devon Bowen of Buffalo University and Torbjorn Granlund of the Swedish Institute of Computer Science.

Keith Knowles at the Open Software Foundation wrote the original MIPS back end (tc-mips.c, tc-mips.h), and contributed Rose format support that hasn't been merged in yet. Ralph Campbell worked with the MIPS code to support a.out format.

Support for the Zilog Z8k and Hitachi H8/300, H8/500 and SH processors (tc-z8k, tc-h8300, tc-h8500, tc-sh), and IEEE 695 object file format (obj-ieee), was written by Steve Chamberlain of Cygnus Solutions. Steve also modified the COFF back end (obj-coffbfd) to use BFD for some low-level operations, for use with the Hitachi, 29k and Zilog targets.

John Gilmore built the AMD 29000 support, added .include support, and simplified the configuration of which versions accept which pseudo-ops. He updated the 68k machine description so that Motorola's opcodes always produced fixed-size instructions (e.g. jsr), while synthetic instructions remained shrinkable (jbsr). John fixed many bugs, including true tested cross-compilation support, and one bug in relaxation that took a week and required the proverbial one-bit fix.

Ian Lance Taylor of Cygnus Solutions merged the Motorola and MIT syntaxes for the 68k, completed support for some COFF targets (68k, i386 SVR3, and SCO Unix), wrote the ECOFF support based on Michael Meissner's mips-tfile program, wrote the PowerPC and RS/6000 support, and made a few other minor patches. He handled the binutils releases for versions 2.7 through 2.9.

David Edelsohn contributed fixes for the PowerPC and AIX support.

Steve Chamberlain made gas able to generate listings.

Support for the HP9000/300 was contributed by Glenn Engel of HP.

Support for ELF format files has been worked on by Mark Eichin of Cygnus Solutions (original, incomplete implementation), Pete Hoogenboom at the University of Utah (HPPA mainly), Michael Meissner of the Open Software Foundation (i386 mainly), and Ken Raeburn of Cygnus Solutions (sparc, initial 64-bit support).

Several engineers at Cygnus Solutions have also provided many small bug fixes and configuration enhancements.
The initial Alpha support was contributed by Carnegie-Mellon University. Additional work was done by Ken Raeburn of Cygnus Solutions. Richard Henderson then rewrote much of the Alpha support.

Ian Dall updated the support code for the National Semiconductor 32000 series, and added support for Mach 3 and NetBSD running on the PC532.

Klaus Kaempf ported the assembler and the binutils to openVMS/Alpha.

Steve Haworth contributed the support for the Texas Instruction c30 (tms320c30).

H.J. Lu has contributed many patches and much testing.

Alan Modra reworked much of the i386 backend, improving the error checking, updating the code, and improving the 16 bit support, using patches from the work of Martynas Kunigelis and H.J. Lu.

Many others have contributed large or small bugfixes and enhancements. If you've contributed significant work and are not mentioned on this list, and want to be, let us know. Some of the history has been lost; we aren't intentionally leaving anyone out.

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1.27 apache-httpclient 4.3.2

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### 1.30 json-smart 2.2.1

### 1.31 jopt-simple 4.9

### 1.32 zlib 1.2.8

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 version 1.2.11, January 15th, 2017

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1.33 zlib 1.0.4

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1.34 tomcat 8.5.32

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- javaee_web_services_1_2.xsd
- javaee_web_services_client_1_2.xsd
- javaee_6.xsd
- javaee_web_services_1_3.xsd
- javaee_web_services_client_1_3.xsd
- jsp_2_2.xsd
- web-app_3_0.xsd
- web-common_3_0.xsd
- web-fragment_3_0.xsd
- javaee_7.xsd
- javaee_web_services_1_4.xsd
- javaee_web_services_client_1_4.xsd
- jsp_2_3.xsd
- web-app_3_1.xsd
- web-common_3_1.xsd
- web-fragment_3_1.xsd
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Appendix: How to Apply These Terms to Your New Programs

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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the program's name and a brief idea of what it does.>
Copyright (C) 19yy  <name of author>

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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

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Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it under certain conditions; type `show c' for details.
The hypothetical commands `show w` and `show c` should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w` and `show c`; they could even be mouse-clicks or menu items—whatever suits your program.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the program `Gnomovision' (a program to direct compilers to make passes at assemblers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!
[I have snipped the snail mail address of the FSF because it has changed in the past and is likely to change again. The current address should be at http://www.gnu.org/]

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Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

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For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link a program with the library, you must provide complete object files to the recipients so that they can relink them with the library, after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

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Also, for each distributor's protection, we want to make certain that everyone understands that there is no warranty for this free library. If the library is modified by someone else and passed on, we want its recipients to know that what they have is not the original version, so that any problems introduced by others will not reflect on the original authors' reputations.

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Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a
derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work which has been distributed under these terms. A "work based on the Library" means either the Library or any derivative work under copyright law: that is to say, a work containing the Library or a portion of it, either verbatim or with modifications and/or translated straightforwardly into another language. (Hereinafter, translation is included without limitation in the term "modification".)
"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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   (For example, a function in a library to compute square roots has
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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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of these things:

   a) Accompany the work with the complete corresponding
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      changes were used in the work (which must be distributed under
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      with the Library, with the complete machine-readable "work that
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      executable containing the modified Library. (It is understood
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   which the executable runs, unless that component itself accompanies
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   It may happen that this requirement contradicts the license
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b) Give prominent notice with the combined library of the fact
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END OF TERMS AND CONDITIONS

Appendix: How to Apply These Terms to Your New Libraries

If you develop a new library, and you want it to be of the greatest possible use to the public, we recommend making it free software that everyone can redistribute and change. You can do so by permitting redistribution under these terms (or, alternatively, under the terms of the ordinary General Public License).

To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

<one line to give the library's name and a brief idea of what it does.>
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Also add information on how to contact you by electronic and paper mail.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the library, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the library `Frob' (a library for tweaking knobs) written by James Random Hacker.

<!--signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

1.38 ASM 5.0.3
1.38.1 Available under license :
No license file was found, but licenses were detected in source scan.

/ ***
* ASM: a very small and fast Java bytecode manipulation framework
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*/

Found in path(s):
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/ClassVisitor.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/MethodWriter.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/TableSwitchInsnNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/util/CheckAnnotationAdapter.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/util/CheckFieldAdapter.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/LdcInsnNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/LabelNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/analysis/BasicValue.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/FieldNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/IntInsnNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/LabelNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/analysis/BasicValue.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/FieldNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/IntInsnNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/TableSwitchInsnNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/LabelNode.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/tree/analysis/BasicValue.java
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-

jar/org/objectweb/asm/AnnotationWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/FieldInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingAnnotationAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/analysis/Subroutine.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/ClassWriter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/VarnInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingFieldAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/ConstantPool.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/LookupSwitchInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/AbstractInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingFieldAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/NameMapping.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/StaticInitMerger.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/tree/JumpInsnNode.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Frame.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Edge.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingMethodAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/Constant.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/SimpleRemapper.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/commons/RemappingClassAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/Shrinker.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/MethodVisitor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Label.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/Handler.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
jar/org/objectweb/asm/optimizer/JarOptimizer.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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/** *
 * Creates a new {@link AnalyzerAdapter}. <i>Subclasses must not use this
 * constructor</i>. Instead, they must use the
 * { @link #AnalyzerAdapter(int, String, int, String, String, MethodVisitor)}
 * version.
 *
 * @param owner
 * the owner's class name.
 * @param access
 * the method's access flags (see { @link Opcodes}).
 * @param name
 * the method's name.
 * @param desc
 * the method's descriptor (see { @link Type Type}).
 * @param mv
 * the method visitor to which this adapter delegates calls. May
 * be <tt>null</tt>
 * @throws IllegalStateException
 * If a subclass calls this constructor.
 */

Found in path(s):
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
* jar/org/objectweb/asm/xml/SAXCodeAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
* jar/org/objectweb/asm/xml/SAXAnnotationAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
* jar/org/objectweb/asm/xml/ASMContentHandler.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
* jar/org/objectweb/asm/xml/SAXAdapter.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
* jar/org/objectweb/asm/xml/Processor.java
* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
* jar/org/objectweb/asm/xml/SAXFieldAdapter.java
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*/
/**
 * Constructs a new {@link LocalVariableAnnotationNode}. <i>Subclasses must
 * not use this constructor</i>. Instead, they must use the
 * {@link #LocalVariableAnnotationNode(int, TypePath, LabelNode[], LabelNode[], int[], String)}
 * version.
 *
 * @param typeRef
 *            a reference to the annotated type. See { @link TypeReference}.
 * @param typePath
 *            the path to the annotated type argument, wildcard bound, array
 *            element type, or static inner type within 'typeRef'. May be
 *            <tt>null</tt> if the annotation targets 'typeRef' as a whole.
 * @param start
 *            the fist instructions corresponding to the continuous ranges
 *            that make the scope of this local variable (inclusive).
 * @param end
 *            the last instructions corresponding to the continuous ranges
 *            that make the scope of this local variable (exclusive). This
 *            array must have the same size as the 'start' array.
 * @param index
 *            the local variable's index in each range. This array must have
 *            the same size as the 'start' array.
 * @param desc
 *            the class descriptor of the annotation class.
 */

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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources.jar/org/objectweb/asm/tree/LocalVariableAnnotationNode.java
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*/
/**
* Constructs a new [Textifier]. Subclasses must not use this
* constructor. Instead, they must use the [Textifier(int)]
* version.
*
* @throws IllegalStateException
* If a subclass calls this constructor.
*/

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* /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-jar/org/objectweb/asm/TypeReference.java

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*/
/**
 * Constructs a new {@link ClassNode}. <i>Subclasses must not use this
 * constructor</i>. Instead, they must use the {@link #ClassNode(int)}
 * version.
 *
 * @throws IllegalStateException
 *             If a subclass calls this constructor.
 */

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/**
 * Creates a new JSRInliner. <i>Subclasses must not use this
 * constructor</i>. Instead, they must use the
 * { @link #JSRInlinerAdapter(int, MethodVisitor, int, String, String, String, String[])}
 * version.
 *
 * @param mv
 * the <code>MethodVisitor</code> to send the resulting inlined
 * method code to (use <code>null</code> for none).
 * @param access
 * the method's access flags (see { @link Opcodes}). This
 * parameter also indicates if the method is synthetic and/or
 * deprecated.
 * @param name
 * the method's name.
 * @param desc
 * the method's descriptor (see { @link Type}).
 * @param signature
 * the method's signature. May be <tt>null</tt>.
 * @param exceptions
 * the internal names of the method's exception classes (see
 * { @link Type#getInternalName() getInternalName()}). May be
 * <tt>null</tt>.
 * @throws IllegalStateException
 * If a subclass calls this constructor.
 */

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/**
 * Creates a new [GeneratorAdapter]. <i>Subclasses must not use this
 * constructor</i>. Instead, they must use the
 * [GeneratorAdapter(int, MethodVisitor, int, String, String)]
 * version.
 *
 * @param mv
 * the method visitor to which this adapter delegates calls.
 * @param access
 * the method's access flags (see [Opcodes]).
 * @param name
 * the method's name.
 * @param desc
 * the method's descriptor (see [Type]).
 * @throws IllegalStateException
 * If a subclass calls this constructor.
 */

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    jar/org/objectweb/asm/tree/analysis/package.html
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/signature/package.html
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
    jar/org/objectweb/asm/commons/package.html
  * /opt/ws_local/PERMITS_SQL/1018366213_1591904026.58/0/asm-5-0-3-sources-
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1.48 postgresql 7.4.2

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1.50 javax-ws-rs-api 2.0.1

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1.51 jackson-annotations 2.6.1

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1.52 zlib 1.2.11

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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017

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*/

1.53 wiremock-standalone 2.5.1

1.54 protobuf-java 2.2.0

1.55 jetty-http 9.2.1.v20140609

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1.60 python 2.5.1
1.60.1 Available under license :
A. HISTORY OF THE SOFTWARE
============================

Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands
as a successor of a language called ABC. Guido remains Python’s
principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us)
in Reston, Virginia where he released several versions of the
software.

In May 2000, Guido and the Python core development team moved to
BeOpen.com to form the BeOpen PythonLabs team. In October of the same
year, the PythonLabs team moved to Digital Creations (now Zope
Corporation, see http://www.zope.com). In 2001, the Python Software
Foundation (PSF, see http://www.python.org/psf/) was formed, a
non-profit organization created specifically to own Python-related
Intellectual Property. Zope Corporation is a sponsoring member of
the PSF.

All Python releases are Open Source (see http://www.opensource.org for
the Open Source Definition). Historically, most, but not all, Python
releases have also been GPL-compatible; the table below summarizes
the various releases.
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1.62 lsof 4.92.a,8
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1.63 x-text 0.3.0

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1.64 jackson 1.9.13

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1.65 viper 1.3.2

1.66 slf4j-log4j 1.7.5

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1.67 jetty-servlet 9.2.13.v20150730

1.68 tomcat-juli 8.5.32
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1.69 go-playground-validator 9.0.0

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1.70 zjsonpatch 0.2.1

1.71 jetty-http 9.2.13.v20150730

1.72 glib 2.46.2 4.el7

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1.73 ncurses 6.0.0s20171223_1

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Upstream source https://invisible-island.net/ncurses/ncurses.html
This package is used for testing builds of ncurses.

Current ncurses maintainer: Thomas Dickey <dickey@invisible-island.net>

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-- vile: txtmode file-encoding=utf-8

1.74 openssh 7.3

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 * */
/*

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potential issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w)((int*)&(w))/* convert union wait to int */
#define WIFEXITED(w)!(_W_INT(w) & 0377))
#define WIFSTOPPED(w)((_W_INT(w)) & 0100)
#define WIFSIGNALED(w)!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w)(int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1)
#define WTERMSIG(w)(int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1)
#define WCOREFLAG 0x80
#define WCOREDUMP(w)((_W_INT(w)) & WCOREFLAG)

/* Prototype */
pid_t waitpid(int, int *, int);

#endif /* !HAVE_WAITPID */
#endif /* _BSD_WAITPID_H */

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* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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 */

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int setresgid(gid_t, gid_t, gid_t);
#endif
#ifndef HAVE_SETRESUID
int setresuid(uid_t, uid_t, uid_t);
#endif
/*$OpenBSD: poll.h,v 1.11 2003/12/10 23:10:08 millert Exp $ */

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*/

/* OPENBSD ORIGINAL: sys/sys/poll.h */

#ifndef _COMPAT_POLL_H_
#define _COMPAT_POLL_H_

typedef struct pollfd {
    int fd;
    short events;
    short revents;
} pollfd_t;

typedef unsigned int nfds_t;

#define POLLIN 0x0001
#define POLLOUT 0x0004
#define POLLERR 0x0008
#define POLLHUP 0x0010
#define POLLNVAL 0x0020
#if 0
#define POLLPRI 0x0002
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLWRNORM POLLOUT
#define POLLRDBAND 0x0080
#define POLLWRBAND 0x0100
#endif

#define INFTIM (-1) /* not standard */

int poll(struct pollfd *, nfds_t, int);
#endif
#endif /* !_COMPAT_POLL_H_ */
1.75 bouncycastle-fips 1.51

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**1.77 c3p0 0.9.1.1**

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1.94 libjpeg 8b

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IJG is not affiliated with the ISO/IEC JTC1/SC29/WG1 standards committee (previously known as JPEG, together with ITU-T SG16).

DOCUMENTATION ROADMAP

This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
ACKNOWLEDGMENTS Special thanks.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.
Other documentation files in the distribution are:

User documentation:
install.txt       How to configure and install the IJG software.
usage.txt         Usage instructions for cjpeg, djpeg, jpegtran,
                  rdjpgcom, and wrjpcom.
*.1               Unix-style man pages for programs (same info as usage.txt).
wizard.txt        Advanced usage instructions for JPEG wizards only.
change.log        Version-to-version change highlights.

Programmer and internal documentation:
libjpeg.txt       How to use the JPEG library in your own programs.
example.c         Sample code for calling the JPEG library.
structure.txt     Overview of the JPEG library's internal structure.
filelist.txt      Road map of IJG files.
coderules.txt     Coding style rules --- please read if you contribute code.

Please read at least the files install.txt and usage.txt. Some information
can also be found in the JPEG FAQ (Frequently Asked Questions) article. See
ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or
more of the REFERENCES, then looking at the documentation files (in roughly
the order listed) before diving into the code.

OVERVIEW
========

This package contains C software to implement JPEG image encoding, decoding,
and transcoding. JPEG (pronounced "jay-peg") is a standardized compression
method for full-color and grayscale images.

This software implements JPEG baseline, extended-sequential, and progressive
compression processes. Provision is made for supporting all variants of these
processes, although some uncommon parameter settings aren't implemented yet.
We have made no provision for supporting the hierarchical or lossless
processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files,
plus two sample applications "cjpeg" and "djpeg", which use the library to
perform conversion between JPEG and some other popular image file formats.
The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included
considerable functionality beyond the bare JPEG coding/decoding capability;
for example, the color quantization modules are not strictly part of JPEG
decoding, but they are essential for output to colormapped file formats or
colormapped displays. These extra functions can be compiled out of the
library if not required for a particular application.

We have also included "jpegtran", a utility for lossless transcoding between
different JPEG processes, and "rdjpgcom" and "wrjpgcom", two simple
applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and
flexibility, while also making it fast enough to be useful. In particular,
the software is not intended to be read as a tutorial on JPEG. (See the
REFERENCES section for introductory material.) Rather, it is intended to
be reliable, portable, industrial-strength code. We do not claim to have
achieved that goal in every aspect of the software, but we strive for it.

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The Unix configuration script "configure" was produced with GNU Autoconf.
It is copyright by the Free Software Foundation but is freely distributable.
The same holds for its supporting scripts (config.guess, config.sub,
ltmain.sh). Another support script, install-sh, is copyright by X Consortium
but is also freely distributable.

The IJG distribution formerly included code to read and write GIF files.
To avoid entanglement with the Unisys LZW patent (now expired), GIF reading
support has been removed altogether, and the GIF writer has been simplified
to produce "uncompressed GIFs". This technique does not use the LZW
algorithm; the resulting GIF files are larger than usual, but are readable
by all standard GIF decoders.

REFERENCES
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We recommend reading one or more of these references before trying to
understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is
Wallace, Gregory K. "The JPEG Still Picture Compression Standard",
Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44.
(Adjacent articles in that issue discuss MPEG motion picture compression,
applications of JPEG, and related topics.) If you don't have the CACM issue
handy, a PDF file containing a revised version of Wallace's article is
a preprint for an article that appeared in IEEE Trans. Consumer Electronics)
omits the sample images that appeared in CACM, but it includes corrections and some added material. Note: the Wallace article is copyright ACM and IEEE, and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in "The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by M&T Books (New York), 2nd ed. 1996, ISBN 1-55851-434-1. This book provides good explanations and example C code for a multitude of compression methods including JPEG. It is an excellent source if you are comfortable reading C code but don't know much about data compression in general. The book's JPEG sample code is far from industrial-strength, but when you are ready to look at a full implementation, you've got one here...


Although this is by far the most detailed and comprehensive exposition of JPEG publicly available, we point out that it is still missing an explanation of the most essential properties and algorithms of the underlying DCT technology.

If you think that you know about DCT-based JPEG after reading this book, then you are in delusion. The real fundamentals and corresponding potential of DCT-based JPEG are not publicly known so far, and that is the reason for all the mistaken developments taking place in the image coding domain.


IJG JPEG 9 introduces a reversible color transform for improved lossless compression which is described in a contributed document ISO/IEC JTC1/SC29/WG1 N 6080 with title "JPEG 9 Lossless Coding", June/July 2012, Paris, France.

The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, version 2. JFIF version 1 has been adopted as Recommendation ITU-T T.871 (05/2011) :

The TIFF 6.0 file format specification can be obtained by FTP from ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from http://www.iijg.org/files/. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design.

Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note.

ARCHIVE LOCATIONS
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The "official" archive site for this software is www.iijg.org. The most recent released version can always be found there in directory "files". This particular version will be archived as http://www.iijg.org/files/jpegsrc.v9c.tar.gz, and in Windows-compatible "zip" archive format as http://www.iijg.org/files/jpegsr9c.zip.


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send usenet/news.answers/jpeg-faq/part2

ACKNOWLEDGMENTS
=================

Thank to Juergen Bruder for providing me with a copy of the common DCT algorithm article, only to find out that I had come to the same result in a more direct and comprehensible way with a more generative approach.

Thank to Istvan Sebestyen and Joan L. Mitchell for inviting me to the ITU JPEG (Study Group 16) meeting in Geneva, Switzerland.
Thank to Thomas Wiegand and Gary Sullivan for inviting me to the Joint Video Team (MPEG & ITU) meeting in Geneva, Switzerland.

Thank to Thomas Richter and Daniel Lee for inviting me to the ISO/IEC JTC1/SC29/WG1 (previously known as JPEG, together with ITU-T SG16) meeting in Berlin, Germany.

Thank to John Korejwa and Massimo Ballerini for inviting me to fruitful consultations in Boston, MA and Milan, Italy.

Thank to Hendrik Elstner, Roland Fassauer, Simone Zuck, Guenther Maier-Gerber, Walter Stoeber, Fred Schmitz, and Norbert Braunagel for corresponding business development.

Thank to Nico Zschach and Dirk Stelling of the technical support team at the Digital Images company in Halle for providing me with extra equipment for configuration tests.

Thank to Richard F. Lyon (then of Foveon Inc.) for fruitful communication about JPEG configuration in Sigma Photo Pro software.

Thank to Andrew Finkenstadt for hosting the ijg.org site.

Thank to Thomas G. Lane for the original design and development of this singular software package.

Thank to Lars Goehler, Andreas Heinecke, Sebastian Fuss, Yvonne Roebert, Andrej Werner, and Ulf-Dietrich Braumann for support and public relations.

FILE FORMAT WARS
=================

The ISO/IEC JTC1/SC29/WG1 standards committee (previously known as JPEG, together with ITU-T SG16) currently promotes different formats containing the name "JPEG" which is misleading because these formats are incompatible with original DCT-based JPEG and are based on faulty technologies. IJG therefore does not and will not support such momentary mistakes (see REFERENCES).

There exist also distributions under the name "OpenJPEG" promoting such kind of formats which is misleading because they don't support original JPEG images.

We have no sympathy for the promotion of inferior formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, interoperable format standards for JPEG files. Don't use an incompatible file format!

(In any case, our decoder will remain capable of reading existing JPEG image files indefinitely.)
The ISO committee pretends to be "responsible for the popular JPEG" in their public reports which is not true because they don't respond to actual requirements for the maintenance of the original JPEG specification. Furthermore, the ISO committee pretends to "ensure interoperability" with their standards which is not true because their "standards" support only application-specific and proprietary use cases and contain mathematically incorrect code.

There are currently different distributions in circulation containing the name "libjpeg" which is misleading because they don't have the features and are incompatible with formats supported by actual IJG libjpeg distributions. One of those fakes is released by members of the ISO committee and just uses the name of libjpeg for misdirection of people, similar to the abuse of the name JPEG as described above, while having nothing in common with actual IJG libjpeg distributions and containing mathematically incorrect code. The other one claims to be a "derivative" or "fork" of the original libjpeg, but violates the license conditions as described under LEGAL ISSUES above and violates basic C programming properties. We have no sympathy for the release of misleading, incorrect and illegal distributions derived from obsolete code bases. Don't use an obsolete code base!

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A partial list of foreclosed institutions and corporations ("Hall of Shame") is currently prepared and will be published later.
TO DO

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Version 9 is the second release of a new generation JPEG standard to overcome the limitations of the original JPEG specification, and is the first true source reference JPEG codec. More features are being prepared for coming releases...

Please send bug reports, offers of help, etc. to jpeg-info@jpegclub.org.

1.95 stax-api 1.0.1
1.95.1 Available under license:

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1.98 tcsh  6.20.00

1.98.1 Available under license:

/*
 * config.h -- configure various defines for tcsh
 *
 * All source files should #include this FIRST.
 *
 * Edit this to match your system type.
 */

#ifndef _h_config
#define _h_config

/****************** System dependant compilation flags ******************/

/*
 * POSIX	This system supports IEEE Std 1003.1-1988 (POSIX).
 */
#undef POSIX

/*
 * POSIXJOBS	This system supports the optional IEEE Std 1003.1-1988 (POSIX)
 * job control facilities.
 */
#undef POSIXJOBS

/*
 * VFORK	This machine has a vfork().
 * It used to be that for job control to work, this define
 * was mandatory. This is not the case any more.
 * If you think you still need it, but you don't have vfork,
 * define this anyway and then do #define vfork fork.
 * I do this anyway on a Sun because of yellow pages brain damage,
 * [should not be needed under 4.1]
 * and on the iris4d cause SGI's fork is sufficiently "virtual"
 * that vfork isn't necessary. (Besides, SGI's vfork is weird).
 * Note that some machines eg. rs6000 have a vfork, but not
 * with the berkeley semantics, so we cannot use it there either.
 */
#define VFORK

/*
 * BSDJOBS	You have BSD-style job control (both process groups and
 * a tty that deals correctly
 */
#define BSDJOBS

/*
 * BSDTIMES You have BSD-style process time stuff (like rusage)
 * This may or may not be true. For example, Apple Unix
 * (OREO) has BSDJOBS but not BSDTIMES.
 */
#define BSDTIMES

/*
 * BSDLIMIT You have BSD-style resource limit stuff (getrlimit/setrlimit)
 */
#define BSDLIMIT

/*
 * TERMIO You have struct termio instead of struct sgttyb.
 * This is usually the case for SYSV systems, where
 * BSD uses sgttyb. POSIX systems should define this
 * anyway, even though they use struct termios.
 */
#undef TERMIO

/*
 * SYSVREL Your machine is SYSV based (HPUX, A/UX)
 * NOTE: don't do this if you are on a Pyramid -- tcsh is
 * built in a BSD universe.
 * Set SYSVREL to 1, 2, 3, or 4, depending the version of System V
 * you are running. Or set it to 0 if you are not SYSV based
 */
#define SYSVREL 0

/*
 * YPBUGS Work around Sun YP bugs that cause expansion of ~username
 * to send command output to /dev/null
 */
#undef YPBUGS

/******************** local defines *********************/
#endif /* _h_config */
/*-
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1.99 net-snmp 5.7.1

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    set yrange [0:]
    set terminal pngcairo font 'Sans, 8' lw 1 size 1400,1024
    set xtics rotate by -45
    set style histogram errorbars gap 2 lw 1
    set style fill solid border -1
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1.101 jackson-jaxrs-base 2.9.5

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

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Yoyodyne, Inc., hereby disclaims all copyright interest in the library 'Frob' (a library for tweaking knobs) written by James Random Hacker.

<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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Version 2.1, February 1999

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We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.
In other cases, permission to use a particular library in non-free programs enables a greater number of people to use a large body of free software. For example, permission to use the GNU C Library in non-free programs enables many more people to use the whole GNU operating system, as well as its variant, the GNU/Linux operating system.

Although the Lesser General Public License is Less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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   b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

   c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

   d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in
themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the
Library. A suitable mechanism is one that (1) uses at run time a
copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is
interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

d) If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above
specified materials from the same place.

e) Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the
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reproducing the executable from it. However, as a special exception,
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normally distributed (in either source or binary form) with the major
components (compiler, kernel, and so on) of the operating system on
which the executable runs, unless that component itself accompanies
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use both them and the Library together in an executable that you
distribute.

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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!

/* Native-dependent code for modern AMD64 BSD's.

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#ifndef AMD64_BSD_NAT_H
#define AMD64_BSD_NAT_H

#include "x86-bsd-nat.h"

/* Helper functions. See definitions. */
extern void amd64bsd_fetch_inferior_registers (struct regcache *regcache,
int regnum);
extern void amd64bsd_store_inferior_registers (struct regcache *regcache,
    int regnum);

/* A prototype *BSD/AMD64 target. */

template<typename BaseTarget>
class amd64_bsd_nat_target : public x86bsd_nat_target<BaseTarget> 
{ 
public:
    void fetch_registers (struct regcache *regcache, int regnum) override
    { amd64bsd_fetch_inferior_registers (regcache, regnum); } 

    void store_registers (struct regcache *regcache, int regnum) override
    { amd64bsd_store_inferior_registers (regcache, regnum); } 
};

#endif /* i386-bsd-nat.h */

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Version 3, 29 June 2007

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Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

TERMS AND CONDITIONS

0. Definitions.

"This License" refers to version 3 of the GNU General Public License.

"Copyright" also means copyright-like laws that apply to other kinds of works, such as semiconductor masks.
"The Program" refers to any copyrightable work licensed under this License. Each licensee is addressed as "you". "Licensees" and "recipients" may be individuals or organizations.

To "modify" a work means to copy from or adapt all or part of the work in a fashion requiring copyright permission, other than the making of an exact copy. The resulting work is called a "modified version" of the earlier work or a work "based on" the earlier work.

A "covered work" means either the unmodified Program or a work based on the Program.

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To "convey" a work means any kind of propagation that enables other parties to make or receive copies. Mere interaction with a user through a computer network, with no transfer of a copy, is not conveying.

An interactive user interface displays "Appropriate Legal Notices" to the extent that it includes a convenient and prominently visible feature that (1) displays an appropriate copyright notice, and (2) tells the user that there is no warranty for the work (except to the extent that warranties are provided), that licensees may convey the work under this License, and how to view a copy of this License. If the interface presents a list of user commands or options, such as a menu, a prominent item in the list meets this criterion.


The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

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#!/usr/bin/env python

# Copyright (C) 2011-2019 Free Software Foundation, Inc.
#
# This file is part of GDB.
#
# This program is free software; you can redistribute it and/or modify
This script updates the list of years in the copyright notices in
most files maintained by the GDB project.

Usage: cd src/gdb && python copyright.py

Always review the output of this script before committing it!
A useful command to review the output is:
  % filterdiff -x *.c -x *.cc -x *.h -x *.exp updates.diff
This removes the bulk of the changes which are most likely to be correct.

```
import datetime
import os
import os.path
import subprocess

def get_update_list():
    """Return the list of files to update.

    Assumes that the current working directory when called is the root
    of the GDB source tree (NOT the gdb/ subdirectory!).  The names of
    the files are relative to that root directory.
    """
    result = []
    for gdb_dir in ('gdb', 'sim', 'include/gdb'):
        for root, dirs, files in os.walk(gdb_dir, topdown=True):
            for dirname in dirs:
                reldirname = "%s/%s" % (root, dirname)
                if (dirname in EXCLUDE_ALL_LIST
                    or reldirname in EXCLUDE_LIST
                    or reldirname in NOT_FSF_LIST
                    or reldirname in BY_HAND):
                    # Prune this directory from our search list.
                else:
                    result.append(root + '/' + dirname)
    return result
```
dirs.remove(dirmame)

for filename in files:
    relpath = "%s/%s" % (root, filename)
    if (filename in EXCLUDE_ALL_LIST
        or relpath in EXCLUDE_LIST
        or relpath in NOT_FSF_LIST
        or relpath in BY_HAND):
        # Ignore this file.
        pass
    else:
        result.append(relpath)

return result

def update_files(update_list):
    """Update the copyright header of the files in the given list.

    We use gnulib's update-copyright script for that.
    ""
    # We want to use year intervals in the copyright notices, and
    # all years should be collapsed to one single year interval,
    # even if there are "holes" in the list of years found in the
    # original copyright notice (OK'ed by the FSF, case [gnu.org #719834]).
    os.environ['UPDATE_COPYRIGHT_USE_INTERVALS'] = '2'
    # Perform the update, and save the output in a string.
    update_cmd = ['bash', 'gdb/gnulib/import/extra/update-copyright']
    update_cmd += update_list

    p = subprocess.Popen(update_cmd, stdout=subprocess.PIPE,
                         stderr=subprocess.STDOUT)
    update_out = p.communicate()[0]

    # Process the output. Typically, a lot of files do not have
    # a copyright notice :-(. The update-copyright script prints
    # a well defined warning when it did not find the copyright notice.
    # For each of those, do a sanity check and see if they may in fact
    # have one. For the files that are found not to have one, we filter
    # the line out from the output, since there is nothing more to do,
    # short of looking at each file and seeing which notice is appropriate.
    # Too much work! (~4,000 files listed as of 2012-01-03).
    update_out = update_out.splitlines()
    warning_string = ': warning: copyright statement not found'
    warning_len = len(warning_string)

    for line in update_out:
        if line.endswith('w'):
            line = line[:-1]
if line.endswith(warning_string):
    filename = line[:-warning_len]
    if may_have_copyright_notice(filename):
        print line
    else:
        # Unrecognized file format. !?!
        print "*** " + line

def may_have_copyright_notice(filename):
    """Check that the given file does not seem to have a copyright notice.

The filename is relative to the root directory.
This function assumes that the current working directory is that root directory.

The algorithim is fairly crude, meaning that it might return some false positives. I do not think it will return any false negatives... We might improve this function to handle more complex cases later...
"""
    # For now, it may have a copyright notice if we find the word
    # "Copyright" at the (reasonable) start of the given file, say
    # 50 lines...
    MAX_LINES = 50

    fd = open(filename)

    lineno = 1
    for line in fd:
        if 'Copyright' in line:
            return True
        lineno += 1
        if lineno > 50:
            return False
    return False

def main ():
    """The main subprogram.""
    if not os.path.isfile("gnulib/import/extra/update-copyright"):
        print "Error: This script must be called from the gdb directory."
    root_dir = os.path.dirname(os.getcwd())
    os.chdir(root_dir)

    update_list = get_update_list()
    update_files (update_list)
# Remind the user that some files need to be updated by HAND...

if MULTIPLE_COPYRIGHT_HEADERS:
    print
    print("\033[31m"
          "REMINDER: Multiple copyright headers must be updated by hand:"
          "\033[0m")
    for filename in MULTIPLE_COPYRIGHT_HEADERS:
        print " ", filename

if BY_HAND:
    print
    print "\033[31mREMINDER: The following files must be updated by hand." \\
          "\033[0m"
    for filename in BY_HAND:
        print " ", filename

############################################################################
#
# Some constants, placed at the end because they take up a lot of room.
# The actual value of these constants is not significant to the understanding
# of the script.
#
############################################################################

# Files which should not be modified, either because they are
# generated, non-FSF, or otherwise special (e.g. license text,
# or test cases which must be sensitive to line numbering).
#
# Filenames are relative to the root directory.
EXCLUDE_LIST = (  
    'gdb/nat/glibc_thread_db.h',
    'gdb/CONTRIBUTE',
    'gdb/gnulib/import'
)

# Files which should not be modified, either because they are
# generated, non-FSF, or otherwise special (e.g. license text,
# or test cases which must be sensitive to line numbering).
#
# Matches any file or directory name anywhere. Use with caution.
# This is mostly for files that can be found in multiple directories.
# Eg: We want all files named COPYING to be left untouched.

EXCLUDE_ALL_LIST = (  
    "COPYING", "COPYING.LIB", "CVS", "configure", "copying.c",
    "fdl.texi", "gpl.texi", "aclocal.m4",
)
# The list of files to update by hand.

BY_HAND = (
    # Nothing at the moment :-).
)

# Files containing multiple copyright headers. This script is only
# fixing the first one it finds, so we need to finish the update
# by hand.
MULTIPLE_COPYRIGHT_HEADERS = (
    "gdb/doc/gdb.texinfo",
    "gdb/doc/refcard.tex",
    "gdb/gdbarch.sh",
)

# The list of file which have a copyright, but not head by the FSF.
# Filenames are relative to the root directory.
NOT_FSF_LIST = (
    "gdb/exc_request.defs",
    "gdb/gdbtk",
    "gdb/testsuite/gdb gdbtk/",
    "sim/arm/armemu.h", "sim/arm/armos.c", "sim/arm/gdbhost.c",
    "sim/arm/dbg_hif.h", "sim/arm/dbg_conf.h", "sim/arm/communicate.h",
    "sim/arm/armos.h", "sim/arm/armcopro.c", "sim/arm/armemu.c",
    "sim/arm/kid.c", "sim/arm/thumboemu.c", "sim/arm/armdefs.h",
    "sim/arm/armopts.h", "sim/arm/dbg_cp.h", "sim/arm/dbg_rdi.h",
    "sim/arm/parent.c", "sim/arm/armsupp.c", "sim/arm/armrdi.c",
    "sim/arm/bag.c", "sim/arm/armvirt.c", "sim/arm/main.c", "sim/arm/bag.h",
    "sim/arm/communicate.c", "sim/arm/gdbhost.h", "sim/arm/armfpe.h",
    "sim/arm/arminit.c",
    "sim/common/cgen-fpu.c", "sim/common/cgen-fpu.h",
    "sim/common/cgen-accfp.c",
    "sim/mips/m16run.c", "sim/mips/sim-main.c",
    "sim/moxie/moxie gdb.dtp",
    # Not a single file in sim/ppc/ appears to be copyright FSF :-(. 
    "sim/ppc/filter.h", "sim/ppc/gen-support.h", "sim/ppc/ld-inSn.h",
    "sim/ppc/hw_sem.c", "sim/ppc/hw_disk.c", "sim/ppc/decode_branch.h",
    "sim/ppc/sim-endian.h", "sim/ppc/sim-table.c", "sim/ppc/hw core.c",
    "sim/ppc/gen-support.c", "sim/ppc/gen-semantics.h", "sim/ppc/cpu.h",
    "sim/ppc/sim CALLBACKS.h", "sim/ppc/RUN", "sim/ppc/Makefile.in",
    "sim/ppc/emul_chirp.c", "sim/ppc/hw_nvram.c", "sim/ppc/dc-test.01",
    "sim/ppc/hw phb.c", "sim/ppc/hw_eeprom.c", "sim/ppc/bits.h",
    "sim/ppc/hw_vm.c", "sim/ppc/cap.h", "sim/ppc/os_emul.h",
    "sim/ppc/options.h", "sim/ppc/gen idecode.c", "sim/ppc/filter.c",
    "sim/ppc/corefile-n.h", "sim/ppc/std-config.h", "sim/ppc/ld-decode.h",
    "sim/ppc/filter_filename.h", "sim/ppc/hw shm.c",
    "sim/ppc/pk_disklabel.c", "sim/ppc/dc simple", "sim/ppc/misc.h",
    "sim/ppc/device_table.h", "sim/ppc/ld-insn.c", "sim/ppc/inline.c", 
)
if __name__ == '__main__':
    main()

/* Native-dependent code for modern i386 BSD's.

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/* Helper functions. See definitions. */
extern void i386bsd_fetch_inferior Registers (struct regcache *regcache,
     int regnum);
extern void i386bsd_store_inferior Registers (struct regcache *regcache,
     int regnum);

/* A prototype *BSD/i386 target. */

template<typename BaseTarget>
class i386_bsd_nat_target : public x86bsd_nat_target<BaseTarget>
{
    public:
        void fetch_registers (struct regcache *regcache, int regnum) override
            { i386bsd_fetch_inferior_registers (regcache, regnum); }

        void store_registers (struct regcache *regcache, int regnum) override
            { i386bsd_store_inferior_registers (regcache, regnum); }
    }

#endif /* i386-bsd-nat.h */
/* Native-dependent code for x86 BSD's.

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#ifndef X86_BSD_NAT_H
#define X86_BSD_NAT_H

#include "x86-nat.h"

/* Low level x86 XSAVE info. */
extern size_t x86bsd_xsave_len;

/* A prototype *BSD/x86 target. */

template<typename BaseTarget>
class x86bsd_nat_target : public x86_nat_target<BaseTarget>
{
    using base_class = x86_nat_target<BaseTarget>;
	public:
#ifdefined HAVE_PT_GETDBREGS
    void mourn_inferior () override
    {
        x86_cleanup_dregs ();
        base_class::mourn_inferior ();
    }
#endif /* HAVE_PT_GETDBREGS */
}
#endif /* x86-bsd-nat.h */
/* BSD Kernel Data Access Library (libkvm) interface. 

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along with this program.  If not, see <http://www.gnu.org/licenses/>. */

#define _KMEMUSER
#include "defs.h"
#include "cli/cli-cmds.h"
#include "command.h"
#include "frame.h"
#include "regcache.h"
#include "target.h"
#include "process-stratum-target.h"
#include "value.h"
#include "gdbcore.h"
#include "inferior.h" /* for get_exec_file */
#include "gdbthread.h"

#include <fcntl.h>
#include <kvm.h>
#ifdef HAVE_NLIST_H
#include <nlist.h>
#endif
#include <paths.h>
#include "readline/readline.h"
#include <sys/param.h>
#include <sys/proc.h>
#ifdef HAVE_SYS_USER_H
#include <sys/user.h>
#endif
#include "bsd-kvm.h"

/* Kernel memory device file. */
static const char *bsd_kvm_corefile;

/* Kernel memory interface descriptor. */
static kvm_t *core_kd;

/* Address of process control block. */
static struct pcb *bsd_kvm_paddr;

/* Pointer to architecture-specific function that reconstructs the
 register state from PCB and supplies it to REGCACHE. */
static int (*bsd_kvm_supply_pcb)(struct regcache *regcache, struct pcb *pcb);

/* This is the ptid we use while we're connected to kvm. The kvm
target currently doesn't export any view of the running processes,
so this represents the kernel task. */
static ptid_t bsd_kvm_ptid;

/* The libkvm target. */

static const target_info bsd_kvm_target_info = {
"kvm",
N_("Kernel memory interface"),
N_("Use a kernel virtual memory image as a target.


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class bsd_kvm_target final : public process_stratum_target
{
    public:
    bsd_kvm_target () = default;

    const target_info &info () const override
    { return bsd_kvm_target_info; }

    void close () override;

    void fetch_registers (struct regcache *, int) override;
    enum target_xfer_status xfer_partial (enum target_object object, const char *annex, gdb_byte *readbuf, const gdb_byte *writebuf, ULONGEST offset, ULONGEST len, ULONGEST *xfered_len) override;

    void files_info () override;
    bool thread_alive (ptid_t ptid) override;
    const char *pid_to_str (ptid_t) override;

    bool has_memory () override { return true; }
    bool has_stack () override { return true; }
    bool has_registers () override { return true; }
};

/* Target ops for libkvm interface. */
static bsd_kvm_target bsd_kvm_ops;

static void
bsd_kvm_target_open (const char *arg, int from_tty)
{
    char errbuf[_POSIX2_LINE_MAX];
    char *execfile = NULL;
    kvm_t *temp_kd;
    char *filename = NULL;

    target_preopen (from_tty);

    if (arg)
    {
        char *temp;

        filename = tilde_expand (arg);
if (filename[0] != '/')
{
    temp = concat (current_directory, "/", filename, (char *)NULL);
    xfree (filename);
    filename = temp;
}
}

execfile = get_exec_file (0);
temp_kd = kvm_openfiles (execfile, filename, NULL,
    write_files ? O_RDWR : O_RDONLY, errbuf);
if (temp_kd == NULL)
    error (("%s"), errbuf);

bsd_kvm_corefile = filename;
unpush_target (&bsd_kvm_ops);
core_kd = temp_kd;
push_target (&bsd_kvm_ops);

add_thread_silent (bsd_kvm_ptid);
inferior_ptid = bsd_kvm_ptid;

target_fetch_registers (get_current_regcache (), -1);

reinit_frame_cache ();
print_stack_frame (get_selected_frame (NULL), 0, SRC_AND_LOC, 1);
}

void
bsd_kvm_target::close ()
{
    if (core_kd)
    {
        if (kvm_close (core_kd) == -1)
            warning (("%s"), kvm_geterr(core_kd));
        core_kd = NULL;
    }

    inferior_ptid = null_ptid;
discard_all_inferiors ();
}

static LONGEST
bsd_kvm_xfer_memory (CORE_ADDR addr, ULONGEST len,
    gdb_byte *readbuf, const gdb_byte *writebuf)
{
    ssize_t nbytes = len;
enum target_xfer_status
bsd_kvm_target::xfer_partial (enum target_object object,
    const char *annex, gdb_byte *readbuf,
    const gdb_byte *writebuf,
    ULONGEST offset, ULONGEST len, ULONGEST *xfered_len)
{
    switch (object)
    {
    case TARGET_OBJECT_MEMORY:
        {
            LONGEST ret = bsd_kvm_xfer_memory (offset, len, readbuf, writebuf);

            if (ret < 0)
                return TARGET_XFER_E_IO;
            else if (ret == 0)
                return TARGET_XFER_EOF;
            else
            {
                *xfered_len = (ULONGEST) ret;
                return TARGET_XFER_OK;
            }
        }
    default:
        return TARGET_XFER_E_IO;
    }
}

void
bsd_kvm_target::files_info ()
{
    if (bsd_kvm_corefile && strcmp (bsd_kvm_corefile, _PATH_MEM) != 0)
        printf_filtered (_("\tUsing the kernel crash dump %s\n"),
            bsd_kvm_corefile);
    else
        printf_filtered (_("\tUsing the currently running kernel\n"));
}

/* Fetch process control block at address PADDR. */

static int
bsd_kvm_fetch_pcb (struct regcache *regcache, struct pcb *paddr) {
    struct pcb pcb;

    if (kvm_read (core_kd, (unsigned long) paddr, &pcb, sizeof pcb) == -1)
        error ("%s", kvm_geterr (core_kd));

    gdb_assert (bsd_kvm_supply_pcb);
    return bsd_kvm_supply_pcb (regcache, &pcb);
}

void
bsd_kvm_target::fetch_registers (struct regcache *regcache, int regnum) {
    struct nlist nl[2];

    if (bsd_kvm_paddr)
        bsd_kvm_fetch_pcb (regcache, bsd_kvm_paddr);
    return;
}

/* On dumping core, BSD kernels store the faulting context (PCB)
   in the variable "dumppcb". */
memset (nl, 0, sizeof nl);
nl[0].n_name = "dumppcb";

if (kvm_nlist (core_kd, nl) == -1)
    error ("%s", kvm_geterr (core_kd));

if (nl[0].n_value != 0)
    /* Found dumppcb. If it contains a valid context, return
       immediately. */
    if (bsd_kvm_fetch_pcb (regcache, (struct pcb *) nl[0].n_value))
        return;

/* Traditional BSD kernels have a process proc0 that should always
   be present. The address of proc0's PCB is stored in the variable
   "proc0paddr". */
memset (nl, 0, sizeof nl);
nl[0].n_name = "proc0paddr";

if (kvm_nlist (core_kd, nl) == -1)
    error ("%s", kvm_geterr (core_kd));
if (nl[0].n_value != 0)
{
    struct pcb *paddr;

    /* Found proc0paddr. */
    if (kvm_read (core_kd, nl[0].n_value, &paddr, sizeof paddr) == -1)
        error (("%s"), kvm_geterr (core_kd));

    bsd_kvm_fetch_pcb (regcache, paddr);
    return;
}

#ifdef HAVE_STRUCT_THREAD_TD_PCB
/* In FreeBSD kernels for 5.0-RELEASE and later, the PCB no longer
lives in `struct proc' but in `struct thread'. The `struct
thread' for the initial thread for proc0 can be found in the
variable "thread0". */

memset (nl, 0, sizeof nl);
nl[0].n_name = "_thread0";

if (kvm_nlist (core_kd, nl) == -1)
    error (("%s"), kvm_geterr (core_kd));

if (nl[0].n_value != 0)
{
    struct pcb *paddr;

    /* Found thread0. */
    nl[0].n_value += offsetof (struct thread, td_pcb);
    if (kvm_read (core_kd, nl[0].n_value, &paddr, sizeof paddr) == -1)
        error (("%s"), kvm_geterr (core_kd));

    bsd_kvm_fetch_pcb (regcache, paddr);
    return;
}
#endif

/* i18n: PCB == "Process Control Block". */
error (_("Cannot find a valid PCB"));
}

/* Kernel memory interface commands. */
struct cmd_list_element *bsd_kvm_cmdlist;

static void
bsd_kvm_cmd (const char *arg, int fromtty)
static void
bsd_kvm_proc_cmd (const char *arg, int fromtty)
{
    CORE_ADDR addr;

    if (arg == NULL)
        error_no_arg ("proc address");

    if (core_kd == NULL)
        error ("No kernel memory image.");

    addr = parse_and_eval_address (arg);
    #ifdef HAVE_STRUCT_LWP
    addr += offsetof (struct lwp, l_addr);
    #else
    addr += offsetof (struct proc, p_addr);
    #endif

    if (kvm_read (core_kd, addr, &bsd_kvm_paddr, sizeof bsd_kvm_paddr) == -1)
        error ("%s", kvm_geterr (core_kd));

    target_fetch_registers (get_current_regcache (), -1);

    reinit_frame_cache ();
    print_stack_frame (get_selected_frame (NULL), 0, SRC_AND_LOC, 1);
}

static void
bsd_kvm_pcb_cmd (const char *arg, int fromtty)
{
    if (arg == NULL)
        /* i18n: PCB == "Process Control Block". */
        error_no_arg ("pcb address");

    if (core_kd == NULL)
        error ("No kernel memory image.");

    bsd_kvm_paddr = (struct pcb *) (u_long) parse_and_eval_address (arg);

    target_fetch_registers (get_current_regcache (), -1);
void
bsd_kvm_add_target (int (*supply_pcb)(struct regcache *, struct pcb *))
{
    gdb_assert (bsd_kvm_supply_pcb == NULL);
    bsd_kvm_supply_pcb = supply_pcb;

    add_target (bsd_kvm_target_info, bsd_kvm_target_open);

    add_prefix_cmd ("kvm", class_obscure, bsd_kvm_cmd, _("Generic command for manipulating the kernel memory interface.")),
        &bsd_kvm_cmdlist, "kvm ", 0, &cmdlist);

    #ifndef HAVE_STRUCT_THREAD_TD_PCB
    add_cmd ("proc", class_obscure, bsd_kvm_proc_cmd,
            _("Set current context from proc address"), &bsd_kvm_cmdlist);
    #endif
    add_cmd ("pcb", class_obscure, bsd_kvm_pcb_cmd,
            /* i18n: PCB == "Process Control Block". */
            _("Set current context from pcb address"), &bsd_kvm_cmdlist);

    /* Some notes on the ptid usage on this target.

    The pid field represents the kvm inferior instance. Currently,
    we don't support multiple kvm inferiors, but we start at 1
    anyway. The lwp field is set to != 0, in case the core wants to
refer to the whole kvm inferior with ptid(1,0,0).

If kvm is made to export running processes as gdb threads, the following form can be used:
ptid (1, 1, 0) -> kvm inferior 1, in kernel
ptid (1, 1, 1) -> kvm inferior 1, process 1
ptid (1, 1, 2) -> kvm inferior 1, process 2
ptid (1, 1, n) -> kvm inferior 1, process n */

bsd_kvm_ptid = ptid_t (1, 1, 0);
}

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FS="\n";
  print "/# == Do not modify this file!! " \n".-*- buffer-read-only: t -*- vi" \n".set ro:";
  print " It is created automatically by copying.awk."
  print " Modify copying.awk instead. <<= */
  print ""
  print "#include "defs.h"
  print "#include "command.h"
  print "#include "gdbcmd.h"
  print ""
  print "static void show_copying_command (const char *, int);"
  print ""
  print "static void show_warranty_command (const char *, int);"
  print ""
  print "static void"
  print "show_copying_command (const char *ignore, int from_tty)"
  print ","
}
print ")";
    for (i = 1; i < NF; i++)
    printf "\n", $NF;
}
}
END{
  printf filtered ("\n";
  for (i = 1; i < NF; i++)
  printf "%s\n", $i;
  printf "%s\n\n", $NF;
}
}

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@c End:

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   Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the
users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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END OF TERMS AND CONDITIONS

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To apply these terms, attach the following notices to the library. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

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Some devices are designed to deny users access to install or run modified versions of the software inside them, although the manufacturer can do so. This is fundamentally incompatible with the aim of protecting users' freedom to change the software. The systematic pattern of such abuse occurs in the area of products for individuals to use, which is precisely where it is most unacceptable. Therefore, we have designed this version of the GPL to prohibit the practice for those products. If such problems arise substantially in other domains, we stand ready to extend this provision to those
domains in future versions of the GPL, as needed to protect the freedom of users.

Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

@heading TERMS AND CONDITIONS

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*Source Code.*

The ``source code'' for a work means the preferred form of the work for making modifications to it. ``Object code'' means any non-source form of a work.

A ``Standard Interface'' means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The ```System Libraries'' of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A ``Major Component'', in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

The ```Corresponding Source'' for a work in object code form means all the source code needed to generate, install, and (for an executable work) run the object code and to modify the work, including scripts to control those activities. However, it does not include the work's System Libraries, or general-purpose tools or generally available free programs which are used unmodified in performing those activities but which are not part of the work. For example, Corresponding Source includes interface definition files associated with source files for the work, and the source code for shared libraries and dynamically linked subprograms that the work is specifically designed to require, such as by intimate data communication or control flow between those subprograms and other parts of the work.

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The work must carry prominent notices stating that you modified it, and giving a relevant date.

@item
The work must carry prominent notices stating that it is released under this License and any conditions added under section 7. This requirement modifies the requirement in section 4 to "keep intact all notices".

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@end enumerate

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@enumerate a
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1.104 tomcat-websocket 8.5.32

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1.105 python 2.6

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Python was created in the early 1990s by Guido van Rossum at Stichting
Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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1.106 tokyo-cabinet 1.4.48

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1.110 openssh 5.4p1

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 *
 */

#ifndef _BSD_WAITPID_H
#define _BSD_WAITPID_H

#ifndef HAVE_WAITPID
/* Clean out any potential issues */
#undef WIFEXITED
#undef WIFSTOPPED
#undef WIFSIGNALED

/* Define required functions to mimic a POSIX look and feel */
#define _W_INT(w) (*((int*)&(w))/* convert union wait to int */
#define WIFEXITED(w) (!((_W_INT(w)) & 0377))
#define WIFSTOPPED(w)((_W_INT(w)) & 0100)
#define WIFSIGNALED(w) (!WIFEXITED(w) && !WIFSTOPPED(w))
#define WEXITSTATUS(w) ((int)(WIFEXITED(w) ? ((_W_INT(w) >> 8) & 0377) : -1))
#define WTERMSIG(w) ((int)(WIFSIGNALED(w) ? (_W_INT(w) & 0177) : -1))
#define WCOREFLAG 0x80
#define WCOREDUMP(w) ((_W_INT(w)) & WCOREFLAG)
#endif
#endif

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/* Prototype */
int waitpid(int, int *, int);

#ifndef HAVE_WAITPID */
#endif /* _BSD_WAITPID_H */
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 */

#ifndef HAVE_SETRESGID
int setresgid(gid_t, gid_t, gid_t);
#endif
#ifndef HAVE_SETRESUID
int setresuid(uid_t, uid_t, uid_t);
#endif

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* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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*/

/* OPENBSD ORIGINAL: sys/sys/poll.h */

#if !defined(HAVE_POLL) && !defined(HAVE_POLL_H)
#endifCompat_POLL_H
#defineCompat_POLL_H

typedef struct pollfd {
    int fd;
    short events;
    short revents;
} pollfd_t;
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typedef unsigned int nfds_t;

#define POLLIN 0x0001
#define POLLOUT 0x0004
#define POLLERR 0x0008
#define POLLHUP 0x0010
#define POLLNVAL 0x0020
#if 0
/* the following are currently not implemented */
#define POLLPRI 0x0002
#define POLLRDNORM 0x0040
#define POLLNORM POLLRDNORM
#define POLLRNORM POLLOUT
#define POLLRDBAND 0x0080
#define POLLWRBAND 0x0100
#endif

#define INFTIM (-1) /* not standard */

int poll(struct pollfd *, nfds_t, int);
#endif /* !_COMPAT_POLL_H_ */
#endif /* !HAVE_POLL_H */
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From strings/apr_strnatcmp.c, include/apr_strings.h:

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The copyright law in the United States has a doctrine called fair use. Fair use provides a defense to copyright infringement in some circumstances. For example, fair use allows documentary filmmakers to use very short clips of copyrighted movies, music and news footage without permission from the copyright owner. Fair use is a difficult concept because determining whether something is a fair use involves weighing four factors. Unfortunately, weighing the fair use factors rarely results in a clear-cut answer.

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its purpose remains meaningful.

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<html>
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<meta http-equiv="Content-Type" content="text/html; charset=UTF-8">
<meta name="generator" content="DocBook XSL Stylesheets V1.78.1">
<link rel="home" href="index.html" title="JSON-GLib Reference Manual">
<link rel="prev" href="annotation-glossary.html" title="Annotation Glossary">
<link rel="stylesheet" href="style.css" type="text/css">
</head>
<body bgcolor="white" text="black" link="#0000FF" vlink="#840084" alink="#0000FF">
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<td width="100%" align="left" class="shortcuts"></td>
<td><a accesskey="h" href="index.html"><img src="home.png" width="16" height="16" border="0" alt="Home" /></a></td>
<td><img src="up-insensitive.png" width="16" height="16" border="0"></td>
<td><a accesskey="p" href="annotation-glossary.html"><img src="left.png" width="16" height="16" border="0" alt="Prev" /></a></td>
</tr></table>
</body>
</html>
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define internal void @innerSmall() "min-legal-vector-width"="128" {
   ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
   ret void
}

define internal void @innerNoAttribute() {
   ret void
}

; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
   call void @innerLarge()
   ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
   call void @innerLarge()
   ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
   call void @innerSmall()
   ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
   call void @innerNoAttribute()
   ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
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<table>
<thead>
<tr>
<th>Program</th>
<th>Directory</th>
</tr>
</thead>
</table>
| <none yet>          | <none yet>

; RUN: llc -march=hexagon < %s
; REQUIRES: asserts

; The two loads based on %struct.0, loading two different data types
; cause LSR to assume type "void" for the memory type. This would then
; cause an assert in isLegalAddressingMode. Make sure we no longer crash.

target triple = "hexagon"

%struct.0 = type [ i8*, i8, %union.anon.0 ]
%union.anon.0 = type [ i8* ]

define hidden fastcc void @fred() unnamed_addr #0 {
entry:
  br i1 undef, label %while.end, label %while.body.lr.ph

while.body.lr.ph: ; preds = %entry
  br label %while.body

while.body: ; preds = %exit.2, %while.body.lr.ph
  %lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
  switch i32 undef, label %exit [
    i32 1, label %sw.bb.i
    i32 2, label %sw.bb3.i
  ]

  sw.bb.i: ; preds = %while.body
    unreachable

  sw.bb3.i: ; preds = %while.body
    unreachable

exit: ; preds = %while.body
  switch i32 undef, label %exit.2 [
    i32 1, label %sw.bb.i17
    i32 2, label %sw.bb3.i20
  ]

  sw.bb.i17: ; preds = %.exit
%0 = bitcast %struct.0* %lsr.iv to i32*
%1 = load i32, i32* %0, align 4
unreachable

sw.bb3.i20: ; preds = %exit
%2 = bitcast %struct.0* %lsr.iv to i8**
%3 = load i8*, i8** %2, align 4
unreachable

exit.2: ; preds = %exit
%cgep22 = getelementptr %struct.0, %struct.0* %lsr.iv, i32 1
br label %while.body

while.end: ; preds = %entry
ret void
}

attributes #0 = { nounwind optsize "target-cpu"="hexagonv55" }

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; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s
define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
  ; CHECK-LABEL: test_sitofp_fixed:

  ; First, extend each i32 to i64
  ; CHECK-DAG: sshll2.2d [[BLOCK0_HI:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK1_HI:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK2_HI:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll2.2d [[BLOCK3_HI:v[0-9]+]], v3, #0
  ; CHECK-DAG: sshll.2d [[BLOCK0_LO:v[0-9]+]], v0, #0
  ; CHECK-DAG: sshll.2d [[BLOCK1_LO:v[0-9]+]], v1, #0
  ; CHECK-DAG: sshll.2d [[BLOCK2_LO:v[0-9]+]], v2, #0
  ; CHECK-DAG: sshll.2d [[BLOCK3_LO:v[0-9]+]], v3, #0

  ; Next, convert each to double.
  ; CHECK-DAG: scvtf.2d v0, [[BLOCK0_LO]]
  ; CHECK-DAG: scvtf.2d v1, [[BLOCK0_HI]]
  ; CHECK-DAG: scvtf.2d v2, [[BLOCK1_LO]]
  ; CHECK-DAG: scvtf.2d v3, [[BLOCK1_HI]]
  ; CHECK-DAG: scvtf.2d v4, [[BLOCK2_LO]]
  ; CHECK-DAG: scvtf.2d v5, [[BLOCK2_HI]]
  ; CHECK-DAG: scvtf.2d v6, [[BLOCK3_LO]]
  ; CHECK-DAG: scvtf.2d v7, [[BLOCK3_HI]]

  ; CHECK: ret
  %flt = sitofp <16 x i32> %in to <16 x double>
  %res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
  ret <16 x double> %res
; This one is small enough to satisfy isSimple, but still illegally large.

define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {  
; CHECK-LABEL: test_sitofp_fixed_shortish:

; CHECK-DAG: scvtf.2d v0, v0  
; CHECK-DAG: scvtf.2d v1, v1

; CHECK: ret

%flt = sitofp <4 x i64> %in to <4 x double>
%res = fdiv <4 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0>
ret <4 x double> %res


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; RUN: llc -s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
; D31946
; Check that we dont end up with the "LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.

declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)
define fp128 @TestSelect(fp128 %a, fp128 %b) {
    %cmp = fcmp ogt fp128 %a, %b
    %sub = fsub fp128 %a, %b
    %res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
    ret fp128 %res

    ; CHECK-LABEL: TestSelect:
    ; CHECK     movaps 16(%rsp), %xmm1
    ; CHECK-NEXT  callq __subtf3
    ; CHECK-NEXT  testl %ebx, %ebx
    ; CHECK-NEXT  jg .LBB0_2
    ; #bb.1:
    ; CHECK-NEXT  movaps .LCPI0_0(%rip), %xmm0
    ; CHECK-NEXT  .LBB0_2:
    ; CHECK-NEXT  addq $32, %rsp
    ; CHECK-NEXT  popq %rbx
define fp128 @TestFabs(fp128 %a) {
  %res = call fp128 @llvm.fabs.f128(fp128 %a)
  ret fp128 %res
}

define fp128 @TestCopysign(fp128 %a, fp128 %b) {
  %res = call fp128 @llvm.copysign.f128(fp128 %a, fp128 %b)
  ret fp128 %res
}

define fp128 @TestFneg(fp128 %a) {
  %mul = fmul fp128 %a, %a
  %res = fsub fp128 0xL00000000000000008000000000000000, %mul
  ret fp128 %res
}

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Developed by:

LLVM Team

University of Illinois at Urbana-Champaign

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</table>
define void @zext256() "min-legal-vector-width"="256" {
  ; VEC256-LABEL: 'zext256'
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; VEC512-LABEL: 'zext256'
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  
  ; %A = zext <8 x i16> undef to <8 x i64>
  %B = zext <8 x i32> undef to <8 x i64>
  %C = zext <16 x i8> undef to <16 x i32>
  %D = zext <16 x i16> undef to <16 x i32>
  %E = zext <32 x i8> undef to <32 x i16>
  ret void
}

define void @zext512() "min-legal-vector-width"="512" {

define void @sext256() "min-legal-vector-width"="256" { 
    ; VEC256-LABEL: 'sext256'
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %C = sext <16 x i8> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %D = sext <16 x i16> undef to <16 x i32>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %E = sext <32 x i8> undef to <32 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %F = sext <32 x i16> undef to <32 x i64>
    ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
}
define void @sext512() "min-legal-vector-width"="512" {  
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <8 x i32> undef to <8 x i64>
  %D = sext <16 x i8> undef to <16 x i32>
  %E = sext <16 x i16> undef to <16 x i32>
  %F = sext <32 x i8> undef to <32 x i16>
  ret void
}

define void @sext128() "min-legal-vector-width"="128" {  
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <8 x i32> undef to <8 x i64>
  %D = sext <16 x i8> undef to <16 x i32>
  %E = sext <16 x i16> undef to <16 x i32>
  %F = sext <32 x i8> undef to <32 x i16>
  ret void
}
define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
    # %bb.0:
    CHECK:    .vmovdqa (%rdi), %ymm0
    CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    CHECK-NEXT: vpadd (%rsi), %ymm0, %ymm0
    CHECK-NEXT: vpaddq 32(%rsi), %ymm1, %ymm1
}
define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
  %d = load <16 x i32>, <16 x i32>* %a
  %e = load <16 x i32>, <16 x i32>* %b
  %f = add <16 x i32> %d, %e
  store <16 x i32> %f, <16 x i32>* %c
  ret void
}

define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
  %1 = load <64 x i8>, <64 x i8>* %a
  %2 = load <64 x i8>, <64 x i8>* %b
  %3 = zext <64 x i8> %1 to <64 x i32>
  %4 = zext <64 x i8> %2 to <64 x i32>
  %5 = add nuw nsw <64 x i32> %3, %4
  %6 = add nuw nsw <64 x i32> %5, %4
  %7 = lshr <64 x i32> %6, %7
  ret void
}

; CHECK-NEXT:    vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:    vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:    vzeroupper
; CHECK-NEXT:    retq
%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}
define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
; CHECK-LABEL: avg_v64i8_512:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, %4
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, %5
%8 = trunc <64 x i32> %7 to <64 x i8>
store <64 x i8> %8, <64 x i8>* undef, align 4
ret void
}

define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" {
; CHECK-LABEL: pmaddwd_32_256:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: pmaddwd_32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa64 (%rdi), %zmm0
    ; CHECK-NEXT:    vpmaddwd (%rsi), %zmm0, %zmm0
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    %a = sext <32 x i16> %A to <32 x i32>
    %b = sext <32 x i16> %B to <32 x i32>
    %m = mul nsw <32 x i32> %a, %b
    ;CHECK-NEXT:    shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> %i32 0, %i32 2, %i32 4, %i32 6, %i32 8, %i32 10, %i32 12, %i32 14, %i32 16, %i32 18, %i32 20, %i32 22, %i32 24, %i32 26, %i32 28, %i32 30>
    ;CHECK-NEXT:    shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> %i32 1, %i32 3, %i32 5, %i32 7, %i32 9, %i32 11, %i32 13, %i32 15, %i32 17, %i32 19, %i32 21, %i32 23, %i32 25, %i32 27, %i32 29, %i32 31>
    %ret = add <16 x i32> %odd, %even
    store <16 x i32> %ret, <16 x i32>* %CPtr
    ret void
}

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: psubus_64i8_max_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:    vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:    vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:    vpsubusb (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT:    vpsubusb 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %x = load <64 x i8>, <64 x i8>* %xptr
    %y = load <64 x i8>, <64 x i8>* %yptr
    %z = load <64 x i8>, <64 x i8>* %zptr
    ret void
define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: psubus_64i8_max_512:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
  ; CHECK-NEXT:  vpsubusb (%rsi), %zmm0, %zmm0
  ; CHECK-NEXT:  vmovdqa64 %zmm0, (%rdx)
  ; CHECK-NEXT:  vzeroupper
  ; CHECK-NEXT:  retq
  %x = load <64 x i8>, <64 x i8>* %xptr
  %y = load <64 x i8>, <64 x i8>* %yptr
  %cmp = icmp ult <64 x i8> %x, %y
  %max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
  %res = sub <64 x i8> %max, %y
  store <64 x i8> %res, <64 x i8>* %zptr
  ret void
}

define void @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: _Z9test_charPcS_i_256:
  ; CHECK:       # %bb.0: # %entry
  ; CHECK-NEXT:  movl %edx, %eax
  ; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT:  xorl %ecx, %ecx
  ; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1
  ; CHECK-NEXT:  .p2align 4, 0x90
  ; CHECK-NEXT: .LBB8_1: # %vector.body
  ; CHECK-NEXT:  # =>This Inner Loop Header: Depth=1
  ; CHECK-NEXT:  vpmovsxbw (%rdi,%rcx), %ymm3
  ; CHECK-NEXT:  vpmovsxbw 16(%rdi,%rcx), %ymm4
  ; CHECK-NEXT:  vpmovsxbw (%rsi,%rcx), %ymm5
  ; CHECK-NEXT:  vpmaddwd %ymm3, %ymm5, %ymm3
  ; CHECK-NEXT:  vpaddd %ymm1, %ymm3, %ymm1
  ; CHECK-NEXT:  vmovsxbw 16(%rsi,%rcx), %ymm4
  ; CHECK-NEXT:  vpmaddwd %ymm4, %ymm3, %ymm3
  ; CHECK-NEXT:  vpaddd %ymm2, %ymm3, %ymm2
  ; CHECK-NEXT:  addq $32, %rcx
  ; CHECK-NEXT:  cmpq %rcx, %rax
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = add nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
    # CHECK-LABEL: _Z9test_charPcS_i_512:
    # CHECK:       # %bb.0: # %entry
    # CHECK-NEXT:    movl %edx, %eax
    # CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
    # CHECK-NEXT:    xorl %ecx, %ecx
    # CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
    # CHECK-NEXT:    .p2align 4, 0x90
    # CHECK-NEXT:  .LBB9_1: # %vector.body
    # =>This Inner Loop Header: Depth=1
    # CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
    # CHECK-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
    # CHECK-NEXT:    vpmaddwd %zmm2, %zmm3, %zmm2
    # CHECK-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
    # CHECK-NEXT:    addq $32, %rcx
    # CHECK-NEXT:    cmpq %rcx, %rax
    # CHECK-NEXT:    jne .LBB9_1
    # CHECK-NEXT:  # %bb.2: # %middle.block
    # CHECK-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
    # CHECK-NEXT:    vextracti64x4 $1, %zmm0, %zmm1
    # CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
    # CHECK-NEXT:    vextracti128 $1, %zmm0, %zmm1
    # CHECK-NEXT:    vpshufd {{.*#+}} xmm0 = xmm0[2,3,0,1]
    # CHECK-NEXT:    vpaddd %zmm1, %zmm0, %zmm0
    # CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[1,1,2,3]
    # CHECK-NEXT:    vmovd %zmm0, %eax
    # CHECK-NEXT:    vzeroupper
    # CHECK-NEXT:    retq
}
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> %i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx1 = add <32 x i32> %11, %rdx.shuf1
%rdx.shuf = shufflevector <32 x i32> %bin.rdx1, <32 x i32> undef, <32 x i32> %i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx = add <32 x i32> %bin.rdx1, %rdx.shuf
%rdx.shuf15 = shufflevector <32 x i32> %bin.rdx, <32 x i32> undef, <32 x i32> %i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx32 = add <32 x i32> %bin.rdx, %rdx.shuf15
%rdx.shuf17 = shufflevector <32 x i32> %bin.rdx32, <32 x i32> undef, <32 x i32> %i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx18 = add <32 x i32> %bin.rdx32, %rdx.shuf17
%rdx.shuf19 = shufflevector <32 x i32> %bin.rdx18, <32 x i32> undef, <32 x i32> %i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef
%bin.rdx20 = add <32 x i32> %bin.rdx18, %rdx.shuf19
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
define i32 @sad_16i8_256() "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sad_16i8_256:
    ; CHECK:       # %bb.0: # %entry
    ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
    ; CHECK-NEXT:    movq $-1024, %rax # imm = 0xFC00
    ; CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
    ; CHECK-NEXT:    .p2align 4, 0x90
    ; CHECK-NEXT:  .LBB10_1: # %vector.body
    ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
    ; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm2
    ; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm2
    ; CHECK-NEXT:    vpaddd %ymm1, %ymm2, %ymm1
    ; CHECK-NEXT:    addq $4, %rax
    ; CHECK-NEXT:    jne .LBB10_1
    ; CHECK-NEXT:  # %bb.2: # %middle.block
    ; CHECK-NEXT:    vpaddd %ymm0, %ymm1, %ymm0
    ; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1
    ; CHECK-NEXT:    vpaddd %ymm1, %ymm0, %ymm0
    ; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]
    ; CHECK-NEXT:    vpaddd %ymm1, %ymm0, %ymm0
    ; CHECK-NEXT:    vmovd %xmm0, %eax
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    entry:
    br label %vector.body
}

vector.body:
    %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
    %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
    %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
    %1 = bitcast i8* %0 to <16 x i8>*
    %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
    %2 = zext <16 x i8> %wide.load to <16 x i32>
    %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
    %4 = bitcast i8* %3 to <16 x i8>*
    %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
    %5 = zext <16 x i8> %wide.load1 to <16 x i32>
    %6 = sub nsw <16 x i32> %2, %5
    %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index.next, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
}
define i32 @sad_16i8_512() "min-legal-vector-width"="512" {
  ; CHECK-LABEL: sad_16i8_512:
  ; CHECK:    ; %bb.0: ; %entry
  ; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT: movq $-1024, %rax # imm = 0xFC00
  ; CHECK-NEXT: .p2align 4, 0x90
  ; CHECK-NEXT: .LBB11_1: ; %vector.body
  ; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
  ; CHECK-NEXT: vmovdqu a+1024(%rax), %xmm1
  ; CHECK-NEXT: vpsadbw b+1024(%rax), %xmm1, %xmm1
  ; CHECK-NEXT: vpadd %zmm0, %zmm1, %zmm0
  ; CHECK-NEXT: addq $4, %rax
  ; CHECK-NEXT: jne .LBB11_1
  ; CHECK-NEXT: ; %bb.2: ; %middle.block
  ; CHECK-NEXT: vextracti64x4 %s1, %zmm0, %yrm1
  ; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
  ; CHECK-NEXT: vextracti128 %s1, %yrm0, %xrm1
  ; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
  ; CHECK-NEXT: vpsadbw [[.*#+]] xmm1 = xmm0[2,3,0,1]
  ; CHECK-NEXT: vpadd %zmm1, %zmm0, %zmm0
}
vector.body:

%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
%vec.phi = phi <16 x i32> [ zeroinitialize, %entry ], [ %10, %vector.body ]
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
%1 = bitcast i8* %0 to <16 x i8>*
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4
%2 = zext <16 x i8> %wide.load to <16 x i32>
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
%4 = bitcast i8* %3 to <16 x i8>*
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
%5 = zext <16 x i8> %wide.load1 to <16 x i32>
%6 = sub nsw <16 x i32> %2, %5
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
%8 = sub nsw <16 x i32> zeroinitialize, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:

%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef> %bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {  
; CHECK-LABEL: sbto16f32_256:  
; CHECK:    # %bb.0:  
; CHECK-NEXT:    vpmovw2m %ymm0, %k0  
; CHECK-NEXT:    kshiftrw $8, %k0, %k1  
; CHECK-NEXT:    vpmovm2d %k1, %ymm0  
; CHECK-NEXT:    vcvtdq2ps %ymm0, %ymm0  
; CHECK-NEXT:    vpmovm2d %k0, %ymm1  
; CHECK-NEXT:    vcvtdq2ps %ymm1, %ymm1  
; CHECK-NEXT:    vmovaps %ymm1, (%rdi)  
; CHECK-NEXT:    vzeroupper  
; CHECK-NEXT:    retq  

%mask = icmp slt <16 x i16> %a, zeroinitializer  
%1 = sitofp <16 x i1> %mask to <16 x float>  
store <16 x float> %1, <16 x float>* %res  
ret void  
}  

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {  
; CHECK-LABEL: sbto16f32_512:  
; CHECK:    # %bb.0:  
; CHECK-NEXT:    vpmovw2m %ymm0, %k0  
; CHECK-NEXT:    vpmovm2d %k0, %zmm0  
; CHECK-NEXT:    vcvtdq2ps %zmm0, %zmm0  
; CHECK-NEXT:    vmovaps %zmm0, (%rdi)  
; CHECK-NEXT:    vzeroupper  
; CHECK-NEXT:    retq  

%mask = icmp slt <16 x i16> %a, zeroinitializer  
%1 = sitofp <16 x i1> %mask to <16 x float>  
store <16 x float> %1, <16 x float>* %res  
ret void  
}  

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {  
; CHECK-LABEL: sbto16f64_256:  
; CHECK:    # %bb.0:  
; CHECK-NEXT:    vpmovw2m %ymm0, %k0  
; CHECK-NEXT:    kshiftrw $8, %k0, %k1  
; CHECK-NEXT:    vpmovm2d %k1, %ymm0  
; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymm0  
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm0  
; CHECK-NEXT:    vcvtdq2pd %xmm0, %ymmm1  
; CHECK-NEXT:    vpmovm2d %k0, %ymmm2  
; CHECK-NEXT:    vcvtdq2pd %xmm2, %ymmm3  
; CHECK-NEXT:    vextracti128 $1, %ymmm2, %xmm2  

}
define void @sbtol64f32_512(%a, %res) "min-legal-vector-width"="512" {
    %mask = icmp slt <16 x i16> %a, zero.initializer
    %l = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %l, <16 x double>* %res
    ret void
}

define void @ubtol64f32_256(%a, %res) "min-legal-vector-width"="256" {
    %mask = icmp slt <16 x i16> %a, zero.initializer
    %l = uitofp <16 x i1> %mask to <16 x float>
    store <16 x float> %l, <16 x float>* %res
    ret void
}
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  vpmovm2d %k0, %zmm0
; CHECK-NEXT:  vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT:  vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT:  vmovaps %zmm0, (%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitialize
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vpmovw2m %ymm0, %k0
; CHECK-NEXT:  kshiftrw $8, %k0, %k1
; CHECK-NEXT:  vpmovm2d %k1, %ymm0
; CHECK-NEXT:  vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT:  vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT:  vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT:  vpmovm2d %k0, %ymm2
; CHECK-NEXT:  vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT:  vpmovm2d %k0, %ymm2
; CHECK-NEXT:  vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT:  vmvaps %ymm2, 32(%rdi)
; CHECK-NEXT:  vmvaps %ymm3, 32(%rdi)
; CHECK-NEXT:  vmvaps %ymm0, 96(%rdi)
; CHECK-NEXT:  vmvaps %ymm1, 64(%rdi)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
%mask = icmp slt <16 x i16> %a, zeroinitialize
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {  
    CHECK-LABEL: test_16f32toub_256:  
    CHECK:  # %bb.0:  
      CHECK-NEXT:  cvttps2dq (%rdi), %ymm1  
      CHECK-NEXT:  vpslld S31, %ymm1, %ymm1  
      CHECK-NEXT:  vpmovd2m %ymm1, %k0  
      CHECK-NEXT:  cvtpps2dq 32(%rdi), %ymm1  
      CHECK-NEXT:  vpslld S31, %ymm1, %ymm1  
      CHECK-NEXT:  vpmovd2m %ymm1, %k1  
      CHECK-NEXT:  kunpckbw %k0, %k1, %k1  
      CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}  
      CHECK-NEXT:  retq  
      %a = load <16 x float>, <16 x float>* %ptr  
      %mask = fptoui <16 x float> %a to <16 x i1>  
      %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitialize  
      ret <16 x i16> %select  
}  

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
    CHECK-LABEL: test_16f32toub_512:  
    CHECK:  # %bb.0:  
      CHECK-NEXT:  cvtpps2dq (%rdi), %zmm1  
      CHECK-NEXT:  vpslld S31, %zmm1, %zmm1  
      CHECK-NEXT:  vpmovd2m %zmm1, %k1  
      CHECK-NEXT:  vmovdqu16 %ymm0, %zmm0 {%k1} {z}  
      CHECK-NEXT:  retq  
      %a = load <16 x float>, <16 x float>* %ptr  
      %mask = fptoui <16 x float> %a to <16 x i1>
define <16 x i1> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: test_16f32tosb_256:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vcvtps2dq (%rdi), %ymm1
    ; CHECK-NEXT: vpmovd2m %ymm1, %k0
    ; CHECK-NEXT: vcvtps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT: vpmovd2m %ymm1, %k1
    ; CHECK-NEXT: kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 [%k1] [z]
    ; CHECK-NEXT: retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: test_16f32tosb_512:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vcvtps2dq (%rdi), %zmm1
    ; CHECK-NEXT: vpmovd2m %zmm1, %k1
    ; CHECK-NEXT: vmovdqu16 %ymm0, %ymm0 [%k1] [z]
    ; CHECK-NEXT: retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: mul256:
    ; CHECK:     # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT: vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT: vpunpckhbw {[*#*]} %ymm4 =
    ymm2[8], ymm0[8], ymm2[9], ymm0[9], ymm2[10], ymm0[10], ymm2[11], ymm0[11], ymm2[12], ymm0[12], ymm2[13], ymm0[13], ymm2[14], ymm0[14], ymm2[15], ymm0[15], ymm2[24], ymm0[24], ymm2[25], ymm0[25], ymm2[26], ymm0[26], ymm2[27], ymm0[27], ymm2[28], ymm0[28], ymm2[29], ymm0[29], ymm2[30], ymm0[30], ymm2[31], ymm0[31]
    ; CHECK-NEXT:   vpunpckhbw {[*#*]} %ymm5 =

ymm0[8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT:  vpmullw %ymm4, %ymm5, %ymm4
; CHECK-NEXT:  vmovdqa {{.*#+}} ymm5 =
; CHECK-NEXT:  vpand %ymm5, %ymm4, %ymm4
; CHECK-NEXT:  vpunpcklbw {{.*#+}} ymm2 =
 ymm2[0],ymm0[0],ymm2[1],ymm0[1],ymm2[2],ymm0[2],ymm2[3],ymm0[3],ymm2[4],ymm0[4],ymm2[5],ymm0[5],ymm2[6],ymm0[6],ymm2[7],ymm0[7],ymm2[16],ymm0[16],ymm2[17],ymm0[17],ymm2[18],ymm0[18],ymm2[19],ymm0[19],ymm2[20],ymm0[20],ymm2[21],ymm0[21],ymm2[22],ymm0[22],ymm2[23],ymm0[23]
; CHECK-NEXT:  vpunpcklbw {{.*#+}} ymm0 =
 ymm0[0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT:  vpmullw %ymm2, %ymm0, %ymm0
; CHECK-NEXT:  vpand %ymm5, %ymm0, %ymm0
; CHECK-NEXT:  vpackuswb %ymm4, %ymm0, %ymm0
; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm2 =
 ymm3[8],ymm0[8],ymm3[9],ymm0[9],ymm3[10],ymm0[10],ymm3[11],ymm0[11],ymm3[12],ymm0[12],ymm3[13],ymm0[13],ymm3[14],ymm0[14],ymm3[15],ymm0[15],ymm3[24],ymm0[24],ymm3[25],ymm0[25],ymm3[26],ymm0[26],ymm3[27],ymm0[27],ymm3[28],ymm0[28],ymm3[29],ymm0[29],ymm3[30],ymm0[30],ymm3[31],ymm0[31]
; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
 ymm1[8],ymm0[8],ymm1[9],ymm0[9],ymm1[10],ymm0[10],ymm1[11],ymm0[11],ymm1[12],ymm0[12],ymm1[13],ymm0[13],ymm1[14],ymm0[14],ymm1[15],ymm0[15],ymm1[24],ymm0[24],ymm1[25],ymm0[25],ymm1[26],ymm0[26],ymm1[27],ymm0[27],ymm1[28],ymm0[28],ymm1[29],ymm0[29],ymm1[30],ymm0[30],ymm1[31],ymm0[31]
; CHECK-NEXT:  vpmullw %ymm2, %ymm4, %ymm2
; CHECK-NEXT:  vpand %ymm5, %ymm2, %ymm2
; CHECK-NEXT:  vpunpcklbw {{.*#+}} ymm3 =
 ymm3[0],ymm0[0],ymm3[1],ymm0[1],ymm3[2],ymm0[2],ymm3[3],ymm0[3],ymm3[4],ymm0[4],ymm3[5],ymm0[5],ymm3[6],ymm0[6],ymm3[7],ymm0[7],ymm3[16],ymm0[16],ymm3[17],ymm0[17],ymm3[18],ymm0[18],ymm3[19],ymm0[19],ymm3[20],ymm0[20],ymm3[21],ymm0[21],ymm3[22],ymm0[22],ymm3[23],ymm0[23]
; CHECK-NEXT:  vpunpcklbw {{.*#+}} ymm1 =
 ymm1[0],ymm0[0],ymm1[1],ymm0[1],ymm1[2],ymm0[2],ymm1[3],ymm0[3],ymm1[4],ymm0[4],ymm1[5],ymm0[5],ymm1[6],ymm0[6],ymm1[7],ymm0[7],ymm1[16],ymm0[16],ymm1[17],ymm0[17],ymm1[18],ymm0[18],ymm1[19],ymm0[19],ymm1[20],ymm0[20],ymm1[21],ymm0[21],ymm1[22],ymm0[22],ymm1[23],ymm0[23]
; CHECK-NEXT:  vpmullw %ymm3, %ymm1, %ymm1
; CHECK-NEXT:  vpand %ymm5, %ymm1, %ymm1
; CHECK-NEXT:  vpackuswb %ymm2, %ymm1, %ymm1
; CHECK-NEXT:  vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT:  vmovdqa %ymm0, (%rdx)
; CHECK-NEXT:  vzeroupper
; CHECK-NEXT:  retq
  %d = load <64 x i8>, <64 x i8>* %a
  %e = load <64 x i8>, <64 x i8>* %b
  %f = mul <64 x i8> %d, %e
  store <64 x i8> %f, <64 x i8>* %c
  ret void
}
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512" 
; CHECK-LABEL: mul512:
; CHECK:       # %bb.0:
; CHECK-NEXT:  vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT:  vmovdqa64 (%rsi), %zmm1
; CHECK-NEXT:  vpunpckhbw ([.]%*++) zmm2 =
; CHECK-NEXT:  zmm1[8],zmm0[8],zmm1[9],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13],
; CHECK-NEXT:  zmm0[13],zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[16],zmm0[16],zmm1[17],zmm0[17],zmm1[18],zmm0[18],
; CHECK-NEXT:  zmm0[19],zmm1[20],zmm0[20],zmm1[21],zmm0[21],zmm1[22],zmm0[22],zmm1[23],zmm0[23],zmm1[32],zmm0[32],
; CHECK-NEXT:  zmm1[33],zmm0[33],zmm1[34],zmm0[34],zmm1[35],zmm0[35],zmm1[36],zmm0[36],zmm1[37],zmm0[37],
; CHECK-NEXT:  zmm1[38],zmm0[38],zmm1[39],zmm0[39],zmm1[48],zmm0[48],zmm1[49],zmm0[49],zmm1[50],zmm0[50],zmm1[51],zmm0[51],zmm1[52],zmm0[52],zmm1[53],zmm0[53],zmm1[54],zmm0[54],zmm1[55],zmm0[55]
; CHECK-NEXT:  vpunpckhbw ([.]%*++) zmm3 =
; CHECK-NEXT:  zmm0[8],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13],zmm0[13],
; CHECK-NEXT:  zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[16],zmm0[16],zmm1[17],zmm0[17],zmm1[18],zmm0[18],zmm1[19],
; CHECK-NEXT:  zmm0[20],zmm1[20],zmm0[21],zmm1[21],zmm0[21],zmm1[22],zmm0[22],zmm1[23],zmm0[23],zmm1[32],zmm0[32],
; CHECK-NEXT:  zmm1[33],zmm0[33],zmm1[34],zmm0[34],zmm1[35],zmm0[35],zmm1[36],zmm0[36],zmm1[37],zmm0[37],
; CHECK-NEXT:  zmm1[38],zmm0[38],zmm1[39],zmm0[39],zmm1[48],zmm0[48],zmm1[49],zmm0[49],zmm1[50],zmm0[50],zmm1[51],zmm0[51],zmm1[52],zmm0[52],zmm1[53],zmm0[53],zmm1[54],zmm0[54],zmm1[55],zmm0[55]
; CHECK-NEXT:  vpmullw %zmm2, %zmm3, %zmm2
; CHECK-NEXT:  vpmullw %zmm3, %zmm2, %zmm2
; CHECK-NEXT:  vpackuswb %zmm0, %zmm2, %zmm2
; CHECK-NEXT:  store <64 x i8> %f, <64 x i8>* %c
; CHECK-NEXT:  retq

%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void

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; RUN: opt -consthoist -S -o - %s | FileCheck %s

target triple = "i686-unknown-linux-gnu"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than
; pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9]??]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9]??]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]??]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %[[M1:const_mat[0-9]??]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
@goo = global i8* undef

define void @foo_i8() {
  entry:
    %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
    %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
    %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
    %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
    %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
    store i8 %4, i8* inttoptr(i32 805873688 to i8*)
    store i8 %3, i8* inttoptr(i32 805873719 to i8*)
    store i8 %2, i8* inttoptr(i32 805873720 to i8*)
    store i8 %1, i8* inttoptr(i32 805873727 to i8*)
    store i8* inttoptr(i32 805874800 to i8*), i8** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]?]%] = bitcast i32 805874752 to i32
; CHECK-DAG: %[[C2:const[0-9]?]%] = bitcast i32 805874688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i16*
; CHECK-NEXT: %1 = load volatile i16, i16* %0, align 2
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i16*
; CHECK-NEXT: %3 = load volatile i16, i16* %2, align 2
define void @foo_i16() {
  entry:
  %0 = load volatile i16, i16* inttoptr (i32 805874688 to i16*), align 2
  %1 = load volatile i16, i16* inttoptr (i32 805874692 to i16*), align 2
  %2 = load volatile i16, i16* inttoptr (i32 805874720 to i16*), align 2
  %3 = load volatile i16, i16* inttoptr (i32 805874750 to i16*), align 2
  %4 = load volatile i16, i16* inttoptr (i32 805874752 to i16*), align 2
  %5 = load volatile i16, i16* inttoptr (i32 805874774 to i16*), align 2
  ret void
}

check: foo_i16

define void @foo_i32() {
  entry:
  %0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
  %1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
  %2 = load volatile i32, i32* inttoptr (i32 805874720 to i32*), align 4
  %3 = load volatile i32, i32* inttoptr (i32 805874750 to i32*), align 4
  %4 = load volatile i32, i32* inttoptr (i32 805874752 to i32*), align 4
  %5 = load volatile i32, i32* inttoptr (i32 805874774 to i32*), align 4
  ret void
}

check: foo_i32
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}

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1.142 tomcat-jdbc 8.5.32

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1.145 expat 2.0.1

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1.146 tomcat-storeconfig 8.5.32

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1.148 go-toml 1.3.0

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1.149 python 2.7.13
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In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (see http://www.digicool.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Digital Creations is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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1.161 python 2.6.4

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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/* parsermodule.c
 *
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* This module serves to replace the original parser module written
* by Guido. The functionality is not matched precisely, but the
* original may be implemented on top of this. This is desirable
* since the source of the text to be parsed is now divorced from
* this interface.

* Unlike the prior interface, the ability to give a parse tree
* produced by Python code as a tuple to the compiler is enabled by
* this module. See the documentation for more details.

* I've added some annotations that help with the lint code-checking
* program, but they're not complete by a long shot. The real errors
* that lint detects are gone, but there are still warnings with
* Py_[X]DECREF() and Py_[X]INCREF() macros. The lint annotations
* look like "NOTE(...)".

*/

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***

Additional handlers for the logging package for Python. The core package is
based on PEP 282 and comments thereto in comp.lang.python, and influenced by
Apache's log4j system.

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A C-program for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using init_genrand(seed)
or init_by_array(init_key, key_length).

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl/) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us/) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation; see http://www.zope.com/). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org/ for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

+----------------+--------------+-----------+------------+-----------------+
| Release        | Derived from | Year      | Owner      | GPL compatible? |
+----------------+--------------+-----------+------------+-----------------+
| 0.9.0 thru 1.2  | n/a          | 1991-1995 | CWI        | yes             |
+----------------+--------------+-----------+------------+-----------------+
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Mersenne Twister
--------------

The :mod:`_random` module includes code based on a download from
http://www.math.keio.ac.jp/ matumoto/MT2002/emt19937ar.html. The following are
the verbatim comments from the original code::

    A C-program for MT19937, with initialization improved 2002/1/26.
    Coded by Takuji Nishimura and Makoto Matsumoto.

    Before using, initialize the state by using init_genrand(seed)
    or init_by_array(init_key, key_length).

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Sockets
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MD5 message digest algorithm

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L. Peter Deutsch
ghost@aladdin.com

Independent implementation of MD5 (RFC 1321).

This code implements the MD5 Algorithm defined in RFC 1321, whose text is available at

http://www.ietf.org/rfc/rfc1321.txt

The code is derived from the text of the RFC, including the test suite (section A.5) but excluding the rest of Appendix A. It does not include any code or documentation that is identified in the RFC as being copyrighted.

The original and principal author of md5.h is L. Peter Deutsch <ghost@aladdin.com>. Other authors are noted in the change history that follows (in reverse chronological order):

2002-04-13 lpd Removed support for non-ANSI compilers; removed references to Ghostscript; clarified derivation from RFC 1321; now handles byte order either statically or dynamically.
1999-11-04 lpd Edited comments slightly for automatic TOC extraction.
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Modified by Jack Jansen, CWI, July 1995:
- Use binascii module to do the actual line-by-line conversion between ascii and binary. This results in a 1000-fold speedup. The C version is still 5 times faster, though.
- Arguments more compliant with python standard

XML Remote Procedure Calls

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1.167 pcre 8.40

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-------------------------------

Written by: Philip Hazel
Email local part: ph10
PCRE JUST-IN-TIME COMPILATION SUPPORT
-------------------------------------
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Email local part: hzmester
Email domain: freemail.hu

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1.168 tdb 1.3.12,1

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1.173 jackson-databind 2.6.1

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AES in libhcrypto

rijndael-alg-fst.c

Optimised ANSI C code for the Rijndael cipher (now AES)

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DES core in libhcrypto

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is invoked, then you must make a good faith effort to ensure that,
in the event an application does not supply such function or
table, the facility still operates, and performs whatever part of
its purpose remains meaningful.

(For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!
digraph G {
rankdir="LR";
label = "label demo";
labeljust="left";
subgraph cluster0 {
  a;
  b;
a->b;
  label = "cluster0";
  labelloc="bottom"
}
This package was originally debianized by Stephen M Moraco <stephen@debian.org> on Fri, 31 Dec 1999 15:37:17 -0700.

It was downloaded from http://www.graphviz.org/

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Stephen North, AT&T Research (dot, neato)
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The Debian packaging was started by Stephen M Moraco <stephen@debian.org> and then was taken over in late 2006 by Cyril Brulebois, placing his work under the same terms as graphviz itself.

2006-2008, Cyril Brulebois <cyril.brulebois@enst-bretagne.fr>

digraph G {
  rankdir="LR";
  label = "label demo";
  subgraph cluster0 {
    a;
    b;
    a->b;
    label = "cluster0";
    labeljust="left";
  }
  subgraph cluster1 {
    c;
    d;
    c->d;
    label = "cluster1";
  }
}

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1.179 zlib 1.2.3

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/* zlib.h -- interface of the 'zlib' general purpose compression library
version 1.2.11, January 15th, 2017

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*/

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1.180 libidn 1.33

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1.182 hk2-utils 2.4.0

1.183 fsnotify 1.4.7

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1.184 bison 3.0.4

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#!/usr/bin/perl -0777 -pi

# Update b4_copyright invocations or b4_copyright_years definitions to
# include the current year.

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use strict;
use warnings;

my $margin = 72;

my $this_year = $ENV{UPDATE_COPYRIGHT_YEAR};
if (!$this_year || $this_year !~ m/^\d{4}$/)
{
    my ($sec, $min, $hour, $mday, $month, $year) = localtime (time ());
    $this_year = $year + 1900;
}
my $old_re = <<'EOF'

(?::\n
#BEFORE

?:
b4_copyright\[[[]\*]\n
| m4_(?:push|pop)\[b4_copyright_years\]

)

#AFTER

)

?:

,\s*

(\[\s* (?:\d{4}(?:,|\-))* (\d{4}) \s*\]

)

)

EOF

:

while (/($old_re)/gx)
{
    my $start = pos() - length ($1);
    my $b4_copyright_line = $2;
    my $year_lines = $3;
    my $final_year = $4;
    $year_lines .= ');
    # If there was a second argument, it contains years, so update them.
    if ($final_year)
    {
        $b4_copyright_line .= ');
        if ($final_year != $this_year)
        {
            # Update the year.
            $year_lines =~ s/$final_year/$final_year, $this_year/;
        }
    }
}
# Normalize all whitespace.
$year_lines =~ s/\s+/ /g;

# Put spaces after commas.
$year_lines =~ s/\s*,/?/g;

# Compress to intervals.
$year_lines =~
  s/
    \d{4}
  (?=\n    (\-|
      (?!\n        if \ ($2 eq \'-\') \ \d{4}\n        elsif \(!$3\) \ $1 + 1; \n        else \ $3 + 1; \n      ))\n    )+/\1-$3/gx;

# Format within margin.
my $year_lines_new;
my $indent = index ($b4_copyright_line, '\[');
--$indent if ($b4_copyright_line =~ m/\n/);
while (length $year_lines)
{
  my $text_margin = $margin - $indent;
  if ($year_lines =~ s/^(.{1,$text_margin})(?: |$)//)
    || ($year_lines =~ s/^([^\S\n]+)(?: |$)//))
  {
    my $line = \n . ('x$indent) . $1;
    ++$indent if (!$year_lines_new);
    $year_lines_new .= $line;
  }
  else
  {
    # Should be unreachable, but we don't want an infinite
    # loop if it can be reached.
    die;
  }
}

# Replace the old invocation. Should never die.
die if (!s/$old_re\G/$b4_copyright_line$year_lines_new/x);

# Prepare for the next search.
pos () = $start + length ($b4_copyright_line$year_lines_new);
while (/\bb4_copyright\(/g)
{
    my $start = pos () - length ($1);
    my $end = pos ();
    my $re = $old_re;
    pos () = $start;
    $re =~ s/#BEFORE/#G/;
    if (!/$re/x)
    {
        my $line = (substr ($_, 0, $start) =~ s/\n/\n/g) + 1;
        print STDERR
            "$ARGV:$line: warning: failed to update a b4_copyright\n";
    }
    pos () = $end;
}

while (/\bb4_copyright_years\]/g)
{
    my $start = pos () - length ($1);
    my $end = pos ();
    my $re = $old_re;
    $re =~ s/#AFTER/#G/;
    if (!/$re/x)
    {
        # The substr operation blows away pos (), so restoring pos ()
        # at the end is necessary.
        my $line = (substr ($_, 0, $start) =~ s/\n/\n/g) + 1;
        print STDERR
            "$ARGV:$line: warning: failed to update a"
            . " b4_copyright_years\n";
    }
    pos () = $end;
}

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Version 3, 29 June 2007

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#
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#
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1.190 perl 5.24.1
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#!/perl

=head1 NAME

copyright.t

=head1 DESCRIPTION

Tests that the latest copyright years in the top-level README file and the C<perl -v> output match each other.

If the test fails, update at least one of README and perl.c so that they match reality.

Optionally you can pass the C<--now> option to check they are at the current
year. This isn't checked by default, so that it doesn't fail for people working on older releases. It should be run before making a new release.

=cut

use strict;
use Config;
BEGIN { require './test.pl' }

if ( $Config{usecrosscompile} ) { 
    skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so # that tests still pass for people intentionally working on older versions:
if ($opt eq '--now') {
    my $current_year = (gmtime)[5] + 1900;
    is $v_year, $current_year, 'perl -v copyright includes current year';
    is $readme_year, $current_year, 'README copyright includes current year';
}

# Otherwise simply check that the two copyright dates match each other: else {
    is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;

sub readme_year
# returns the latest copyright year from the top-level README file {

    open my $readme, '<', '../README' or die "Opening README failed: $!";

    # The copyright message is the first paragraph:
    local $/ = "
    my $copyright_msg = <$readme>;

    my ($year) = $copyright_msg =~ /.*\b(\d\d\d\d)\b/; 
    or die "Year not found in README copyright message '$copyright_msg'";

sub v_year
# returns the latest copyright year shown in perl -v
{
    my $output = runperl switches => ['-v'];
    my ($year) = $output =~ /copyright 1987.*\b\d{4,}/i
        or die "Copyright statement not found in perl -v output '$output'";
    $year;
}
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---
abstract: 'Build and install Perl modules'
author:
- 'Ken Williams <kwilliams@cpan.org>'
- "Development questions, bug reports, and patches should be sent to the Module-Build mailing list at <module-build@perl.org>.'
built_requires:
  File::Temp: 0.15
  Test::Harness: 3.16
  Test::More: 0.49
generated_by: 'Module::Build version 0.3608'
license: gpl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: 1.4
name: Module-Build
resources:
  MailingList: mailto:module-build@perl.org
  license: http://dev.perl.org/licenses/
  repository: http://github.com/dagolden/module-build/
  version: 3

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

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1.194 pillow 1.1.6

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GIMP Palette
Name: custompalette
Columns: 4
#
  0  0  0  Index 3
  65  38  30  Index 4
 103  62  49  Index 6
  79  73  72  Index 7
 114 101  97  Index 8
 208 127 100  Index 9
 151 144 142  Index 10
 221 207 199  Index 11
GIMP Palette
Name: badpalettefile
Columns: 4
#
0 0 0  Index 3
01234567890123456789012345678901234567890123456789012345678901234567890123456789012345678901234567890123456789012345678901234567890
103 62 49  Index 6
79 73 72  Index 7
114 101 97  Index 8
208 127 100  Index 9
151 144 142  Index 10
221 207 199  Index 11

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(from https://github.com/googlei18n/noto-fonts)

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GIMP Palette
Name: badpaletteentry
Columns: 4
#
0 0 0 Index 3
65 38
103 62 49 Index 6
79 73 72 Index 7
114 101 97 Index 8
208 127 100 Index 9
151 144 142 Index 10
221 207 199 Index 11
GIMP Palette
Name: Test
Columns: 0
#

1.195 elfutils 0.168
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1.203 libjpeg 6b

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The Independent JPEG Group's JPEG software

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README for release 6b of 27-Mar-1998

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Serious users of this software (particularly those incorporating it into larger programs) should contact IJG at jpeg-info@uunet.uu.net to be added to our electronic mailing list. Mailing list members are notified of updates and have a chance to participate in technical discussions, etc.

This software is the work of Tom Lane, Philip Gladstone, Jim Boucher, Lee Crocker, Julian Minguillon, Luis Ortiz, George Phillips, Davide Rossi, Guido Vollbeding, Ge' Weijers, and other members of the Independent JPEG Group.

IJG is not affiliated with the official ISO JPEG standards committee.

DOCUMENTATION ROADMAP

This file contains the following sections:

OVERVIEW General description of JPEG and the IJG software.
LEGAL ISSUES Copyright, lack of warranty, terms of distribution.
REFERENCES Where to learn more about JPEG.
ARCHIVE LOCATIONS Where to find newer versions of this software.
RELATED SOFTWARE Other stuff you should get.
FILE FORMAT WARS Software *not* to get.
TO DO Plans for future IJG releases.

Other documentation files in the distribution are:

User documentation:
install.doc How to configure and install the IJG software.
usage.doc Usage instructions for cjpeg, djpeg, jpegtran,
            rdjpgcom, and wrjpgcom.
*.1 Unix-style man pages for programs (same info as usage.doc).
wizard.doc Advanced usage instructions for JPEG wizards only.
change.log Version-to-version change highlights.

Programmer and internal documentation:
ljbpef.doc How to use the JPEG library in your own programs.
example.c Sample code for calling the JPEG library.
structure.doc Overview of the JPEG library's internal structure.
filelist.doc Road map of IJG files.
coderules.doc Coding style rules --- please read if you contribute code.

Please read at least the files install.doc and usage.doc. Useful information can also be found in the JPEG FAQ (Frequently Asked Questions) article. See ARCHIVE LOCATIONS below to find out where to obtain the FAQ article.

If you want to understand how the JPEG code works, we suggest reading one or more of the REFERENCES, then looking at the documentation files (in roughly the order listed) before diving into the code.

OVERVIEW
========

This package contains C software to implement JPEG image compression and decompression. JPEG (pronounced "jay-peg") is a standardized compression method for full-color and gray-scale images. JPEG is intended for compressing "real-world" scenes; line drawings, cartoons and other non-realistic images are not its strong suit. JPEG is lossy, meaning that the output image is not exactly identical to the input image. Hence you must not use JPEG if you have to have identical output bits. However, on typical photographic images, very good compression levels can be obtained with no visible change, and remarkably high compression levels are possible if you can tolerate a low-quality image. For more details, see the references, or just experiment with various compression settings.

This software implements JPEG baseline, extended-sequential, and progressive compression processes. Provision is made for supporting all variants of these processes, although some uncommon parameter settings aren't implemented yet. For legal reasons, we are not distributing code for the arithmetic-coding variants of JPEG; see LEGAL ISSUES. We have made no provision for supporting the hierarchical or lossless processes defined in the standard.

We provide a set of library routines for reading and writing JPEG image files, plus two sample applications "cjpeg" and "djpeg", which use the library to perform conversion between JPEG and some other popular image file formats. The library is intended to be reused in other applications.

In order to support file conversion and viewing software, we have included considerable functionality beyond the bare JPEG coding/decoding capability; for example, the color quantization modules are not strictly part of JPEG decoding, but they are essential for output to colormapped file formats or colormapped displays. These extra functions can be compiled out of the library if not required for a particular application. We have also included "jpegtran", a utility for lossless transcoding between different JPEG
processes, and "rdjpgcom" and "wrjpgcom", two simple applications for inserting and extracting textual comments in JFIF files.

The emphasis in designing this software has been on achieving portability and flexibility, while also making it fast enough to be useful. In particular, the software is not intended to be read as a tutorial on JPEG. (See the REFERENCES section for introductory material.) Rather, it is intended to be reliable, portable, industrial-strength code. We do not claim to have achieved that goal in every aspect of the software, but we strive for it.

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that you must include source code if you redistribute it. (See the file
ansi2knr.c for full details.) However, since ansi2knr.c is not needed as part
of any program generated from the IJG code, this does not limit you more than
the foregoing paragraphs do.

The Unix configuration script "configure" was produced with GNU Autoconf.
It is copyright by the Free Software Foundation but is freely distributable.
The same holds for its supporting scripts (config.guess, config.sub,
ltconfig, ltmain.sh). Another support script, install-sh, is copyright
by M.I.T. but is also freely distributable.

It appears that the arithmetic coding option of the JPEG spec is covered by
patents owned by IBM, AT&T, and Mitsubishi. Hence arithmetic coding cannot
legally be used without obtaining one or more licenses. For this reason,
support for arithmetic coding has been removed from the free JPEG software.
(Since arithmetic coding provides only a marginal gain over the unpatented
Huffman mode, it is unlikely that very many implementations will support it.)
So far as we are aware, there are no patent restrictions on the remaining
code.

The IJG distribution formerly included code to read and write GIF files.
To avoid entanglement with the Unisys LZW patent, GIF reading support has
been removed altogether, and the GIF writer has been simplified to produce
"uncompressed GIFs". This technique does not use the LZW algorithm; the
resulting GIF files are larger than usual, but are readable by all standard
GIF decoders.
We are required to state that
"The Graphics Interchange Format(c) is the Copyright property of
CompuServe Incorporated. GIF(sm) is a Service Mark property of
CompuServe Incorporated."

REFERENCES
========
We highly recommend reading one or more of these references before trying to
understand the innards of the JPEG software.

The best short technical introduction to the JPEG compression algorithm is
Wallace, Gregory K. "The JPEG Still Picture Compression Standard",
Communications of the ACM, April 1991 (vol. 34 no. 4), pp. 30-44.
(Adjacent articles in that issue discuss MPEG motion picture compression,
applications of JPEG, and related topics.) If you don't have the CACM issue
handy, a PostScript file containing a revised version of Wallace's article is
available at ftp://ftp.uu.net/graphics/jpeg/wallace.ps.gz. The file (actually
a preprint for an article that appeared in IEEE Trans. Consumer Electronics)
omits the sample images that appeared in CACM, but it includes corrections
and some added material. Note: the Wallace article is copyright ACM and IEEE,
and it may not be used for commercial purposes.

A somewhat less technical, more leisurely introduction to JPEG can be found in
"The Data Compression Book" by Mark Nelson and Jean-loup Gailly, published by
good explanations and example C code for a multitude of compression methods
including JPEG. It is an excellent source if you are comfortable reading C
code but don't know much about data compression in general. The book's JPEG
sample code is far from industrial-strength, but when you are ready to look
at a full implementation, you've got one here...

The best full description of JPEG is the textbook "JPEG Still Image Data
Compression Standard" by William B. Pennebaker and Joan L. Mitchell, published
The book includes the complete text of the ISO JPEG standards (DIS 10918-1
and draft DIS 10918-2). This is by far the most complete exposition of JPEG
in existence, and we highly recommend it.

The JPEG standard itself is not available electronically; you must order a
paper copy through ISO or ITU. (Unless you feel a need to own a certified
official copy, we recommend buying the Pennebaker and Mitchell book instead;
it's much cheaper and includes a great deal of useful explanatory material.)
In the USA, copies of the standard may be ordered from ANSI Sales at (212)
642-4900, or from Global Engineering Documents at (800) 854-7179. (ANSI
doesn't take credit card orders, but Global does.) It's not cheap: as of
1992, ANSI was charging $95 for Part 1 and $47 for Part 2, plus 7%
The JPEG standard does not specify all details of an interchangeable file format. For the omitted details we follow the "JFIF" conventions, revision 1.02. A copy of the JFIF spec is available from:

Literature Department
C-Cube Microsystems, Inc.
1778 McCarthy Blvd.
Milpitas, CA 95035
phone (408) 944-6300, fax (408) 944-6314
A PostScript version of this document is available by FTP at
ftp://ftp.uu.net/graphics/jpeg/jfif.ps.gz. There is also a plain text version at ftp://ftp.uu.net/graphics/jpeg/jfif.txt.gz, but it is missing the figures.

The TIFF 6.0 file format specification can be obtained by FTP from
ftp://ftp.sgi.com/graphics/tiff/TIFF6.ps.gz. The JPEG incorporation scheme found in the TIFF 6.0 spec of 3-June-92 has a number of serious problems. IJG does not recommend use of the TIFF 6.0 design (TIFF Compression tag 6). Instead, we recommend the JPEG design proposed by TIFF Technical Note #2 (Compression tag 7). Copies of this Note can be obtained from ftp.sgi.com or from ftp://ftp.uu.net/graphics/jpeg/. It is expected that the next revision of the TIFF spec will replace the 6.0 JPEG design with the Note's design. Although IJG's own code does not support TIFF/JPEG, the free libtiff library uses our library to implement TIFF/JPEG per the Note. libtiff is available from ftp://ftp.sgi.com/graphics/tiff/.

ARCHIVE LOCATIONS

The "official" archive site for this software is ftp.uu.net (Internet address 192.48.96.9). The most recent released version can always be found there in directory graphics/jpeg. This particular version will be archived as ftp://ftp.uu.net/graphics/jpeg/jpegsrc.v6b.tar.gz. If you don't have direct Internet access, UUNET's archives are also available via UUCP; contact help@uunet.uu.net for information on retrieving files that way.
Numerous Internet sites maintain copies of the UUNET files. However, only ftp.uu.net is guaranteed to have the latest official version.

You can also obtain this software in DOS-compatible "zip" archive format from the SimTel archives (ftp://ftp.simtel.net/pub/simtelnet/msdos/graphics/), or on CompuServe in the Graphics Support forum (GO CIS:GRAPHSUP), library 12 "JPEG Tools". Again, these versions may sometimes lag behind the ftp.uu.net release.

The JPEG FAQ (Frequently Asked Questions) article is a useful source of general information about JPEG. It is updated constantly and therefore is not included in this distribution. The FAQ is posted every two weeks to Usenet newsgroups comp.graphics.misc, news.answers, and other groups. It is available on the World Wide Web at http://www.faqs.org/faqs/jpeg-faq/ and other news.answers archive sites, including the official news.answers archive at rtfm.mit.edu: ftp://rtfm.mit.edu/pub/usenet/news.answers/jpeg-faq/.

If you don't have Web or FTP access, send e-mail to mail-server@rtfm.mit.edu with body

send usenet/news.answers/jpeg-faq/part1
send usenet/news.answers/jpeg-faq/part2

RELATED SOFTWARE
================

Numerous viewing and image manipulation programs now support JPEG. (Quite a few of them use this library to do so.) The JPEG FAQ described above lists some of the more popular free and shareware viewers, and tells where to obtain them on Internet.

If you are on a Unix machine, we highly recommend Jef Poskanzer's free PBMPLUS software, which provides many useful operations on PPM-format image files. In particular, it can convert PPM images to and from a wide range of other formats, thus making cjpeg/djpeg considerably more useful. The latest version is distributed by the NetPBM group, and is available from numerous sites, notably ftp://wuarchive.wustl.edu/graphics/graphics/packages/NetPBM/.

Unfortunately PBMPLUS/NETPBM is not nearly as portable as the IJG software is; you are likely to have difficulty making it work on any non-Unix machine.

A different free JPEG implementation, written by the PVRG group at Stanford, is available from ftp://havefun.stanford.edu/pub/jpeg/. This program is designed for research and experimentation rather than production use; it is slower, harder to use, and less portable than the IJG code, but it is easier to read and modify. Also, the PVRG code supports lossless JPEG, which we do not. (On the other hand, it doesn't do progressive JPEG.)

FILE FORMAT WARS
Some JPEG programs produce files that are not compatible with our library. The root of the problem is that the ISO JPEG committee failed to specify a concrete file format. Some vendors "filled in the blanks" on their own, creating proprietary formats that no one else could read. (For example, none of the early commercial JPEG implementations for the Macintosh were able to exchange compressed files.)

The file format we have adopted is called JFIF (see REFERENCES). This format has been agreed to by a number of major commercial JPEG vendors, and it has become the de facto standard. JFIF is a minimal or "low end" representation. We recommend the use of TIFF/JPEG (TIFF revision 6.0 as modified by TIFF Technical Note #2) for "high end" applications that need to record a lot of additional data about an image. TIFF/JPEG is fairly new and not yet widely supported, unfortunately.

The upcoming JPEG Part 3 standard defines a file format called SPIFF. SPIFF is interoperable with JFIF, in the sense that most JFIF decoders should be able to read the most common variant of SPIFF. SPIFF has some technical advantages over JFIF, but its major claim to fame is simply that it is an official standard rather than an informal one. At this point it is unclear whether SPIFF will supersede JFIF or whether JFIF will remain the de-facto standard. IJG intends to support SPIFF once the standard is frozen, but we have not decided whether it should become our default output format or not. (In any case, our decoder will remain capable of reading JFIF indefinitely.)

Various proprietary file formats incorporating JPEG compression also exist. We have little or no sympathy for the existence of these formats. Indeed, one of the original reasons for developing this free software was to help force convergence on common, open format standards for JPEG files. Don't use a proprietary file format!

TO DO
======

The major thrust for v7 will probably be improvement of visual quality. The current method for scaling the quantization tables is known not to be very good at low Q values. We also intend to investigate block boundary smoothing, "poor man's variable quantization", and other means of improving quality-vs-file-size performance without sacrificing compatibility.

In future versions, we are considering supporting some of the upcoming JPEG Part 3 extensions --- principally, variable quantization and the SPIFF file format.

As always, speeding things up is of great interest.
Please send bug reports, offers of help, etc. to jpeg-info@uunet.uu.net.

1.204 sqlite 3.15.2

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1.205 subversion 1.9.7

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#!/usr/bin/env python
#
# check if a file has the proper license in it
#
# USAGE: check-license.py [-C] file1 file2 ... fileN
#
# A 'file' may in fact be a directory, in which case it is recursively
# searched.
#
# If the license cannot be found, then the filename is printed to stdout.
# Typical usage:
#   $ check-license.py . > bad-files
#
# -C switch is used to change licenses.
# Typical usage:
#   $ check-license.py -C file1 file2 ... fileN
#

import sys, os, re

# Note: Right now, OLD_LICENSE and NEW_LICENSE are the same, because
# r878444 updated all the license blocks. In the future, if we update
# the license block again, change just NEW_LICENSE and use this script.

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# Yes, this is an empty tuple. No types that fit in this category uniformly
# have a copyright block.
# Possible types to add here:
# ('.bat', '.py', '.pl', '.in')
sh_comment_suffices = ()

def check_file(fname, old_re, new_lic):
    s = open(fname).read()
    if (not old_re.search(s)
        and not re_EXCLUDE.search(s)):
        print(fname)

def change_license(fname, old_re, new_lic):
    s = open(fname).read()
    m = old_re.search(s)
    if not m:
        print('ERROR: missing old license: %s' % fname)
    else:
        s = s[:m.start()] + new_lic + s[m.end():]
        open(fname, 'w').write(s)
        print('Changed: %s' % fname)

def visit(baton, dirname, dircontents):
    file_func = baton
    for i in dircontents:
        # Don't recurse into certain directories
        if i in ('.svn', '.libs'):
            dircontents.remove(i)
            continue
        extension = os.path.splitext(i)[1]
        fullname = os.path.join(dirname, i)

        if os.path.isdir(fullname):
            continue

        if extension in c_comment_suffices:
            file_func(fullname, re_OLD, NEW_LICENSE)
        elif extension in sh_comment_suffices:
            file_func(fullname, re_SH_OLD, SH_NEW_LICENSE)

def main():
    file_func = check_file
    if sys.argv[1] == '-C':
        print('Changing license text...
    del sys.argv[1]
    file_func = change_license
for f in sys.argv[1:]:
    if os.path.isdir(f):
        baton = file_func
        for dirpath, dirs, files in os.walk(f):
            visit(baton, dirpath, dirs + files)
    else:
        baton = file_func
        dir, i = os.path.split(f)
        visit(baton, dir, i)

if __name__ == '__main__':
    main()

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* Markus Kuhn -- 2007-05-26 (Unicode 5.0)
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* configure.ac, Makefile.am: The original versions were derived from the ones in the XML Catalog Manager project, version 2.2.
Author: Julio Merino <jmmv@users.sourceforge.net>

* atf-c/ui.c: The format_paragraph and format_text functions were derived from the ones in the Monotone project, revision 3a0982da308228d796df35f98d787c5cfff2bb5b6.

Author: Julio Merino <jmmv@NetBSD.org>

* atf-c++/detail/io.hpp, atf-c++/detail/io.cpp, atf-c++/detail/io_test.cpp: These files were derived from the file_handle, systembuf, pipe and pistream classes and tests found in the Boost.Process library.

Author: Julio Merino <jmmv84@gmail.com>

* admin/check-style.sh, admin/check-style-common.awk, admin/check-style-cpp.awk, admin/check-style-shell.awk: These files, except the first one, were first implemented in the Buildtool project. They were later adapted to be part of Boost.Process and, during that process, the shell script was created.

Author: Julio Merino <jmmv84@gmail.com>

===========================================================================
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-
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-->

<!-- $Id$ -->

<!-- Generate ISC copyright comments from Docbook copyright metadata. -->

<xsl:stylesheet version="1.0"
xmlns:xsl="http://www.w3.org/1999/XSL/Transform"
xmlns:xi="http://www.w3.org/2001/XInclude"
xmlns:db="http://docbook.org/ns/docbook">

<xsl:template name="isc.copyright.format">
  <xsl:param name="text"/>
  <xsl:value-of select="normalize-space(substring-before($text, '&#10;'))"/>
  <xsl:text>&#10;</xsl:text>
  <xsl:variable name="rest" select="substring-after($text, '&#10;')"/>
</xsl:template>

</xsl:stylesheet>
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The actual classes of The Simple API for CSS (SAC) came from:
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Version: GnuPG v1.0.7 (GNU/Linux)

iD8DBQFE85cgaq9Frj/CIrIRAmuJAKCFgi4W0UOH8iUn+SV6PBHRF3BnLgCcDqqC
Zoktk0bTHfwaa5TtxQbScw=
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Full name: Nicanor Gonzalez
E-Mail: ngonzalez@exist.com
Mailing Address: 37 TwinHill St., New Manila Rolling Hills, Q.C., Philippines

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E-Mail: janb@eclipse.com
Mailing Address: 62 Church St Balmain NSW 2041 Australia

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Version: GnuPG v1.4.1 (GNU/Linux)

iD8DBQFDjMI6j97Uv2IW248RAmGNAJ9/krpkiYJRrJTMXVkJ3cdnVvfU+QCFYFehpN0h9U/xdFTRMFsXYFHQeN4=

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iD8DBQFFoniQVhI5gQRAmJmA1jwL5y1IoohvQH1CsparvHMQuwqwCgiZ+FyLBDVaa1bJ1v1EHY901kPcg=
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iD8DBQFEbh9SoTb97cQk4RAnCMAKCuNGYLHa6n/Ot3GEdwCCLeQxsMPAcD EhnhE/ stizRWWZZkeLbgfzdQCE=
+piHm
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Version: GnuPG v1.4.10 (GNU/Linux)

iQEeBAEBAgJQBjBmtgF91HDCYH/2nQDPuPztWFrBi6nEdL6F6Ji
RUkfZApALd7ueDrL1RDodmz4VIF2+fKeBYQquZXfXeEghz+tKriK3
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iD8DBQFD37/4dL6IZr4c+6kRAtsIAJ41tfd3lj4OM6sIMfJfTODyT1bxwCdGgWv
8sfMxEDZqlqhVbfZU2c76U=
=8WW7
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https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html

javax.servlet:javax.servlet-api
javax.servlet.jsp:javax.servlet.jsp-api
org.glassfish.web:javax.servlet.jsp
org.glassfish.web:javax.servlet.jsp
org.glassfish.web:javax.servlet.jsp.jstl
org.eclipse.jetty.orbit.jsr166:javax.servlet.jsp.jstl

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Apache

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org.apache.taglibs:taglibs-standard-spec
org.apache.taglibs:taglibs-standard-impl

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org.mortbay.jasper:apache-jsp
org.apache.tomcat:tomcat-jasper
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https://glassfish.dev.java.net/nonav/public/CDDL+GPL.html

org.eclipse.jetty.toolchain:jetty-schemas

Assorted

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1.220 jersey 2.22

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1.225 tomcat-catalina-ant 8.5.32

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a) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.

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c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

e) Convey the object code using peer-to-peer transmission, provided you inform other peers where the object code and Corresponding Source of the work are being offered to the general public at no charge under subsection 6d.

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Because of this blurred distinction, using the ordinary General
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concluded that weaker conditions might promote sharing better.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which
must be distributed under the terms of Sections 1 and 2 above on a
medium customarily used for software interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
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However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or
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a) Accompany the work with the complete corresponding machine-readable source code for the Library including whatever changes were used in the work (which must be distributed under Sections 1 and 2 above); and, if the work is an executable linked with the Library, with the complete machine-readable "work that uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

d) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

It may happen that this requirement contradicts the license restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

c) If distribution of the work is made by offering access to copy from a designated place, offer equivalent access to copy the above specified materials from the same place.

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END OF TERMS AND CONDITIONS

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The “source code” for a work means the preferred form of the work for making modifications to it. "Object code” means any non-source form of a work.

A "Standard Interface” means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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Ty Coon, President of Vice
@end example
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1.240 snake-yaml 1.18
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1.241 gorilla 1.7.1

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1.242 curl 7.64.1

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1.247 protobuf 3.5.1.1

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1.248 bean-validation-api 1.1.0

1.249 chardet 3.0.4
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1.251 go-yaml 2.0

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1.252 sbsigntool 0.7

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for
National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations, which became Zope Corporation. In 2001, the Python Software Foundation (PSF, see https://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation was a sponsoring member of the PSF.

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Mersenne Twister

------------

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A C-program for MT19937, with initialization improved 2002/1/26.
Coded by Takuji Nishimura and Makoto Matsumoto.

Before using, initialize the state by using `init_genrand(seed)`
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http://www.math.sci.hiroshima-u.ac.jp/~m-mat/MT/emt.html
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Sockets
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Modified by Jack Jansen, CWI, July 1995:
- Use binascii module to do the actual line-by-line conversion
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- Arguments more compliant with Python standard

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Original location:
https://github.com/majek/csiphash/

Solution inspired by code from:
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-------

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libmpdec
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The :mod:`decimal` module is built using an included copy of the libmpdec library unless the build is configured `--with-system-libmpdec`:

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David Megginson <sax@megginson.com>
1998-05-11
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Version 2, June 1991
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1.260 sudo 1.8.28
1.260.1 Available under license:
The following list of people, sorted by last name, have contributed code or patches to this implementation of sudo since I began maintaining it in 1993. This list is known to be incomplete—if you believe you should be listed, please send a note to sudo@sudo.ws.

Ackeret, Matt
Adler, Mark
Allbery, Russ
Anderson, Jamie
Andrew, Nick
Andric, Dimitry
Barron, Danny
Bates, Tom
Behan, Zdenk
Bellis, Ray
Benali, Elias
Beverly, Jamie
Boardman, Spider
Bos, Sander
Bostley, P.J.
Bowes, Keith
Boyce, Keith Garry
Brantley, Michael
Braun, Rob
Bezina, Pavel
Brooks, Pieter
Brown, Jerry
Burr, Michael E
Burton, Ross
Bussjæger, Andreas
Calvin, Gary
Campbell, Aaron
Chazelas, Stephane
Cheloha, Scott
ek, Václav
Kopeek, Daniel
Kranenburg, Paul
Krause, David
Lakin, Eric
Larsen, Case
Levin, Dmitry V.
Libby, Kendall
Lobbes, Phillip E.
McIntyre, Jason
MacKenzie, David J.
McLaughlin, Tom
Makey, Jeff
Mallayya, Sangamesh
Marchionna, Michael D.
Markham, Paul
Martinian, Emin
Meskes, Michael
Michael, David
Miller, Todd C.
Minier, Loc
Moffat, Darren
Moldung, Jan Thomas
Morris, Charles
Mueller, Andreas
Miller, Dworkin
Nieuusma, Jeff
Nikitser, Peter A.
Nussel, Ludwig
Ouellet, Jean-Philippe
Paquet, Eric
Paradis, Chantal
Pasteleurs, Frederic
Percival, Ted
Perera, Andres
Peron, Christian S.J.
Peschel, Aaron
Peslyak, Alexander
Peterson, Toby
Petten, Diego Elio
Pickett, Joel
Plotnick, Alex
de Raadt, Theo
Rasch, Gudleik
Reid, Steve
Richards, Matt
Rossum, Guido van
Rouillard, John P.
Rowe, William A., Jr.
Roy, Alain
Ruusame, Elan
Ryabinkin, Eygene
Sato, Yuichi
Sánchez, Wilfredo
Sanders, Miguel
Sasaki, Kan
Saucier, Jean-Francois
Schoenfeld, Patrick
Schuring, Arno
Schwarze, Ingo
Scott, Dougal
Sieger, Nick
Simon, Thor Lancelot
Slemko, Marc
Smith, Andy
Sobrado, Igor
Soulen, Steven
Spangler, Aaron
Spradling, Cloyce D.
Spradling, Michael
Stier, Matthew
Stoeckmann, Tobias
Street, Russell
Stritzky, Tilo
Stroucken, Michael
Tarrall, Robert
Thomas, Matthew
Todd, Giles
Toft, Martin
Torek, Chris
Tucker, Darren
Uhl, Robert
Uzel, Petr
Valery, Reznic
Van Dinter, Theo
Venckus, Martynas
de Vries, Maarten
Wagner, Klaus
Walsh, Dan
Warburton, John
Webb, Kirk
Wetzel, Timm
Wieringen, Marco van
Wilk, Jakub
Winiger, Gary
Wood, David
Zacarias, Gustavo
Zolnowsky, John

The following people have worked to translate sudo into other languages as part of the Translation Project, see https://translationproject.org for more details.

Albuquerque, Pedro
Blttermann, Mario
Bogusz, Jakub
Buo-ren, Lin
Casagrande, Milo
Castro, Felipe
Cho, Seong-ho
Chornoivan, Yuri
Diguez, Francisco
Fontenelle, Rafael
Garca-Fontes, Walter
Gezer, Volkan
Hamasaki, Takeshi
Hamming, Peter
Hansen, Joe
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Hein, Jochen
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 ** presents to client programs. If a C-function, structure, datatype,
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 ** notice, and should not be referenced by programs that use SQLite.
 **
 ** Some of the definitions that are in this file are marked as
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 ** features recently added to SQLite. We do not anticipate changes
 ** to experimental interfaces but reserve the right to make minor changes
 ** if experience from use "in the wild" suggest such changes are prudent.
 */
**
** The official C-language API documentation for SQLite is derived
** from comments in this file. This file is the authoritative source
** on how SQLite interfaces are suppose to operate.
**
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1.266 json-c 0.12.1

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1.267 ldns 1.6.17

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interface, howver that is not a very good interface for python. Its
documentation is incomplete and some functions don't work as
described. And some objects don't have a full python API.
ldnsx aims to fix this. It wraps around the ldns python bindings,
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1.269 jersey-common 2.22

1.270 Ldb 1.2.840

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1.271 jackson-databind 1.9.13

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1.272 pflag 1.0.5

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1.274 zlib 1.1.3

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1.277 libpng 1.6.20

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1.291 hk2-api 2.4.0

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@c Local Variables:
c ispell-local-pdict: "ispell-dict"
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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

@item
A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being compiled or
linked with it, is called a "work that uses the Library". Such a
work, in isolation, is not a derivative work of the Library, and
therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a
derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

@item
As an exception to the Sections above, you may also combine or
link a "work that uses the Library" with the Library to produce a
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directing the user to the copy of this License. Also, you must do one
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@enumerate
@item
Accompany the work with the complete corresponding
machine-readable source code for the Library including whatever
changes were used in the work (which must be distributed under
Sections 1 and 2 above); and, if the work is an executable linked
with the Library, with the complete machine-readable "work that
uses the Library", as object code and/or source code, so that the
user can modify the Library and then relink to produce a modified
executable containing the modified Library. (It is understood
that the user who changes the contents of definitions files in the
Library will not necessarily be able to recompile the application
to use the modified definitions.)

@item
Use a suitable shared library mechanism for linking with the Library. A
suitable mechanism is one that (1) uses at run time a copy of the
library already present on the user's computer system, rather than
copying library functions into the executable, and (2) will operate
properly with a modified version of the library, if the user installs
one, as long as the modified version is interface-compatible with the
version that the work was made with.

@item
Accompany the work with a written offer, valid for at
least three years, to give the same user the materials
specified in Subsection 6a, above, for a charge no more
than the cost of performing this distribution.

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If distribution of the work is made by offering access to copy
from a designated place, offer equivalent access to copy the above
specified materials from the same place.

@item
Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.
@end enumerate

For an executable, the required form of the "work that uses the
Library" must include any data and utility programs needed for
reproducing the executable from it. However, as a special exception,
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@enumerate a
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```
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```
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@var{signature of Ty Coon}, 1 April 1990
Ty Coon, President of Vice
@end smallexample
```

That's all there is to it!

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Version 3, 29 June 2007

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.
d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

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* AOL
http://www.aleksey.com/pipermail/xmlsec/attachments/20030729/0e25648e/attachment.htm

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Preamble

The licenses for most software are designed to take away your
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Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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1.314 stunnel 5.03
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The original code is FirewallControl.pas, released April 16, 2007.

The initial developer of the original code is Rainer Budde (http://www.speed-soft.de).

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1.320 jackson-module-jaxb-annotations 2.9.5

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1.322 clang 3.4.1

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; RUN: opt -consthoist -S -o - %s | FileCheck %s
target triple = "thumbv6m-none--musleabi"

; Check that for i8 type, the maximum legal offset is 31.
; Also check that an constant used as value to be stored rather than pointer in a store instruction is hoisted.
; CHECK: foo_i8
; CHECK-DAG: %[[C1:const[0-9-]]] = bitcast i32 805874720 to i32
; CHECK-DAG: %[[C2:const[0-9-]]] = bitcast i32 805874688 to i32
; CHECK-DAG: %[[C3:const[0-9-]]] = bitcast i32 805873720 to i32
; CHECK-DAG: %[[C4:const[0-9]??]] = bitcast i32 805873688 to i32
; CHECK: %0 = inttoptr i32 %[[C2]] to i8*
; CHECK-NEXT: %1 = load volatile i8, i8* %0
; CHECK-NEXT: %[[M1:const_mat[0-9]??]] = add i32 %[[C2]], 4
; CHECK-NEXT: %2 = inttoptr i32 %[[M1]] to i8*
; CHECK-NEXT: %3 = load volatile i8, i8* %2
; CHECK-NEXT: %[[M2:const_mat[0-9]??]] = add i32 %[[C2]], 31
; CHECK-NEXT: %4 = inttoptr i32 %[[M2]] to i8*
; CHECK-NEXT: %5 = load volatile i8, i8* %4
; CHECK-NEXT: %6 = inttoptr i32 %[[C1]] to i8*
; CHECK-NEXT: %7 = load volatile i8, i8* %6
; CHECK-NEXT: %[[M3:const_mat[0-9]??]] = add i32 %[[C1]], 7
; CHECK-NEXT: %8 = inttoptr i32 %[[M3]] to i8*
; CHECK-NEXT: %9 = load volatile i8, i8* %8
; CHECK-NEXT: %10 = inttoptr i32 %[[C4]] to i8*
; CHECK-NEXT: store i8 %9, i8* %10
; CHECK-NEXT: %[[M4:const_mat[0-9]??]] = add i32 %[[C4]], 31
; CHECK-NEXT: %11 = inttoptr i32 %[[M4]] to i8*
; CHECK-NEXT: store i8 %7, i8* %11
; CHECK-NEXT: %12 = inttoptr i32 %[[C3]] to i8*
; CHECK-NEXT: store i8 %5, i8* %12
; CHECK-NEXT: %[[M5:const_mat[0-9]??]] = add i32 %[[C3]], 7
; CHECK-NEXT: %13 = inttoptr i32 %[[M5]] to i8*
; CHECK-NEXT: store i8 %3, i8* %13
; CHECK-NEXT: %[[M6:const_mat[0-9]??]] = add i32 %[[C1]], 80
; CHECK-NEXT: %14 = inttoptr i32 %[[M6]] to i8*
; CHECK-NEXT: store i8* %14, i8** @goo

@goo = global i8* undef

define void @foo_i8() {
  entry:
  %0 = load volatile i8, i8* inttoptr (i32 805874688 to i8*)
  %1 = load volatile i8, i8* inttoptr (i32 805874692 to i8*)
  %2 = load volatile i8, i8* inttoptr (i32 805874719 to i8*)
  %3 = load volatile i8, i8* inttoptr (i32 805874720 to i8*)
  %4 = load volatile i8, i8* inttoptr (i32 805874727 to i8*)
  store i8 %4, i8* inttoptr(i32 805873688 to i8*)
  store i8 %3, i8* inttoptr(i32 805873719 to i8*)
  store i8 %2, i8* inttoptr(i32 805873720 to i8*)
  store i8 %1, i8* inttoptr(i32 805873727 to i8*)
  store i8* inttoptr(i32 805874800 to i8*), i8** @goo
  ret void
}

; Check that for i16 type, the maximum legal offset is 62.
; CHECK: foo_i16
; CHECK-DAG: %[[C1:const[0-9]??]] = bitcast i32 805874752 to i32
define void @foo_i32() {
entry:
%0 = load volatile i32, i32* inttoptr (i32 805874688 to i32*), align 4
%1 = load volatile i32, i32* inttoptr (i32 805874692 to i32*), align 4
%2 = load volatile i32, i32* inttoptr (i32 805874812 to i32*), align 4
%3 = load volatile i32, i32* inttoptr (i32 805874816 to i32*), align 4
%4 = load volatile i32, i32* inttoptr (i32 805874824 to i32*), align 4
%5 = load volatile i32, i32* inttoptr (i32 805874828 to i32*), align 4
ret void
}
; RUN: llc -mtriple=aarch64-apple-ios %s -o - | FileCheck %s

define <16 x double> @test_sitofp_fixed(<16 x i32> %in) {
; CHECK-LABEL: test_sitofp_fixed:
; First, extend each i32 to i64
; CHECK-DAG: sshll2.2d [[[BLOCK0_HI:v[0-9]+]], v0, #0
; CHECK-DAG: sshll2.2d [[[BLOCK1_HI:v[0-9]+]], v1, #0
; CHECK-DAG: sshll2.2d [[[BLOCK2_HI:v[0-9]+]], v2, #0
; CHECK-DAG: sshll2.2d [[[BLOCK3_HI:v[0-9]+]], v3, #0
; CHECK-DAG: sshll.2d [[[BLOCK0_LO:v[0-9]+]], v0, #0
; CHECK-DAG: sshll.2d [[[BLOCK1_LO:v[0-9]+]], v1, #0
; CHECK-DAG: sshll.2d [[[BLOCK2_LO:v[0-9]+]], v2, #0
; CHECK-DAG: sshll.2d [[[BLOCK3_LO:v[0-9]+]], v3, #0
; Next, convert each to double.
; CHECK-DAG: scvtf.2d v0, [[[BLOCK0_LO]]
; CHECK-DAG: scvtf.2d v1, [[[BLOCK0_HI]]
; CHECK-DAG: scvtf.2d v2, [[[BLOCK1_LO]]
; CHECK-DAG: scvtf.2d v3, [[[BLOCK1_HI]]
; CHECK-DAG: scvtf.2d v4, [[[BLOCK2_LO]]
; CHECK-DAG: scvtf.2d v5, [[[BLOCK2_HI]]
; CHECK-DAG: scvtf.2d v6, [[[BLOCK3_LO]]
; CHECK-DAG: scvtf.2d v7, [[[BLOCK3_HI]]]
; CHECK: ret
%flt = sitofp <16 x i32> %in to <16 x double>
%res = fdiv <16 x double> %flt, <double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0, double 64.0>
ret <16 x double> %res
}

; This one is small enough to satisfy isSimple, but still illegally large.
define <4 x double> @test_sitofp_fixed_shortish(<4 x i64> %in) {
; CHECK-LABEL: test_sitofp_fixed_shortish:

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#
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target triple = "hexagon"

%struct.0 = type { i8*, i8, %union.anon.0 }
%union.anon.0 = type { i8* }

define hidden fastcc void @fred() unnamed_addr #0 {
  entry:
  br i1 undef, label %while.end, label %while.body.lr.ph

  while.body.lr.ph:
    ; preds = %entry
    br label %while.body

  while.body:
    ; preds = %exit.2, %while.body.lr.ph
    %lsr.iv = phi %struct.0* [ %cgep22, %exit.2 ], [ undef, %while.body.lr.ph ]
    switch i32 undef, label %exit [
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; RUN: opt %s -inline -S | FileCheck %s

define internal void @innerSmall() "min-legal-vector-width"="128" {
    ret void
}

define internal void @innerLarge() "min-legal-vector-width"="512" {
    ret void
}

define internal void @innerNoAttribute() {
    ret void
}
; We should not add an attribute during inlining. No attribute means unknown.
; Inlining doesn't change the fact that we don't know anything about this
; function.
define void @outerNoAttribute() {
call void @innerLarge()
ret void
}

define void @outerConflictingAttributeSmall() "min-legal-vector-width"="128" {
call void @innerLarge()
ret void
}

define void @outerConflictingAttributeLarge() "min-legal-vector-width"="512" {
call void @innerSmall()
ret void
}

; We should remove the attribute after inlining since the callee's
; vector width requirements are unknown.
define void @outerAttribute() "min-legal-vector-width"="128" {
call void @innerNoAttribute()
ret void
}

; CHECK: define void @outerNoAttribute() {
; CHECK: define void @outerConflictingAttributeSmall() #0
; CHECK: define void @outerConflictingAttributeLarge() #0
; CHECK: define void @outerAttribute() {
; CHECK: attributes #0 = { "min-legal-vector-width"="512" }
; RUN: llc < %s -mtriple=x86_64-linux-android -mattr=+mmx -enable-legalize-types-checking | FileCheck %s
;
; D31946
; Check that we dont end up with the "LLVM ERROR: Cannot select" error.
; Additionally ensure that the output code actually put fp128 values in SSE registers.
declare fp128 @llvm.fabs.f128(fp128)
declare fp128 @llvm.copysign.f128(fp128, fp128)

define fp128 @TestSelect(fp128 %a, fp128 %b) {
%cmp = fcmp ogt fp128 %a, %b
%sub = fsub fp128 %a, %b
%res = select i1 %cmp, fp128 %sub, fp128 0xL00000000000000000000000000000000
ret fp128 %res
; CHECK-LABEL: TestSelect:
; CHECK movaps 16(%rsp), %xmm1
; CHECK-NEXT callq __subtf3
; CHECK-NEXT testl %ebx, %ebx
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; NOTE: Assertions have been autogenerated by utils/update_llc_test_checks.py
; RUN: llc < %s -mtriple=x86_64-unknown-unknown -mattr=avx512vl,avx512bw,avx512dq,prefer-256-bit | FileCheck %s

; This file primarily contains tests for specific places in X86ISelLowering.cpp that needed be made aware of the legalizer not allowing 512-bit vectors due to prefer-256-bit even though AVX512 is enabled.

define void @add256(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="256" {
  ; CHECK-LABEL: add256:
  ; CHECK:       # %bb.0:
  ; CHECK-NEXT:   vmovdqa (%rdi), %ymm0
  ; CHECK-NEXT:   vmovdqa 32(%rdi), %ymm1
  ; CHECK-NEXT:   vpadd (%rsi), %ymm0, %ymm0
  ; CHECK-NEXT:   vpadd 32(%rsi), %ymm1, %ymm1
}
define void @add512(<16 x i32>* %a, <16 x i32>* %b, <16 x i32>* %c) "min-legal-vector-width"="512" {
; CHECK-LABEL: add512:
; CHECK:     # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpadd (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%d = load <16 x i32>, <16 x i32>* %a
%e = load <16 x i32>, <16 x i32>* %b
%f = add <16 x i32> %d, %e
store <16 x i32> %f, <16 x i32>* %c
ret void
}

define void @avg_v64i8_256(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="256" {
; CHECK-LABEL: avg_v64i8_256:
; CHECK:     # %bb.0:
; CHECK-NEXT: vmovdqa (%rsi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rsi), %ymm1
; CHECK-NEXT: vpavgb (%rdi), %ymm0, %ymm0
; CHECK-NEXT: vpavgb 32(%rdi), %ymm1, %ymm1
; CHECK-NEXT: vmovdqu %ymm1, (%rax)
; CHECK-NEXT: vmovdqu %ymm0, (%rax)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%1 = load <64 x i8>, <64 x i8>* %a
%2 = load <64 x i8>, <64 x i8>* %b
%3 = zext <64 x i8> %1 to <64 x i32>
%4 = zext <64 x i8> %2 to <64 x i32>
%5 = add nuw nsw <64 x i32> %3, <i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1, i32 1>
%6 = add nuw nsw <64 x i32> %5, %4
%7 = lshr <64 x i32> %6, %32
%8 = add <64 x i32> %7, %32
%9 = add <64 x i32> %8, %32
%10 = lshr <64 x i32> %9, %32
%11 = add <64 x i32> %10, %32
define void @avg_v64i8_512(<64 x i8>* %a, <64 x i8>* %b) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: avg_v64i8_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT: vmovdqa64 (%rsi), %zmm0
    ; CHECK-NEXT: vpavgb (%rdi), %zmm0, %zmm0
    ; CHECK-NEXT: vmovdqu64 %zmm0, (%rax)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq

    %1 = load <64 x i8>, <64 x i8>* %a
    %2 = load <64 x i8>, <64 x i8>* %b
    %3 = zext <64 x i8> %1 to <64 x i32>
    %4 = zext <64 x i8> %2 to <64 x i32>
    %5 = add nuw nsw <64 x i32> %3, %4
    %6 = add nuw nsw <64 x i32> %5, %6
    %7 = lshr <64 x i32> %6, %7
    store <64 x i8> %7, <64 x i8>* undef, align 4
    ret void
}

define void @pmaddwd_32_256(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: pmaddwd_32_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT: vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT: vpmaddwd (%rsi), %ymm0, %ymm0
    ; CHECK-NEXT: vpmaddwd 32(%rsi), %ymm1, %ymm1
    ; CHECK-NEXT: vmovdqa %ymm0, 32(%rdx)
    ; CHECK-NEXT: vzeroupper
    ; CHECK-NEXT: retq

    %A = load <32 x i16>, <32 x i16>* %APtr
    %B = load <32 x i16>, <32 x i16>* %BPtr
    store <32 x i16> %A, <32 x i16>* undef, align 4
    ret void
}
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

define void @pmaddwd_32_512(<32 x i16>* %APtr, <32 x i16>* %BPtr, <16 x i32>* %CPtr) "min-legal-vector-width"="512" {
; CHECK-LABEL: pmaddwd_32_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpmaddwd (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%A = load <32 x i16>, <32 x i16>* %APtr
%B = load <32 x i16>, <32 x i16>* %BPtr
%a = sext <32 x i16> %A to <32 x i32>
%b = sext <32 x i16> %B to <32 x i32>
%m = mul nsw <32 x i32> %a, %b
%odd = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 0, i32 2, i32 4, i32 6, i32 8, i32 10, i32 12, i32 14, i32 16, i32 18, i32 20, i32 22, i32 24, i32 26, i32 28, i32 30>
%even = shufflevector <32 x i32> %m, <32 x i32> undef, <16 x i32> <i32 1, i32 3, i32 5, i32 7, i32 9, i32 11, i32 13, i32 15, i32 17, i32 19, i32 21, i32 23, i32 25, i32 27, i32 29, i32 31>
%ret = add <16 x i32> %odd, %even
store <16 x i32> %ret, <16 x i32>* %CPtr
ret void
}

define void @psubus_64i8_max_256(<64 x i8>* %xptr, <64 x i8>* %y.ptr, <64 x i8>* %zptr) "min-legal-vector-width"="256" {
; CHECK-LABEL: psubus_64i8_max_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vmovdqa (%rdi), %ymm0
; CHECK-NEXT: vmovdqa 32(%rdi), %ymm1
; CHECK-NEXT: vpsubusb (%rsi), %ymm0, %ymm0
; CHECK-NEXT: vpsubusb 32(%rsi), %ymm1, %ymm1
; CHECK-NEXT: vsubvpsubusb %ymm1, 32(%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

define void @psubus_64i8_max_512(<64 x i8>* %xptr, <64 x i8>* %yptr, <64 x i8>* %zptr) "min-legal-vector-width"="512" {
; CHECK-LABEL: psubus_64i8_max_512:
; CHECK:    # %bb.0:
; CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
; CHECK-NEXT: vpsubusb (%rsi), %zmm0, %zmm0
; CHECK-NEXT: vmovdqa64 %zmm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%x = load <64 x i8>, <64 x i8>* %xptr
%y = load <64 x i8>, <64 x i8>* %yptr
%cmp = icmp ult <64 x i8> %x, %y
%max = select <64 x i1> %cmp, <64 x i8> %y, <64 x i8> %x
%res = sub <64 x i8> %max, %y
store <64 x i8> %res, <64 x i8>* %zptr
ret void
}

define i32 @_Z9test_charPcS_i_256(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="256" {
; CHECK-LABEL: _Z9test_charPcS_i_256:
; CHECK:    # %bb.0: # %entry
; CHECK-NEXT: movl %edx, %eax
; CHECK-NEXT: vpxor %xmm0, %xmm0, %xmm0
; CHECK-NEXT: xorl %ecx, %ecx
; CHECK-NEXT: vpxor %xmm1, %xmm1, %xmm1
; CHECK-NEXT: vpxor %xmm2, %xmm2, %xmm2
; CHECK-NEXT: .p2align 4, 0x90
; CHECK-NEXT: .LBB8_1: # %vector.body
; CHECK-NEXT: # =>This Inner Loop Header: Depth=1
; CHECK-NEXT: vpmovsxbw (%rdi,%rcx), %ymm3
; CHECK-NEXT: vpmovsxbw 16(%rdi,%rcx), %ymm4
; CHECK-NEXT: vpmovsxbw (%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm3, %ymm5, %ymm3
; CHECK-NEXT: vpadd %ymm1, %ymm3, %ymm1
; CHECK-NEXT: vpmovsxbw 16(%rsi,%rcx), %ymm5
; CHECK-NEXT: vpmaddwd %ymm4, %ymm5, %ymm3
; CHECK-NEXT: vpadd %ymm2, %ymm3, %ymm2
; CHECK-NEXT: addq $32, %rcx
; CHECK-NEXT: cmpq %rcx, %rax
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i32> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
define i32 @_Z9test_charPcS_i_512(i8* nocapture readonly, i8* nocapture readonly, i32) "min-legal-vector-width"="512" {
  ; CHECK-LABEL: _Z9test_charPcS_i_512:
  ; CHECK:       # %bb.0: # %entry
  ; CHECK-NEXT:    movl %edx, %eax
  ; CHECK-NEXT:    vpxor %xmm0, %xmm0, %xmm0
  ; CHECK-NEXT:    xorl %ecx, %ecx
  ; CHECK-NEXT:    vpxor %xmm1, %xmm1, %xmm1
  ; CHECK-NEXT:    .p2align 4, 0x90
  ; CHECK-NEXT:  .LBB9_1: # %vector.body
  ; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1
  ; CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm2
  ; CHECK-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm3
  ; CHECK-NEXT:    vpmaddwd %zmm2, %zmm3, %zmm2
  ; CHECK-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
  ; CHECK-NEXT:    addq $32, %rcx
  ; CHECK-NEXT:    cmpq %rcx, %rax
  ; CHECK-NEXT:    jne .LBB9_1
  ; CHECK-NEXT:  # %bb.2: # %middle.block
  ; CHECK-NEXT:    vpaddd %zmm1, %zmm2, %zmm1
  ; CHECK-NEXT:    addq $32, %rcx
  ; CHECK-NEXT:    cmpq %rcx, %rax
  ; CHECK-NEXT:    jne .LBB9_1
  ; CHECK-NEXT:    eppmsk [0-255] %zmm0, %zmm0, %zmm0
  ; CHECK-NEXT:    vpmovsxbw (%rdi,%rcx), %zmm4
  ; CHECK-NEXT:    vpmovsxbw (%rsi,%rcx), %zmm5
  ; CHECK-NEXT:    vpmaddwd %zmm4, %zmm5, %zmm4
  ; CHECK-NEXT:    vpaddd %zmm1, %zmm4, %zmm1
  ; CHECK-NEXT:    addq $32, %rcx
  ; CHECK-NEXT:    cmpq %rcx, %rax
  ; CHECK-NEXT:    jne .LBB9_1
  ; CHECK-NEXT:    eppmsk [0-255] %zmm0, %zmm0, %zmm0
  ; CHECK-NEXT:    ... more code...
  ; CHECK-NEXT:    retq
}
entry:
%3 = zext i32 %2 to i64
br label %vector.body

vector.body:
%index = phi i64 [ %index.next, %vector.body ], [ 0, %entry ]
%vec.phi = phi <32 x i32> [ %11, %vector.body ], [ zeroinitializer, %entry ]
%4 = getelementptr inbounds i8, i8* %0, i64 %index
%5 = bitcast i8* %4 to <32 x i8>*
%wide.load = load <32 x i8>, <32 x i8>* %5, align 1
%6 = sext <32 x i8> %wide.load to <32 x i32>
%7 = getelementptr inbounds i8, i8* %1, i64 %index
%8 = bitcast i8* %7 to <32 x i8>*
%wide.load14 = load <32 x i8>, <32 x i8>* %8, align 1
%9 = sext <32 x i8> %wide.load14 to <32 x i32>
%10 = mul nsw <32 x i8> %9, %6
%11 = add nsw <32 x i32> %10, %vec.phi
%index.next = add i64 %index, 32
%12 = icmp eq i64 %index.next, %3
br i1 %12, label %middle.block, label %vector.body

middle.block:
%rdx.shuf1 = shufflevector <32 x i32> %11, <32 x i32> undef, <32 x i32> <i32 16, i32 17, i32 18, i32 19, i32 20, i32 21, i32 22, i32 23, i32 24, i32 25, i32 26, i32 27, i32 28, i32 29, i32 30, i32 31, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx1 = add <32 x i32> undef, <32 x i32> undef
%rdx.shuf = shufflevector <32 x i32> %11, <32 x i32> <i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <32 x i32> undef, <32 x i32> undef
%rdx.shuf15 = shufflevector <32 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx32 = add <32 x i32> undef, <32 x i32> undef
%rdx.shuf17 = shufflevector <32 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx18 = add <32 x i32> undef, <32 x i32> undef
%rdx.shuf19 = shufflevector <32 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 unde
%bin.rdx20 = add <32 x i32> undef, <32 x i32> undef
%13 = extractelement <32 x i32> %bin.rdx20, i32 0
define i32 @sad_16i8_256() "min-legal-vector-width"="256" {  
; CHECK-LABEL: sad_16i8_256:  
; CHECK:        # %bb.0: # %entry  
; CHECK-NEXT:  vpxor %xmm0, %xmm0, %xmm0  
; CHECK-NEXT:  movq $-1024, %rax # imm = 0xFC00  
; CHECK-NEXT:  vpxor %xmm1, %xmm1, %xmm1  
; CHECK-NEXT:  .p2align 4, 0x90  
; CHECK-NEXT:  .LBB10_1: # %vector.body  
; CHECK-NEXT:    # =>This Inner Loop Header: Depth=1  
; CHECK-NEXT:    vmovdqu a+1024(%rax), %xmm2  
; CHECK-NEXT:    vpsadbw b+1024(%rax), %xmm2, %xmm2  
; CHECK-NEXT:    vpaddd %ymm1, %ymm2, %ymm1  
; CHECK-NEXT:    addq $4, %rax  
; CHECK-NEXT:    vpaddd %ymm1, %ymm0, %ymm0  
; CHECK-NEXT:    vextracti128 $1, %ymm0, %xmm1  
; CHECK-NEXT:    vpaddd %ymm1, %ymm0, %ymm0  
; CHECK-NEXT:    vpshufd {{.*#+}} xmm1 = xmm0[2,3,0,1]  
; CHECK-NEXT:    vpaddd %ymm1, %ymm0, %ymm0  
; CHECK-NEXT:    vmovd %xmm0, %eax  
; CHECK-NEXT:    vzeroupper  
; CHECK-NEXT:    retq  
entry:  
br label %vector.body  

vector.body:  
%index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]  
%vec.phi = phi <16 x i32> [ zeroinitialize, %entry ], [ %10, %vector.body ]  
%0 = getelementptr inbounds [1024 x i8], [1024 x i8]@a, i64 0, i64 %index  
%1 = bitcast i8* %0 to <16 x i8>*  
%wide.load = load <16 x i8>, <16 x i8>* %1, align 4  
%2 = zext <16 x i8> %wide.load to <16 x i32>  
%3 = getelementptr inbounds [1024 x i8], [1024 x i8]@b, i64 0, i64 %index  
%4 = bitcast i8* %3 to <16 x i8>*  
%wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4  
%5 = zext <16 x i8> %wide.load1 to <16 x i32>  
%6 = sub nsw <16 x i32> %2, %5  
%7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>  

%8 = sub nsw <16 x i32> zeroinitializer, %6
%9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
%10 = add nsw <16 x i32> %9, %vec.phi
%index.next = add i64 %index, 4
%11 = icmp eq i64 %index, 1024
br i1 %11, label %middle.block, label %vector.body

middle.block:
%.lcssa = phi <16 x i32> [ %10, %vector.body ]
%rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> undef, <i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx = add <16 x i32> %.lcssa, %rdx.shuf
%rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> <i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
%rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> <i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
%rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> <i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
%bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
%12 = extractelement <16 x i32> %bin.rdx4, i32 0
ret i32 %12
entry:
  br label %vector.body

vector.body:
  %index = phi i64 [ 0, %entry ], [ %index.next, %vector.body ]
  %vec.phi = phi <16 x i32> [ zeroinitializer, %entry ], [ %10, %vector.body ]
  %0 = getelementptr inbounds [1024 x i8], [1024 x i8]* @a, i64 0, i64 %index
  %1 = bitcast i8* %0 to <16 x i8>*
  %wide.load = load <16 x i8>, <16 x i8>* %1, align 4
  %2 = zext <16 x i8> %wide.load to <16 x i32>
  %3 = getelementptr inbounds [1024 x i8], [1024 x i8]* @b, i64 0, i64 %index
  %4 = bitcast i8* %3 to <16 x i8>*
  %wide.load1 = load <16 x i8>, <16 x i8>* %4, align 4
  %5 = zext <16 x i8> %wide.load1 to <16 x i32>
  %6 = sub nsw <16 x i32> %2, %5
  %7 = icmp sgt <16 x i32> %6, <i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1, i32 -1>
  %8 = sub nsw <16 x i32> zeroinitializer, %6
  %9 = select <16 x i1> %7, <16 x i32> %6, <16 x i32> %8
  %10 = add nsw <16 x i32> %9, %vec.phi
  %index.next = add i64 %index, 4
  %11 = icmp eq i64 %index.next, 1024
  br i1 %11, label %middle.block, label %vector.body

middle.block:
  %.lcssa = phi <16 x i32> [ %10, %vector.body ]
  %rdx.shuf = shufflevector <16 x i32> %.lcssa, <16 x i32> undef, <16 x i32> i32 8, i32 9, i32 10, i32 11, i32 12, i32 13, i32 14, i32 15, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx = add <16 x i32> %lcssa, %rdx.shuf
  %rdx.shuf2 = shufflevector <16 x i32> %bin.rdx, <16 x i32> undef, <16 x i32> i32 4, i32 5, i32 6, i32 7, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx2 = add <16 x i32> %bin.rdx, %rdx.shuf2
  %rdx.shuf3 = shufflevector <16 x i32> %bin.rdx2, <16 x i32> undef, <16 x i32> i32 2, i32 3, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx3 = add <16 x i32> %bin.rdx2, %rdx.shuf3
  %rdx.shuf4 = shufflevector <16 x i32> %bin.rdx3, <16 x i32> undef, <16 x i32> i32 1, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef, i32 undef>
  %bin.rdx4 = add <16 x i32> %bin.rdx3, %rdx.shuf4
  %12 = extractelement <16 x i32> %bin.rdx4, i32 0
  ret i32 %12
define void @sbto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbto16f32_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:   vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:   vcvtdq2ps %ymm0, %ymm0
    ; CHECK-NEXT:   vpmovm2d %k0, %ymm1
    ; CHECK-NEXT:   vcvtdq2ps %ymm1, %ymm1
    ; CHECK-NEXT:   vmovaps %ymm1, (%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i16> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @sbto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: sbto16f32_512:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %zmm0, %k0
    ; CHECK-NEXT:   vpmovm2d %zmm0, %zmm0
    ; CHECK-NEXT:   vcvtdq2ps %zmm0, %zmm0
    ; CHECK-NEXT:   vmovaps %zmm0, (%rdi)
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i16> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}

define void @sbto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: sbto16f64_256:
    ; CHECK:       # %bb.0:
    ; CHECK-NEXT:   vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:   kshiftrw $8, %k0, %k1
    ; CHECK-NEXT:   vpmovm2d %k1, %ymm0
    ; CHECK-NEXT:   vcvtdq2pd %xmm0, %xmm0
    ; CHECK-NEXT:   vextracti128 $1, %ymm0, %xmm0
    ; CHECK-NEXT:   vcvtdq2pd %xmm0, %ymm0
    ; CHECK-NEXT:   vmovaps %ymm0, (%rdi)
    ; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm3
    ; CHECK-NEXT:   vcvtdq2pd %xmm2, %ymm2
    ; CHECK-NEXT:   vextracti128 $1, %ymm2, %xmm2
    ; CHECK-NEXT:   vzeroupper
    ; CHECK-NEXT:   retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i16> %mask to <16 x float>
    store <16 x float> %1, <16 x float>* %res
    ret void
}
define void @sbto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {  
    # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
    ; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
    ; CHECK-NEXT:    vmovaps %zmm0, 64(%rdi)
    ; CHECK-NEXT:    vmovaps %zmm1, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = sitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}

define void @ubto16f32_256(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="256" {  
    # %bb.0:
    ; CHECK-NEXT:    vpmovw2m %ymm0, %k0
    ; CHECK-NEXT:    vpmovm2d %k0, %zmm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm1
    ; CHECK-NEXT:    vextracti64x4 $1, %zmm0, %ymm0
    ; CHECK-NEXT:    vcvtdq2pd %ymm0, %zmm0
    ; CHECK-NEXT:    vpmovm2d %zmm0, %zmm1
    ; CHECK-NEXT:    vpmovaps %zmm1, (%rdi)
    ; CHECK-NEXT:    vzeroupper
    ; CHECK-NEXT:    retq
    %mask = icmp slt <16 x i16> %a, zeroinitializer
    %1 = uitofp <16 x i1> %mask to <16 x double>
    store <16 x double> %1, <16 x double>* %res
    ret void
}
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f32_512(<16 x i16> %a, <16 x float>* %res) "min-legal-vector-width"="512" {
; CHECK-LABEL: ubto16f32_512:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: vpmovm2d %k0, %zmm0
; CHECK-NEXT: vpsrld $31, %zmm0, %zmm0
; CHECK-NEXT: vcvtdq2ps %zmm0, %zmm0
; CHECK-NEXT: vmovaps %zmm0, (%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x float>
store <16 x float> %1, <16 x float>* %res
ret void
}

define void @ubto16f64_256(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="256" {
; CHECK-LABEL: ubto16f64_256:
; CHECK: # %bb.0:
; CHECK-NEXT: vpmovw2m %ymm0, %k0
; CHECK-NEXT: kshiftrw $8, %k0, %k1
; CHECK-NEXT: vpmovm2d %k1, %ymm0
; CHECK-NEXT: vpsrld $31, %ymm0, %ymm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm1
; CHECK-NEXT: vextracti128 $1, %ymm0, %xmm0
; CHECK-NEXT: vcvtdq2pd %xmm0, %ymm0
; CHECK-NEXT: vpmovm2d %k0, %ymm2
; CHECK-NEXT: vpsrld $31, %ymm2, %ymm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm3
; CHECK-NEXT: vextracti128 $1, %ymm2, %xmm2
; CHECK-NEXT: vcvtdq2pd %xmm2, %ymm2
; CHECK-NEXT: vmovaps %ymm2, 32(%rdi)
; CHECK-NEXT: vmovaps %ymm3, 64(%rdi)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%mask = icmp slt <16 x i16> %a, zeroinitializer
%1 = uitofp <16 x i1> %mask to <16 x double>
store <16 x double> %1, <16 x double>* %res
ret void
}

define void @ubto16f64_512(<16 x i16> %a, <16 x double>* %res) "min-legal-vector-width"="512" {

define <16 x i16> @test_16f32toub_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {  
    ; CHECK-LABEL: test_16f32toub_256:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:   vcvttps2dq (%rdi), %ymm1  
    ; CHECK-NEXT:   vpslld S31, %ymm1, %ymm1  
    ; CHECK-NEXT:   vpmovd2m %ymm1, %k0  
    ; CHECK-NEXT:   vcvttps2dq 32(%rdi), %ymm1  
    ; CHECK-NEXT:   vpslld S31, %ymm1, %ymm1  
    ; CHECK-NEXT:   vpmovd2m %ymm1, %k1  
    ; CHECK-NEXT:   kunpckbw %k0, %k1, %k1  
    ; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 {%k1} {z}  
    ; CHECK-NEXT:   retq  
    %a = load <16 x float>, <16 x float>* %ptr  
    %mask = fptoui <16 x float> %a to <16 x i1>  
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer  
    ret <16 x i16> %select  
}  

define <16 x i16> @test_16f32toub_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {  
    ; CHECK-LABEL: test_16f32toub_512:  
    ; CHECK:       # %bb.0:  
    ; CHECK-NEXT:   vcvttps2dq (%rdi), %zmm1  
    ; CHECK-NEXT:   vpslld S31, %zmm1, %zmm1  
    ; CHECK-NEXT:   vpmovd2m %zmm1, %k1  
    ; CHECK-NEXT:   vmovdqu16 %ymm0, %ymm0 {%k1} {z}  
    ; CHECK-NEXT:   retq  
    %a = load <16 x float>, <16 x float>* %ptr  
    %mask = fptoui <16 x float> %a to <16 x i1>  

define <16 x i16> @test_16f32tosb_256(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: test_16f32tosb_256:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT:  vcvtpps2dq (%rdi), %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k0
    ; CHECK-NEXT:  vcvtpps2dq 32(%rdi), %ymm1
    ; CHECK-NEXT:  vpmovd2m %ymm1, %k1
    ; CHECK-NEXT:  kunpckbw %k0, %k1, %k1
    ; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
    ; CHECK-NEXT:  retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define <16 x i16> @test_16f32tosb_512(<16 x float>* %ptr, <16 x i16> %passthru) "min-legal-vector-width"="512" {
    ; CHECK-LABEL: test_16f32tosb_512:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT:  vcvtpps2dq (%rdi), %zmm1
    ; CHECK-NEXT:  vpmovd2m %zmm1, %k1
    ; CHECK-NEXT:  vmovdqu16 %ymm0, %ymm0 {%k1} {z}
    ; CHECK-NEXT:  retq
    %a = load <16 x float>, <16 x float>* %ptr
    %mask = fptosi <16 x float> %a to <16 x i1>
    %select = select <16 x i1> %mask, <16 x i16> %passthru, <16 x i16> zeroinitializer
    ret <16 x i16> %select
}

define void @mul256(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="256" {
    ; CHECK-LABEL: mul256:
    ; CHECK: # %bb.0:
    ; CHECK-NEXT:  vmovdqa (%rdi), %ymm0
    ; CHECK-NEXT:  vmovdqa 32(%rdi), %ymm1
    ; CHECK-NEXT:  vmovdqa (%rsi), %ymm2
    ; CHECK-NEXT:  vmovdqa 32(%rsi), %ymm3
    ; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm4 =
    ymm2[8], ymm0[8], ymm2[9], ymm0[9], ymm2[10], ymm0[10], ymm2[11], ymm0[11], ymm2[12], ymm0[12], ymm2[13]
    , ymm0[13], ymm2[14], ymm0[14], ymm2[15], ymm0[15], ymm2[24], ymm0[24], ymm2[25], ymm0[25], ymm2[26], ymm
    0[26], ymm2[27], ymm0[27], ymm2[28], ymm0[28], ymm2[29], ymm0[29], ymm2[30], ymm0[30], ymm2[31], ymm0[31]
    ; CHECK-NEXT:  vpunpckhbw {{.*#+}} ymm5 =
ymm0[8,8,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31]
; CHECK-NEXT: vpmullw %ymm4, %ymm5, %ymm4
; CHECK-NEXT: vmovdqa {.*#+} ymm5 =
; CHECK-NEXT: vpand %ymm5, %ymm4, %ymm4
; CHECK-NEXT: vpunpcklbw {.*#+} ymm2 =
ymm2[0],ymm0[0],ymm2[1],ymm0[1],ymm2[2],ymm0[2],ymm2[3],ymm0[3],ymm2[4],ymm0[4],ymm2[5],ymm0[5],ymm2[6],ymm0[6],ymm2[7],ymm0[7],ymm2[16],ymm0[16],ymm2[17],ymm0[17],ymm2[18],ymm0[18],ymm2[19],ymm0[19],ymm2[20],ymm0[20],ymm2[21],ymm0[21],ymm2[22],ymm0[22],ymm2[23],ymm0[23]
; CHECK-NEXT: vpunpcklbw {.*#+} ymm0 =
ymm0[0,0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,17,17,18,18,19,19,20,20,21,21,22,22,23,23]
; CHECK-NEXT: vpmullw %ymm2, %ymm0, %ymm0
; CHECK-NEXT: vpand %ymm5, %ymm0, %ymm0
; CHECK-NEXT: vpackuswb %ymm4, %ymm0, %ymm0
; CHECK-NEXT: vpunpckhbw {.*#+} ymm2 =
ymm3[8],ymm0[8],ymm3[9],ymm0[9],ymm3[10],ymm0[10],ymm3[11],ymm0[11],ymm3[12],ymm0[12],ymm3[13],ymm0[13],ymm3[14],ymm0[14],ymm3[15],ymm0[15],ymm3[24],ymm0[24],ymm3[25],ymm0[25],ymm3[26],ymm0[26],ymm3[27],ymm0[27],ymm3[28],ymm0[28],ymm3[29],ymm0[29],ymm3[30],ymm0[30],ymm3[31],ymm0[31]
; CHECK-NEXT: vpunpckhbw {.*#+} ymm4 =
ymm1[8],ymm0[8],ymm1[9],ymm0[9],ymm1[10],ymm0[10],ymm1[11],ymm0[11],ymm1[12],ymm0[12],ymm1[13],ymm0[13],ymm1[14],ymm0[14],ymm1[15],ymm0[15],ymm1[24],ymm0[24],ymm1[25],ymm0[25],ymm1[26],ymm0[26],ymm1[27],ymm0[27],ymm1[28],ymm0[28],ymm1[29],ymm0[29],ymm1[30],ymm0[30],ymm1[31],ymm0[31]
; CHECK-NEXT: vpmullw %ymm2, %ymm4, %ymm2
; CHECK-NEXT: vpand %ymm5, %ymm2, %ymm2
; CHECK-NEXT: vpunpcklbw {.*#+} ymm3 =
ymm3[0],ymm0[0],ymm3[1],ymm0[1],ymm3[2],ymm0[2],ymm3[3],ymm0[3],ymm3[4],ymm0[4],ymm3[5],ymm0[5],ymm3[6],ymm0[6],ymm3[7],ymm0[7],ymm3[16],ymm0[16],ymm3[17],ymm0[17],ymm3[18],ymm0[18],ymm3[19],ymm0[19],ymm3[20],ymm0[20],ymm3[21],ymm0[21],ymm3[22],ymm0[22],ymm3[23],ymm0[23]
; CHECK-NEXT: vpunpcklbw {.*#+} ymm1 =
ymm1[0],ymm0[0],ymm1[1],ymm0[1],ymm1[2],ymm0[2],ymm1[3],ymm0[3],ymm1[4],ymm0[4],ymm1[5],ymm0[5],ymm1[6],ymm0[6],ymm1[7],ymm0[7],ymm1[16],ymm0[16],ymm1[17],ymm0[17],ymm1[18],ymm0[18],ymm1[19],ymm0[19],ymm1[20],ymm0[20],ymm1[21],ymm0[21],ymm1[22],ymm0[22],ymm1[23],ymm0[23]
; CHECK-NEXT: vpmullw %ymm3, %ymm1, %ymm1
; CHECK-NEXT: vpand %ymm5, %ymm1, %ymm1
; CHECK-NEXT: vpackuswb %ymm2, %ymm1, %ymm1
; CHECK-NEXT: vmovdqa %ymm1, 32(%rdx)
; CHECK-NEXT: vmovdqa %ymm0, (%rdx)
; CHECK-NEXT: vzeroupper
; CHECK-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}
define void @mul512(<64 x i8>* %a, <64 x i8>* %b, <64 x i8>* %c) "min-legal-vector-width"="512"{
  CHECK-LABEL: mul512:
  CHECK:    # %bb.0:
  CHECK-NEXT: vmovdqa64 (%rdi), %zmm0
  CHECK-NEXT: vmovdqa64 (%rsi), %zmm1
  CHECK-NEXT: vpunpckhbw {{.*#+}} zmm2 = zmm1[8],zmm0[8],zmm1[9],zmm0[9],zmm1[10],zmm0[10],zmm1[11],zmm0[11],zmm1[12],zmm0[12],zmm1[13],zmm0[13],zmm1[14],zmm0[14],zmm1[15],zmm0[15],zmm1[24],zmm0[24],zmm1[25],zmm0[25],zmm1[26],zmm0[26],zmm1[27],zmm0[27],zmm1[28],zmm0[28],zmm1[29],zmm0[29],zmm1[30],zmm0[30],zmm1[31],zmm0[31],zmm1[40],zmm0[40],zmm1[41],zmm0[41],zmm1[42],zmm0[42],zmm1[43],zmm0[43],zmm1[44],zmm0[44],zmm1[45],zmm0[45],zmm1[46],zmm0[46],zmm1[47],zmm0[47],zmm1[56],zmm0[56],zmm1[57],zmm0[57],zmm1[58],zmm0[58],zmm1[59],zmm0[59],zmm1[60],zmm0[60],zmm1[61],zmm0[61],zmm1[62],zmm0[62],zmm1[63],zmm0[63]
  CHECK-NEXT: vpunpckhbw {{.*#+}} zmm3 = zmm0[8,9,9,9,10,10,11,11,12,12,13,13,14,14,15,15,24,24,25,25,26,26,27,27,28,28,29,29,30,30,31,31,40,40,41,41,4,2,42,43,43,44,44,45,45,46,46,47,47,56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63]
  CHECK-NEXT: vpmullw %zmm2, %zmm3, %zmm2
  CHECK-NEXT: vpandq %zmm3, %zmm0
  CHECK-NEXT: vpunpcklbw {{.*#+}} zmm1 = zmm1[0],zmm0[0],zmm1[1],zmm0[1],zmm1[2],zmm0[2],zmm1[3],zmm0[3],zmm1[4],zmm0[4],zmm1[5],zmm0[5],zmm1[6],zmm0[6],zmm1[7],zmm0[7],zmm1[16],zmm0[16],zmm1[17],zmm0[17],zmm1[18],zmm0[18],zmm1[19],zmm0[19],zmm1[20],zmm0[20],zmm1[21],zmm0[21],zmm1[22],zmm0[22],zmm1[23],zmm0[23],zmm1[32],zmm0[32],zmm1[33],zmm0[33],zmm1[34],zmm0[34],zmm1[35],zmm0[35],zmm1[36],zmm0[36],zmm1[37],zmm0[37],zmm1[38],zmm0[38],zmm1[39],zmm0[39],zmm1[48],zmm0[48],zmm1[49],zmm0[49],zmm1[50],zmm0[50],zmm1[51],zmm0[51],zmm1[52],zmm0[52],zmm1[53],zmm0[53],zmm1[54],zmm0[54],zmm1[55],zmm0[55]
  CHECK-NEXT: vpunpcklbw {{.*#+}} zmm0 = zmm0[0,1,1,2,2,3,3,4,4,5,5,6,6,7,7,16,16,16,17,18,18,19,19,20,20,20,21,21,22,22,23,23,32,32,33,33,34,34,35,35,36,36,36,37,37,38,38,39,39,48,48,49,49,50,50,51,51,52,52,53,53,54,54,55,55]
  CHECK-NEXT: vpmullw %zmm1, %zmm0, %zmm0
  CHECK-NEXT: vpandq %zmm3, %zmm0
  CHECK-NEXT: vpackuswb %zmm2, %zmm0, %zmm0
  CHECK-NEXT: vzeroupper
  CHECK-NEXT: retq
%d = load <64 x i8>, <64 x i8>* %a
%e = load <64 x i8>, <64 x i8>* %b
%f = mul <64 x i8> %d, %e
store <64 x i8> %f, <64 x i8>* %c
ret void
}

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Developed by:

LLVM Team

University of Illinois at Urbana-Champaign

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; NOTE: Assertions have been autogenerated by utils/update_analyze_test_checks.py
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx2 | FileCheck %s --check-prefixes=CHECK,VEC256,AVX
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,+prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC256,SKX256
; RUN: opt < %s  -cost-model -analyze -mtriple=x86_64-apple-macosx10.8.0 -mattr=+avx512vl,+avx512bw,+avx512dq,-prefer-256-bit | FileCheck %s --check-prefixes=CHECK,VEC512

define void @zext256() "min-legal-vector-width"="256" {  
 ; VEC256-LABEL: 'zext256'  
 ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>  
 ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>  
 ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>  
 ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>  
 ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>  
 ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void  
 ;  
 ; VEC512-LABEL: 'zext256'  
 ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>  
 ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x
define void @zext512() "min-legal-vector-width"="512" {  
    ; AVX-LABEL: 'zext512'
    ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    ; SKX256-LABEL: 'zext512'
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
    
    ; VEC512-LABEL: 'zext512'
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %A = zext <8 x i16> undef to <8 x i64>
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = zext <8 x i32> undef to <8 x i64>
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = zext <16 x i8> undef to <16 x i32>
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = zext <16 x i16> undef to <16 x i32>
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = zext <32 x i8> undef to <32 x i16>
    ; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
}

; Open Source Used In Cisco Web Security Appliances AsyncOS 12.5  2838
define void @sext256() "min-legal-vector-width"="256" {
  ; VEC256-LABEL: 'sext256'
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; VEC256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; VEC512-LABEL: 'sext256'
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <8 x i32> undef to <8 x i64>
  %D = sext <16 x i8> undef to <16 x i32>
  %E = sext <16 x i16> undef to <16 x i32>
}
%F = sext <32 x i8> undef to <32 x i16>
ret void
}

define void @sext512() "min-legal-vector-width"="512" {
  ; AVX-LABEL: 'sext512'
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 7 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 4 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 3 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; AVX-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; SKX256-LABEL: 'sext512'
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; SKX256-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; VEC512-LABEL: 'sext512'
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  ; VEC512-LABEL: 'sext512'
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 24 for instruction: %A = sext <8 x i8> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %B = sext <8 x i16> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %C = sext <8 x i32> undef to <8 x i64>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %D = sext <16 x i8> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %E = sext <16 x i16> undef to <16 x i32>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 1 for instruction: %F = sext <32 x i8> undef to <32 x i16>
  ; VEC512-NEXT: Cost Model: Found an estimated cost of 0 for instruction: ret void
  ;
  %A = sext <8 x i8> undef to <8 x i64>
  %B = sext <8 x i16> undef to <8 x i64>
  %C = sext <8 x i32> undef to <8 x i64>
  %D = sext <16 x i8> undef to <16 x i32>
%E = sext <16 x i16> undef to <16 x i32>
%F = sext <32 x i8> undef to <32 x i16>

ret void
}

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====================

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Developed by:

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; RUN: llc < %s -mtriple=s390x-linux-gnu -mcpu=zEC12 -verify-machineinstrs | FileCheck %s
;
; Test that early if conversion produces LOCR with operands of the right register classes.

define void @autogen_SD4739(i8*) {  
; CHECK-NOT: Expected a GR32Bit register, but got a GRX32Bit register
BB:
  %L34 = load i8, i8* %0
  %Cmp56 = icmp sgt i8 undef, %L34
  br label %CF246

CF246:
  ; preds = %CF246, %BB
  %SI163 = select i1 %Cmp56, i8 %L34, i8 undef
  br i1 undef, label %CF246, label %CF248

CF248:
  ; preds = %CF248, %CF246
  store i8 %SI163, i8* %0
  br label %CF248
1.323 slf4j 1.7.5

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