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   1.231.1 Available under license
1.232 tunctl 1.5 :r0
   1.232.1 Available under license
1.233 tzdata 2010l-1
   1.233.1 Available under license
1.234 udev 162-2
   1.234.1 Available under license
1.235 udev 182.0 :r9.4.6
   1.235.1 Available under license
1.236 update-rc.d 0.7 :r5
   1.236.1 Available under license
1.237 upstart 0.6.6-3
   1.237.1 Available under license
1.238 usbutils 7.0 :r0
   1.238.1 Available under license
1.239 usbutils 0.87-4
   1.239.1 Available under license
1.240 ustr 1.0.4 :r2.1
   1.240.1 Available under license
1.241 util-linux 2.17.2-0ubuntu1
   1.241.1 Available under license
1.242 util-linux 2.26.2 :r0.3
   1.242.1 Available under license
1.243 v86d 0.1.10 :r2
   1.243.1 Available under license
1.244 xkb-data 1.8-1ubuntu8
   1.244.1 Available under license
1.245 xz-utils 4.999.9beta+20100527-1
   1.245.1 Available under license
1.246 yajl 2.1.0 :r0.1
   1.246.1 Available under license
1.247 zlib 1.2.8 :r0
   1.247.1 Available under license
1.248 zlib1g 1:1.2.3.4.dfsg-3ubuntu1
1.1 acl 2.2.52 :r0

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1.9 base-files 5.0.0ubuntu23

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   (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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1.10 base-files 3.0.14 :r89.1

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The reason we have a separate public license for some libraries is that
they blur the distinction we usually make between modifying or adding to a
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changing the library, is in some sense simply using the library, and is
analogous to running a utility program or application program. However, in
a textual and legal sense, the linked executable is a combined work, a
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treats it as such.

Because of this blurred distinction, using the ordinary General
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sharing, because most developers did not use the libraries. We
concluded that weaker conditions might promote sharing better.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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I would like to thank the following people (in alphabetical order):

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If I've missed anyone, it's due to oversight. Drop me a line and I'll rectify the situation as quickly as possible.

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1.17 bsdutils 1:2.17.2-0ubuntu1

1.17.1 Available under license:

size: 8388608, sector size: 512, PT: dos, offset: 446

---

#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)

size: 4456448, sector size: 512, PT: bsd, offset: 512

---

#1: 7936 4864 0x7
#2: 12544 3584 0x7

BZh91AY&SY†~@H R h% CFTC@OQ6_@
tJmAHl11/F0HA]BAk@
BZh91AY&SY'hC+{HyL*UT{JDCCMhd?TPJ
4b'LLLD3M0fh
4JM&hC&A
4h
P44QP4&FCFz()"{W">CN
@JEBx=Q
\%HFYP@R "o"="U*R
)Cbw"XZTKsO%"ZjSi!~9%m/cB7D4F"N7ro2&HLP\p9p)N\a~?<h9Q@++CvT
]0U"h-Jl[Z(ekZmkzP[@0@l6YlSTK6jSi}@SS @$ II$/$ U$ $I SI S$ $I
ISIS1c!]$TA'PPPPUW]jU@UYVUUU-U-mW[UeKjHR
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#XZ":{nl} }@?Sx(8HXo<@@>bdo>6BFJNRVZ^8y
Tb))i+j,k,l-D
y{}/^FH:ZzZ;Cv}BBAISISISISISB<A<8aU\J7UUUU3333330ISISISPur8@>>&}@ISISIS~M#q'lx+[jVZ:a
H-k\WULR\&lVl;]Hs1p07R@mb\b.IEE2m"DSyP(Ejwq_X5u IBgXH mr+]S^QrdC ;1JU@@-h@1C-1^A5u -
7@\Ei.E7q\DP0.5:
}@9@/@/Di.p

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This is the Debian GNU/Linux prepackaged version of util-linux.

This package was put together by Guy Maor <maor@debian.org> and later maintained by Sean 'Shaleh' Perry <shaleh@debian.org>, and Adrian Bunk <bunk@stusta.de>.

The current maintainer is LaMont Jones <lamont@debian.org>.
See also: git://git.debian.org/~lamont/util-linux.git
It was downloaded from:

Upstream maintainers include:
Maintainer: Karel Zak <kzak@redhat.com>
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This is libblkid, previously part of e2fsprogs this is now part of
util-linux-ng and has thus moved to the util-linux Debian source
package.

Upstream Author: Theodore Ts'o <tytso@mit.edu>

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jseward@bzip.org

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1.21 ca-certificates 2.0150426E7 :r0

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1.26 coreutils 8.5-1ubuntu3

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1.46 e2fsprogs 1.42.9 :r0

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Let no one say political correctness isn't dead....

```c
#ifndef _ss_h
#define _ss_h __FILE__

#include <ss/ss_err.h>

#define __SS_CONST const
#define __SS_PROTO (int, const char * const *, int, void *)

#ifdef __GNUC__
#define __SS_ATTR(x) __attribute__(x)
#else
#define __SS_ATTR(x)
#endif

typedef __SS_CONST struct _ss_request_entry {
  __SS_CONST char * __SS_CONST *command_names; /* whatever */
  void (*) __SS_PROTO (const char * const *, int, void *)
  __SS_CONST char * __SS_CONST info_string; /* NULL */
  int flags; /* 0 */
} ss_request_entry;

typedef __SS_CONST struct _ss_request_table {
  int version;
  ss_request_entry *requests;
} ss_request_table;

#define SS_RQT_TBL_V2 2

typedef struct _ss_rp_options { /* DEFAULT VALUES */
  int version; /* SS_RP_V1 */
  void (*) __SS_PROTO (const char * const *, int, void *)
  int allow_suspend;
  int catch_int;
} ss_rp_options;

#define SS_RP_V1 1

#define SS_OPT_DONT_LIST 0x0001
#define SS_OPT_DONT_SUMMARIZE 0x0002
```
void ss_help __SS_PROTO;
#if 0
char *ss_current_request(); /* This is actually a macro */
#endif

char *ss_name(int sci_idx);
void ss_error (int, long, char const *, ...)
__SS_ATTR((format(printf, 3, 4)));
void ss_perror (int, long, char const *);

int ss_create_invocation(const char *, const char *, void *,
    ss_request_table *, int *);
void ss_delete_invocation(int);
int ss_listen(int);
int ss_execute_line(int, char *);
void ss_add_request_table(int, ss_request_table *, int, int *);
void ss_delete_request_table(int, ss_request_table *, int *);
void ss_abort_subsystem(int sci_idx, int code);
void ss_quit(int argc, const char * const *argv, int sci_idx, void *infop);
void ss_self_identify(int argc, const char * const *argv, int sci_idx, void *infop);
void ss_subsystem_name(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_subsystem_version(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_unimplemented(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_set_prompt(int argc, const char * const *argv,
    int sci_idx, void *infop);
char *ss_get_prompt(int sci_idx);
void ss_get_readline(int sci_idx);
char *ss_safe_getenv(const char *arg);
extern ss_request_table ss_std_requests;
#endif /* _ss_h */

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Index: tdbsa/tdb.c
===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
*/
/**
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
- Copyright (C) Andrew Tridgell 2005
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#
# This is a Makefile stub which handles the creation of BSD shared
# libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo " MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image: $(BSD_LIB)

$(BSD_LIB): $(OBS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS) $(OBS))
$(MV) pic/$(BSD_LIB) .
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) \

`echo $(my_dir) | sed -e 's;lib/;;' /$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) \ $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)

install-strip: install

install-shlibs-strip: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)

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Gadi Oxman, August 1995

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```
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```
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It is part of the main e2fsprogs distribution, which can be found at:

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Theodore Ts'o
23-June-2007

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1.48 eee-acpi-scripts 1.1.12+git0+9d4cdedca2
:ro

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1.53 findutils 4.4.2-1ubuntu1

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/* Perl-Compatible Regular Expressions */
******************************************************************************/

/* This is the public header file for the PCRE library, to be #included by
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******************************************************************************/

/*

#ifndef _PCRE_H
#define _PCRE_H

#endif

#include <PCRE.h>

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/* The current PCRE version information. */

#define PCRE_MAJOR          8
#define PCRE_MINOR          31
#define PCRE_PRERELEASE
#define PCRE_DATE           2012-07-06

/* When an application links to a PCRE DLL in Windows, the symbols that are imported have to be identified as such. When building PCRE, the appropriate export setting is defined in pcre_internal.h, which includes this file. So we don't change existing definitions of PCRE_EXPDECL and PCRECDECL. */

#if defined(_WIN32) && !defined(PCRE_STATIC)
    ifndef PCRE_EXPDECL
        define PCRE_EXPDECL extern __declspec(dllimport)
    endif
    ifndef __cplusplus
        ifndef PCRECDECL
            define PCRECDECL extern __declspec(dllimport)
        endif
        ifndef PCRECDEFN
            define PCRECDEFN __declspec(dllimport)
        endif
    endif
#endif

/* By default, we use the standard "extern" declarations. */

#ifndef PCRE_EXPDECL
    ifndef __cplusplus
        define PCRE_EXPDECL extern
    endif
#endif

#ifndef __cplusplus
    ifndef PCRECDECL
        define PCRECDECL extern \
    endif
#endif

#ifndef __cplusplus
    ifndef PCRECDEFN
        define PCRECDEFN 
    endif
#endif

#ifndef __cplusplus
    ifndef PCRECDEFN
        define PCRECDEFN
    endif
#endif

/* Have to include stdlib.h in order to ensure that size_t is defined; it is needed here for malloc. */
/* Allow for C++ users */

#ifdef __cplusplus
extern "C" {
#endif

/* Options. Some are compile-time only, some are run-time only, and some are both, so we keep them all distinct. However, almost all the bits in the options word are now used. In the long run, we may have to re-use some of the compile-time only bits for runtime options, or vice versa. In the comments below, "compile", "exec", and "DFA exec" mean that the option is permitted to be set for those functions; "used in" means that an option may be set only for compile, but is subsequently referenced in exec and/or DFA exec. Any of the compile-time options may be inspected during studying (and therefore JIT compiling). */

#define PCRE_CASELESS 0x00000001 /* Compile */
#define PCRE_MULTILINE 0x00000002 /* Compile */
#define PCRE_DOTALL 0x00000004 /* Compile */
#define PCRE_EXTENDED 0x00000008 /* Compile */
#define PCRE_ANCHORED 0x00000010 /* Compile, exec, DFA exec */
#define PCRE_DOLLAR_ENDONLY 0x00000020 /* Compile, used in exec, DFA exec */
#define PCRE_EXTRA 0x00000040 /* Compile */
#define PCRE_NOTBOL 0x00000080 /* Exec, DFA exec */
#define PCRE_NOTEOL 0x00000100 /* Exec, DFA exec */
#define PCRE_UNGREEDY 0x00000200 /* Compile */
#define PCRE_NOTEMPTY 0x00000400 /* Exec, DFA exec */
/* The next two are also used in exec and DFA exec */
#define PCRE_UTF8 0x00000800 /* Compile (same as PCRE_UTF16) */
#define PCRE_UTF16 0x00000800 /* Compile (same as PCRE_UTF8) */
#define PCRE_NO_AUTO_CAPTURE 0x00001000 /* Compile */
/* The next two are also used in exec and DFA exec */
#define PCRE_NO_UTF8_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF16_CHECK) */
#define PCRE_NO_UTF16_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF8_CHECK) */
#define PCRE_AUTO_CALLOUT 0x00004000 /* Compile */
#define PCRE_PARTIAL_SOFT 0x00008000 /* Exec, DFA exec */
#define PCRE_PARTIAL 0x00008000 /* Backwards compatible synonym */
#define PCRE_DFA_SHORTEST 0x00010000 /* DFA exec */
#define PCRE_DFA_RESTART 0x00020000 /* DFA exec */
#define PCRE_FIRSTLINE 0x00040000 /* Compile, used in exec, DFA exec */
#define PCRE_DUPNAMES 0x00080000 /* Compile */
#define PCRE_NEWLINE_CR 0x00010000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_LF 0x00020000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_CRLF 0x00030000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_ANY 0x00040000 /* Compile, exec, DFA exec */
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#define PCRE_NEWLINE_ANYCRLF 0x00500000 /* Compile, exec, DFA exec */
#define PCRE_BSR_ANYCRLF 0x00800000 /* Compile, exec, DFA exec */
#define PCRE_BSR_UNICODE 0x01000000 /* Compile, exec, DFA exec */
#define PCRE_JAVASCRIPT_COMPAT 0x02000000 /* Compile, used in exec */
#define PCRE_NO_START_OPTIMIZE 0x04000000 /* Compile, exec, DFA exec */
#define PCRE_NO_START_OPTIMISE 0x04000000 /* Synonym */
#define PCRE_PARTIAL_HARD 0x08000000 /* Exec, DFA exec */
#define PCRE_NOTEMPTY_ATSTART 0x10000000 /* Exec, DFA exec */
#define PCRE_UCP 0x20000000 /* Compile, used in exec, DFA exec */

/* Exec-time and get/set-time error codes */
#define PCRE_ERROR_NOMATCH (-1)
#define PCRE_ERROR_NULL (-2)
#define PCRE_ERROR_BADOPTION (-3)
#define PCRE_ERROR_BADMAGIC (-4)
#define PCRE_ERROR_UNKNOWN_OPCODE (-5)
#define PCRE_ERROR_UNKNOWN_NODE (-5) /* For backward compatibility */
#define PCRE_ERROR_NOMEMORY (-6)
#define PCRE_ERROR_NOSUBSTRING (-7)
#define PCRE_ERROR_MATCHLIMIT (-8)
#define PCRE_ERROR_CALLOUT (-9) /* Never used by PCRE itself */
#define PCRE_ERROR_BADUTF8 (-10) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF16 (-10) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF8_OFFSET (-11) /* Same for 8/16 */
#define PCRE_ERROR_BADUTF16_OFFSET (-11) /* Same for 8/16 */
#define PCRE_ERROR_PARTIAL (-12)
#define PCRE_ERROR_BADPARTIAL (-13)
#define PCRE_ERROR_INTERNAL (-14)
#define PCRE_ERROR_BADCOUNT (-15)
#define PCRE_ERROR_DFA_UITEM (-16)
#define PCRE_ERROR_DFA_UCOND (-17)
#define PCRE_ERROR_DFA_UMLIMIT (-18)
#define PCRE_ERROR_DFA_WSSIZE (-19)
#define PCRE_ERROR_DFA_RECURSE (-20)
#define PCRE_ERROR_RECURSIONLIMIT (-21)
#define PCRE_ERROR_NULLWSLIMIT (-22) /* No longer actually used */
#define PCRE_ERROR_BADNEWLINE (-23)
#define PCRE_ERROR_BADOFFSET (-24)
#define PCRE_ERROR_SHORTUTF8 (-25)
#define PCRE_ERROR_SHORTUTF16 (-25) /* Same for 8/16 */
#define PCRE_ERROR_RECURSELOOP (-26)
#define PCRE_ERROR_JIT_STACKLIMIT (-27)
#define PCRE_ERROR_BADMODE (-28)
#define PCRE_ERROR_BADENDIANNESS (-29)
#define PCRE_ERROR_DFA_BADRESTART (-30)

/* Specific error codes for UTF-8 validity checks */
#define PCRE_UTF8_ERR0 0
#define PCRE_UTF8_ERR1 1
#define PCRE_UTF8_ERR2 2
#define PCRE_UTF8_ERR3 3
#define PCRE_UTF8_ERR4 4
#define PCRE_UTF8_ERR5 5
#define PCRE_UTF8_ERR6 6
#define PCRE_UTF8_ERR7 7
#define PCRE_UTF8_ERR8 8
#define PCRE_UTF8_ERR9 9
#define PCRE_UTF8_ERR10 10
#define PCRE_UTF8_ERR11 11
#define PCRE_UTF8_ERR12 12
#define PCRE_UTF8_ERR13 13
#define PCRE_UTF8_ERR14 14
#define PCRE_UTF8_ERR15 15
#define PCRE_UTF8_ERR16 16
#define PCRE_UTF8_ERR17 17
#define PCRE_UTF8_ERR18 18
#define PCRE_UTF8_ERR19 19
#define PCRE_UTF8_ERR20 20
#define PCRE_UTF8_ERR21 21

/* Specific error codes for UTF-16 validity checks */

#define PCRE_UTF16_ERR0 0
#define PCRE_UTF16_ERR1 1
#define PCRE_UTF16_ERR2 2
#define PCRE_UTF16_ERR3 3
#define PCRE_UTF16_ERR4 4

/* Request types for pcre_fullinfo() */

#define PCRE_INFO_OPTIONS 0
#define PCRE_INFO_SIZE 1
#define PCRE_INFO_CAPTURECOUNT 2
#define PCRE_INFO_BACKREFMAX 3
#define PCRE_INFO_FIRSTBYTE 4
#define PCRE_INFO_FIRSTCHAR 4 /* For backwards compatibility */
#define PCRE_INFO_FIRSTTABLE 5
#define PCRE_INFO_LASTLITERAL 6
#define PCRE_INFO_NAMEENTRYSIZE 7
#define PCRE_INFO_NAMECOUNT 8
#define PCRE_INFO_NAMETABLE 9
#define PCRE_INFO_STUDYSIZE 10
#define PCRE_INFO_DEFAULT_TABLES 11
#define PCRE_INFO_OKPARTIAL 12
#define PCRE_INFO_JCHANGED          13
#define PCRE_INFO_HASCRORLF         14
#define PCRE_INFO_MINLENGTH         15
#define PCRE_INFO_JIT               16
#define PCRE_INFO_JITSIZE           17
#define PCRE_INFO_MAXLOOKBEHIND     18

/* Request types for pcre_config(). Do not re-arrange, in order to remain
compatible. */

#define PCRE_CONFIG_UTF8                    0
#define PCRE_CONFIG_NEWLINE                 1
#define PCRE_CONFIG_LINK_SIZE               2
#define PCRE_CONFIG_POSIX_MALLOC_THRESHOLD  3
#define PCRE_CONFIG_MATCH_LIMIT             4
#define PCRE_CONFIG_STACKRECURSE            5
#define PCRE_CONFIG_UNICODE_PROPERTIES      6
#define PCRE_CONFIG_MATCH_LIMIT_RECURSION   7
#define PCRE_CONFIG_BSR                     8
#define PCRE_CONFIG_JIT                     9
#define PCRE_CONFIG_UTF16                  10
#define PCRE_CONFIG_JITTARGET              11

/* Request types for pcre_study(). Do not re-arrange, in order to remain
compatible. */

#define PCRE_STUDY_JIT_COMPILE                0x0001
#define PCRE_STUDY_JIT_PARTIAL_SOFT_COMPILE   0x0002
#define PCRE_STUDY_JIT_PARTIAL_HARD_COMPILE   0x0004

/* Bit flags for the pcre[16]_extra structure. Do not re-arrange or redefine
these bits, just add new ones on the end, in order to remain compatible. */

#define PCRE_EXTRA_STUDY_DATA             0x0001
#define PCRE_EXTRA_MATCH_LIMIT            0x0002
#define PCRE_EXTRA_CALLOUT_DATA           0x0004
#define PCRE_EXTRA_TABLES                 0x0008
#define PCRE_EXTRA_MATCH_LIMIT_RECURSION  0x0010
#define PCRE_EXTRA_MARK                   0x0020
#define PCRE_EXTRA_EXECUTABLE_JIT         0x0040

/* Types */

struct real_pcre; /* declaration; the definition is private */
typedef struct real_pcre pcre;

struct real_pcre16; /* declaration; the definition is private */
typedef struct real_pcre16 pcre16;
struct real_pcre_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre_jit_stack pcre_jit_stack;

struct real_pcre16_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre16_jit_stack pcre16_jit_stack;

/* If PCRE is compiled with 16 bit character support, PCRE_UCHAR16 must contain
a 16 bit wide signed data type. Otherwise it can be a dummy data type since
pcre16 functions are not implemented. There is a check for this in pcre_internal.h. */
#ifndef PCRE_UCHAR16
#define PCRE_UCHAR16 unsigned short
#endif

#ifndef PCRE_SPTR16
#define PCRE_SPTR16 const PCRE_UCHAR16 *
#endif

/* When PCRE is compiled as a C++ library, the subject pointer type can be
replaced with a custom type. For conventional use, the public interface is a
const char *. */
#ifndef PCRE_SPTR
#define PCRE_SPTR const char *
#endif

/* The structure for passing additional data to pcre_exec(). This is defined in
such a way as to be extensible. Always add new fields at the end, in order to
remain compatible. */

typedef struct pcre_extra {
    unsigned long int flags; /* Bits for which fields are set */
    void *study_data; /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data; /* Data passed back in callouts */
    const unsigned char *tables; /* Pointer to character tables */
    unsigned long int match_limit_recursion; /* Max recursive calls to match() */
    unsigned char **mark; /* For passing back a mark pointer */
    void *executable_jit; /* Contains a pointer to a compiled jit code */
} pcre_extra;

/* Same structure as above, but with 16 bit char pointers. */

typedef struct pcre16_extra {
    unsigned long int flags; /* Bits for which fields are set */
    void *study_data; /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data; /* Data passed back in callouts */
} pcre16_extra;
const unsigned char *tables; /* Pointer to character tables */
unsigned long int match_limit_recursion; /* Max recursive calls to match() */
PCRE_UCHAR16 **mark; /* For passing back a mark pointer */
void *executable_jit; /* Contains a pointer to a compiled jit code */
}
pcre16_extra;

/* The structure for passing out data via the pcre_callout_function. We use a
structure so that new fields can be added on the end in future versions,
without changing the API of the function, thereby allowing old clients to work
without modification. */

typedef struct pcre_callout_block {
int version; /* Identifies version of block */
/* ------------------------ Version 0 ------------------------------- */
int callout_number; /* Number compiled into pattern */
int *offset_vector; /* The offset vector */
PCRE_SPTR subject; /* The subject being matched */
int subject_length; /* The length of the subject */
int start_match; /* Offset to start of this match attempt */
int current_position; /* Where we currently are in the subject */
int capture_top; /* Max current capture */
int capture_last; /* Most recently closed capture */
void *callout_data; /* Data passed in with the call */
/* ------------------- Added for Version 1 -------------------------- */
int pattern_position; /* Offset to next item in the pattern */
int next_item_length; /* Length of next item in the pattern */
/* ------------------- Added for Version 2 -------------------------- */
const unsigned char *mark; /* Pointer to current mark or NULL */
/* ------------------------------------------------------------------ */
} pcre_callout_block;

/* Same structure as above, but with 16 bit char pointers. */

typedef struct pcre16_callout_block {
int version; /* Identifies version of block */
/* ------------------------ Version 0 ------------------------------- */
int callout_number; /* Number compiled into pattern */
int *offset_vector; /* The offset vector */
PCRE_SPTR subject; /* The subject being matched */
int subject_length; /* The length of the subject */
int start_match; /* Offset to start of this match attempt */
int current_position; /* Where we currently are in the subject */
int capture_top; /* Max current capture */
int capture_last; /* Most recently closed capture */
void *callout_data; /* Data passed in with the call */
/* ------------------- Added for Version 1 -------------------------- */
int pattern_position; /* Offset to next item in the pattern */
int next_item_length; /* Length of next item in the pattern */
/* ------------------- Added for Version 2 -------------------------- */
const unsigned char *mark; /* Pointer to current mark or NULL */
/* ------------------------------------------------------------------ */
} pcre16_callout_block;
const PCRE_UCHAR16 *mark;  /* Pointer to current mark or NULL */

/* Indirection for store get and free functions. These can be set to
alternative malloc/free functions if required. Special ones are used in the
non-recursive case for "frames". There is also an optional callout function
that is triggered by the (?) regex item. For Virtual Pascal, these definitions
have to take another form. */

#ifndef VPCOMPAT
#define PCRE_EXP_DECL void *(pcre_malloc)(size_t);
#define PCRE_EXP_DECL void  (pcre_free)(void *);
#define PCRE_EXP_DECL void *(pcre_stack_malloc)(size_t);
#define PCRE_EXP_DECL void  (pcre_stack_free)(void *);
#define PCRE_EXP_DECL int   (pcre_callout)(pcre_callout_block *);
#endif  /* VPCOMPAT */

typedef pcre_jit_stack *(pcre_jit_callback)(void *);
typedef pcre16_jit_stack *(pcre16_jit_callback)(void *);

PCRE_EXP_DECL int pcre_compile(const char *, int, const char **, int *, const unsigned char *);
PCRE_EXP_DECL pcre16_compile(PCRE_SPTR16, int, const char **, int *, const unsigned char *);
PCRE_EXP_DECL const unsigned char *pcre_maketables(void);
PCRE_EXP_DECL const unsigned char *pcre16_maketables(void);
PCRE_EXP_DECL int pcre_refcount(pcre *, int);
PCRE_EXP_DECL int pcre16_refcount(pcre16 *, int);
PCRE_EXP_DECL pcre_extra *pcre_study(const pcre *, int, const char **);
PCRE_EXP_DECL pcre16_extra *pcre16_study(const pcre16 *, int, const char **);
PCRE_EXP_DECL void pcre_free_study(pcre_extra *);
PCRE_EXP_DECL void pcre16_free_study(pcre16_extra *);
PCRE_EXP_DECL const char *pcre_version(void);
PCRE_EXP_DECL const char *pcre16_version(void);

/* Utility functions for byte order swaps. */
PCRE_EXP_DECL int pcre_pattern_to_host_byte_order(pcre *, pcre_extra *,
const unsigned char *);
PCRE_EXP_DECL int pcre16_pattern_to_host_byte_order(pcre16 *, pcre16_extra *,
const unsigned char *);
PCRE_EXP_DECL int pcre16_utf16_to_host_byte_order(PCRE_UCHAR16 *,
PCRE_SPTR16, int, int *, int);

/* JIT compiler related functions. */
PCRE_EXP_DECL pcre_jit_stack *pcre_jit_stack_alloc(int, int);
PCRE_EXP_DECL pcre16_jit_stack *pcre16_jit_stack_alloc(int, int);
PCRE_EXP_DECL void pcre_jit_stack_free(pcre_jit_stack *);
PCRE_EXP_DECL void pcre16_jit_stack_free(pcre16_jit_stack *);
PCRE_EXP_DECL void pcre_assign_jit_stack(pcre_extra *,
pcre_jit_callback, void *);
PCRE_EXP_DECL void pcre16_assign_jit_stack(pcre16_extra *,
pcre16_jit_callback, void *);

#ifdef __cplusplus
} /* extern "C" */
#endif
#endif /* End of pcre.h */

PCRE LICENCE

Please see the file LICENCE in the PCRE distribution for licensing details.

End

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   (For example, a function in a library to compute square roots has
a purpose that is entirely well-defined independent of the
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be optional: if the application does not supply it, the square
root function must still compute square roots.)

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1.59 gnutls 3.3.17.1 :r0

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Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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Version 3, 29 June 2007

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

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@c End:
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1.61 grep 2.21 :r0
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1.66 init-ifupdown 1.0 :r7.2
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1.70 initscripts 2.87dsf-4ubuntu18

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integrated it into the base system maintained it until the end of 1995. Miquel van Smoorenburg <miquels@cistron.nl> implemented Debian support. In version 2.85 the package was split into three (sysvinit, sysv-rc, and initscripts) in order to make room for alternative rc mechanisms.

The upstream source is available at:

Primary-Site:   ftp.cistron.nl /pub/people/miquels/software
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setleds.c setmetamode.c setvesablank.c showconsolefont.c
spawn_console.c spawn_login.c
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src/kbd_mode.c, src/loadunimap.c, src/psffontop.c, src/psfstriptable.c,
src/resizecons.c, src/screendump.c, src/setkeycodes.c, src/setleds.c,
src/setmetamode.c, src/setsesablank.c, src/showconsolefont.c,
src/spawn_console.c, src/spawn_login.c, man/man1/chvt.1,
man/man1/deallocvt.1, man/man1/kbd_mode.1, man/man1/setleds.1,
man/man1/setmetamode.1, man/man8/getkeycodes.8, man/man8/loadunimap.8,
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1.78 libacl1 2.2.49-3

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To compute the message digest of a chunk of bytes, declare an MD5Context structure, pass it to MD5Init, call MD5Update as needed on buffers full of bytes, and then call MD5Final, which will fill a supplied 16-byte array with the digest.

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

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**1.86 libc6 2.12.1-0ubuntu6**

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Description library. It is currently distributed together with the EXT2 file
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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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This package was put together by Yann Dirson <dirson@debian.org>,
from sources obtained from a mirror of:
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system utilities (e2fsck, mke2fs, etc.). The EXT2 utilities were
written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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Theodore Ts'o <tytso@mit.edu> on Fri Dec 14 22:24:35 EST 2007

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This is the Debian GNU/Linux prepackaged version of the EXT2 file
system utilities (e2fsck, mke2fs, etc.). The EXT2 utilities were
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Sources were obtained from http://sourceforge.net/projects/e2fsprogs

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That's all there is to it!

#
# This is a Makefile stub which handles the creation of BSD shared
# libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo " MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image: $(BSD_LIB)
$(BSD_LIB): $(OBJS)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(OBJS))
$(RM) -f ../$(BSD_LIB)
(cd ..; $(LN) $(LINK_BUILD_FLAGS) `echo $(my_dir) | sed -e 's;lib/;;'`/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo " INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) \

---

Open Source Used In Cisco IOS 15.6(3)M2

1185
$(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)

install-strip: install

install-shlibs-strip: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)

Index: tdbsa/tdb.c
===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@
Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
 */
/*
- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
-
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1.92 libdbus-1-3 1.4.0-0ubuntu1

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in src/lib/gssapi, including the following files:

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lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kropd_rpc.c
slave/kroplog.c

and marked portions of the following files:

lib/krb5/os/hst_realm.c

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lib/gssapi/mechglue/g_acquire_cred.c
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lib/gssapi/mechglue/g_exp_sec_context.c
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```
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kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
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lib/libgss/g_unseal.c
lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
uts/common/gssapi/include/gssapi_err_generic.h
uts/common/gssapi/include/mechglueP.h

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lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
and the initial implementation of incremental propagation, including
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kadmin/server/ipropd_svc.c
lib/kdb/prop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
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kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
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    lib/gssapi/mechglue/g_accept_sec_context.c
    lib/gssapi/mechglue/g_acquire_cred.c
    lib/gssapi/mechglue/g_canon_name.c
    lib/gssapi/mechglue/g_compare_name.c
    lib/gssapi/mechglue/g_context_time.c
    lib/gssapi/mechglue/g_delete_sec_context.c
    lib/gssapi/mechglue/g_dsp_name.c
    lib/gssapi/mechglue/g_dsp_status.c
    lib/gssapi/mechglue/g_dup_name.c
    lib/gssapi/mechglue/g_exp_sec_context.c
    lib/gssapi/mechglue/g_export_name.c
    lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
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lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_spnego.h
lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplg.c

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lib/krb5/os/hst_realm.c

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lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
@end smallexample
and the initial implementation of incremental propagation, including
the following new or changed files:

```plaintext
@smallexample
include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/krb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c
@end smallexample
```

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```plaintext
@quotation
@ifex
@smallfonts @rm
@end ifex
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@vskip 12pt
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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
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lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
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lib/libgss/g_inquire_names.c
lib/libgss/g_process_context.c
lib/libgss/g_rel_buffer.c
lib/libgss/g_rel_cred.c
lib/libgss/g_rel_name.c
lib/libgss/g_rel_oid_set.c
lib/libgss/g_seal.c
lib/libgss/g_sign.c
lib/libgss/g_store_cred.c
lib/libgss/g_unseal.c
lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
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lib/gssapi/mechglue/g_acquire_cred.c
and the initial implementation of incremental propagation, including the following new or changed files:

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kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
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lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
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lib/gssapi/mechglue/g_exp_sec_context.c
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lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
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lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
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```
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kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproblog.c
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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/etc/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
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Subject: Re: XZ utils for Debian
Date: Sun, 19 Jul 2009 13:28:23 +0300
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1.116 libpam 1.2.1 :r5.3.1

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1.117 libpam-modules 1.1.1-4ubuntu1

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1.119 libpam0g 1.1.1-4ubuntu1

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1.121 libpci3 1:3.1.7-4ubuntu2
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1.123 libpcre 8.38 :r09.1

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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Motorola 68020 20mhz, NetBSD

SSLeay 0.9.0t 29-May-1998
built on Fri Jun 5 12:42:23 EST 1998
options:bn(64,32) md2(char) rc4(idx,int) des(idx,cisc,16,long) idea(int) blowfish(idx)
C flags:gcc -DTERMOIS -O3 -fomit-frame-pointer -Wall -DB_ENDIAN
The 'numbers' are in 1000s of bytes per second processed.

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<th>Type</th>
<th>8 bytes</th>
<th>64 bytes</th>
<th>256 bytes</th>
<th>1024 bytes</th>
<th>8192 bytes</th>
</tr>
</thead>
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<td>2176.00</td>
<td>5994.67</td>
<td>8079.73</td>
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<td>127.31k</td>
<td>209.66k</td>
<td>250.50k</td>
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<td>60.78k</td>
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<td>cast cbc</td>
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<td>0.0774s</td>
<td>1.3</td>
<td>12.9</td>
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<td>1.3</td>
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<td></td>
</tr>
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<th>8192 bytes</th>
</tr>
</thead>
<tbody>
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<td>md2</td>
<td>130.99k</td>
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<td>499.09k</td>
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</tr>
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<td>mdc2</td>
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</tr>
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<td>sha1</td>
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<td>8636.88k</td>
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<td>des ede3</td>
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<td>1562.48k</td>
<td>1562.00k</td>
<td>1563.33k</td>
</tr>
<tr>
<td>idea cbc</td>
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</tr>
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<td>blowfish cbc</td>
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<td>0.0010s</td>
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That's all there is to it!

1.145 libuuid1 2.17.2-0ubuntu1

1.145.1 Available under license:

size: 8388608, sector size: 512, PT: dos, offset: 446

---
#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
---
size: 4456448, sector size: 512, PT: bsd, offset: 512

---
#1: 7936 4864 0x7
#2: 12544 3584 0x7

BZh91AY&SYI'~@H R h% CFTC@OQ6_@
tTjmAH11l/F0HA]BAk@
BZh91AY&SYhC+{HlyL*UT{JDCCMhd?TPJ
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P44OP4&FCFz()"{W ">CN
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Tbji)+j,k,t-D
y{"/FH:ZzZ,Cv)BBaISiISISIBiSI<A<8aU J7UUUU3333330ISiSiSPur8@)}> @ISiISiS-M#:qrlx[@(jVZ:a
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If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be
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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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Firmware provided as part of an XC5000 Linux developers kit by Brian
Mathews <bmathews@xceive.com> to Devin Heitmueller <dheitmueller@linuxtv.org>
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agere_sta_fw.bin -- 9.48 Hermes I
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(If you think you should be in this list, but you are not, drop a
line to the DVB mailing list)

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/* nicstar.c  v0.22  Jawaid Bazyar (bazyar@hypermall.com)
* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
* 
* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
* http://www.hypermall.com/
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
*     interrupts us (except possibly for removal/insertion of the cable?)
* 10/4/97 - began heavy inline documentation of the code. Corrected typos
*     and spelling mistakes.
* 10/5/97 - added code to handle PHY interrupts, disable PHY on
*     loss of link, and correctly re-enable PHY when link is
*     re-established. (put back CFG_PHYIE)
* 
* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
* 
*/
Linux driver for the IDT77201 NICSTAR PCI ATM controller.
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
* see init_nicstar() for PHY initialization to change this. This driver
* expects the Linux ATM stack to support scatter-gather lists
* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
* 
* Implementing minimal-copy of received data:
* IDT always receives data into a small buffer, then large buffers
* as needed. This means that data must always be copied to create
* the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
* Fix is simple: make large buffers large enough to hold entire
* SDU, and leave <small_buffer_data> bytes empty at the start. Then
* copy small buffer contents to head of large buffer.
* Trick is to avoid fragmenting Linux, due to need for a lot of large
* buffers. This is done by 2 things:
* 1) skb->destructor / skb->atm.recycle_buffer
*    combined, allow nicstar_free_rx_skb to be called to
*    recycle large data buffers
* 2) skb_clone of received buffers
* See nicstar_free_rx_skb and linearize_buffer for implementation
* details.
* 
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* 
* M. Welsh, 6 July 1996
* 
*/

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for the or51211 and or51132 frontend drivers, and
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single nxt200x frontend driver.

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/* nicstar.c  v0.22  Jawaid Bazyar (bazyar@hypermall.com) */
/* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk) */
/* */
/* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc. */
/* http://www.hypermall.com/ */
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
  interrupts us (except possibly for removal/insertion of the cable?)
* 10/4/97 - began heavy inline documentation of the code. Corrected typos
  and spelling mistakes.
* 10/5/97 - added code to handle PHY interrupts, disable PHY on
  loss of link, and correctly re-enable PHY when link is
  re-established. (put back CFG_PHYIE)
*  
  Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
*  
  R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
*  
  Linux driver for the IDT77201 NICStAR PCI ATM controller.
*  PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
  see init_nicstar() for PHY initialization to change this. This driver
  expects the Linux ATM stack to support scatter-gather lists
  (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
*  
  Implementing minimal-copy of received data:
  *  IDT always receives data into a small buffer, then large buffers
  *  as needed. This means that data must always be copied to create
  *  the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
  *  Fix is simple: make large buffers large enough to hold entire
  *  SDU, and leave <small_buffer_data> bytes empty at the start. Then
  *  copy small buffer contents to head of large buffer.
  *  Trick is to avoid fragmenting Linux, due to need for a lot of large
  *  buffers. This is done by 2 things:
  *  1) skb->destructor / skb->atm.recycle_buffer
  *     combined, allow nicstar_free_rx_skb to be called to
  *     recycle large data buffers
  *  2) skb_clone of received buffers
  *  See nicstar_free_rx_skb and linearize_buffer for implementation
  *  details.
  
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M. Welsh, 6 July 1996

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Here is a list of the main contributors to lm-sensors version 3.

* Frodo Looijaard
  Original author of libsensors, sensors-detect, sensors and isadump.
* Merlin Hughes
  Original author of sensord.
* Bob Schlaermann
  Dynamic chip feature detection (a.k.a. generic chip support) in libsensors and sensors.
* Mark M. Hoffman
  Many improvements to the libsensors configuration file scanner.
* Jean Delvare
  New libsensors API, and migration of sensors and sensord thereto.
  Many optimizations in libsensors and sensors.
  Configuration file converter.
  Rewrite of sensors-detect.
  Support for multiple configuration files in libsensors.

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*/
* imap_gssapi.c is partially derived from sample code in:
* GSS-API Programming Guide
* Part No: 816-1331-11
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/*
 *
 * hmac.c is derived from:

Network Working Group         H. Krawczyk
Request for Comments: 2104     IBM
Category: Informational        M. Bellare
                UCSD
                R. Canetti
                IBM
February 1997

HMAC: Keyed-Hashing for Message Authentication

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Appendix -- Sample Code
==========================================================================
Parts of nss.c are derived from the Mozilla NSS 3.9.2 source, mozilla/security/nss/cmd/smimetools/cmsutil.c. Therefore:

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Version 1.1

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It was downloaded from ftp.redhat.com, as a source .rpm from the 5.1 release.

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No explicit copyright is asserted. Nick Holloway is the earliest author recorded in the sourcecode. I queried him for an explicit statement regarding the license status of this work, and this is his reply:

Date: Tue, 21 Jul 1998 19:57:10 +0100
From: Nick Holloway <Nick.Holloway@alfie.demon.co.uk>
Message-Id: <199807211857.TAA19068@alfie.demon.co.uk>
To: Bdale Garbee <bdale@gag.com>
Subject: Re: makedev license?

> I maintain the makedev package for Debian GNU/Linux. I am about to move
> from the ill-fated makedev-1.6 to the makedev-2.3.1 derived from your work
> by the folks at Redhat. I don't see any evidence of a copyright assertion
> or explicit license statement in the source. Your name appears to be the
> earliest attached to the current sourcecode. Am I correct in assuming the
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> with our Debian Free Software Guidelines, so I'd like an explicit statement.

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The more recent modifications were done by Michael K. Johnson at Redhat. I think the understanding was that he would be taking over the maintenance of MAKEDEV (our discussion took place last September).

The only previous history was (according to an old posting to comp.os.linux) that I started with Jim Winstead's script.

So, as far as I am concerned, it is consistent with the Debian FSG.

--
'O O' | Home: Nick.Holloway@alfie.demon.co.uk  http://www.alfie.demon.co.uk/
// ^ \| Work: Nick.Holloway@parallax.co.uk

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```
size: 8388608, sector size: 512, PT: dos, offset: 446
---
#1:         32       7648  0x83
#2:       7680       8704  0xa5
#5:       7936       4864  0x7 (freebsd)
#6:      12544       3584  0x7 (freebsd)
size: 4456448, sector size: 512, PT: bsd, offset: 512
---
#1:       7936       4864  0x7
#2:      12544       3584  0x7
```

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1.171 mountall 2.19

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8. Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
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28. George Lindholm <lindholm@acs.ubc.ca> SunOS 5.1 port
29. Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
30. Lars H. Mathiesen <torirn@diku.dk> adaptation of foundation code
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4. [5] Michael Barone <michael.barone@lmco.com> GPSVME fixes
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23. [25] Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
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discipline, authentication, precision kernel; clock drivers:
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1.182 openssh-client 1:5.5p1-4ubuntu4

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This package contains a Gnome based implementation of ssh-askpass written by Damien Miller.

It is split out from the main package to isolate the dependency on the Gnome and X11 libraries.

It was packaged for Debian by Philip Hands <phil@hands.com>.

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**
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It was downloaded from here:
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1.183 openssh-server 1:5.5p1-4ubuntu4

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It is split out from the main package to isolate the dependency on the Gnome and X11 libraries.

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It was downloaded from here:
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1.184 openssl 1.0.2h :r0.4
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1.186 OpenSSL 0.9.8j

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Pentium Pro 200mhz
FreeBSD 2.1.5
gcc 2.7.2.2

SSLeay 0.7.0 30-Jan-1997
built on Tue Apr 22 12:14:36 EST 1997
options:bn(64,32) md2(int) rc4(idx,int) des(ptr.risc1,16,long) idea(int) blowfish(ptr2)
C flags:gcc -DTERMIOS -D_ANSI_SOURCE -fomit-frame-pointer -O3 -m486 -Wall
The 'numbers' are in 1000s of bytes per second processed.

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<th>256 bytes</th>
<th>1024 bytes</th>
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<td>8636.88k</td>
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<td>1071.55k</td>
<td>4572.50k</td>
<td>7459.98k</td>
<td>8791.96k</td>
<td>9341.61k</td>
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<td>rc4</td>
<td>10724.22k</td>
<td>14546.25k</td>
<td>15240.18k</td>
<td>15259.50k</td>
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<td>des cbc</td>
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<td>3883.01k</td>
<td>3968.25k</td>
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<td>3979.14k</td>
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<tr>
<td>des ede3</td>
<td>1442.98k</td>
<td>1548.33k</td>
<td>1562.48k</td>
<td>1562.00k</td>
<td>1563.33k</td>
</tr>
<tr>
<td>idea cbc</td>
<td>2195.69k</td>
<td>2506.39k</td>
<td>2529.59k</td>
<td>2545.66k</td>
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<td>rc2 cbc</td>
<td>806.00k</td>
<td>833.52k</td>
<td>837.58k</td>
<td>838.52k</td>
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<td>blowfish cbc</td>
<td>4687.34k</td>
<td>5949.97k</td>
<td>6182.43k</td>
<td>6248.11k</td>
<td>6226.09k</td>
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<tr>
<td>rsa 512 bits</td>
<td>0.010s</td>
<td></td>
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<tr>
<td>rsa 1024 bits</td>
<td>0.045s</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>rsa 2048 bits</td>
<td>0.260s</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>rsa 4096 bits</td>
<td>1.690s</td>
<td></td>
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</table>

Motorolla 68020 20mhz, NetBSD

SSLeay 0.9.0t 29-May-1998
built on Fri Jun  5 12:42:23 EST 1998
options:bn(64,32) md2(char) rc4(idx.int) des(idx,cisc,16,long) idea(int) blowfish(idx)
C flags:gcc -DTERMIOS -O3 -fomit-frame-pointer -Wall -DB_ENDIAN
The 'numbers' are in 1000s of bytes per second processed.

<table>
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<tr>
<th>type</th>
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<th>64 bytes</th>
<th>256 bytes</th>
<th>1024 bytes</th>
<th>8192 bytes</th>
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<tbody>
<tr>
<td>md2</td>
<td>2176.00</td>
<td>5994.67</td>
<td>8079.73</td>
<td>8845.18</td>
<td>9077.01</td>
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<td>mdc2</td>
<td>5730.67</td>
<td>6122.67</td>
<td>6167.66</td>
<td>6176.51</td>
<td>6174.87</td>
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<tr>
<td>md5</td>
<td>29.10k</td>
<td>127.31k</td>
<td>209.66k</td>
<td>250.50k</td>
<td>263.99k</td>
</tr>
<tr>
<td>hmac(md5)</td>
<td>12.33k</td>
<td>73.02k</td>
<td>160.17k</td>
<td>228.04k</td>
<td>261.15k</td>
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<tr>
<td>sha1</td>
<td>11.27k</td>
<td>49.37k</td>
<td>84.31k</td>
<td>102.40k</td>
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<td>rmd160</td>
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<td>78.76k</td>
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<td>rc4</td>
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</table>

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<thead>
<tr>
<th>Algorithm</th>
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<th>Verify Time (s)</th>
<th>Sign/Verify Time (s)</th>
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<tr>
<td>IDEA CBC</td>
<td>26.74k</td>
<td>29.23k</td>
<td>29.45k</td>
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<tr>
<td>RC2 CBC</td>
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<td>RC5-32/12 CBC</td>
<td>64.31k</td>
<td>83.18k</td>
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<tr>
<td>Blowfish CBC</td>
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<td>59.18k</td>
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<td>CAST CBC</td>
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<td>RSA 1024 bits</td>
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<td>0.2</td>
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<td>RSA 2048 bits</td>
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<td>DSA 2048 bits</td>
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<td>11.8200s</td>
<td>0.1</td>
</tr>
</tbody>
</table>

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1.190 ossp-uuid 1.6.2 :r2
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OSSP uuid - Universally Unique Identifier
Version 1.6.2 (04-Jul-2008)

ABSTRACT

OSSP uuid is an ISO-C:1999 application programming interface (API) and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant Universally Unique Identifier (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood of uniqueness over space and time and are computationally difficult to guess. They are globally unique identifiers which can be locally generated without contacting a global registration authority. UUIDs are intended as unique identifiers for both mass tagging objects with an extremely short lifetime and to reliably identifying very persistent objects across a network.

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of UUIDs which can found at http://www.ossp.org/pkg/lib/uuid/

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HOME AND DOCUMENTATION

The documentation and latest release can be found on

  o http://www.ossp.org/pkg/lib/uuid/
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**
**  uuid_md5.c: MD5 API implementation
*/

/* own headers (part 1/2) */
#include "uuid_ac.h"

/* system headers */
#include <stdlib.h>
#include <string.h>

/* own headers (part 2/2) */
#include "uuid_md5.h"

/*
* This is a RFC 1321 compliant Message Digest 5 (MD5) algorithm
* implementation. It is directly derived from the RSA code published in
* RFC 1321 with just the following functionality preserving changes:
* - converted function definitions from K&R to ANSI C
* - included contents of the "global.h" and "md5.h" headers
* - moved the SXX defines into the MD5Transform function
* - replaced MD5_memcp() with memcpy(3) and MD5_memset() with memset(3)
* - renamed "index" variables to "idx" to avoid namespace conflicts
* - reformatted C style to conform with OSSP C style
* - added own OSSP style frontend API
*/

/*
** ===== BEGIN RFC 1321 CODE =====
*/

/*
* RSA Data Security, Inc., MD5 message-digest algorithm
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*]
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 * documentation and/or software.
 */

/* POINTER defines a generic pointer type */
typedef unsigned char *POINTER;

/* UINT4 defines a four byte word */
#if SIZEOF_UNSIGNED_SHORT  == 4
typedef unsigned short int  UINT4;
#elif SIZEOF_UNSIGNED_INT   == 4
typedef unsigned int        UINT4;
#elif SIZEOF_UNSIGNED_LONG  == 4
typedef unsigned long int   UINT4;
#elif SIZEOF_UNSIGNED_LONG_LONG == 4
typedef unsigned long long int UINT4;
#else
#error ERROR: unable to determine UINT4 type (four byte word)
#endif

/* MD5 context. */
typedef struct {
    UINT4 state[4];        /* state (ABCD) */
    UINT4 count[2];        /* number of bits, modulo 2^64 (lsb first) */
    unsigned char buffer[64];    /* input buffer */
} MD5_CTX;

/* prototypes for internal functions */
static void MD5Init (MD5_CTX *ctx);
static void MD5Update (MD5_CTX *ctx, unsigned char *, unsigned int);
static void MD5Final (unsigned char [], MD5_CTX *);
static void MD5Transform (UINT4 [], unsigned char [], unsigned int);
static void Encode (unsigned char *, UINT4 *, unsigned int);
static void Decode (UINT4 *, unsigned char *, unsigned int);

/* finalization padding */
static unsigned char PADDING[64] = {
  0x80, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0,
  0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0,
  0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0
};

/* F, G, H and I are basic MD5 functions. */
#define F(x, y, z) (((x) & (y)) | ((~x) & (z)))
#define G(x, y, z) (((x) & (z)) | ((y) & (~z)))
#define H(x, y, z) ((x) ^ (y) ^ (z))
#define I(x, y, z) ((y) ^ ((x) | (~z)))

/* ROTATE_LEFT rotates x left n bits. */
#define ROTATE_LEFT(x, n) (((x) << (n)) | ((x) >> (32-(n))))

/* FF, GG, HH, and II transformations for rounds 1, 2, 3, and 4. 
Rotation is separate from addition to prevent recomputation. */
#define FF(a, b, c, d, x, s, ac) { 
  (a) += F ((b), (c), (d)) + (x) + (UINT4)(ac); 
  (a) = ROTATE_LEFT ((a), (s)); 
  (a) += (b); 
}
#define GG(a, b, c, d, x, s, ac) { 
  (a) += G ((b), (c), (d)) + (x) + (UINT4)(ac); 
  (a) = ROTATE_LEFT ((a), (s)); 
  (a) += (b); 
}
#define HH(a, b, c, d, x, s, ac) { 
  (a) += H ((b), (c), (d)) + (x) + (UINT4)(ac); 
  (a) = ROTATE_LEFT ((a), (s)); 
  (a) += (b); 
}
#define II(a, b, c, d, x, s, ac) { 
  (a) += I ((b), (c), (d)) + (x) + (UINT4)(ac); 
  (a) = ROTATE_LEFT ((a), (s)); 
  (a) += (b); 
}

/* MD5 initialization. Begins an MD5 operation, writing a new context. */
static void MD5Init(
  MD5_CTX *context)
{
  context->count[0] = context->count[1] = 0;
  /* Load magic initialization constants. */
  context->state[0] = 0x67452301;
  context->state[1] = 0xefcdab89;
  context->state[2] = 0x98badcfe;
context->state[3] = 0x10325476;
return;
}

/* MD5 block update operation. Continues an MD5 message-digest
operation, processing another message block, and updating the
context. */
static void MD5Update(
    MD5_CTX *context,                                        /* context */
    unsigned char *input,                                /* input block */
    unsigned int inputLen)                     /* length of input block */
{
    unsigned int i, idx, partLen;

    /* Compute number of bytes mod 64 */
    idx = (unsigned int)((context->count[0] >> 3) & 0x3F);

    /* Update number of bits */
    if (((context->count[0] += ((UINT4)inputLen << 3)) < ((UINT4)inputLen << 3))
      context->count[1]++;
    context->count[1] += ((UINT4)inputLen >> 29);
    partLen = (unsigned int)64 - idx;

    /* Transform as many times as possible. */
    if (inputLen >= partLen) {
      memcpy((POINTER)&context->buffer[idx], (POINTER)input, (size_t)partLen);
      MD5Transform(context->state, context->buffer);
      for (i = partLen; i + 63 < inputLen; i += 64)
        MD5Transform(context->state, &input[i]);
      idx = 0;
    } else
      i = 0;

    /* Buffer remaining input */
    memcpy((POINTER)&context->buffer[idx], (POINTER)&input[i], (size_t)(inputLen - i));
}

/* MD5 finalization. Ends an MD5 message-digest operation, writing the
the message digest and zeroizing the context. */
static void MD5Final(
    unsigned char digest[],                                 /* message digest */
    MD5_CTX *context)                                       /* context */
{
    unsigned char bits[8];
    unsigned int idx, padLen;
/* Save number of bits */
Encode(bits, context->count, 8);

/* Pad out to 56 mod 64. */
idx = (unsigned int)((context->count[0] >> 3) & 0x3f);
padLen = (idx < 56) ? ((unsigned int)56 - idx) : ((unsigned int)120 - idx);
MD5Update(context, PADDING, padLen);

/* Append length (before padding) */
MD5Update(context, bits, 8);

/* Store state in digest */
Encode(digest, context->state, 16);

/* Zeroize sensitive information. */
memset((POINTER)context, 0, sizeof(*context));
}

/* MD5 basic transformation. Transforms state based on block. */
static void MD5Transform(
    UINT4 state[],
    unsigned char block[])
{
    UINT4 a = state[0], b = state[1], c = state[2], d = state[3], x[16];

    Decode(x, block, 64);

    /* Round 1 */
    #define S11 7
    #define S12 12
    #define S13 17
    #define S14 22
    FF (a, b, c, d, x[ 0], S11, 0xd76aa478); /* 1 */
    FF (d, a, b, c, x[ 1], S12, 0xe8c7b756); /* 2 */
    FF (c, d, a, b, x[ 2], S13, 0x242070db); /* 3 */
    FF (b, c, d, a, x[ 3], S14, 0xabbdceee); /* 4 */
    FF (a, b, c, d, x[ 4], S11, 0xf57c0faf); /* 5 */
    FF (d, a, b, c, x[ 5], S12, 0x4787c62a); /* 6 */
    FF (c, d, a, b, x[ 6], S13, 0xa8304613); /* 7 */
    FF (b, c, d, a, x[ 7], S14, 0xfd469501); /* 8 */
    FF (a, b, c, d, x[ 8], S11, 0x698098d8); /* 9 */
    FF (d, a, b, c, x[ 9], S12, 0x8b44f7af); /* 10 */
    FF (c, d, a, b, x[10], S13, 0xff5ff5bb); /* 11 */
    FF (b, c, d, a, x[11], S14, 0x895cd7be); /* 12 */
    FF (a, b, c, d, x[12], S11, 0x6b901122); /* 13 */
    FF (d, a, b, c, x[13], S12, 0xfb871Fd3); /* 14 */
    FF (c, d, a, b, x[14], S13, 0xa679438e); /* 15 */
    FF (b, c, d, a, x[15], S14, 0x49b40821); /* 16 */
/* Round 2 */
#define S21 5
#define S22 9
#define S23 14
#define S24 20
GG (a, b, c, d, x[ 1], S21, 0xf61e2562); /* 17 */
GG (d, a, b, c, x[ 6], S22, 0xce040b340); /* 18 */
GG (c, d, a, b, x[11], S23, 0x265e5a51); /* 19 */
GG (b, c, d, a, x[ 0], S24, 0xe9b6e7aa); /* 20 */
GG (a, b, c, d, x[ 5], S21, 0xd62f105d); /* 21 */
GG (d, a, b, c, x[10], S22, 0x2441453); /* 22 */
GG (c, d, a, b, x[15], S23, 0xd8a1e681); /* 23 */
GG (b, c, d, a, x[ 4], S24, 0xe7d3fbc8); /* 24 */
GG (a, b, c, d, x[ 9], S21, 0x21e1cde6); /* 25 */
GG (d, a, b, c, x[14], S22, 0xc33707d6); /* 26 */
GG (c, d, a, b, x[ 3], S23, 0xf4d50d87); /* 27 */
GG (b, c, d, a, x[ 8], S24, 0x455a14ed); /* 28 */
GG (a, b, c, d, x[13], S21, 0xa9e3e905); /* 29 */
GG (d, a, b, c, x[ 2], S22, 0xfcefa3f8); /* 30 */
GG (c, d, a, b, x[ 7], S23, 0x676f02d9); /* 31 */
GG (b, c, d, a, x[12], S24, 0x289b7ec6); /* 32 */

/* Round 3 */
#define S31 4
#define S32 11
#define S33 16
#define S34 23
HH (a, b, c, d, x[ 5], S31, 0xfffa3942); /* 33 */
HH (d, a, b, c, x[ 8], S32, 0x8771f681); /* 34 */
HH (c, d, a, b, x[11], S33, 0x6d9d6122); /* 35 */
HH (b, c, d, a, x[14], S34, 0xfded5380c); /* 36 */
HH (a, b, c, d, x[ 1], S31, 0xa4beea44); /* 37 */
HH (d, a, b, c, x[ 4], S32, 0x4bdecfa9); /* 38 */
HH (c, d, a, b, x[ 7], S33, 0xf6bb4b60); /* 39 */
HH (b, c, d, a, x[10], S34, 0xbefbfbc70); /* 40 */
HH (a, b, c, d, x[13], S31, 0x289b7ec6); /* 41 */
HH (d, a, b, c, x[ 0], S32, 0x8ea127fa); /* 42 */
HH (c, d, a, b, x[ 3], S33, 0xd4ef3085); /* 43 */
HH (b, c, d, a, x[ 6], S34, 0x4881d05); /* 44 */
HH (a, b, c, d, x[ 9], S31, 0x9d94d039); /* 45 */
HH (d, a, b, c, x[12], S32, 0xe6db99e5); /* 46 */
HH (c, d, a, b, x[15], S33, 0x1fa27cf8); /* 47 */
HH (b, c, d, a, x[ 2], S34, 0xc44ac5665); /* 48 */

/* Round 4 */
#define S41 6
#define S42 10
#define S43 15
#define S44 21
II (a, b, c, d, x[ 0], S41, 0xf4292244); /* 49 */
II (d, a, b, c, x[ 7], S42, 0x432aff97); /* 50 */
II (c, d, a, b, x[14], S43, 0xab9423a7); /* 51 */
II (b, c, d, a, x[ 5], S44, 0xfc93a039); /* 52 */
II (a, b, c, d, x[12], S41, 0x655b59c3); /* 53 */
II (d, a, b, c, x[ 3], S42, 0x8f0ccc92); /* 54 */
II (c, d, a, b, x[10], S43, 0xffeff47d); /* 55 */
II (b, c, d, a, x[ 1], S44, 0x85845dd1); /* 56 */
II (a, b, c, d, x[ 8], S41, 0x6fa87e4f); /* 57 */
II (d, a, b, c, x[15], S42, 0xcfe26ee0); /* 58 */
II (c, d, a, b, x[ 6], S43, 0xa3014314); /* 59 */
II (b, c, d, a, x[13], S44, 0x85845dd1); /* 60 */
II (a, b, c, d, x[ 4], S41, 0x7537e82); /* 61 */
II (d, a, b, c, x[11], S42, 0xb3d3af235); /* 62 */
II (c, d, a, b, x[ 2], S43, 0x2ad7d2bb); /* 63 */
II (b, c, d, a, x[ 9], S44, 0xe8b6d391); /* 64 */

state[0] += a;
state[1] += b;
state[2] += c;
state[3] += d;

/* Zeroize sensitive information. */
memset((POINTER)x, 0, sizeof(x));
}

/* Encodes input (UINT4) into output (unsigned char).
Assumes len is a multiple of 4. */
static void Encode(
    unsigned char *output,
    UINT4 *input,
    unsigned int len)
{
    unsigned int i, j;

    for (i = 0, j = 0; j < len; i++, j += 4) {
        output[j] = (unsigned char)(input[i] & 0xff);
        output[j+1] = (unsigned char)((input[i] >> 8) & 0xff);
        output[j+2] = (unsigned char)((input[i] >> 16) & 0xff);
        output[j+3] = (unsigned char)((input[i] >> 24) & 0xff);
    }
    return;
}

/* Decodes input (unsigned char) into output (UINT4).
Assumes len is a multiple of 4. */
static void Decode(
    UINT4 *output,
    unsigned char *input,
    unsigned int len)
{
    unsigned int i, j;

    for (i = 0, j = 0; j < len; i++, j += 4)
        output[i] =   ( (UINT4)input[j])
            | (((UINT4)input[j+1]) << 8 )
            | (((UINT4)input[j+2]) << 16)
            | (((UINT4)input[j+3]) << 24);
    return;
}

/*
* ===== END RFC 1321 CODE =====
*/

struct md5_st {    
    MD5_CTX ctx;
};

md5_rc_t md5_create(md5_t **md5)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    if ((*md5 = (md5_t *)malloc(sizeof(md5_t))) == NULL)
        return MD5_RC_MEM;
    MD5Init(&((*md5)->ctx));
    return MD5_RC_OK;
}

md5_rc_t md5_init(md5_t *md5)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    MD5Init(&(md5->ctx));
    return MD5_RC_OK;
}

md5_rc_t md5_update(md5_t *md5, const void *data_ptr, size_t data_len)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    MD5Update(&((md5)->ctx), (unsigned char *)data_ptr, (unsigned int)data_len);
    return MD5_RC_OK;
}
md5_rc_t md5_store(md5_t *md5, void **data_ptr, size_t *data_len)
{
    MD5_CTX ctx;

    if (md5 == NULL || data_ptr == NULL)
        return MD5_RC_ARG;
    if (*data_ptr == NULL) {
        if ((*data_ptr = malloc(MD5_LEN_BIN)) == NULL)
            return MD5_RC_MEM;
        if (data_len != NULL)
            *data_len = MD5_LEN_BIN;
    } else {
        if (data_len != NULL) {
            if (*data_len < MD5_LEN_BIN)
                return MD5_RC_MEM;
            *data_len = MD5_LEN_BIN;
        }
    }
    memcpy((void *)(&ctx), (void *)(&(md5->ctx)), sizeof(MD5_CTX));
    MD5Final((unsigned char *)(*data_ptr), &(ctx));
    return MD5_RC_OK;
}

md5_rc_t md5_format(md5_t *md5, char **data_ptr, size_t *data_len)
{
    static const char hex[] = "0123456789abcdef";
    unsigned char buf[MD5_LEN_BIN];
    unsigned char *bufptr;
    size_t buflen;
    md5_rc_t rc;
    int i;

    if (md5 == NULL || data_ptr == NULL)
        return MD5_RC_ARG;
    if (*data_ptr == NULL) {
        if (((*data_ptr = (char *)malloc(MD5_LEN_STR+1)) == NULL)
            return MD5_RC_MEM;
        if (data_len != NULL)
            *data_len = MD5_LEN_STR+1;
    } else {
        if (data_len != NULL) {
            if (*data_len < MD5_LEN_STR+1)
                return MD5_RC_MEM;
            *data_len = MD5_LEN_STR+1;
        }
    }
bufptr = buf;
buflen = sizeof(buf);
if ((rc = md5_store(md5, (void **)((void *)&bufptr), &buflen)) != MD5_RC_OK)
    return rc;
for (i = 0; i < (int)buflen; i++) {
    (*data_ptr)[(i*2)+0] = hex[(int)(bufptr[i] >> 4)];
    (*data_ptr)[(i*2)+1] = hex[(int)(bufptr[i] & 0x0f)];
}
(*data_ptr)[(i*2)] = '\0';
return MD5_RC_OK;
}

md5_rc_t md5_destroy(md5_t *md5)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    free(md5);
    return MD5_RC_OK;
}

1.191 parted 3.2 :r1.2.3

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

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The End

#!perl

=head1 NAME
copyright.t

=head1 DESCRIPTION

Tests that the latest copyright years in the top-level README file and the C<perl -v> output match each other.

If the test fails, update at least one of README and perl.c so that they match reality.

Optionally you can pass the C<--now> option to check they are at the current year. This isn't checked by default, so that it doesn't fail for people working on older releases. It should be run before making a new release.

=cut
use TestInit;
use strict;
use Config;
BEGIN { require 'test.pl' }

if ( $Config{usecrosscompile} ) {
    skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so
# that tests still pass for people intentionally working on older versions:
if ($opt eq '--now') {
    my $current_year = (gmtime)[5] + 1900;
    is $v_year, $current_year, 'perl -v copyright includes current year';
    is $readme_year, $current_year, 'README copyright includes current year';
}

# Otherwise simply check that the two copyright dates match each other:
else {
    is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;

sub readme_year
# returns the latest copyright year from the top-level README file
{
    open my $readme, '<', '../README' or die "Opening README failed: $!";

    # The copyright message is the first paragraph:
    local $/ = "";
    my $copyright_msg = <$readme>;

    my ($year) = $copyright_msg =~ /.*\b\d{4,}/s
        or die "Year not found in README copyright message $copyright_msg";

    $year;
}
sub v_year
# returns the latest copyright year shown in perl -v
{

my $output = runperl switches => ['-v'];
my ($year) = $output =~ /copyright 1987.*\b\d\d\d\d\b/i
  or die "Copyright statement not found in perl -v output $output";

$year;
}

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Gnomovision version 69, Copyright (C) 19xx name of author
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This is free software, and you are welcome to redistribute it
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program `Gnomovision' (a program to direct compilers to make passes
at assemblers) written by James Hacker.

<typename of Ty Coon>, 1 April 1989
Ty Coon, President of Vice

That's all there is to it!

-------------------------------------------------------------------------------------------------

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

---
abstract: 'Build and install Perl modules'
author:
- 'ken Williams <kwilliams@cpan.org>'
- "Development questions, bug reports, and patches should be sent to the module-build mailing list at <module-build@perl.org>.'
built_requires:
File::Temp: 0.15
Test::Harness: 3.16
Test::More: 0.49
generated_by: 'Module::Build version 0.3608'
license: gpl
meta-spec:
url: http://module-build.sourceforge.net/META-spec-v1.4.html
version: 1.4
name: Module-Build
resources:
MailingList: mailto:module-build@perl.org
license: http://dev.perl.org/licenses/
repository: http://github.com/dagolden/module-build/
version: 3
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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

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--------------------------------------------------------------------------

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

--------------------------------------------------------------------------

1.198 pixman 0.32.6 :r0

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 *
 * Author: Siarhei Siamashka (siarhei.siamashka@nokia.com)
 */

/*
 * This file contains a macro (generate_composite_function) which can
 * construct 2D image processing functions, based on a common template.
 * Any combinations of source, destination and mask images with 8bpp,
 * 16bpp, 24bpp, 32bpp color formats are supported.
 */
* This macro takes care of:
  * handling of leading and trailing unaligned pixels
  * doing most of the work related to L2 cache preload
  * encourages the use of software pipelining for better instructions
  * scheduling

* The user of this macro has to provide some configuration parameters
  * (bit depths for the images, prefetch distance, etc.) and a set of
  * macros, which should implement basic code chunks responsible for
  * pixels processing. See 'pixman-arm-neon-asm.S' file for the usage
  * examples.

* TODO:
  * try overlapped pixel method (from Ian Rickards) when processing
  * exactly two blocks of pixels
  * maybe add an option to do reverse scanline processing

/ *

/*
 * Bit flags for 'generate_composite_function' macro which are used
 * to tune generated functions behavior.
 */
.set FLAG_DST_WRITEONLY, 0
.set FLAG_DST_READWRITE, 1
.set FLAG_DEINTERLEAVE_32BPP, 2

/*
 * Offset in stack where mask and source pointer/stride can be accessed
 * from 'init' macro. This is useful for doing special handling for solid mask.
 */
.set ARG5_STACK_OFFSET, 40

/*
 * Constants for selecting preferable prefetch type.
 */
.set PREFETCH_TYPE_NONE, 0 /* No prefetch at all */
.set PREFETCH_TYPE_SIMPLE, 1 /* A simple, fixed-distance-ahead prefetch */
.set PREFETCH_TYPE_ADVANCED, 2 /* Advanced fine-grained prefetch */

/*
 * Definitions of supplementary pixld/pixst macros (for partial load/store of
 * pixel data).
 */
.macro pixldst1 op, elem_size, reg1, mem_operand, abits
  if abits > 0
    op&.&elem_size {d&reg1}, [mem_operand& , :&abits&]!
.macro pixldst2 op, elem_size, reg1, reg2, mem_operand, abits
  .if abits > 0
    .op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&, :&abits&]!
  .else
    .op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&]!
  .endif
.endm

.macro pixldst4 op, elem_size, reg1, reg2, reg3, reg4, mem_operand, abits
  .if abits > 0
    .op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&, :&abits&]!
  .else
    .op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&]!
  .endif
.endm

.macro pixldst0 op, elem_size, reg1, idx, mem_operand, abits
  .op&.&elem_size {d&reg1[idx]}, [&mem_operand&]!
.endm

.macro pixldst3 op, elem_size, reg1, reg2, reg3, mem_operand
  .op&.&elem_size {d&reg1, d&reg2, d&reg3}, [&mem_operand&]!
.endm

.macro pixldst0 op, elem_size, reg1, idx, mem_operand, abits
  .op&.&elem_size {d&reg1[idx]}, [&mem_operand&]!
.endm

.macro pixldst30 op, elem_size, reg1, reg2, reg3, idx, mem_operand
  .op&.&elem_size {d&reg1[idx], d&reg2[idx], d&reg3[idx]}, [&mem_operand&]!
.endm

.macro pixldst numbytes, op, elem_size, basereg, mem_operand, abits
  .if numbytes == 32
    pixldst4 op, elem_size, %(%basereg+4), %(%basereg+5),
    %(%basereg+6), %(%basereg+7), mem_operand, abits
  .elseif numbytes == 16
    pixldst2 op, elem_size, %(%basereg+2), %(%basereg+3), mem_operand, abits
  .elseif numbytes == 8
    pixldst1 op, elem_size, %(%basereg+1), mem_operand, abits
  .elseif numbytes == 4
    .if !RESPECT STRICT_ALIGNMENT || (elem_size == 32)
      pixldst0 op, 32, %(%basereg+0), 1, mem_operand, abits
    .elseif elem_size == 16
      pixldst0 op, 16, %(%basereg+0), 2, mem_operand, abits
      pixldst0 op, 16, %(%basereg+0), 3, mem_operand, abits
    .else

pixldst0 op, 8, %basereg+0, 4, mem_operand, abits
pixldst0 op, 8, %basereg+0, 5, mem_operand, abits
pixldst0 op, 8, %basereg+0, 6, mem_operand, abits
pixldst0 op, 8, %basereg+0, 7, mem_operand, abits
.endif
.elseif numbytes == 2
    .if !RESPECT STRICT_ALIGNMENT || (elem_size == 16)
        pixldst0 op, 16, %basereg+0, 1, mem_operand, abits
    .else
        pixldst0 op, 8, %basereg+0, 2, mem_operand, abits
        pixldst0 op, 8, %basereg+0, 3, mem_operand, abits
    .endif
.elseif numbytes == 1
    pixldst0 op, 8, %basereg+0, 1, mem_operand, abits
.else
    .error "unsupported size: numbytes"
.endif
.endm

.macro pixld numpix, bpp, basereg, mem_operand, abits=0
    .if bpp > 0
        .if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE 32BPP ENABLED != 0)
            pixldst4 vld4, 8, %basereg+4, %basereg+5, %basereg+6, %basereg+7, mem_operand, abits
        . elseif (bpp == 24) && (numpix == 8)
            pixldst3 vld3, 8, %basereg+3, %basereg+4, %basereg+5, mem_operand
        .elseif (bpp == 24) && (numpix == 4)
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 4, mem_operand
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 5, mem_operand
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 6, mem_operand
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 7, mem_operand
        .elseif (bpp == 24) && (numpix == 2)
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 2, mem_operand
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 3, mem_operand
        .elseif (bpp == 24) && (numpix == 1)
            pixldst30 vld3, 8, %basereg+0, %basereg+1, %basereg+2, 1, mem_operand
        .else
            pixldst %numpix * bpp / 8, vld1, %bpp, basereg, mem_operand, abits
        .endif
    .endif
.endm

.macro pixst numpix, bpp, basereg, mem_operand, abits=0
    .if bpp > 0
        .if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE 32BPP ENABLED != 0)
            pixldst4 vs4, 8, %basereg+4, %basereg+5, %basereg+6, %basereg+7, mem_operand, abits
        .elseif (bpp == 24) && (numpix == 8)
            pixldst %numpix * bpp / 8, vld1, %bpp, basereg, mem_operand, abits
        .endif
    .endif
.endm
pixldst3 vst3, 8, %(%basereg+3), %(%basereg+4), %(%basereg+5), mem_operand
elseif (bpp == 24) && (numpix == 4)
pixldst30 vst3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 4, mem_operand
pixldst30 vs3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 5, mem_operand
pixldst30 vs3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 6, mem_operand
pixldst30 vs3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 7, mem_operand
elseif (bpp == 24) && (numpix == 2)
pixldst30 vs3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 2, mem_operand
pixldst30 vs3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 3, mem_operand
elseif (bpp == 24) && (numpix == 1)
pixldst30 vs3, 8, %(%basereg+0), %(%basereg+1), %(%basereg+2), 1, mem_operand
else
pixldst %(%numpix * bpp / 8), vst1, %(%bpp), basereg, mem_operand, abits
endif
endif
endm

.macro pixld_a numpix, bpp, basereg, mem_operand
.if (bpp * numpix) <= 128
pixld numpix, bpp, basereg, mem_operand, %(%bpp * numpix)
.else
pixld numpix, bpp, basereg, mem_operand, 128
endif
.endm

.macro pixst_a numpix, bpp, basereg, mem_operand
.if (bpp * numpix) <= 128
pixst numpix, bpp, basereg, mem_operand, %(%bpp * numpix)
.else
pixst numpix, bpp, basereg, mem_operand, 128
endif
.endm

/*
 * Pixel fetcher for nearest scaling (needs TMP1, TMP2, VX, UNIT_X register
 * aliases to be defined)
*/
.macro pixld1_s elem_size, reg1, mem_operand
.if elem_size == 16
    mov     TMP1, VX, asr #16
    adds    VX, VX, UNIT_X
5:  subpls  VX, VX, SRC_WIDTH_FIXED
      bpl     5b
    add     TMP1, mem_operand, TMP1, asl #1
    mov     TMP2, VX, asr #16
    adds    VX, VX, UNIT_X
5:  subpls  VX, VX, SRC_WIDTH_FIXED
      bpl     5b
endif
.endm
add    TMP2, mem_operand, TMP2, asl #1
vld1.16 {d&reg1&[0]}, [TMP1, :16]
mov    TMP1, VX, asr #16
adds   VX, VX, UNIT_X
      5: subpls VX, VX, SRC_WIDTH_FIXED
          bpl  .5b
add    TMP1, mem_operand, TMP1, asl #1
vld1.16 {d&reg1&[1]}, [TMP2, :16]
mov    TMP2, VX, asr #16
adds   VX, VX, UNIT_X
      5: subpls VX, VX, SRC_WIDTH_FIXED
          bpl  .5b
add    TMP1, mem_operand, TMP1, asl #1
vld1.16 {d&reg1&[2]}, [TMP1, :16]
vld1.16 {d&reg1&[3]}, [TMP2, :16]
      .elseif elem_size == 32
mov    TMP1, VX, asr #16
adds   VX, VX, UNIT_X
      5: subpls VX, VX, SRC_WIDTH_FIXED
          bpl  .5b
add    TMP1, mem_operand, TMP1, asl #2
mov    TMP2, VX, asr #16
adds   VX, VX, UNIT_X
      5: subpls VX, VX, SRC_WIDTH_FIXED
          bpl  .5b
add    TMP2, mem_operand, TMP2, asl #1
vld1.16 {d&reg1&[0]}, [TMP1, :32]
vld1.16 {d&reg1&[1]}, [TMP2, :32]
      .else
      .error "unsupported"
      .endif
      .endm

.macro pixld2_s elem_size, reg1, reg2, mem_operand
      .if 0 /* elem_size == 32 */
      mov    TMP1, VX, asr #16
      add    VX, VX, UNIT_X, asl #1
      add    TMP1, mem_operand, TMP1, asl #2
      mov    TMP2, VX, asr #16
      sub    VX, VX, UNIT_X
      add    TMP2, mem_operand, TMP2, asl #2
      vld1.32 {d&reg1&[0]}, [TMP1, :32]
vld1.32 {d&reg1&[1]}, [TMP2, :32]
      mov    TMP1, VX, asr #16
      add    VX, VX, UNIT_X, asl #1
      add    TMP1, mem_operand, TMP1, asl #2
      vld1.32 {d&reg2&[0]}, [TMP2, :32]
      mov    TMP2, VX, asr #16
      add    VX, VX, UNIT_X
      .endm

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add     TMP2, mem_operand, TMP2, asl #2
vld1.32 {d&reg1&[1]}, [TMP1, :32]
vld1.32 {d&reg2&[1]}, [TMP2, :32]
else
pixld1_s elem_size, reg1, mem_operand
pixld1_s elem_size, reg2, mem_operand
endif
.endm

.macro pixld0_s elem_size, reg1, idx, mem_operand
    .if elem_size == 16
        mov     TMP1, VX, asr #16
        adds    VX, VX, UNIT_X
5:  subpls VX, VX, SRC_WIDTH_FIXED
            bpl     5b
        add     TMP1, mem_operand, TMP1, asl #1
        vld1.16 {d&reg1&[idx]}, [TMP1, :16]
    .elseif elem_size == 32
        mov     TMP1, VX, asr #16
        adds    VX, VX, UNIT_X
5:  subpls VX, VX, SRC_WIDTH_FIXED
            bpl     5b
        add     TMP1, mem_operand, TMP1, asl #2
        vld1.32 {d&reg1&[idx]}, [TMP1, :32]
    .endif
.endm

.macro pixld_s_internal numbytes, elem_size, basereg, mem_operand
    .if numbytes == 32
        pixld2_s elem_size, %(basereg+4), %(basereg+5), mem_operand
        pixld2_s elem_size, %(basereg+6), %(basereg+7), mem_operand
        pixdeinterleave elem_size, %(basereg+4)
    .elseif numbytes == 16
        pixld2_s elem_size, %(basereg+2), %(basereg+3), mem_operand
    .elseif numbytes == 8
        pixld1_s elem_size, %(basereg+1), mem_operand
    .elseif numbytes == 4
        .if elem_size == 32
            pixld0_s elem_size, %(basereg+0), 1, mem_operand
        .elseif elem_size == 16
            pixld0_s elem_size, %(basereg+0), 2, mem_operand
        .else
            pixld0_s elem_size, %(basereg+0), 4, mem_operand
            pixld0_s elem_size, %(basereg+0), 5, mem_operand
            pixld0_s elem_size, %(basereg+0), 6, mem_operand
            pixld0_s elem_size, %(basereg+0), 7, mem_operand
        .endif
    .endif
.endm
.elseif numbytes == 2
  .if elem_size == 16
    pixld0_s elem_size, (%(basereg+0), 1, mem_operand
  .else
    pixld0_s elem_size, (%(basereg+0), 2, mem_operand
    pixld0_s elem_size, (%(basereg+0), 3, mem_operand
  .endif
.elseif numbytes == 1
  pixld0_s elem_size, (%(basereg+0), 1, mem_operand
.else
  .error "unsupported size: numbytes"
endif
endm

.macro pixld_s numpix, bpp, basereg, mem_operand
  .if bpp > 0
    pixld_s_internal %(numpix * bpp / 8), %(bpp), basereg, mem_operand
  .endif
endm

.macro vuzp8 reg1, reg2
  vuzp.8 d&reg1, d&reg2
endm

.macro vzip8 reg1, reg2
  vzip.8 d&reg1, d&reg2
endm

/* deinterleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixdeinterleave bpp, basereg
  .if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    vuzp8 %(basereg+0), %(basereg+1)
    vuzp8 %(basereg+2), %(basereg+3)
    vuzp8 %(basereg+1), %(basereg+3)
    vuzp8 %(basereg+0), %(basereg+2)
  .endif
endm

/* interleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixinterleave bpp, basereg
  .if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    vzip8 %(basereg+0), %(basereg+2)
    vzip8 %(basereg+1), %(basereg+3)
    vzip8 %(basereg+2), %(basereg+3)
    vzip8 %(basereg+0), %(basereg+1)
  .endif
endm
/*
 This is a macro for implementing cache preload. The main idea is that
 cache preload logic is mostly independent from the rest of pixels
 processing code. It starts at the top left pixel and moves forward
 across pixels and can jump across scanlines. Prefetch distance is
 handled in an 'incremental' way: it starts from 0 and advances to the
 optimal distance over time. After reaching optimal prefetch distance,
 it is kept constant. There are some checks which prevent prefetching
 unneeded pixel lines below the image (but it still can prefetch a bit
 more data on the right side of the image - not a big issue and may
 be actually helpful when rendering text glyphs). Additional trick is
 the use of LDR instruction for prefetch instead of PLD when moving to
 the next line, the point is that we have a high chance of getting TLB
 miss in this case, and PLD would be useless.
 *
 This sounds like it may introduce a noticeable overhead (when working with
 fully cached data). But in reality, due to having a separate pipeline and
 instruction queue for NEON unit in ARM Cortex-A8, normal ARM code can
 execute simultaneously with NEON and be completely shadowed by it. Thus
 we get no performance overhead at all (*). This looks like a very nice
 feature of Cortex-A8, if used wisely. We don't have a hardware prefetcher,
 but still can implement some rather advanced prefetch logic in software
 for almost zero cost!
 *
 (*) The overhead of the prefacher is visible when running some trivial
 pixels processing like simple copy. Anyway, having prefetch is a must
 when working with the graphics data.
 */
.macro PF a, x:vararg
.if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_ADVANCED)
a x
.endif
.endm

.macro cache_preload std_increment, boost_increment
.if (src_bpp_shift >= 0) || (dst_r_bpp != 0) || (mask_bpp_shift >= 0)
.if regs_shortage
PF ldr ORIG_W, [sp] /* If we are short on regs, ORIG_W is kept on stack */
.endif
.if std_increment != 0
PF add PF_X, PF_X, #std_increment
.endif
PF tst PF_CTL, #0xF
PF addne PF_X, PF_X, #boost_increment
PF subne PF_CTL, PF_CTL, #1
PF cmp PF_X, ORIG_W
.if src_bpp_shift >= 0
PF pld, [PF_SRC, PF_X, lsl #src_bpp_shift]
.endif
.endm
.ifdef
.if dst_r_bpp != 0
    PF pld, [PF_DST, PF_X, lsl #dst_bpp_shift]
.endif
.if mask_bpp_shift >= 0
    PF pld, [PF_MASK, PF_X, lsl #mask_bpp_shift]
.endif
    PF subge PF_X, PF_X, ORIG_W
    PF subges PF_CTL, PF_CTL, #0x10
.if src_bpp_shift >= 0
    PF ldrgeb DUMMY, [PF_SRC, SRC_STRIDE, lsl #src_bpp_shift]!
.endif
.if dst_r_bpp != 0
    PF ldrgeb DUMMY, [PF_DST, DST_STRIDE, lsl #dst_bpp_shift]!
.endif
.endm

.macro cache_preload_simple
.if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_SIMPLE)
.if src_bpp > 0
    pld [SRC, #(PREFETCH_DISTANCE_SIMPLE * src_bpp / 8)]
.endif
.if dst_r_bpp > 0
    pld [DST_R, #(PREFETCH_DISTANCE_SIMPLE * dst_r_bpp / 8)]
.endif
.if mask_bpp > 0
    pld [MASK, #(PREFETCH_DISTANCE_SIMPLE * mask_bpp / 8)]
.endif
.endm

.macro fetch_mask_pixblock
    pixld pixblock_size, mask_bpp,
    (mask_basereg - pixblock_size * mask_bpp / 64), MASK
.endm

/*
 * Macro which is used to process leading pixels until destination
 * pointer is properly aligned (at 16 bytes boundary). When destination
 * buffer uses 16bpp format, this is unnecessary, or even pointless.
 */
.macro ensure_destination_ptr_alignment process_pixblock_head, 
    process_pixblock_tail, 
    process_pixblock_tail_head
if dst_w_bpp != 24
    tst      DST_R, #0xF
    beq      2f

.irp lowbit, 1, 2, 4, 8, 16
local skip1
.if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
    .if lowbit < 16 /* we don't need more than 16-byte alignment */
    tst      DST_W, #lowbit
    beq      1f
    .endif
    pixld    (lowbit * 8 / dst_w_bpp), src_bpp, src_basereg, SRC
    pixld    (lowbit * 8 / dst_w_bpp), mask_bpp, mask_basereg, MASK
    .if dst_r_bpp > 0
    pixld_a  (lowbit * 8 / dst_r_bpp), dst_r_bpp, dst_r_basereg, DST_R
    .else
    add      DST_R, DST_R, #lowbit
    .endif
    PF add    PF_X, PF_X, #(lowbit * 8 / dst_w_bpp)
    sub      W, W, #(lowbit * 8 / dst_w_bpp)
1:
    .endif
    .endr
    pixdeinterleave src_bpp, src_basereg
    pixdeinterleave mask_bpp, mask_basereg
    pixdeinterleave dst_r_bpp, dst_r_basereg

    process_pixblock_head
    cache_preload 0, pixblock_size
    cache_preload_simple
    process_pixblock_tail

    pixinterleave dst_w_bpp, dst_w_basereg
    .irp lowbit, 1, 2, 4, 8, 16
    .if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
    .if lowbit < 16 /* we don't need more than 16-byte alignment */
    tst      DST_W, #lowbit
    beq      1f
    .endif
    pixst_a  (lowbit * 8 / dst_w_bpp), dst_w_bpp, dst_w_basereg, DST_W
1:
    .endif
    .endr
    .endif
2:
.endm
/*
* Special code for processing up to (pixblock_size - 1) remaining trailing pixels. As SIMD processing performs operation on pixblock_size pixels, anything smaller than this has to be loaded and stored in a special way. Loading and storing of pixel data is performed in such a way that we fill some 'slots' in the NEON registers (some slots naturally are unused), then perform compositing operation as usual. In the end, the data is taken from these 'slots' and saved to memory.

* cache_preload_flag - allows to suppress prefetch if set to 0
* dst_aligned_flag - selects whether destination buffer is aligned

/.macro process_trailing_pixels cache_preload_flag, \ dst_aligned_flag, \ process_pixblock_head, \ process_pixblock_tail, \ process_pixblock_tail_head
  tst W, #(pixblock_size - 1)
  beq 2f
  .irp chunk_size, 16, 8, 4, 2, 1
  .if pixblock_size > chunk_size
  tst W, #chunk_size
  beq 1f
  pixld_src chunk_size, src_bpp, src_basereg, SRC
  pixld chunk_size, mask_bpp, mask_basereg, MASK
  .if dst_aligned_flag != 0
  pixld_a chunk_size, dst_r_bpp, dst_r_basereg, DST_R
  .else
  pixld chunk_size, dst_r_bpp, dst_r_basereg, DST_R
  .endif
  .if cache_preload_flag != 0
  cache_preload 0, pixblock_size
  cache_preload_simple
  .endif
  1:
  .endif
.endr
pixdeinterleave src_bpp, src_basereg
pixdeinterleave mask_bpp, mask_basereg
pixdeinterleave dst_r_bpp, dst_r_basereg

  process_pixblock_head
  .if cache_preload_flag != 0
  cache_preload 0, pixblock_size
  cache_preload_simple
  .endif
  process_pixblock_tail
pixinterleave dst_w_bpp, dst_w_basereg
.irp chunk_size, 16, 8, 4, 2, 1
.if pixblock_size > chunk_size
   tst   W, #chunk_size
   beq   1f
.if dst_aligned_flag != 0
   pixst_a chunk_size, dst_w_bpp, dst_w_basereg, DST_W
 else
   pixst chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.endif
1:
.endif
.endr
2:
.endm

/*
 * Macro, which performs all the needed operations to switch to the next
 * scanline and start the next loop iteration unless all the scanlines
 * are already processed.
 */
.macro advance_to_next_scanline start_of_loop_label
 .if regs_shortage
    ldrd W, [sp] /* load W and H (width and height) from stack */
.else
   mov W, ORIG_W
.endif
   add DST_W, DST_W, DST_STRIDE, lsl #dst_bpp_shift
.if src_bpp != 0
   add SRC, SRC, SRC_STRIDE, lsl #src_bpp_shift
.endif
.if mask_bpp != 0
   add MASK, MASK, MASK_STRIDE, lsl #mask_bpp_shift
.endif
.if (dst_w_bpp != 24)
   sub DST_W, DST_W, W, lsl #dst_bpp_shift
.endif
.if (src_bpp != 24) && (src_bpp != 0)
   sub SRC, SRC, W, lsl #src_bpp_shift
.endif
.if (mask_bpp != 24) && (mask_bpp != 0)
   sub MASK, MASK, W, lsl #mask_bpp_shift
.endif
.subs H, H, #1
.mov DST_R, DST_W
.if regs_shortage
   str H, [sp, #4] /* save updated height to stack */
.endif
* Registers are allocated in the following way by default:
  * d0, d1, d2, d3 - reserved for loading source pixel data
  * d4, d5, d6, d7 - reserved for loading destination pixel data
  * d24, d25, d26, d27 - reserved for loading mask pixel data
  * d28, d29, d30, d31 - final destination pixel data for writeback to memory

/.macro generate_composite_function fname,\n    src_bpp_,\n    mask_bpp_,\n    dst_w_bpp_,\n    flags,\n    pixblock_size_,\n    prefetch_distance,\n    init,\n    cleanup,\n    process_pixblock_head,\n    process_pixblock_tail,\n    process_pixblock_tail_head,\n    dst_w_basereg_ = 28,\n    dst_r_basereg_ = 4,\n    src_basereg_ = 0,\n    mask_basereg_ = 24

.func fname
    .global fname
    /* For ELF format also set function visibility to hidden */
    #ifdef __ELF__
        .hidden fname
        .type fname, %function
    #endif
    fname:
        push  {r4-r12, lr}  /* save all registers */

    /*
    * Select prefetch type for this function. If prefetch distance is
    * set to 0 or one of the color formats is 24bpp, SIMPLE prefetch
    * has to be used instead of ADVANCED.
    */
    .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_DEFAULT
    .if prefetch_distance == 0
        .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
    .elseif (PREFETCH_TYPE_CURRENT > PREFETCH_TYPE_SIMPLE) && \n      ((src_bpp_ == 24) || (mask_bpp_ == 24) || (dst_w_bpp_ == 24))
        .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_SIMPLE
    .else
        .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_ADVANCED
    .endif

    /"
endif

/*
 * Make some macro arguments globally visible and accessible
 * from other macros
 */

.set src_bpp, src_bpp_
.set mask_bpp, mask_bpp_
.set dst_w_bpp, dst_w_bpp_
.set pixblock_size, pixblock_size_
.set dst_w_basereg, dst_w_basereg_
.set dst_r_basereg, dst_r_basereg_
.set src_basereg, src_basereg_
.set mask_basereg, mask_basereg_

.macro pixld_src x:vararg
    pixld x
.endm

.macro fetch_src_pixblock
    pixld_src pixblock_size, src_bpp, \ 
       (src_basereg - pixblock_size * src_bpp / 64), SRC
.endm

/*
 * Assign symbolic names to registers
 */

W     .req        r0      /* width (is updated during processing) */
H     .req        r1      /* height (is updated during processing) */
DST_W .req        r2      /* destination buffer pointer for writes */
DST_STRIDE .req r3     /* destination image stride */
SRC   .req        r4      /* source buffer pointer */
SRC_STRIDE .req r5    /* source image stride */
DST_R .req        r6      /* destination buffer pointer for reads */

MASK  .req        r7      /* mask pointer */
MASK_STRIDE .req r8     /* mask stride */

PF_CTL .req        r9      /* combined lines counter and prefetch */
               /* distance increment counter */
PF_X   .req        r10     /* pixel index in a scanline for current */
               /* preetch position */
PF_SRC .req        r11     /* pointer to source scanline start */
               /* for prefetch purposes */
PF_DST .req        r12     /* pointer to destination scanline start */
               /* for prefetch purposes */
PF_MASK .req        r14     /* pointer to mask scanline start */
               /* for prefetch purposes */

/*
 * Check whether we have enough registers for all the local variables.
 */
* If we don't have enough registers, original width and height are
* kept on top of stack (and 'regs_shortage' variable is set to indicate
* this for the rest of code). Even if there are enough registers, the
* allocation scheme may be a bit different depending on whether source
* or mask is not used.
*/
.if (PREFETCH_TYPE_CURRENT < PREFETCH_TYPE_ADVANCED)
  ORIG_W .req r10 /* saved original width */
  DUMMY .req r12 /* temporary register */
  .set regs_shortage, 0
.elseif mask_bpp == 0
  ORIG_W .req r7  /* saved original width */
  DUMMY .req r8  /* temporary register */
  .set regs_shortage, 0
.elseif src_bpp == 0
  ORIG_W .req r4  /* saved original width */
  DUMMY .req r5  /* temporary register */
  .set regs_shortage, 0
.else
  ORIG_W .req r1  /* saved original width */
  DUMMY .req r1  /* temporary register */
  .set regs_shortage, 1
.endif

  .set mask_bpp_shift, -1
.if src_bpp == 32
  .set src_bpp_shift, 2
.elseif src_bpp == 24
  .set src_bpp_shift, 0
.elseif src_bpp == 16
  .set src_bpp_shift, 1
.elseif src_bpp == 8
  .set src_bpp_shift, 0
.elseif src_bpp == 0
  .set src_bpp_shift, -1
.else
  .error "requested src bpp (src_bpp) is not supported"
.endif
.if mask_bpp == 32
  .set mask_bpp_shift, 2
.elseif mask_bpp == 24
  .set mask_bpp_shift, 0
.elseif mask_bpp == 8
  .set mask_bpp_shift, 0
.elseif mask_bpp == 0
  .set mask_bpp_shift, -1
.else
  .error "requested mask bpp (mask_bpp) is not supported"
.endif
.if dst_w_bpp == 32
  .set dst_bpp_shift, 2
.elseif dst_w_bpp == 24
  .set dst_bpp_shift, 0
.elseif dst_w_bpp == 16
  .set dst_bpp_shift, 1
.elseif dst_w_bpp == 8
  .set dst_bpp_shift, 0
.else
  .error "requested dst bpp (dst_w_bpp) is not supported"
.endif
.if (((flags) & FLAG_DST_READWRITE) != 0)
  .set dst_r_bpp, dst_w_bpp
.else
  .set dst_r_bpp, 0
.endif
.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
  .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
  .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif
.if prefetch_distance < 0 || prefetch_distance > 15
  .error "invalid prefetch distance (prefetch_distance)"
.endif
.if src_bpp > 0
  ldr SRC, [sp, #40]
.endif
.if mask_bpp > 0
  ldr MASK, [sp, #48]
.endif
  PF mov PF_X, #0
.if src_bpp > 0
  ldr SRC_STRIDE, [sp, #44]
.endif
.if mask_bpp > 0
  ldr MASK_STRIDE, [sp, #52]
.endif
  mov DST_R, DST_W
.if src_bpp == 24
  sub SRC_STRIDE, SRC_STRIDE, W
  sub SRC_STRIDE, SRC_STRIDE, W, lsl #1
.endif
.if mask_bpp == 24
sub    MASK_STRIDE, MASK_STRIDE, W
sub    MASK_STRIDE, MASK_STRIDE, W, lsl #1
endif
if dst_w_bpp == 24
sub    DST_STRIDE, DST_STRIDE, W
sub    DST_STRIDE, DST_STRIDE, W, lsl #1
endif

/*
 * Setup advanced pref heter initial state
 */
PF mov    PF_SRC, SRC
PF mov    PF_DST, DST_R
PF mov    PF_MASK, MASK
/* PF_CTL = prefetch_distance | ((h - 1) << 4) */
PF mov    PF_CTL, H, lsl #4
PF add    PF_CTL, #(prefetch_distance - 0x10)

init
if regs_shortage
push    {r0, r1}
endif
subs    H, H, #1
if regs_shortage
str    H, [sp, #4] /* save updated height to stack */
else
mov    ORIG_W, W
endif
blt    9f
cmp    W, #(pixblock_size * 2)
blt    8f

/*
 * This is the start of the pipelined loop, which if optimized for
 * long scanlines
 */
0:

ensure_destination_ptr_alignment process_pixblock_head, 
    process_pixblock_tail,
    process_pixblock_tail_head

/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
pixld_a pixblock_size, dst_r_bpp,\
    (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld   pixblock_size, mask_bpp,\
    (mask_basereg - pixblock_size * mask_bpp / 64), MASK
PF add   PF_X, PF_X, #pixblock_size
process_pixblock_head
cache_preload 0, pixblock_size
cache_preload_simple
subs  W, W, #(pixblock_size * 2)
blt  2f
1:

process_pixblock_tail_head

I:

cache_preload_simple
subs  W, W, #pixblock_size
bge  1b
2:

process_pixblock_tail

pixst_a  pixblock_size, dst_w_bpp, \n         (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W

/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 1, 1, \n         process_pixblock_head, \n         process_pixblock_tail, \n         process_pixblock_tail_head

advance_to_next_scanline 0b
#endif

pop  {r0, r1}
endif

cleanup
pop  {r4-r12, pc} /* exit */
#endif

/* This is the start of the loop, designed to process images with small width */
/* (less than pixblock_size * 2 pixels). In this case neither pipelining */
/* nor prefetch are used. */
8:

/* Process exactly pixblock_size pixels if needed */
tst  W, #pixblock_size
beq  1f
pixld  pixblock_size, dst_r_bpp, \n       (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld  pixblock_size, mask_bpp, \n       (mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
process_pixblock_tail
pixst  pixblock_size, dst_w_bpp, \n       (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
1:
/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 0, 0, \n         process_pixblock_head, \n
process_pixblock_tail, \
process_pixblock_tail_head 

advance_to_next_scanline 8b 9:
.if regs_shortage
.pop {r0, r1}
.endif
.cleanup
.pop {r4-r12, pc} /* exit */

.purgem fetch_src_pixblock
.purgem pixld_src

.unreq SRC
.unreq MASK
.unreq DST_R
.unreq DST_W
.unreq ORIG_W
.unreq W
.unreq H
.unreq SRC_STRIDE
.unreq DST_STRIDE
.unreq MASK_STRIDE
.unreq PF_CTL
.unreq PF_X
.unreq PF_SRC
.unreq PF_DST
.unreq PF_MASK
.unreq DUMMY
.endfunc
.endm

/*
* A simplified variant of function generation template for a single
* scanline processing (for implementing pixman combine functions)
*/
.macro generate_composite_function_scanline use_nearest_scaling,\
    fname,\
    src_bpp_,\
    mask_bpp_,\
    dst_w_bpp_,\
    flags,\
    pixblock_size_,\
    init,\
    cleanup,\
    process_pixblock_head,\
    process_pixblock_tail,\
    process_pixblock_tail_head,\
dst_w_basereg_ = 28,
dst_r_basereg_ = 4,
src_basereg_   = 0,
mask_basereg_  = 24

.func fname
.global fname
/* For ELF format also set function visibility to hidden */
#ifdef __ELF__
.hidden fname
.type fname, %function
#endif
fname:
.set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
/*
* Make some macro arguments globally visible and accessible
* from other macros
*/
.set src_bpp, src_bpp_
.set mask_bpp, mask_bpp_
.set dst_w_bpp, dst_w_bpp_
.set pixblock_size, pixblock_size_
.set dst_w_basereg, dst_w_basereg_
.set dst_r_basereg, dst_r_basereg_
.set src_basereg, src_basereg_
.set mask_basereg, mask_basereg_

.if use_nearest_scaling != 0
/*
* Assign symbolic names to registers for nearest scaling
*/
    W .req r0
    DST_W .req r1
    SRC .req r2
    VX .req r3
    UNIT_X .req ip
    MASK .req lr
    TMP1 .req r4
    TMP2 .req r5
    DST_R .req r6
    SRC_WIDTH_FIXED .req r7

.macro pixld_src x:vararg
    pixld_s x
.endm

ldr    UNIT_X, [sp]
push   {r4-r8, lr}
ldr     SRC_WIDTH_FIXED, [sp, #(24 + 4)]
.endif

.ifdef mask_bpp != 0
ldr     MASK, [sp, #(24 + 8)]
.endif
.else
/*
* Assign symbolic names to registers
*/
W       .req    r0      /* width (is updated during processing) */
DST_W   .req    r1      /* destination buffer pointer for writes */
SRC     .req    r2      /* source buffer pointer */
DST_R   .req    r3      /* destination buffer pointer for reads */
MASK    .req    ip      /* mask pointer */

.macro pixld_src x:vararg
    pixld x
    .endm
.endif

.ifdef (((flags) & FLAG_DST_READWRITE) != 0)
    .set dst_r_bpp, dst_w_bpp
.else
    .set dst_r_bpp, 0
.endif
.ifdef (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
    .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
    .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif

.macro fetch_src_pixblock
    pixld_src pixblock_size, src_bpp,\
        (src_basereg - pixblock_size * src_bpp / 64), SRC
    .endm
.init
mov     DST_R, DST_W

cmp     W, #pixblock_size
blt     8f

ensure_destination_ptr_alignment process_pixblock_head,\
    process_pixblock_tail,\
    process_pixblock_tail_head

subs    W, W, #pixblock_size
blt     7f
/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
pixld_a pixblock_size, dst_r_bpp,
        (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld pixblock_size, mask_bpp,
        (mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
subs W, W, #pixblock_size
blt 2f
1:
    process_pixblock_tail_head
subs W, W, #pixblock_size
bge 1b
2:
    process_pixblock_tail
pixst_a pixblock_size, dst_w_bpp,
        (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
7:
    /* Process the remaining trailing pixels in the scanline (dst aligned) */
    process_trailing_pixels 0, 1,
                      process_pixblock_head,
                      process_pixblock_tail,
                      process_pixblock_tail_head

cleanup
.if use_nearest_scaling != 0
    pop {r4-r8, pc} /* exit */
.else
    bx lr /* exit */
.endif
8:
    /* Process the remaining trailing pixels in the scanline (dst unaligned) */
    process_trailing_pixels 0, 0,
                      process_pixblock_head,
                      process_pixblock_tail,
                      process_pixblock_tail_head

cleanup
.if use_nearest_scaling != 0
    pop {r4-r8, pc} /* exit */

.unreq DST_R
.unreq SRC
.unreq W
.unreq VX
.unreq UNIT_X
.unreq TMP1
.unreq TMP2
.unreq DST_W
.unreq MASK
.unreq SRC_WIDTH_FIXED

ELSE
  bx     lr  /* exit */

.unreq SRC
.unreq MASK
.unreq DST_R
.unreq DST_W
.unreq W

ENDIF

.purgem fetch_src_pixblock
.purgem pixld_src

.endfunc
.endm

.macro generate_composite_function_single_scanline x:vararg
  generate_composite_function_scanline 0, x
.endm

.macro generate_composite_function_nearest_scanline x:vararg
  generate_composite_function_scanline 1, x
.endm

/* Default prologue/epilogue, nothing special needs to be done */

.macro default_init
.endm

.macro default_cleanup
.endm

/*
 * Prologue/epilogue variant which additionally saves/restores d8-d15
 * registers (they need to be saved/restored by callee according to ABI).
 * This is required if the code needs to use all the NEON registers.
 */

.macro default_init_need_all_regs
  vpush    {d8-d15}
.endm

.macro default_cleanup_need_all_regs
vpop   {d8-d15}
.endm

/******************************************************************************/
/*
* Conversion of 8 r5g6b6 pixels packed in 128-bit register (in)
* into a planar a8r8g8b8 format (with a, r, g, b color components
* stored into 64-bit registers out_a, out_r, out_g, out_b respectively).
* Warning: the conversion is destructive and the original
*         value (in) is lost.
*/
.macro convert_0565_to_8888 in, out_a, out_r, out_g, out_b
  vshrn.u16   out_r, in,    #8
  vshrn.u16   out_g, in,    #3
  vsli.u16    in,    in,    #5
  vmov.u8     out_a, #255
  vsri.u8     out_r, out_r, #5
  vsri.u8     out_g, out_g, #6
  vshrn.u16   out_b, in,    #2
.endm

.macro convert_0565_to_x888 in, out_r, out_g, out_b
  vshrn.u16   out_r, in,    #8
  vshrn.u16   out_g, in,    #3
  vsli.u16    in,    in,    #5
  vsri.u8     out_r, out_r, #5
  vsri.u8     out_g, out_g, #6
  vshrn.u16   out_b, in,    #2
.endm

/******************************************************************************/
/*
* Conversion from planar a8r8g8b8 format (with a, r, g, b color components
* in 64-bit registers in_a, in_r, in_g, in_b respectively) into 8 r5g6b6
* pixels packed in 128-bit register (out). Requires two temporary 128-bit
* registers (tmp1, tmp2)
*/
.macro convert_8888_to_0565 in_r, in_g, in_b, out, tmp1, tmp2
  vshll.u8    tmp1, in_g, #8
  vshll.u8    out, in_r, #8
  vshll.u8    tmp2, in_b, #8
  vsri.u16    out, tmp1, #5
  vsri.u16    out, tmp2, #11
.endm

/******************************************************************************/
/*
* Conversion of four r5g6b5 pixels (in) to four x8r8g8b8 pixels
*/
* returned in (out0, out1) registers pair. Requires one temporary
* 64-bit register (tmp). 'out1' and 'in' may overlap, the original
* value from 'in' is lost
*/

.macro convert_four_0565_to_x888_packed in, out0, out1, tmp
  vshl.u16    out0, in,   #5  /* G top 6 bits */
  vshl.u16    tmp,  in,   #11 /* B top 5 bits */
  vsri.u16    in,   in,   #5  /* R is ready in top bits */
  vsri.u16    out0, out0, #6  /* G is ready in top bits */
  vsri.u16    tmp,  tmp,  #5  /* B is ready in top bits */
  vshr.u16    out1, in,   #8  /* R is in place */
  vsri.u16    out0, tmp,  #8  /* G & B is in place */
  vzip.u16    out0, out1      /* everything is in place */
.endm

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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python's principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.

In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

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<th>License</th>
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1.204 python-jsonschema 2.5.1 :r0

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1.205 python-netaddr 0.7.14 :r0

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1.206 python-pam 0.1.4 :r0

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from setuptools import setup, find_packages
import sys, os

version = '0.1.4'

setup(name='pam',
      version=version,
      description="PAM interface using ctypes",
      long_description="***

```python
from setuptools import setup, find_packages
import sys, os

version = '0.1.4'

setup(name='pam',
      version=version,
      description='PAM interface using ctypes',
      long_description='***
```
An interface to the Pluggable Authentication Modules (PAM) library on Linux, written in pure Python (using ctypes)

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classifiers=["Development Status :: 3 - Alpha",
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    "License :: OSI Approved :: MIT License",
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zip_safe=True,
install_requires=[],
entry_points=""""
# -*- Entry points: -*-
"""

1.207 python-psutil 2.2.1 :r0

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1.208 python-pyudev 0.16.1 :r0

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1.209 python-pyyaml 3.11 :r0
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1.210 python-smartpm 1.5+git0+407a7eca76 :r0.4

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1.211 qemu 0.13.0

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Index: debug.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/debug.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- debug.c 7 Sep 2007 10:01:21 -0000 1.1
+++ debug.c 15 Jan 2008 13:49:25 -0000 1.3
@@ -52,7 +52,11 @@
void X86EMU_trace_regs (void)
{
    if (DEBUG_TRACE()) {
-        x86emu_dump_regs();
+        if (M.x86.mode & (SYSMODE_PREFIX_DATA | SYSMODE_PREFIX_ADDR)) {
+            x86emu_dump_xregs();
+        } else {

static void print_decoded_instruction (void)
Index: ops2.c

===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops2.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- ops2.c 7 Sep 2007 10:01:21 -0000 1.1
+++ ops2.c 20 Mar 2008 15:48:34 -0000 1.3
@@ -149,8 +149,69 @@
target += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", target);
    TRACE_AND_STEP();
- if (cond)
+ if (cond) { 
    M.x86.R_IP = (u16)target;
+ JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " LONG COND ");
+ } 
+ DECODE CLEAR SEGOVR();
+ END OF INSTR();
+
+/*REMARKS:
+Handles opcode 0x0f,0xC8-0xCF*/
+*******************************************************************************
+s32 x86emu_bswap(s32 reg)
+{
+    // perform the byte swap
+    s32 temp = reg;
+    reg = (temp & 0xFF000000) >> 24;
+    reg |= (temp & 0xFF0000) >> 8;
+    reg |= (temp & 0xFF00) << 8;
+    reg |= (temp & 0xFF) << 24;
+    return reg;
void x86emuOp2_bswap(u8 op2) {
    /* byte swap 32 bit register */
    START_OF_INSTR();
    DECODE_PRINTF("BSWAP\t");
    switch (op2) {
        case 0xc8:
            DECODE_PRINTF("EAX\n");
            M.x86.R_EAX = x86emu_bswap(M.x86.R_EAX);
            break;
        case 0xc9:
            DECODE_PRINTF("ECX\n");
            M.x86.R_ECX = x86emu_bswap(M.x86.R_ECX);
            break;
        case 0xca:
            DECODE_PRINTF("EDX\n");
            M.x86.R_EDX = x86emu_bswap(M.x86.R_EDX);
            break;
        case 0xcb:
            DECODE_PRINTF("EBX\n");
            M.x86.R_EBX = x86emu_bswap(M.x86.R_EBX);
            break;
        case 0xcc:
            DECODE_PRINTF("ESP\n");
            M.x86.R_ESP = x86emu_bswap(M.x86.R_ESP);
            break;
        case 0xcd:
            DECODE_PRINTF("EBP\n");
            M.x86.R_EBP = x86emu_bswap(M.x86.R_EBP);
            break;
        case 0xce:
            DECODE_PRINTF("ESI\n");
            M.x86.R_ESI = x86emu_bswap(M.x86.R_ESI);
            break;
        case 0xcf:
            DECODE_PRINTF("EDI\n");
            M.x86.R_EDI = x86emu_bswap(M.x86.R_EDI);
            break;
    }
    TRACE_AND_STEP();
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}

/* 0xc5 */ x86emuOp2_illegal_op,
/* 0xc6 */ x86emuOp2_illegal_op,
/* 0xc7 */ x86emuOp2_illegal_op,
/* 0xc8 */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xc9 */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xca */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcb */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcc */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcd */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xce */ x86emuOp2_illegal_op, /* TODO: bswap */
/* 0xcf */ x86emuOp2_illegal_op,

/* 0xc8 */ x86emuOp2_bswap,
/* 0xc9 */ x86emuOp2_bswap,
/* 0xca */ x86emuOp2_bswap,
/* 0xcb */ x86emuOp2_bswap,
/* 0xcc */ x86emuOp2_bswap,
/* 0xcd */ x86emuOp2_bswap,
/* 0xce */ x86emuOp2_bswap,
/* 0xcf */ x86emuOp2_bswap,

/* 0xd0 */ x86emuOp2_illegal_op,
/* 0xd1 */ x86emuOp2_illegal_op,

Index: ops.c

===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops.c,v
retrieving revision 1.1
diff -u -u -r1.1 ops.c
--- ops.c 7 Sep 2007 10:01:21 -0000 1.1
+++ ops.c 20 Mar 2008 16:52:00 -0000
@@ -1061,7 +1061,11 @@
  imm = (s8)fetch_byte_imm();
  DECODE_PRINTF2("PUSH\t%d\n", imm);
  TRACE_AND_STEP();
- push_word(imm);
+ if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+   push_long(imm);
+ } else {
+   push_word(imm);
+ }
  DECODE_CLEAR_SEGOVR();
  END_OF_INSTR();
}
@@ -1256,8 +1260,10 @@
  target = (u16)(M.x86.R_IP + (s16)offset);
  DECODE_PRINTF2("%x\n", target);
  TRACE_AND_STEP();
- if (cond)
+ if (cond) {
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " NEAR COND ");
+}
```c
++ }
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
}
@@ -2516,9 +2522,11 @@
count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* move them until CX is ZERO. */
-        count = M.x86.R_CX;
+        /* move them until (E)CX is ZERO. */
+        count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+            M.x86.R_ECX = 0;
        M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
    while (count--) {
@@ -2526,6 +2534,8 @@
        store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, val);
        M.x86.R_SI += inc;
        M.x86.R_DI += inc;
+        if (M.x86.intr & INTR_HALTED)
+            break;
    }
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -2559,9 +2569,11 @@
count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* move them until CX is ZERO. */
-        count = M.x86.R_CX;
+        /* move them until (E)CX is ZERO. */
+        count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+            M.x86.R_ECX = 0;
        M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
    while (count--) {
@@ -2574,6 +2586,8 @@
        M.x86.R_SI += inc;
        M.x86.R_DI += inc;
+        if (M.x86.intr & INTR_HALTED)
+            break;
    }
```
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* REPE */
    /* move them until CX is ZERO. */
    while (M.x86.R_CX != 0) {
        /* move them until (E)CX is ZERO. */
        while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
            val1 = fetch_data_byte(M.x86.R_SI);
            val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
            cmp_byte(val1, val2);
            M.x86.R_CX -= 1;
        }
        if (M.x86.intr & INTR_HALTED)
            break;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
    TRACE_AND_STEP();
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* REPE */
        /* move them until CX is ZERO. */
        while (M.x86.R_CX != 0) {
            /* move them until (E)CX is ZERO. */
            while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
                val1 = fetch_data_long(M.x86.R_SI);
                val2 = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
                cmp_word((u16)val1, (u16)val2);
                if (M.x86.intr & INTR_HALTED)
                    break;
            }
        }
        M.x86.R_CX -= 1;
    }
    M.x86.R_SI += inc;
M.x86.R_DI += inc;
if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && ACCESS_FLAG(F_ZF) == 0 ) break;
if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
   + if (M.x86.intr & INTR_HALTED)
   +    break;
}
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
   @@ -2741,11 +2765,16 @@
    TRACE_AND_STEP();
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
      - /* move them until CX is ZERO. */
      - while (M.x86.R_CX != 0) {
      + /* move them until (E)CX is ZERO. */
      +   while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
         store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AL);
         - M.x86.R_CX -= 1;
         + M.x86.R_ECX -= 1;
         - else
         +   else
         +      M.x86.R_CX = 1;
         +      M.x86.R_ECX = 0;
         +      M.x86.R_DI += inc;
         +         if (M.x86.intr & INTR_HALTED)
         +             break;
      }
      M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
   } else {
   @@ -2783,9 +2812,11 @@
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
      /* dont care whether REPE or REPNE */
      - /* move them until CX is ZERO. */
      -    count = M.x86.R_CX;
      + /* move them until (E)CX is ZERO. */
      +    count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
      M.x86.R_CX = 0;
      + if (M.x86.mode & SYSMODE_32BIT_REP)
      +     M.x86.R_ECX = 0;
      M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    } while (count--) {
   @@ -2795,6 +2826,8 @@
       store_data_word_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AX);
    }
    M.x86.R_DI += inc;
    + if (M.x86.intr & INTR_HALTED)
    +     break;
inc = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    /* move them until CX is ZERO. */
    - while (M.x86.R_CX != 0) {
    + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        M.x86.R_AL = fetch_data_byte(M.x86.R_SI);
        - M.x86.R_CX -= 1;
        + if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX -= 1;
        else
            M.x86.R_CX -= 1;
        M.x86.R_SI += inc;
        + if (M.x86.intr & INTR_HALTED)
            break;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
        /* move them until CX is ZERO. */
        - count = M.x86.R_CX;
        + count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
        + if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX = 0;
        M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
    while (count--) {
    }
    M.x86.R_AX = fetch_data_word(M.x86.R_SI);

    M.x86.R_SI += inc;
    + if (M.x86.intr & INTR_HALTED)
        break;
}
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
inc = 1;
if (M.x86.mode & SYSMODE_PREFIX_REPE) {
    /* REPE */
    - /* move them until CX is ZERO. */
    - while (M.x86.R_CX != 0) {
        + /* move them until (E)CX is ZERO. */
        + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
            val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
            cmp_byte(M.x86.R_AL, val2);
            - M.x86.R_CX -= 1;
            + if (M.x86.mode & SYSMODE_32BIT_REP)
                M.x86.R_ECX -= 1;
            else
                M.x86.R_CX -= 1;
            M.x86.R_DI += inc;
            if (ACCESS_FLAG(F_ZF) == 0)
                break;
            + if (M.x86.intr & INTR_HALTED)
                break;
        }
    }
    M.x86.mode &= ~SYSMODE_PREFIX_REPE;
} else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
    /* REPNE */
    - /* move them until CX is ZERO. */
    - while (M.x86.R_CX != 0) {
        + /* move them until (E)CX is ZERO. */
        + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
            val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
            cmp_byte(M.x86.R_AL, val2);
            - M.x86.R_CX -= 1;
            + if (M.x86.mode & SYSMODE_32BIT_REP)
                M.x86.R_ECX -= 1;
            else
                M.x86.R_CX -= 1;
            M.x86.R_DI += inc;
            if (ACCESS_FLAG(F_ZF))
                break;    /* zero flag set means equal */
            + if (M.x86.intr & INTR_HALTED)
                break;
        }
    }
    M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
} else {
    @@ -2951,8 +3003,8 @@
    TRACE_AND_STEP();
    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
        /* REPE */
        - /* move them until CX is ZERO. */
        - while (M.x86.R_CX != 0) {
            + /* move them until (E)CX is ZERO. */
            + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
                val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
                cmp_byte(M.x86.R_AL, val2);
                - M.x86.R_CX -= 1;
                + if (M.x86.mode & SYSMODE_32BIT_REP)
                    M.x86.R_ECX -= 1;
                else
                    M.x86.R_CX -= 1;
                M.x86.R_DI += inc;
                if (ACCESS_FLAG(F_ZF) == 0)
                    break;
                + if (M.x86.intr & INTR_HALTED)
                    break;
            }
        }
    }
}
while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
        val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_long(M.x86.R_EAX, val);
    } else if (M.x86.mode & SYSMODE_PREFIX_REP) {
        M.x86.R_CX -= 1;
        if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX -= 1;
        else
            M.x86.R_CX -= 1;
        M.x86.R_DI += inc;
        if (ACCESS_FLAG(F_ZF) == 0)
            break;
        M.x86.mode &= ~SYSMODE_PREFIX_REPE;
    } else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
        /* REPNE */
        /* move them until CX is ZERO. */
        while (M.x86.R_CX != 0) {
            /* move them until (E)CX is ZERO. */
            while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
                if (M.x86.mode & SYSMODE_PREFIX_DATA) {
                    val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
                    cmp_long(M.x86.R_EAX, val);
                } else if (M.x86.mode & SYSMODE_PREFIX_REP) {
                    M.x86.R_CX -= 1;
                    if (M.x86.mode & SYSMODE_32BIT_REP)
                        M.x86.R_ECX -= 1;
                    else
                        M.x86.R_CX -= 1;
                    M.x86.R_DI += inc;
                    if (ACCESS_FLAG(F_ZF))
                        break; /* zero flag set means equal */
                    M.x86.R_CX -= 1;
                    M.x86.R_DI += inc;
                    if (ACCESS_FLAG(F_ZF))
                        break; /* zero flag set means equal */
                } else {
                    M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
                }
            }
        }
    }
}
DECODE_PRINTF("RET\u");
imm = fetch_word_imm();
DECODE_PRINTF2("%x\n", imm);
- RETURN_TRACE("RET", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
M.x86.R_IP = pop_word();
+ RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
M.x86.R_SP += imm;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -3254,9 +3316,9 @@
{
    START_OF_INSTR();
    DECODE_PRINTF(" RETn ");
- RETURN_TRACE(" RET", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
+ RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "NEAR");
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -3471,10 +3533,10 @@
DECODE_PRINTF(" RETF\n");
    imm = fetch_word_imm();
    DECODE_PRINTF2("%x\n", imm);
- RETURN_TRACE(" RETF", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
    M.x86.R_CS = pop_word();
+ RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
    M.x86.R_SP += imm;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -3488,10 +3550,10 @@
{
    START_OF_INSTR();
    DECODE_PRINTF(" RETF\n");
- RETURN_TRACE(" RETF", M.x86.saved_cs, M.x86.saved_ip);
TRACE_AND_STEP();
    M.x86.R_IP = pop_word();
    M.x86.R_CS = pop_word();
+ RETURN_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
- M.x86.R_CX = 1;
- if (M.x86.R_CX == 0 && !ACCESS_FLAG(F_ZF)) /* CX != 0 and 'ZF'*/
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
    + M.x86.R_ECX -= 1;
+ else
+ + M.x86.R_CX = 1;
+ + if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
+ !ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and 'ZF'*/
    + M.x86.R_IP = ip;
    + DECODE_CLEAR_SEGOVR();
    + END_OF_INSTR();
@@ -4041,8 +4106,11 @@
    - M.x86.R_CX -= 1;
    - if (M.x86.R_CX != 0 && ACCESS_FLAG(F_ZF))       /* CX != 0 and ZF */
+    + if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        + M.x86.R_ECX -= 1;
+    + else
+        + M.x86.R_CX -= 1;
+    + if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
+ ACCESS_FLAG(F_ZF))      /* (E)CX != 0 and ZF */
        + M.x86.R_IP = ip;
        + DECODE_CLEAR_SEGOVR();
        + END_OF_INSTR();
@@ -4062,8 +4130,11 @@
    - M.x86.R_CX -= 1;
    - if (M.x86.R_CX != 0 && ACCESS_FLAG(F_ZF))      /* CX != 0 and 'ZF'*/
+    + if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        + M.x86.R_ECX -= 1;
+    + else
+        + M.x86.R_CX -= 1;
+    + if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0)      /* (E)CX != 0 */
        + M.x86.R_IP = ip;
        + DECODE_CLEAR_SEGOVR();
        + END_OF_INSTR();
@@ -4085,8 +4156,10 @@
    - M.x86.R_CX -= 1;
    - if (M.x86.R_CX == 0)
+    + if (M.x86.R_CX == 0) {
+        + JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " CXZ ");
+    + }
+ target = (u16)(M.x86.R_IP + offset);
+ DECODE_PRINTF2("%04x\n", target);
+ TRACE_AND_STEP();
- if (M.x86.R_CX == 0)
+ if (M.x86.R_CX == 0) {
    + target = (u16)(M.x86.R_IP + offset);
    + DECODE_PRINTF2("%04x\n", target);
    + TRACE_AND_STEP();
- if (M.x86.R_CX == 0)
+ if (M.x86.R_CX == 0) {
+ } 
  DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
}
@@ -4213,6 +4286,7 @@
  ip = (s16)fetch_word_imm();
  ip += (s16)M.x86.R_IP;
  DECODE_PRINTF2("%04x", ip);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, ip, " NEAR ");
  TRACE_AND_STEP();
  M.x86.R_IP = (u16)ip;
  DECODE_CLEAR_SEGOVR();
@@ -4233,6 +4307,7 @@
  cs = fetch_word_imm();
  DECODE_PRINTF2("%04x", cs);
  DECODE_PRINTF2("%04x", ip);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, cs, ip, " FAR ");
  TRACE_AND_STEP();
  M.x86.R_IP = ip;
  M.x86.R_CS = cs;
@@ -4254,6 +4329,7 @@
  offset = (s8)fetch_byte_imm();
  target = (u16)(M.x86.R_IP + offset);
  DECODE_PRINTF2("%x", target);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, target, " BYTE ");
  TRACE_AND_STEP();
  M.x86.R_IP = target;
  DECODE_CLEAR_SEGOVR();
@@ -4357,12 +4433,14 @@
  DECODE_PRINTF("REPNE\n");
  TRACE_AND_STEP();
  M.x86.mode |= SYSMODE_PREFIX_REPNE;
+    if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        M.x86.mode |= SYSMODE_32BIT_REP;
  DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
}
@@ -4371,6 +4449,8 @@
  DECODE_PRINTF("REPE\n");
  TRACE_AND_STEP();
  M.x86.mode |= SYSMODE_PREFIX_REPE;
+    if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        M.x86.mode |= SYSMODE_32BIT_REP;
  DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
}
case 4: /* jmp word ptr ... */
    destval = fetch_data_word(destoffset);
    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, destval, " WORD ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    break;

case 5: /* jmp far ptr ... */
    destval = fetch_data_word(destoffset);
    destval2 = fetch_data_word(destoffset + 2);
    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, destval2, destval, " FAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    M.x86.R_CS = destval2;

Index: prim_ops.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/prim_ops.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- prim_ops.c 7 Sep 2007 10:01:21 -0000 1.1
+++ prim_ops.c 16 Jan 2008 14:18:15 -0000 1.3
@@ -1921,7 +1921,7 @@
 void imul_long_direct(u32 *res_lo, u32* res_hi,u32 d, u32 s)
 {
 #ifdef __HAS_LONG_LONG__
-    s64 res = (s64)d * (s64)s;
+    s64 res = (s32)d * (s64)(s32)s;
 *
     *res_lo = (u32)res;
     *res_hi = (u32)(res >> 32);
@@ -2013,7 +2013,7 @@
 void mul_long(u32 s)
 {
 #ifdef __HAS_LONG_LONG__
-    u64 res = (u32)M.x86.R_EAX * (u32)s;
+    u64 res = (u64)M.x86.R_EAX * s;
 *
     M.x86.R_EAX = (u32)res;
     M.x86.R_EDX = (u32)(res >> 32);
@@ -2312,16 +2312,15 @@
 }
 if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
     /* dont care whether REPE or REPNE */
-    /* in until CX is ZERO. */
-    u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
-        /* in until (E)CX is ZERO. */
        u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
            M.x86.R_ECX : M.x86.R_CX);
while (count--) {
    single_in(size);
    M.x86.R_DI += inc;
}
M.x86.R_CX = 0;
- if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+ if (M.x86.mode & SYSMODE_32BIT_REP) {
    M.x86.R_ECX = 0;
}
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
@@ -2355,15 +2354,15 @@
    }
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* out until CX is ZERO. */
-        u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
+        /* out until (E)CX is ZERO. */
+        u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
            M.x86.R_ECX : M.x86.R_CX);
        while (count--) {
            single_out(size);
            M.x86.R_SI += inc;
        }
        M.x86.R_CX = 0;
    - if (M.x86.mode & SYSMODE_PREFIX_DATA) {
    + if (M.x86.mode & SYSMODE_32BIT_REP) {
        M.x86.R_ECX = 0;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
Index: sys.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/sys.c,v
retrieving revision 1.1
retrieving revision 1.2
diff -u -u -r1.1 -r1.2
--- sys.c 7 Sep 2007 10:01:21 -0000 1.1
+++ sys.c 7 Sep 2007 10:03:13 -0000 1.2
@@ -45,11 +45,6 @@
 #include <x86emu/regs.h>
 #include "debug.h"
 #include "prim_ops.h"
-#ifdef LINUXBIOS_VERSION
-#include "io.h"
-#else
-#include <sys/io.h>
-#endif
#ifdef IN_MODULE
#include "xf86_ansic.h"
@@ -220,7 +215,7 @@
{
  DB(if (DEBUG_IO_TRACE())
      printk("inb %#04x \n", addr);
- return inb(addr);
+ return 0;
  }

#ifndef IN_MODULE
@@ -235,7 +230,7 @@
{
  DB(if (DEBUG_IO_TRACE())
      printk("inw %#04x \n", addr);
- return inw(addr);
+ return 0;
  }

#ifndef IN_MODULE
@@ -250,7 +245,7 @@
{
  DB(if (DEBUG_IO_TRACE())
      printk("inl %#04x \n", addr);
- return inl(addr);
+ return 0;
  }

#ifndef IN_MODULE
@@ -264,7 +259,6 @@
{
  DB(if (DEBUG_IO_TRACE())
      printk("outb %#02x -> %#04x \n", val, addr);
- outb(val, addr);
    return;
  }

#ifndef IN_MODULE
@@ -279,7 +273,6 @@
{
  DB(if (DEBUG_IO_TRACE())
      printk("outw %#04x -> %#04x \n", val, addr);
- outw(val, addr);
    return;
  }

#ifndef IN_MODULE
@@ -295,7 +288,6 @@
DB(if (DEBUG_IO_TRACE())
      printk("outl %#08x -> %#04x \n", val, addr);

void X86EMU_setMemBase(void *base, size_t size)
{
    - M.mem_base = (int) base;
    + M.mem_base = (unsigned long) base;
    M.mem_size = size;
}

Index: include/x86emu/debug.h
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/debug.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
   --- include/x86emu/debug.h 7 Sep 2007 10:01:21 -0000 1.1
   +++ include/x86emu/debug.h 20 Mar 2008 15:25:27 -0000 1.4
@@ -405,6 +397,6 @@
void X86EMU_setMemBase(void *base, size_t size)
{
    - M.mem_base = (int) base;
    + M.mem_base = (unsigned long) base;
    M.mem_size = size;
}
Index: include/x86emu/debug.h
@@ -169,14 +171,20 @@
x86emu_dump_regs();
    \   
    if (DEBUG_TRACECALL())
-   printk("%04x:%04x: CALL %s%04x:%04x\n", u , v , w , x);
+   printk("%04x:%04x: CALL %s%04x:%04x\n", u , v , w , x);
+   # define RETURN_TRACE(n,u,v)
+   # define RETURN_TRACE(u,v,w,x,s)
    + if (DEBUG_TRACECALLREGS())         
+      x86emu_dump_regs();
    + if (DEBUG_TRACECALLREGS())         
    -   printk("%04x:%04x: %s
",u,v,n);
+   printk("%04x:%04x: RET %s %04x:%04x\n",u,v,s,w,x);
+   # define  JMP_TRACE(u,v,w,x,s)                                 
+      if (DEBUG_TRACEJMPREGS())
+          x86emu_dump_regs();
+      if (DEBUG_TRACEJMP())
+          printk("%04x:%04x: JMP %s%04x:%04x\n", u , v, s , w , x);
    #else
    # define CALL_TRACE(u,v,w,x,s)
-   # define RETURN_TRACE(n,u,v)
+   # define RETURN_TRACE(u,v,w,x,s)
+   # define  JMP_TRACE(u,v,w,x,s)
    #endif

    #ifdef DEBUG
Index: include/x86emu/regs.h
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/regs.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/regs.h 7 Sep 2007 10:01:21 -0000 1.1
+++ include/x86emu/regs.h 15 Jan 2008 13:46:40 -0000 1.4
@@ -231,6 +231,9 @@
    SYSMODE_PREFIX_ADDR    | 
    -       SYSMODE_PREFIX_ADDR)
+       SYSMODE_PREFIX_ADDR    | 
+phueper: for REP(E|NE) Instructions, we need to decide wether it should be using
+the 32bit ECX register as or the 16bit CX register as count register
+#define SYSMODE_32BIT_REP  0x00000800
+#define SYSMODE_INTR_PENDING  0x10000000
+#define SYSMODE_EXTRN_INTR   0x20000000
+#define SYSMODE_HALTED       0x40000000

    SYSMODE_SEGOVR_GS      | 
    SYSMODE_SEGOVR_SS      | 
    SYSMODE_PREFIX_DATA    | 
    -       SYSMODE_PREFIX_ADDR)
+       SYSMODE_PREFIX_ADDR    | 

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+ SYSMODE_32BIT_REP)

#define INTR_SYNCH  0x1
#define INTR_ASYNCH 0x2
@@ -274,9 +278,9 @@
 */
 u32                          mode;
 volatile int                intr;  /* mask of pending interrupts */
- int                         debug;
+    volatile int                         debug;
#ifdef DEBUG
- int                         check;
+    int                         check;
 u16                         saved_ip;
 u16                         saved_cs;
 int                          enc_pos;
Index: include/x86emu/x86emu.h
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/x86emu.h,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- include/x86emu/x86emu.h 7 Sep 2007 10:01:21 -0000 1.1
+++ include/x86emu/x86emu.h 19 Oct 2007 08:42:15 -0000 1.3
@@ -47,6 +47,7 @@
#include <console.h>
#define printk(x...) printk(BIOS_DEBUG, x)
#else
+#include <stdio.h>
#define printk printf
#endif
@@ -189,6 +181,8 @@
#define DEBUG_TRACECALL_REGS_F  0x004000
#define DEBUG_DECODE_NOPRINT_F  0x008000
#define DEBUG_SAVE_IP_CS_F      0x010000
+#define DEBUG_TRACEJMP_F        0x020000
+#define DEBUG_TRACEJMP_REGS_F   0x040000
#define DEBUG_SYS_F             (DEBUG_SVC_F|DEBUG_FS_F|DEBUG_PROC_F)
void  X86EMU_trace_regs(void);
@@ -200,5 +194,4 @@
#endif /* __cplusplus */
}                          /* End of "C" linkage for C++ */
#endif
-    /* __X86EMU_X86EMU_H */
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Stefan Reinauer

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Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while
preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based
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6. As an exception to the Sections above, you may also compile or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

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it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

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-- Wolfgang Denk
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{ TARGET_FREEBSD_NR___acl_aclcheck_fd, "__acl_aclcheck_fd", "%s(%d, %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_aclcheck_file, "__acl_aclcheck_file", "%s(v"%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_aclcheck_link, "__acl_aclcheck_link", "%s(v"%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_fd, "__acl_delete_fd", "%s(%d, %d)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_file, "__acl_delete_file", "%s(v"%s", %d)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_delete_link, "__acl_delete_link", "%s(v"%s", %d)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_get_fd, "__acl_get_fd", "%s(%d, %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_get_file, "__acl_get_file", "%s(v"%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_get_link, "__acl_get_link", "%s(v"%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_set_fd, "__acl_set_fd", "%s(%d, %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_set_file, "__acl_set_file", "%s(v"%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___acl_set_link, "__acl_set_link", "%s(v"%s", %d, %#x)", NULL, NULL },
{ TARGET_FREEBSD_NR___semctl, "__semctl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR___sysctl, "__sysctl", NULL, print_sysctl, NULL },
{ TARGET_FREEBSD_NR___umtx_op, "__umtx_op", "%s(%d, %d, %d, %#x)", NULL, NULL },

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{ TARGET_FREEBSD_NR_accept, "accept", "%s(%d,%#x,%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_access, "access", "%s("%s",%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_acct, "acct", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_adjtime, "adjtime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_bind, "bind", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_break, "break", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chdir, "chdir", "%s(%s)", NULL, NULL },
{ TARGET_FREEBSD_NR_chflags, "chflags", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chmod, "chmod", "%s("%s",%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_chown, "chown", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_chroot, "chroot", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_getres, "clock_getres", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_gettime, "clock_gettime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_clock_settime, "clock_settime", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_connect, "connect", "%s(%d,%#x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_dup, "dup", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_dup2, "dup2", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_eaccess, "eaccess", "%s("%s",%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_execve, "execve", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_exit, "exit", "%s(%d)\n", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattrctl, "exitattrctl", "%s("%s",%d,%d,%d,%d,"%s")", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_delete_fd, "exitattr_delete_fd", "%s(%d,%d,"%s")", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_delete_file, "exitattr_delete_file", "%s("%s",%d,%d,"%s")", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_delete_link, "exitattr_delete_link", "%s("%s",%d,"%s")", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_get_fd, "exitattr_get_fd", "%s(%d,%d,"%s",%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_get_file, "exitattr_get_file", "%s("%s",%d,"%s",%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_get_link, "exitattr_get_link", "%s("%s",%d,"%s",%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_list_fd, "exitattr_list_fd", "%s(%d,%d,%x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_list_file, "exitattr_list_file", "%s("%s",%d,%x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_list_link, "exitattr_list_link", "%s("%s",%d,%x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_set_fd, "exitattr_set_fd", "%s(%d,%d,"%s",%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_set_file, "exitattr_set_file", "%s("%s",%d,"%s",%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_exitattr_set_link, "exitattr_set_link", "%s("%s",%d,"%s",%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_fchdir, "fchdir", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fchflags, "fchflags", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fchmod, "fchmod", "%s(%d,%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_fchown, "fchown", "%s(%d,%d,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_fcntl, "fcntl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fexecve, "fexecve", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fhopen, "fhopen", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fhstat, "fhstat", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fhstatfs, "fhstatfs", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_flock, "flock", NULL, NULL, NULL },

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{ TARGET_FREEBSD_NR_fork, "fork", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_fpathconf, "fpathconf", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_fstat, "fstat", "%s(%d,%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_fstatfs, "fstatfs", "%s(%d,%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_freebsd_mmap, "freebsd6_mmap", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getcontext, "getcontext", "%s(%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_getdirentries, "getdirentries", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getfh, "getfh", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getfsstat, "getfsstat", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getgid, "getgid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_getgroups, "getgroups", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getitimer, "getitimer", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getlogin, "getlogin", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getpeername, "getpeername", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getpgid, "getpgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getpriority, "getpriority", "%s(%#x,%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_getresgid, "getresgid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getresuid, "getresuid", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getrlimit, "getrlimit", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getrusage, "getrusage", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getsockname, "getsockname", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getsockopt, "getsockopt", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_gettimeofday, "gettimeofday", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_getuid, "getuid", "%s()", NULL, NULL },
{ TARGET_FREEBSD_NR_ioctl, "ioctl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_lchown, "lchown", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_link, "link", "%s(""%s",""%s")", NULL, NULL },
{ TARGET_FREEBSD_NR_lstat, "lstat", "%s(""%s",%p)", NULL, NULL },
{ TARGET_FREEBSD_NR_madvise, "madvise", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mincore, "mincore", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_minherit, "minherit", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mkdir, "mkdir", "%s(%s,%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_mkfifo, "mkfifo", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mknod, "mknod", "%s(%s,%#o,%#x)", NULL, NULL },
{ TARGET_FREEBSD_NR_mlock, "mlock", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mlockall, "mlockall", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mmap, "mmap", NULL, NULL, print_syscall_ret_addr },
{ TARGET_FREEBSD_NR_mmount, "mmount", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_mprotect, "mprotect", "%s(%#x,%#x,%d)", NULL, NULL },
{ TARGET_FREEBSD_NRmsgctl, "msgctl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_msgget, "msgget", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_msgrcv, "msgrcv", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_msgsnd, "msgsnd", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_msync, "msync", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_munlock, "munlock", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_munlockall, "munlockall", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_munmap, "munmap", "%s(%p,%d)", NULL, NULL },
{ TARGET_FREEBSD_NR_nanosleep, "nanosleep", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_nfssvc, "nfssvc", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_open, "open", "%s(%d,%#d,%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_openat, "openat", "%s(%d, "%s",%#x,%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_pathconf, "pathconf", "%s(%s,%#x,%#o)", NULL, NULL },
{ TARGET_FREEBSD_NR_pathconf, "pathconf", "%s(%s,%#d)", NULL, NULL },
{ TARGET_FREEBSD_NR_pipe, "pipe", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_poll, "poll", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_pread, "pread", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_preadv, "preadv", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_pwrite, "pwrite", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_pwritev, "pwritev", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_quotactl, "quotactl", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_read, "read", "%s(%d,%#x,%#d)", NULL, NULL },
{ TARGET_FREEBSD_NR_readlink, "readlink", "%s(%s,%#x,%#d)", NULL, NULL },
{ TARGET_FREEBSD_NR_readv, "readv", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_reboot, "reboot", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_recvfrom, "recvfrom", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_recvmsg, "recvmsg", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rename, "rename", "%s(%s,%s"
"%s)", NULL, NULL },
{ TARGET_FREEBSD_NR_revoke, "revoke", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rfork, "rfork", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rmdir, "rmdir", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_rtprio_thread, "rtprio_thread", "%s(%d,%d, %p)", NULL, NULL },
{ TARGET_FREEBSD_NR_sbrk, "sbrk", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sched_yield, "sched_yield", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_select, "select", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_semget, "semget", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_semop, "semop", NULL, NULL, NULL },
{ TARGET_FREEBSD_NR_sendmsg, "sendmsg", NULL, NULL, NULL },
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This package was put together by Matthias Klose <doko@debian.org>, derived from the bash package by Guy Maor <maor@debian.org>, from the GNU sources at ftp.gnu.org:/pub/gnu/readline/readline-6.0.tar.gz.

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```
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1.215 rpm 5.4.14 :r0.6

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- Seth Arnold, for contributing to the documentation.
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If I've missed anyone, it's due to oversight. Drop me a line and I'll
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THE BASIC LIBRARY FUNCTIONS
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PCRE JUST-IN-TIME COMPILATION SUPPORT
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Written by: Zoltan Herczeg
Email local part: hzmester
Email domain: freemail.hu

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============
Test 1.1:101
============
Testing argument parsing
First type
e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_2
e1_l1_3
e1_l1_3
e1_l1_4
e1_l1_4
e1_l1_5
e1_l1_5
e1_l1_6
e1_l1_6
e1_l1_7
e1_l1_7
e1_l1_8
e1_l1_8
e1_l1_9
e1_l1_9
e1_l1_10
e1_l1_10
e1_l1_11
e1_l1_11
e1_l1_12
e1_l1_12
e1_l1_13
e1_l1_13
e1_l1_14
e1_l1_14
Test 1.2:102

e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
e1_l1_5
e1_l1_6
e1_l1_7
e1_l1_8
e1_l1_9
e1_l1_10
e1_l1_11
e1_l1_12
e1_l1_13
e1_l1_14

Test 1.3:103

e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_2
e1_l1_3
e1_l1_3
e1_l1_4
e1_l1_4
e1_l1_5
e1_l1_5
e1_l1_6
e1_l1_6
e1_l1_7
e1_l1_7
e1_l1_8
e1_l1_8
e1_l1_9
e1_l1_9
e1_l1_10
e1_l1_10
e1_l1_11
e1_l1_11
e1_l1_12
e1_l1_12
e1_l1_13
e1_l1_13
Test 1.4:104
==============
el_1_1
el_1_2
el_1_3
el_1_4
el_1_5
el_1_6
el_1_7
el_1_8
el_1_9
el_1_10
el_1_11
el_1_12
el_1_13
el_1_14
Second type

Test 1.4.1:105
==============
l_1_1
l_1_2
l_1_3
l_1_4
l_1_5
l_1_6
l_1_7
l_1_8
l_1_9
l_1_10
l_1_11
l_1_12
l_1_13
l_1_14

Test 1.5:106
==============
s_1_1
s_1_2
s_1_3
s1_l1_3
s1_l1_4
s1_l1_4
s1_l1_5
s1_l1_5
s1_l1_6
s1_l1_6
s1_l1_7
s1_l1_7
s1_l1_8
s1_l1_8
s1_l1_9
s1_l1_9
s1_l1_10
s1_l1_10
s1_l1_11
s1_l1_11
s1_l1_12
s1_l1_12
s1_l1_13
s1_l1_13
s1_l1_14
s1_l1_14

============= Test 1.6:107 =============
---
Test 1.7:108
---
e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_2
e1_l1_3
e1_l1_3
e1_l1_4
e1_l1_4
e1_l1_5
e1_l1_5
e1_l1_6
e1_l1_6
e1_l1_7
e1_l1_7
e1_l1_8
e1_l1_8
e1_l1_9
e1_l1_9
e1_l1_10
e1_l1_10
e1_l1_11
e1_l1_11
e1_l1_12
e1_l1_12
e1_l1_13
e1_l1_13
e1_l1_14

e1_l1_14
---
Test 1.8:109
---
e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_2
e1_l1_3
el_I1_3
el_I1_4
el_I1_4
el_I1_5
el_I1_5
el_I1_6
el_I1_6
el_I1_7
el_I1_7
el_I1_8
el_I1_8
el_I1_9
el_I1_9
el_I1_10
el_I1_10
el_I1_11
el_I1_11
el_I1_12
el_I1_12
el_I1_13
el_I1_13
el_I1_14
el_I1_14

============
Test 1.9:110
============
s1_I1_1
s1_I1_2
s1_I1_3
s1_I1_4
s1_I1_5
s1_I1_6
s1_I1_7
s1_I1_8
s1_I1_9
s1_I1_10
s1_I1_11
s1_I1_12
s1_I1_13
s1_I1_14

============
Test 1.10:111
============
s1_I1_1
s1_I1_2
s1_I1_3
s1_l1_4
s1_l1_5
s1_l1_6
s1_l1_7
s1_l1_8
s1_l1_9
s1_l1_10
s1_l1_11
s1_l1_12
s1_l1_13
s1_l1_14

============
Test 1.11:112
============
e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
e1_l1_5
e1_l1_6
e1_l1_7
e1_l1_8
e1_l1_9
e1_l1_10
e1_l1_11
e1_l1_12
e1_l1_13
e1_l1_14

============
Test 1.12:113
============
e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
e1_l1_5
e1_l1_6
e1_l1_7
e1_l1_8
e1_l1_9
e1_l1_10
e1_l1_11
e1_l1_12
e1_l1_13
e1_l1_14
Test 1.13:114

Test 1.14:115
s1_l1_1
s2_s1_l1_1
s2_s1_l1_1
s1_l1_2
s2_s1_l1_2
s2_s1_l1_2
s1_l1_3
s2_s1_l1_3
s2_s1_l1_3
s1_l1_4
s2_s1_l1_4
s2_s1_l1_4
s1_l1_5
s2_s1_l1_5
s2_s1_l1_5
s1_l1_6
s2_s1_l1_6
s2_s1_l1_6
s1_l1_7
s2_s1_l1_7
s2_s1_l1_7
s1_l1_8
s2_s1_l1_8
s2_s1_l1_8
s1_l1_9
s2_s1_l1_9
s2_s1_l1_9
s1_l1_10
s2_s1_l1_10
s2_s1_l1_10
s1_l1_11
s2_s1_l1_11
s2_s1_l1_11
s1_l1_12
s2_s1_l1_12
s2_s1_l1_12
s1_l1_13
s2_s1_l1_13
s2_s1_l1_13
s1_l1_14
s2_s1_l1_14
s2_s1_l1_14

=============
Test 1.15:116
============== e1_l1_1
sl_el_l1_1
sl_el_l1_1
e1_l1_2
sl_el_l1_2
sl_el_l1_2
e1_l1_3
sl_el_l1_3
sl_el_l1_3
e1_l1_4
sl_el_l1_4
sl_el_l1_4
e1_l1_5
sl_el_l1_5
sl_el_l1_5
e1_l1_6
sl_el_l1_6
sl_el_l1_6
e1_l1_7
sl_el_l1_7
sl_el_l1_7
e1_l1_8
sl_el_l1_8
sl_el_l1_8
e1_l1_9
sl_el_l1_9
sl_el_l1_9
e1_l1_10
sl_el_l1_10
sl_el_l1_10
e1_l1_11
sl_el_l1_11
sl_el_l1_11
e1_l1_12
sl_el_l1_12
sl_el_l1_12
e1_l1_13
sl_el_l1_13
sl_el_l1_13
e1_l1_14
sl_el_l1_14
sl_el_l1_14

============= Test 1.16:117 =============
e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_12
e1_l1_13
e1_l1_13
e1_l1_14
e1_l1_14

============
Test 1.17:118
============
l1_1
l1_1
l1_2
l1_2
l1_3
l1_3
l1_4
l1_4
l1_5
l1_5
l1_6
l1_6
l1_7
l1_7
l1_8
l1_8
l1_9
l1_9
l1_10
l1_10
l1_11
l1_11
l1_12
l1_12
l1_13
l1_13
l1_14
l1_14

============
Test 1.18:119
============
l1_1
l1_2
l1_3
l1_4
l1_5
l1_6
l1_7
Testing address ranges

================
Test 2.1:120
================
I1_4

================
Test 2.2:121
================
I2_6

================
Test 2.3:122
================
I1_14

================
Test 2.4:123
================
I2_9

================
Test 2.5:124
================

================
Test 2.6:125
================
I2_9

================
Test 2.7:126
================

================
Test 2.9:127
================
I1_7
Test 2.10:128
==============
l1_7

==============
Test 2.11:129
==============
l1_7

==============
Test 2.12:130
==============
l1_1
l1_2
l1_3
l1_4

==============
Test 2.13:131
==============
l1_1
l1_2
l1_3
l1_4
l1_5
l1_6
l1_7
l1_8
l1_9
l1_10
l1_11
l1_12
l1_13
l1_14
l2_1
l2_2
l2_3
l2_4
l2_5
l2_6
l2_7
l2_8
l2_9

==============
Test 2.14:132
==============
l1_1
Test 2.16:134
===============
11_4
11_5
11_6
11_7
11_8
11_9
11_10
11_11
11_12
11_13
11_14
12_1
12_2
12_3
12_4
12_5
12_6

===============
Test 2.17:135
===============
11_4
11_5
11_6
11_7
11_8
11_9
11_10
11_14
12_1
12_2
12_3
12_4
12_5
12_6
12_7
12_8
12_9

===============
Test 2.18:136
===============
12_3
12_4
12_5
12_6
Brace and other grouping

Test 3.1:139

Test 3.2:140
Testing a c d and i commands
after_ibeefore_il1_3
before_il1_4
after_ibebefore_il1_4
before_il1_5
after_ibebefore_il1_5
before_il1_6
after_ibebefore_il1_6
before_il1_7
after_ibebefore_il1_7
before_il1_8
after_ibebefore_il1_8
before_il1_9
after_ibebefore_il1_9
before_il1_10
after_ibebefore_il1_10
before_il1_11
after_ibebefore_il1_11
before_il1_12
after_ibebefore_il1_12
before_il1_13
after_ibebefore_il1_13
before_il1_14
after_ibebefore_il1_14
before_il2_1
after_ibebefore_il2_1
before_il2_2
after_ibebefore_il2_2
before_il2_3
after_ibebefore_il2_3
before_il2_4
after_ibebefore_il2_4
before_il2_5
after_ibebefore il2_5
before_il2_6
inserted
after_ibebefore il2_6
before il2_7
after_ibebefore il2_7
before il2_8
after_ibebefore il2_8
before il2_9
after_ibebefore il2_9

============
Test 4.2.144
============
before_all_1
after_abefore_all_1
before_al2_8
after_abefore_al2_8
before_al2_9
after_abefore_al2_9

==============
Test 4.3:145
==============
^l1_1
^l1_1$
appended
^l1_2
^l1_2$
appended
^l1_3
^l1_3$
appended
^l1_4
^l1_4$
appended
^l1_5
^l1_5$
appended
^l1_6
^l1_6$
appended
^l1_7
^l1_7$
appended
^l1_8
^l1_8$
appended
^l1_8
l1_9$
^l1_10
appended
^l1_10
l1_11$
^l1_12
^l1_12$
appended
^l1_13
^l1_13$
appended
^l1_14
^l1_14$
appended
^l2_1
^l2_1$
Test 4.4:146

hello

Test 4.5:147

hello

Test 4.6:148

hello

Test 4.7:149


hello

==============
Test 4.8:150
==============
Testing labels and branching

==============
Test 5.1:151
==============
label2_l1_1
label3_label2_l1_1
label1_l1_2
label1_l1_3
label1_l1_4
label1_l1_5
label1_l1_6
label1_l1_7
label1_l1_8
label1_l1_9
label1_l1_10
label1_l1_11
label1_l1_12
label2_l1_13
label3_label2_l1_13
label2_l1_14
label3_label2_l1_14

==============
Test 5.2:152
==============
tested l2_1
tested l2_2
tested l2_3
tested l2_4
tested l2_5
tested l2_6
tested l2_7
tested l2_8
tested l2_9
tested l2_10
tested l2_11
tested l2_12
tested l2_13
tested l2_14

==============
Test 5.3:153
Test 5.7:157

Test 5.8:158

Pattern space commands

Test 6.1:159
Test 6.2:160

l1_1
l1_2
l1_3
l1_5
l1_6
l1_7
l1_8
l1_9
l1_10
l1_11
l1_12
l1_13
l1_14

Test 6.3:161

l1_5
l1_6
l1_7
l1_8
l1_9
l1_10
l1_11
l1_12
l1_13
l1_14

Test 6.4:162

l1_1
l1_2
l1_3
l1_2
l1_3
l1_5
l1_2
l1_3
l1_2
l1_3
l1_6
l1_6
l1_7
Test 6.5:163

Test 6.6:164

Testing print and file routines

Test 7.1:165
Test 7.2:166

Test 7.3:167
II_10
II_11
II_12
II_13
II_14
w results
II_3
II_4
II_5
II_6
II_7
II_8
II_9
II_10
II_11
II_12

===============
Test 7.4:168
===============
II_1
II_2
II_3
II_4
II_5
II_6
II_7
II_8
II_9
II_5
II_6
II_7
II_8
II_9
II_10
II_11
II_12
II_13
II_14

===============
Test 7.5:169
===============
II_1
Testing substitution commands
Test 8.2:173

Test 8.3:174

Test 8.4:175

I1_1
II_2
II_3
II_4
II_5
II_6
II_7
II_8
II_9
II_10
II_11
II_12
II_13
II_14

=============
Test 8.5:176
=============
IIX1
IIX2
IIX3
IIX4
IIX5
IIX6
IIX7
IIX8
IIX9
IIX10
IIX11
IIX12
IIX13
IIX14

=============
Test 8.6:177
=============
(1)(1)(1)(1)
(1)(1)(1)(2)
(1)(1)(1)(3)
(1)(1)(1)(4)
(1)(1)(1)(5)
(1)(1)(1)(6)
(1)(1)(1)(7)
(1)(1)(1)(8)
(1)(1)(1)(9)
(1)(1)(1)(10)
(1)(1)(1)(11)
(1)(1)(1)(12)
(1)(1)(1)(13)
I1_X0
I1_X1
I1_X2
I1_X3
I1_X4

==================
Test 8.11:182
==================
IX_1
IX_2
IX_3
IX_4
IX_5
IX_6
IX_7
IX_8
IX_9
IX_10
IX_11
IX_12
IX_13
IX_14
s wfile results
IX_1
IX_2
IX_3
IX_4
IX_5
IX_6
IX_7
IX_8
IX_9
IX_10
IX_11
IX_12
IX_13
IX_14

==================
Test 8.12:183
==================
IX_X
IX_X
IX_X
IX_4
IX_5
IX_6
IX_7
IX_8
IX_9
IX_10
IX_11
IX_12
IX_13
IX_14
Test 8.13:184

Test 8.14:185

Test 8.15:186
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- The make-ssh-known-hosts script is no longer included
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This is the Debian GNU/Linux prepackaged version of System V Init.

Init was written by Miquel van Smoorenburg <miquels@cistron.nl>.

This package was first put together by Bruce Perens <Bruce@Pixar.com> from pre-distribution sources. Ian Murdock <imurdock@debian.org> integrated it into the base system maintained it until the end of 1995. Miquel van Smoorenburg <miquels@cistron.nl> implemented Debian support.

In version 2.85 the package was split into three (sysvinit, sysv-rc, and initscripts) in order to make room for alternative rc mechanisms.

The upstream source is available at:

Primary-Site:   ftp.cistron.nl /pub/people/miquels/software
                92K sysvinit-2.86.tar.gz
Alternate-Site: sunsite.unc.edu /pub/Linux/system/daemons/init
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The sysvinit package in Debian is maintained as an alioth project.
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The of the start-stop-daemon

* A rewrite of the original Debian's start-stop-daemon Perl script
* in C (faster - it is executed many times during system startup).
Written by Marek Michalkiewicz <marekm@i17linuxb.ists.pwr.wroc.pl>,
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This is the Debian GNU/Linux prepackaged version of System V Init.

Init was written by Miquel van Smoorenburg <miquels@cistron.nl>.

This package was first put together by Bruce Perens <Bruce@Pixar.com> from pre-distribution sources. Ian Murdock <imurdock@debian.org> integrated it into the base system maintained it until the end of 1995. Miquel van Smoorenburg <miquels@cistron.nl> implemented Debian support. In version 2.85 the package was split into three (sysvinit, sysv-rc, and initscripts) in order to make room for alternative rc mechanisms.

The upstream source is available at:

Primary-Site:   ftp.cistron.nl /pub/people/miquels/software
               92K sysvinit-2.86.tar.gz
Alternate-Site: sunsite.unc.edu /pub/Linux/system/daemons/init
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The sysvinit package in Debian is maintained as an alioth project. The project page is http://alioth.debian.org/projects/pkg-sysvinit/.

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1.232 tunctl 1.5 :r0
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/* Copyright 2002 Jeff Dike
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   */

#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include <errno.h>
#include <fcntl.h>
#include <unistd.h>
#include <pwd.h>
#include <grp.h>
#include <net/if.h>
#include <sys/ioctl.h>
#include <linux/if_tun.h>

/* TUNSETGROUP appeared in 2.6.23 */
#ifdef TUNSETGROUP
#define TUNSETGROUP _IOW('T', 206, int)
#endif

static void Usage(char *name)
{
    fprintf(stderr, "Create: %s [-b] [-u owner] [-g group] [-t device-name] "
    "[-p|-n] [-f tun-clone-device]\n", name);
    fprintf(stderr, "Delete: %s -d device-name [-f tun-clone-device]\n", name);
    fprintf(stderr, "The default tun clone device is /dev/net/tun - some systems" 
    " use /dev/misc/net/tun instead\n");
    fprintf(stderr, "-b will result in brief output (just the device name)\n");
    fprintf(stderr, "-n will result in a point-to-point tun device\n");
    fprintf(stderr, "-p in an ethernet tap device. Default is a tap\n");
    fprintf(stderr, " except the device contains \"tun\" in the name.\n");
    exit(1);
}

int main(int argc, char **argv)
{
    struct ifreq ifr;
    struct passwd *pw;
    struct group *gr;
    uid_t owner = -1;
    gid_t group = -1;
    int tap_fd, opt, delete = 0, brief = 0, type = 0;
    char *tun = "", *file = "/dev/net/tun", *name = argv[0], *end;

    while((opt = getopt(argc, argv, "bd:f:npt:u:g:h")) > 0){
        switch(opt) {
        case 'b':
            brief = 1;
            break;
        case 'd':
            delete = 1;
            break;
        case 'p':
            if(type != 0)
                Usage(name);
            type = IFF_TAP;
            break;
        case 'n':
            if(type != 0)
                Usage(name);
type = IFF_TUN;
break;
case 'u':
pw = getpwnam(optarg);
if(pw != NULL){
    owner = pw->pw_uid;
    break;
}
    owner = strtol(optarg, &end, 0);
if(*end != '0'){
    fprintf(stderr, ""%s is neither a username nor a numeric uid.\n", optarg);
    Usage(name);
    break;
    case 'g':
gr = getgrnam(optarg);
    if(gr != NULL){
        group = gr->gr_gid;
        break;
    }
    group = strtol(optarg, &end, 0);
if(*end != '0'){
    fprintf(stderr, ""%s is neither a groupname nor a numeric group.\n", optarg);
    Usage(name);
    break;
    case 't':
tun = optarg;
    break;
    case '?':
case 'h':
case 'H':
default:
    Usage(name);
    }
}
argv += optind;
ar gc -= optind;
if(argc > 0)
Usage(name);
if((tap_fd = open(file, O_RDWR)) < 0){
    fprintf(stderr, "Failed to open '%s': ", file);
perror("\n");
exit(1);
}

if(type == 0) {
    type = strstr(tun, "tun") ? IFF_TUN : IFF_TAP;
}

memset(&ifr, 0, sizeof(ifr));

ifr.ifr_flags = type | IFF_NO_PI;
strncpy(ifr.ifr_name, tun, sizeof(ifr.ifr_name) - 1);
if(ioctl(tap_fd, TUNSETIFF, (void *) &ifr) < 0){
    perror("TUNSETIFF");
    exit(1);
}

if(delete){
    if(ioctl(tap_fd, TUNSETPERSIST, 0) < 0){
        perror("disabling TUNSETPERSIST");
        exit(1);
    }
    printf("Set '%s' nonpersistent\n", ifr.ifr_name);
} else {
    /* emulate behaviour prior to TUNSETGROUP */
    if(owner == -1 && group == -1) {
        owner = geteuid();
    }

    if(owner != -1) {
        if(ioctl(tap_fd, TUNSETOWNER, owner) < 0){
            perror("TUNSETOWNER");
            exit(1);
        }
    }

    if(group != -1) {
        if(ioctl(tap_fd, TUNSETGROUP, group) < 0){
            perror("TUNSETGROUP");
            exit(1);
        }
    }

    if(ioctl(tap_fd, TUNSETPERSIST, 1) < 0){
        perror("enabling TUNSETPERSIST");
        exit(1);
    }
}

if(brief)
printf("%s\n", ifr.ifr_name);
else {
    printf("Set '%s' persistent and owned by", ifr.ifr_name);
    if(owner != -1)
        printf(" uid %d", owner);
    if(group != -1)
        printf(" gid %d", group);
    printf("\n");
}
}
return(0);
}

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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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1.236 update-rc.d 0.7 :r5

1.236.1 Available under license:

#!/bin/sh
#
# update-rc.d Update the links in /etc/rc[0-9S].d/
#
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#
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initd="/etc/init.d"
etcd="/etc/rc"
notreally=0
force=0
dostart=0
verbose=0

usage()
{
cat >&2 <<EOF
   update-rc.d [-n] [-r <root>] [-s] <basename> defaults [NN | sNN kNN]
   -n: not really
   -f: force
   -v: verbose
   -r: alternate root path (default is /)
   -s: invoke start methods if appropriate to current runlevel
EOF
}

checklinks()
{
local i dn fn remove=0
if [ "x$1" = "xremove" ]; then
   echo "Removing any system startup links for $bn ..."
   remove=1
}
for i in 0 1 2 3 4 5 6 7 8 9 S; do
dn="$(etcd)${i}.d"
if [ ! -d $dn ]; then
    continue;
fi
for f in $(dn)/[SK]?${bn}; do
    if [ -L $f ]; then
        if [ $remove -eq 0 ]; then
            return 1
        fi
        echo " $f"
        if [ $notreally -eq 1 ]; then
            continue
        fi
        rm $f
        fi
    done
done
return 0
}
dolink()
{
    startstop=$1
    lev=`echo $2 | cut -d/ -f1`
nn=`echo $2 | cut -d/ -f2`
fn="$(etcd)${lev}.d/${startstop}${nn}${bn}"
    [ $verbose -eq 1 ] && echo " $fn -> ../init.d/$bn"
    if [ $notreally -eq 0 ]; then
        mkdir -p `dirname $fn`
        ln -s ../init.d/$bn $fn
        fi
    if [ $dostart -eq 1 ] && [ $startstop = "S" ] && [ $lev = $RUNLEVEL ]; then
        $fn start || true
    fi
}
makelinks()
{
    if ! checklinks; then
        echo " System startup links for $initd/$bn already exist."
        if [ $dostart -eq 1 ] && [ $notreally -eq 0 ] && [ -L $(etcd)$RUNLEVEL..d/S??${bn} ]; then
            $(etcd)${RUNLEVEL}.d/S??${bn} restart || true
        fi
        exit 0
    fi
fi

echo " Adding system startup for $initd/$bn."

for i in $startlinks; do
dolink S $i
done
for i in $stoplinks; do
dolink K $i
done
}

while [ $# -gt 0 ]; do
case $1 in
  -n) notreally=1
    shift
    continue
    ;;
  -v) verbose=1
    shift
    continue
    ;;
  -f) force=1
    shift
    continue
    ;;
  -s) dostart=1
    shift
    continue
    ;;
  -r) shift
    root=$1
    initd="$root$initd"
    etcd="$root$etcd"
    shift
    ;;
  -h | --help)
    usage
    exit 0
    ;;
  -*)
    usage
    exit 1
    ;;
  *)
    break
    ;;
esac
done

if [ $# -lt 2 ]; then
  usage
  exit 1
fi

bn=$1
shift

sn=$initd/$bn
if [ -L "$sn" -a -n "$root" ]; then
  if which readlink >/dev/null; then
    while true; do
      linksn="$(readlink "$sn")"
      if [ -z "$linksn" ]; then
        break
      fi
      case "$sn" in
        */)
          sn="$root$sn"
        ;;
        *)
          sn="$initd/$sn"
        ;;
      esac
      done
    else
      echo "update-rc.d: readlink tool not present, cannot check whether \n$sn symlink points to a valid file." >&2
    fi
  fi

if [ $1 != "remove" ]; then
  if [ ! -f "$sn" ]; then
    echo "update-rc.d: $initd/$bn: file does not exist" >&2
    exit 1
  fi
  else
    if [ -f "$sn" ]; then
      if [ $force -eq 1 ]; then
        echo "update-rc.d: $initd/$bn exists during rc.d purge (continuing)" >&2
      else
        echo "update-rc.d: $initd/$bn exists during rc.d purge (use -f to force)" >&2
      fi
    fi
  fi

if [ $dostart -eq 1 ]; then
RUNLEVEL=`sed 's/.*\[\(.*\)\]\]\]\] < /proc/1/cmdline'`
RUNLEVEL=`runlevel | cut -d" " -f2`
if [ "x$RUNLEVEL" = "x" ]; then
    echo "Unable to determine current runlevel" >&2
    exit 1
fi

case $1 in
    remove)
        checklinks "remove"
        ;;
    defaults)
        if [ $# -gt 3 ]; then
            echo "defaults takes only one or two arguments" >&2
            usage
            exit 1
        fi
        start=20
        stop=20
        if [ $# -gt 1 ]; then
            start=$2
            stop=$2
            fi
        if [ $# -gt 2 ]; then
            stop=$3
            fi
        start=`printf %02d $start`
        stop=`printf %02d $stop`
        stoplinks="0/$stop 1/$stop 6/$stop"
        startlinks="2/$start 3/$start 4/$start 5/$start"
        makelinks
        ;;
    start | stop)
        while [ $# -gt 0 ]; do
            if [ $1 = "start" ]; then
                letter=S
            elif [ $1 = "stop" ]; then
                letter=K
            else
                echo "expected start or stop" >&2
                usage
                exit 1
            fi
            shift
            NN=`printf \%02d $(expr $1 + 0)`
            shift
            #NN=`printf %02d $(expr $1 + 0)`
            #NN=`printf %02d $(expr $1 + 0)`
        esac
        fi
    esac
fi
shift
while [ "x$1" != "x." ]; do
    if [ $# -eq 0 ]; then
        echo "action with list of runlevels not terminated by \\." >&2
        exit 1
    fi
    level=$1
    shift
    case $letter in
        S) startlinks="$startlinks $level/$NN" ;;
        K) stoplinks="$stoplinks $level/$NN" ;;
    esac
done
    shift
done
    makelinks
    ;;

*)
usage
exit 1
;;
esac

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Version 2, June 1991

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However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

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We call this license the "Lesser" General Public License because it does Less to protect the user's freedom than the ordinary General Public License. It also provides other free software developers Less of an advantage over competing non-free programs. These disadvantages are the reason we use the ordinary General Public License for many libraries. However, the Lesser license provides advantages in certain special circumstances.

For example, on rare occasions, there may be a special need to encourage the widest possible use of a certain library, so that it becomes a de-facto standard. To achieve this, non-free programs must be allowed to use the library. A more frequent case is that a free library does the same job as widely used non-free libraries. In this case, there is little to gain by limiting the free library to free software only, so we use the Lesser General Public License.

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Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

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1.241 util-linux 2.17.2-0ubuntu1

1.241.1 Available under license:

size: 8388608, sector size: 512, PT: dos, offset: 446

---
#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
size: 4456448, sector size: 512, PT: bsd, offset: 512
---
#1: 7936 4864 0x7
#2: 12544 3584 0x7

...
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This is the Debian GNU/Linux prepackaged version of util-linux.

This package was put together by Guy Maor <maor@debian.org> and later maintained by Sean 'Shaleh' Perry <shaleh@debian.org>, and Adrian Bunk <bunk@stusta.de>.

The current maintainer is LaMont Jones <lamont@debian.org>.
See also: git://git.debian.org/~lamont/util-linux.git

It was downloaded from:

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Upstream Author: Theodore Ts'o <tytso@mit.edu>

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Initialize empty image
f1c9645db5c14efddc7d8a322685f26eb bsd.img
Create new DOS partition table
57e721e35d1266e2df055067c18f2cf9 bsd.img

---layout--------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Create 1st primary partition
ada64ace122978d00d1c0e5ee45d26 bsd.img

---layout--------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type
<removed>1 2048 4095 2048 1M 83 Linux

-------------------
Create 2st primary partition
1beb87248e05d6e4e62b749da65d023 bsd.img
Set 2nd partition type
2d8e8dffe51a88a045db233418dd73fbe bsd.img

---layout--------
Device     Boot Start   End Sectors Size Id Type
<removed>1 2048 4095  2048 1M 83 Linux
<removed>2 4096 20479 16384  8M a5 FreeBSD

Create default BSD
2e1cee529eb59c9341afef0443f196a1 bsd.img

---layout-------
Welcome to fdisk. Changes will remain in memory only, until you decide to write them. Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors
Geometry: 255 heads, 63 sectors/track, 1 cylinders
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: bsd
partitions: 4

Slice Start   End Sectors  Size Type  Fsize Bsize Cpg
c 4096 20479  16384  8M unused 0 0 0
d 0 16064  16065  7.9M unused 0 0 0

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):
Welcome to fdisk <removed>.
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors
Geometry: 255 heads, 63 sectors/track, 1 cylinders
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: bsd
partitions: 4

Slice Start   End Sectors  Size Type     Fsize Bsize Cpg
a  4096  6144    2049    1M 4.2BSD       0     0   0
b  4096  20479   16384    8M unused       0     0   0
c  0  16064   16065  7.9M unused       0     0   0

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):

Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help):
0 unused      5 4.1BSD     9  4.4LFS     d  boot
1 swap        6 Eighth Edition a unknown     e  ADOS
2 Version 6   7 4.2BSD     b HPFS        f  HFS
3 Version 7   8 MS-DOS      c ISO-9660    10 AdvFS
4 System V

b5c121c2091b2ff26b880551feac7112 bsd.img
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Name: XZ Utils
Maintainer: Lasse Collin <lasse.collin@tukaani.org>
Source: http://tukaani.org/xz
git://ctrl.tukaani.org/xz.git
X-Authors:
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1.248 zlib1g 1:1.2.3.4.dfsg-3ubuntu1

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Acknowledgments:

The deflate format used by zlib was defined by Phil Katz. The deflate and zlib specifications were written by Peter Deutsch. Thanks to all the
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