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threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
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with the Library, with the complete machine-readable "work that
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b) Use a suitable shared library mechanism for linking with the
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copy of the library already present on the user's computer system,
rather than copying library functions into the executable, and (2)
will operate properly with a modified version of the library, if
the user installs one, as long as the modified version is
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1.2 acpi-unix 20100528

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

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      (For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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1.11 base-passwd 3.5.29 :r0

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Public License instead of this License.
1.17 bsdutils 1:2.17.2-0ubuntu1

1.17.1 Available under license:

size: 8388608, sector size: 512, PT: dos, offset: 446

---
#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
size: 4456448, sector size: 512, PT: bsd, offset: 512

---
#1: 7936 4864 0x7
#2: 12544 3584 0x7

BZh91AY&SY"~@H R h%CFTC@OQ6_@
tJmlAH1]|/F0HA|BAk@
BZh91AY&SY'hc+{[H]yL^UT|JDCCMhd?TPJ
4b"LLL3Mf0h
4JM&hC&A
4h
P44OP4&FCFz()"{W "$>CN
@JEBx=Q
\%HFYP@R "o""U*R
)Cbw"XZTKe%"Z]Z[~9%m/cB7D4F"N7r;02&HLP|9p]N^g~?<lm9Q@h+CvT
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7@(|Ei.E7q)DP0,5:
@ )9@ @/Dj;p

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The current maintainer is LaMont Jones <lamont@debian.org>.
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Julian Seward, Cambridge, UK.
jseward@bzip.org
bzip2/libbzip2 version 1.0.4 of 20 December 2006
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jseward@bzip.org
bzip2/libbzip2 version 1.0.4 of 20 December 2006
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1.26 coreutils 8.5-1ubuntu3

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1.30 curl 7.44.0 :r0.1

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1.32 db 6.0.30 :r0.1

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* Let no one say political correctness isn't dead....

```c
#ifndef _ss_h
#define _ss_h __FILE__

#include <ss/ss_err.h>

#define __SS_CONST const
#define __SS_PROTO (int, const char * const *, int, void *)

#ifdef __GNUC__
#define __SS_ATTR(x) __attribute__(x)
#else
#define __SS_ATTR(x)
#endif

typedef __SS_CONST struct _ss_request_entry {
    __SS_CONST char * __SS_CONST *command_names; /* whatever */
    void (*)(__SS_CONST function) __SS_PROTO; /* foo */
    __SS_CONST char * __SS_CONST info_string; /* NULL */
    int flags; /* 0 */
} ss_request_entry;

typedef __SS_CONST struct _ss_request_table {
    int version;
    ss_request_entry *requests;
} ss_request_table;

#define SS_RQT_TBL_V2 2

typedef struct _ss_rp_options {
    /* DEFAULT VALUES */
    int version; /* SS_RP_V1 */
    void (*unknown) __SS_PROTO; /* call for unknown command */
    int allow_suspend;
    int catch_int;
} ss_rp_options;

#define SS_RP_V1 1

#define SS_OPT_DONT_LIST 0x0001
#define SS_OPT_DONT_SUMMARIZE 0x0002
```
void ss_help __SS_PROTO;
#if 0
char *ss_current_request(); /* This is actually a macro */
#endif

char *ss_name(int sci_idx);
void ss_error (int, long, char const *, ...)
__SS_ATTR((format(printf, 3, 4)));
void ss_perror (int, long, char const *);

int ss_create_invocation(const char *, const char *, void *,
    ss_request_table *, int *);
void ss_delete_invocation(int);
int ss_listen(int);
int ss_execute_line(int, char *);
void ss_add_request_table(int, ss_request_table *, int, int *);
void ss_delete_request_table(int, ss_request_table *, int *);
void ss_abort_subsystem(int sci_idx, int code);
void ss_quit(int argc, const char * const *argv, int sci_idx, void *infop);
void ss_self_identify(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_subsystem_name(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_subsystem_version(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_unimplemented(int argc, const char * const *argv,
    int sci_idx, void *infop);
void ss_set_prompt(int sci_idx, char *new_prompt);
char *ss_get_prompt(int sci_idx);
void ss_get_readline(int sci_idx);
char *ss_safe_getenv(const char *arg);

extern ss_request_table ss_std_requests;
#endif /* _ss_h */

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```
# This is a Makefile stub which handles the creation of BSD shared libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic

image:$($BSD_LIB)

$($BSD_LIB): $(OBJ)
(cd pic; ld -Bshareable -o $(BSD_LIB) $(LDFLAGS) $(OBJ))
$(MV) pic$(BSD_LIB) .
$(RM) -f ..$(BSD_LIB)
(cd .; $(LN) $(LINK_BUILD_FLAGS) \

--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371
 Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)
 */
 /*
-   Unix SMB/CIFS implementation.
-   trivial database library - standalone version
-   trivial database library - private includes
-
-   Copyright (C) Andrew Tridgell              2005
+   Copyright (C) Andrew Tridgell              1999-2005
+   Copyright (C) Jeremy Allison               2000-2006
+   Copyright (C) Paul 'Rusty' Russell         2000

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#
'echo $(my_dir) | sed -e 's;lib/;"/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)
@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"
@$(INSTALL_PROGRAM) $(BSD_LIB) $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)
@-$(LDCONFIG)

install-strip: install

install-shlibs-strip: install-shlibs

uninstall-shlibs uninstall::
$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::
$(RM) -rf pic
$(RM) -f $(BSD_LIB)
$(RM) -f ../$(BSD_LIB)

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Gadi Oxman, August 1995

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This package was added to the e2fsprogs debian source package by Theodore Ts'o <tytso@mit.edu> on Fri Dec 14 22:24:35 EST 2007

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Theodore Ts'o <tytso@mit.edu> on Sat Mar 15 15:33:37 EST 2003

It is part of the main e2fsprogs distribution, which can be found at:

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Theodore Ts'o
23-June-2007

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1.47 ebtables 2.0.10+4 :r3

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1.48 eee-acpi-scripts 1.1.12+git0+9d4cdedca2 :r0

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This package was debianized by Ben Armstrong <synrg@sanctuary.nslug.ns.ca> and Nico Golde <nion@debian.org> on Sun, 17 Feb 2008 12:49:45 -0400.

It was downloaded from: http://alioth.debian.org/projects/debian-eeepc/

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```

1.57 glib-2.0 2.44.1 :r0.1

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******************************************************************************
*                           Perl-Compatible Regular Expressions              *
******************************************************************************

/* This is the public header file for the PCRE library, to be #included by applications that call the PCRE functions.

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******************************************************************************
*/

#ifndef _PCRE_H
#define _PCRE_H

#define _PCRE_H

****************************************************************************
/* The current PCRE version information. */

#define PCRE_MAJOR 8
#define PCRE_MINOR 31
#define PCRE_PRERELEASE
#define PCRE_DATE 2012-07-06

/* When an application links to a PCRE DLL in Windows, the symbols that are
imported have to be identified as such. When building PCRE, the appropriate
export setting is defined in pcre_internal.h, which includes this file. So we
don't change existing definitions of PCRE_EXP_DECL and PCRECPP_EXP_DECL. */

#if defined(_WIN32) && !defined(PCRE_STATIC)
#  ifndef PCRE_EXP_DECL
#    define PCRE_EXP_DECL  extern __declspec(dllimport)
#  endif
#  ifdef __cplusplus
#    ifndef PCRECPP_EXP_DECL
#      define PCRECPP_EXP_DECL  extern __declspec(dllimport)
#    endif
#    ifndef PCRECPP_EXP_DEFN
#      define PCRECPP_EXP_DEFN  __declspec(dllimport)
#    endif
#  endif
#endif

/* By default, we use the standard "extern" declarations. */

#endif PCRE_EXPDECL
#  ifdef __cplusplus
#    ifndef PCRECPP_EXP_DECL
#      define PCRECPP_EXP_DECL  extern
#    endif
#    ifndef PCRECPP_EXP_DEFN
#      define PCRECPP_EXP_DEFN
#    endif
#  endif

/* Have to include stdlib.h in order to ensure that size_t is defined;
it is needed here for malloc. */
#include <stdlib.h>

/* Allow for C++ users */

#ifdef __cplusplus
extern "C" {
#endif

/* Options. Some are compile-time only, some are run-time only, and some are both, so we keep them all distinct. However, almost all the bits in the options word are now used. In the long run, we may have to re-use some of the compile-time only bits for runtime options, or vice versa. In the comments below, "compile", "exec", and "DFA exec" mean that the option is permitted to be set for those functions; "used in" means that an option may be set only for compile, but is subsequently referenced in exec and/or DFA exec. Any of the compile-time options may be inspected during studying (and therefore JIT compiling). */

#define PCRE_CASELESS 0x00000001 /* Compile */
#define PCRE_MULTILINE 0x00000002 /* Compile */
#define PCRE_DOTALL 0x00000004 /* Compile */
#define PCRE_EXTENDED 0x00000008 /* Compile */
#define PCRE_ANCHORED 0x00000010 /* Compile, exec, DFA exec */
#define PCRE_DOLLAR_ENDONLY 0x00000020 /* Compile, used in exec, DFA exec */
#define PCRE_EXTRA 0x00000040 /* Compile */
#define PCRE_NOTBOL 0x00000080 /* Exec, DFA exec */
#define PCRE_NOTEOL 0x00000100 /* Exec, DFA exec */
#define PCRE_UNGREEDY 0x00000200 /* Compile */
#define PCRE_NOTEMPTY 0x00000400 /* Exec, DFA exec */
/* The next two are also used in exec and DFA exec */
#define PCRE_UTF8 0x00000800 /* Compile (same as PCRE_UTF16) */
#define PCRE_UTF16 0x00000800 /* Compile (same as PCRE_UTF8) */
#define PCRE_NO_AUTO_CAPTURE 0x00001000 /* Compile */
/* The next two are also used in exec and DFA exec */
#define PCRE_NO_UTF8_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF16_CHECK) */
#define PCRE_NO_UTF16_CHECK 0x00002000 /* Compile (same as PCRE_NO_UTF8_CHECK) */
#define PCRE_AUTO_CALLOUT 0x00004000 /* Compile */
#define PCRE_PARTIAL_SOFT 0x00008000 /* Exec, DFA exec */
#define PCRE_PARTIAL 0x00008000 /* Backwards compatible synonym */
#define PCRE_DFA_SHORTEST 0x00010000 /* DFA exec */
#define PCRE_DFA_RESTART 0x00020000 /* DFA exec */
#define PCRE_FIRSTLINE 0x00040000 /* Compile, used in exec, DFA exec */
#define PCRE_DUPNAMES 0x00080000 /* Compile */
#define PCRE_NEWLINE_CR 0x00100000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_LF 0x00200000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_CRLF 0x00300000 /* Compile, exec, DFA exec */
#define PCRE_NEWLINE_ANY 0x00400000 /* Compile, exec, DFA exec */

#ifdef __cplusplus
}
#endif
#define PCRE.NEWLINE.ANYCRLF 0x00500000 /* Compile, exec, DFA exec */
#define PCRE.BSR.ANYCRLF 0x00800000 /* Compile, exec, DFA exec */
#define PCRE.BSR.UNICODE 0x01000000 /* Compile, exec, DFA exec */
#define PCRE.JAVASCRIPT.COMPAT 0x02000000 /* Compile, used in exec */
#define PCRE.NO.START.OPTIMIZE 0x04000000 /* Compile, exec, DFA exec */
#define PCRE.NO.START.OPTIMISE 0x04000000 /* Synonym */
#define PCRE.PARTIAL.HARD 0x08000000 /* Exec, DFA exec */
#define PCRE.NOTEMPTY.ATSTART 0x10000000 /* Exec, DFA exec */
#define PCRE.UCP 0x20000000 /* Compile, used in exec, DFA exec */

/* Exec-time and get/set-time error codes */

#define PCRE.ERROR.NOMATCH (-1)
#define PCRE.ERROR.NULL (-2)
#define PCRE.ERROR.BADOPTION (-3)
#define PCRE.ERROR.BADMAGIC (-4)
#define PCRE.ERROR.UNKNOWN.OPCODE (-5)
#define PCRE.ERROR.UNKNOWN.NODE (-5) /* For backward compatibility */
#define PCRE.ERROR.NOMEMORY (-6)
#define PCRE.ERROR.NOSUBSTRING (-7)
#define PCRE.ERROR.MATCHLIMIT (-8)
#define PCRE.ERROR.CALLOUT (-9) /* Never used by PCRE itself */
#define PCRE.ERROR.BADUTF8 (-10) /* Same for 8/16 */
#define PCRE.ERROR.BADUTF16 (-10) /* Same for 8/16 */
#define PCRE.ERROR.BADUTF8.OFFSET (-11) /* Same for 8/16 */
#define PCRE.ERROR.BADUTF16.OFFSET (-11) /* Same for 8/16 */
#define PCRE.ERROR.PARTIAL (-12)
#define PCRE.ERROR.BADPARTIAL (-13)
#define PCRE.ERROR.INTERNAL (-14)
#define PCRE.ERROR.BADCOUNT (-15)
#define PCRE.ERROR.DFA.UIITEM (-16)
#define PCRE.ERROR.DFA.UCOND (-17)
#define PCRE.ERROR.DFA.UMLIMIT (-18)
#define PCRE.ERROR.DFA.WSIZEx (-19)
#define PCRE.ERROR.DFA.RECURSE (-20)
#define PCRE.ERROR.RECURSIONLIMIT (-21)
#define PCRE.ERROR.NULLWSLIMIT (-22) /* No longer actually used */
#define PCRE.ERROR.BADNEWLINE (-23)
#define PCRE.ERROR.BADOFFSET (-24)
#define PCRE.ERROR.SHORTUTF8 (-25)
#define PCRE.ERROR.SHORTUTF16 (-25) /* Same for 8/16 */
#define PCRE.ERROR.RECURSELOOP (-26)
#define PCRE.ERROR.JIT_STACKLIMIT (-27)
#define PCRE.ERROR.BADMODE (-28)
#define PCRE.ERROR.BADENDIANNESS (-29)
#define PCRE.ERROR.DFA.BADRESTART (-30)

/* Specific error codes for UTF-8 validity checks */
#define PCRE_UTF8_ERR0 0
#define PCRE_UTF8_ERR1 1
#define PCRE_UTF8_ERR2 2
#define PCRE_UTF8_ERR3 3
#define PCRE_UTF8_ERR4 4
#define PCRE_UTF8_ERR5 5
#define PCRE_UTF8_ERR6 6
#define PCRE_UTF8_ERR7 7
#define PCRE_UTF8_ERR8 8
#define PCRE_UTF8_ERR9 9
#define PCRE_UTF8_ERR10 10
#define PCRE_UTF8_ERR11 11
#define PCRE_UTF8_ERR12 12
#define PCRE_UTF8_ERR13 13
#define PCRE_UTF8_ERR14 14
#define PCRE_UTF8_ERR15 15
#define PCRE_UTF8_ERR16 16
#define PCRE_UTF8_ERR17 17
#define PCRE_UTF8_ERR18 18
#define PCRE_UTF8_ERR19 19
#define PCRE_UTF8_ERR20 20
#define PCRE_UTF8_ERR21 21

/* Specific error codes for UTF-16 validity checks */
#define PCRE_UTF16_ERR0 0
#define PCRE_UTF16_ERR1 1
#define PCRE_UTF16_ERR2 2
#define PCRE_UTF16_ERR3 3
#define PCRE_UTF16_ERR4 4

/* Request types for pcre_fullinfo() */
#define PCRE_INFO_OPTIONS 0
#define PCRE_INFO_SIZE 1
#define PCRE_INFO_CAPTURECOUNT 2
#define PCRE_INFO_BACKREFMAX 3
#define PCRE_INFO_FIRSTBYTE 4
#define PCRE_INFO_FIRSTCHAR 4  /* For backwards compatibility */
#define PCRE_INFO_FIRSTTABLE 5
#define PCRE_INFO_LASTLITERAL 6
#define PCRE_INFO_NAMEENTRYSIZE 7
#define PCRE_INFO_NAMECOUNT 8
#define PCRE_INFO_NAMETABLE 9
#define PCRE_INFO_STUDYSIZE 10
#define PCRE_INFO_OKPARTIAL 11
#define PCRE_INFO_DEFAULT_TABLES 12
/* Request types for pcre_config(). Do not re-arrange, in order to remain compatible. */
#define PCRE_CONFIG_UTF8 0
#define PCRE_CONFIG_NEWLINE 1
#define PCRE_CONFIG_LINK_SIZE 2
#define PCRE_CONFIG_POSIX_MALLOC_THRESHOLD 3
#define PCRE_CONFIG_MATCH_LIMIT 4
#define PCRE_CONFIG_STACKRECURSE 5
#define PCRE_CONFIG_UNICODE_PROPERTIES 6
#define PCRE_CONFIG_MATCH_LIMIT_RECURSION 7
#define PCRE_CONFIG_BSR 8
#define PCRE_CONFIG_JIT 9
#define PCRE_CONFIG_UTF16 10
#define PCRE_CONFIG_JITTARGET 11

/* Request types for pcre_study(). Do not re-arrange, in order to remain compatible. */
#define PCRE_STUDY_JIT_COMPILE 0x0001
#define PCRE_STUDY_JIT_PARTIAL_SOFT_COMPILE 0x0002
#define PCRE_STUDY_JIT_PARTIAL_HARD_COMPILE 0x0004

/* Bit flags for the pcre[16]_extra structure. Do not re-arrange or redefine these bits, just add new ones on the end, in order to remain compatible. */
#define PCRE_EXTRA_STUDY_DATA 0x0001
#define PCRE_EXTRA_MATCH_LIMIT 0x0002
#define PCRE_EXTRA_CALLOUT_DATA 0x0004
#define PCRE_EXTRA_TABLES 0x0008
#define PCRE_EXTRA_MATCH_LIMIT_RECURSION 0x0010
#define PCRE_EXTRA_MARK 0x0020
#define PCRE_EXTRA_EXECUTABLE_JIT 0x0040

/* Types */

struct real_pcre; /* declaration; the definition is private */
typedef struct real_pcre pcre;

struct real_pcre16; /* declaration; the definition is private */
typedef struct real_pcre16 pcre16;
struct real_pcre_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre_jit_stack pcre_jit_stack;

struct real_pcre16_jit_stack; /* declaration; the definition is private */
typedef struct real_pcre16_jit_stack pcre16_jit_stack;

/* If PCRE is compiled with 16 bit character support, PCRE_UCHAR16 must contain a 16 bit wide signed data type. Otherwise it can be a dummy data type since pcre16 functions are not implemented. There is a check for this in pcre_internal.h. */
#ifndef PCRE_UCHAR16
#define PCRE_UCHAR16 unsigned short
#endif

#ifndef PCRE_SPTR16
#define PCRE_SPTR16 const PCRE_UCHAR16 *
#endif

/* When PCRE is compiled as a C++ library, the subject pointer type can be replaced with a custom type. For conventional use, the public interface is a const char *. */
#ifndef PCRE_SPTR
#define PCRE_SPTR const char *
#endif

/* The structure for passing additional data to pcre_exec(). This is defined in such as way as to be extensible. Always add new fields at the end, in order to remain compatible. */
typedef struct pcre_extra {
    unsigned long int flags; /* Bits for which fields are set */
    void *study_data; /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data; /* Data passed back in callouts */
    const unsigned char *tables; /* Pointer to character tables */
    unsigned long int match_limit_recursion; /* Max recursive calls to match() */
    unsigned char **mark; /* For passing back a mark pointer */
    void *executable_jit; /* Contains a pointer to a compiled jit code */
} pcre_extra;

/* Same structure as above, but with 16 bit char pointers. */
typedef struct pcre16_extra {
    unsigned long int flags; /* Bits for which fields are set */
    void *study_data; /* Opaque data from pcre_study() */
    unsigned long int match_limit; /* Maximum number of calls to match() */
    void *callout_data; /* Data passed back in callouts */
} pcre16_extra;
const unsigned char *tables;          /* Pointer to character tables */
unsigned long int match_limit_recursion; /* Max recursive calls to match() */
PCRE_UCHAR16 **mark;                   /* For passing back a mark pointer */
void *executable_jit;                   /* Contains a pointer to a compiled jit code */
} pcre16_extra;

/* The structure for passing out data via the pcre_callout_function. We use a
structure so that new fields can be added on the end in future versions,
without changing the API of the function, thereby allowing old clients to work
without modification. */

typedef struct pcre_callout_block {
  int               version;           /* Identifies version of block */
  int               callout_number;    /* Number compiled into pattern */
  int               offset_vector;     /* The offset vector */
  PCRE_SPTR         subject;          /* The subject being matched */
  int               subject_length;    /* The length of the subject */
  int               start_match;       /* Offset to start of this match attempt */
  int               current_position;  /* Where we currently are in the subject */
  int               capture_top;       /* Max current capture */
  int               capture_last;      /* Most recently closed capture */
  void              *callout_data;      /* Data passed in with the call */
} pcre_callout_block;

/* Same structure as above, but with 16 bit char pointers. */

typedef struct pcre16_callout_block {
  int              version;           /* Identifies version of block */
  int              callout_number;    /* Number compiled into pattern */
  int              offset_vector;     /* The offset vector */
  PCRE_SPTR16      subject;          /* The subject being matched */
  int              subject_length;    /* The length of the subject */
  int              start_match;       /* Offset to start of this match attempt */
  int              current_position;  /* Where we currently are in the subject */
  int              capture_top;       /* Max current capture */
  int              capture_last;      /* Most recently closed capture */
  void             *callout_data;      /* Data passed in with the call */
} pcre16_callout_block;
/* ------------------- Added for Version 2 -------------------------- */
const PCRE_UCHAR16 *mark;    /* Pointer to current mark or NULL    */
/* ------------------------------- End of pcre16_block */
} pcre16_callout_block;

/* Indirection for store get and free functions. These can be set to 
alternative malloc/free functions if required. Special ones are used in the 
non-recursive case for "frames". There is also an optional callout function 
that is triggered by the (?) regex item. For Virtual Pascal, these definitions 
have to take another form. */

#ifndef VPCOMPAT
PCRE_EXP_DECL void *(pcre_malloc)(size_t);
PCRE_EXP_DECL void *(pcre_free)(void *);
PCRE_EXP_DECL void *(pcre_stack_malloc)(size_t);
PCRE_EXP_DECL void *(pcre_stack_free)(void *);
PCRE_EXP_DECL int (pcre_callout)(pcre_callout_block *);

PCRE_EXP_DECL void *(pcre16_malloc)(size_t);
PCRE_EXP_DECL void *(pcre16_free)(void *);
PCRE_EXP_DECL void *(pcre16_stack_malloc)(size_t);
PCRE_EXP_DECL void *(pcre16_stack_free)(void *);
PCRE_EXPDECL int (pcre16_callout)(pcre16_callout_block *);
#else   /* VPCOMPAT */
PCRE_EXP DECL void *pcre_malloc(size_t);
PCRE_EXP DECL void  pcre_free(void *);
PCRE_EXP DECL void *pcre_stack_malloc(size_t);
PCRE_EXP DECL void  pcre_stack_free(void *);
PCRE_EXPDECL int  pcre_callout(pcre_callout_block *);
#endif  /* VPCOMPAT */

/* User defined callback which provides a stack just before the match starts. */
typedef pcre_jit_stack *(pcre_jit_callback)(void *);
typedef pcre16_jit_stack *(pcre16_jit_callback)(void *);

/* Exported PCRE functions */

PCRE_EXP DECL pcre *pcre_compile(const char *, int, const char **, int *,
const unsigned char *);
PCRE_EXP DECL pcre16 *pcre16_compile(PCRE_SPTR16, int, const char **, int *,
const unsigned char *);
PCRE_EXP_DECL pcre *pcre_compile2(const char *, int, int *, const char **, int *, const unsigned char *);
PCRE_EXP_DECL pcre16 *pcre16_compile2(PCRE_SPTR16, int, int *, const char **, int *, const unsigned char *);
PCRE_EXP_DECL int pcre_config(int, void *);
PCRE_EXP_DECL int pcre16_config(int, void *);
PCRE_EXP_DECL int pcre_copy_named_substring(const pcre *, const char *, int *, int, const char *, char *, int);
PCRE_EXP_DECL int pcre16_copy_named_substring(const pcre16 *, PCRE_SPTR16, int *, int, PCRE_SPTR16, PCRE_UCHAR16 *, int);
PCRE_EXP_DECL int pcre_copy_substring(const char *, int *, int, int, char *, int);
PCRE_EXP_DECL int pcre16_copy_substring(PCRE_SPTR16, int *, int, int, PCRE_UCHAR16 *, int);
PCRE_EXP_DECL int pcre_dfa_exec(const pcre *, const pcre_extra *, const char *, int, int, int, int *, int, int *, int);
PCRE_EXP_DECL int pcre16_dfa_exec(const pcre16 *, const pcre16_extra *, PCRE_SPTR16, int, int, int, int *, int, int *, int);
PCRE_EXP_DECL int pcre_exec(const pcre *, const pcre_extra *, PCRE_SPTR, int, int, int, int *, int);
PCRE_EXP_DECL int pcre16_exec(const pcre16 *, const pcre16_extra *, PCRE_SPTR16, int, int, int *, int);
PCRE_EXP_DECL void pcre_free_substring(const char *);
PCRE_EXP_DECL void pcre16_free_substring(PCRE_SPTR16);
PCRE_EXP_DECL void pcre_free_substring_list(const char **);
PCRE_EXP_DECL void pcre16_free_substring_list(PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_fullinfo(const pcre *, const pcre_extra *, int, void *);
PCRE_EXP_DECL int pcre16_fullinfo(const pcre16 *, const pcre16_extra *, int, void *);
PCRE_EXP_DECL int pcre_get_named_substring(const pcre *, const char *, int *, int, const char *, const char **);
PCRE_EXP_DECL int pcre16_get_named_substring(const pcre16 *, PCRE_SPTR16, int *, int, PCRE_SPTR16, PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_get_stringnumber(const pcre *, const char *);
PCRE_EXP_DECL int pcre16_get_stringnumber(const pcre16 *, PCRE_SPTR16);
PCRE_EXP_DECL int pcre_get_stringtable_entries(const pcre *, const char *, char **, char **);
PCRE_EXP_DECL int pcre16_get_stringtable_entries(const pcre16 *, PCRE_SPTR16, PCRE_UCHAR16 **, PCRE_UCHAR16 **);
PCRE_EXP_DECL int pcre_get_substring(const char *, int *, int, int, const char **);
PCRE_EXP_DECL int pcre16_get_substring(PCRE_SPTR16, int *, int, int, PCRE_SPTR16 *);
PCRE_EXP_DECL int pcre_get_substring_list(const char *, int *, int, const char **);
PCRE_EXP_DECL int pcre16_get_substring_list(PCRE_SPTR16, int *, int, PCRE_SPTR16 **);
PCRE_EXP_DECL const unsigned char *pcre_maketables(void);
PCRE_EXP_DECL const unsigned char *pcre16_maketables(void);
PCRE_EXP_DECL int  pcre_refcount(pcre *, int);
PCRE_EXP_DECL int  pcre16_refcount(pcre16 *, int);
PCRE_EXP_DECL pcre_extra *pcre_study(const pcre *, int, const char **);
PCRE_EXP_DECL pcre16_extra *pcre16_study(const pcre16 *, int, const char **);
PCRE_EXP_DECL void pcre_free_study(pcre_extra *);
PCRE_EXP_DECL void pcre16_free_study(pcre16_extra *);
PCRE_EXP_DECL const char *pcre_version(void);
PCRE_EXP_DECL const char *pcre16_version(void);

/* Utility functions for byte order swaps. */
PCRE_EXP_DECL int  pcre_pattern_to_host_byte_order(pcre *, pcre_extra *,
const unsigned char *);
PCRE_EXP_DECL int  pcre16_pattern_to_host_byte_order(pcre16 *, pcre16_extra *,
const unsigned char *);
PCRE_EXP_DECL int  pcre16_utf16_to_host_byte_order(PCRE_UCHAR16 *,
PCRE_SPTR16, int, int *, int);

/* JIT compiler related functions. */
PCRE_EXP_DECL pcre_jit_stack *pcre_jit_stack_alloc(int, int);
PCRE_EXP_DECL pcre16_jit_stack *pcre16_jit_stack_alloc(int, int);
PCRE_EXP_DECL void pcre_jit_stack_free(pcre_jit_stack *);
PCRE_EXP_DECL void pcre16_jit_stack_free(pcre16_jit_stack *);
PCRE_EXP_DECL void pcre_assign_jit_stack(pcre_extra *,
pcre_jit_callback, void *);
PCRE_EXP_DECL void pcre16_assign_jit_stack(pcre16_extra *,
pcre16_jit_callback, void *);

#ifdef __cplusplus
}  /* extern "C" */
#endif

#endif /* End of pcre.h */

PCRE LICENCE

Please see the file LICENCE in the PCRE distribution for licensing details.

End

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Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

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## By Mike Hamburg (Stanford University), 2009
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This package was created by Peter Tobias tobias@et-inf.fho-emden.de on
<ajt@debian.org> until 2001.
It is currently maintained by Marco d'Itri <md@linux.it>.

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The Debian tree is maintained with "git" at:
git://git.debian.org/git/kernel/initramfs-tools.git
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1.70 initscripts 2.87dsf-4ubuntu18

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Init was written by Miquel van Smoorenburg <miquels@cistron.nl>.

This package was first put together by Bruce Perens <Bruce@Pixar.com> from pre-distribution sources. Ian Murdock <imurdock@debian.org>
integrated it into the base system maintained it until the end of 1995.
Miquel van Smoorenburg <miquels@cistron.nl> implemented Debian support.
In version 2.85 the package was split into three (sysvinit, sysv-rc,
and initscripts) in order to make room for alternative rc mechanisms.

The upstream source is available at:

Primary-Site:  ftp.cistron.nl/pub/people/miquels/software
             92K sysvinit-2.86.tar.gz
Alternate-Site: sunsite.unc.edu/pub/Linux/system/daemons/init
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The sysvinit package in Debian is maintained as an alioth project.
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```
# PC Screen (console) Fonts - psf1 header
#
0  leshort  0x0436  psf screen font data
>2  byte  0  256 characters, no index
>2  byte  1  512 characters, no index
>2 byte 2256 characters, Unicode index
>2 byte 3512 characters, Unicode index
>2 byte 4256 characters, Unicode level 2 index
>2 byte 5512 characters, Unicode level 2 index
>3 byte  8x%d

# PC Screen (console) Fonts - psf2 header
#
0  lelong  0x864ab572  psf2 screen font data
>12 lelong & 1  with Unicode index
>16 lelong x b, %d glyphs
>28 lelong x b, %dx
>24 lelong x b %d

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is Copyright (C) 1992 Rickard E. Faith.

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showkey.c keymaps.5 loadkeys.1 dumpkeys.1 showkey.1
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setleds.c setmetamode.c setsvesab plank.c showconsolefont.c
spawn_console.c spawn_login.c
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src/findfile.c, src/getfd.c, src/getkeycodes.c, src/getunimap.c,
src/kbd_mode.c, src/loadunimap.c, src/psffontop.c, src/psfxtable.c,
src/resizecons.c, src/screendump.c, src/setkeycodes.c, src/setleds.c,
src/setmetamode.c, src/setvesablank.c, src/showconsolefont.c,
src/spawn_console.c, src/spawn_login.c, man/man1/chvt.1,
man/man1/deallocvct.1, man/man1/kbd_mode.1, man/man1/setleds.1,
man/man1/setmetamode.1, man/man8/getkeycodes.8, man/man8/loadunimap.8,
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#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
size: 4456448, sector size: 512, PT: bsd, offset: 512
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#1: 7936 4864 0x7
#2: 12544 3584 0x7

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```
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Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that companies distributing free software will individually obtain patent licenses, thus in effect transforming the program into proprietary software. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

Most GNU software, including some libraries, is covered by the ordinary GNU General Public License, which was designed for utility programs. This license, the GNU Library General Public License, applies to certain designated libraries. This license is quite different from the ordinary one; be sure to read it in full, and don't assume that anything in it is the same as in the ordinary license.

The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a program and simply using it. Linking a program with a
library, without changing the library, is in some sense simply using
the library, and is analogous to running a utility program or
application program. However, in a textual and legal sense, the linked
executable is a combined work, a derivative of the original library,
and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General
Public License for libraries did not effectively promote software
sharing, because most developers did not use the libraries. We
concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the
users of those programs of all benefit from the free status of the
libraries themselves. This Library General Public License is intended
to permit developers of non-free programs to use free libraries, while
preserving your freedom as a user of such programs to change the free
libraries that are incorporated in them. (We have not seen how to
achieve this as regards changes in header files, but we have achieved
it as regards changes in the actual functions of the Library.) The
hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and
modification follow. Pay close attention to the difference between a
"work based on the library" and a "work that uses the library". The
former contains code derived from the library, while the latter only
works together with the library.

Note that it is possible for a library to be covered by the ordinary
General Public License rather than by this special one.

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authorized party saying it may be distributed under the terms of
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Each licensee is addressed as "you".

A "library" means a collection of software functions and/or data
prepared so as to be conveniently linked with application programs
(which use some of those functions and data) to form executables.

The "Library", below, refers to any such software library or work
which has been distributed under these terms. A "work based on the
Library" means either the Library or any derivative work under
copyright law: that is to say, a work containing the Library or a
portion of it, either verbatim or with modifications and/or
translated straightforwardly into another language. (Hereinafter,
translation is included without limitation in the term "modification"."

"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

Activities other than copying, distribution and modification are not covered by this License; they are outside its scope. The act of running a program using the Library is not restricted, and output from such a program is covered only if its contents constitute a work based on the Library (independent of the use of the Library in a tool for writing it). Whether that is true depends on what the Library does and what the program that uses the Library does.

1. You may copy and distribute verbatim copies of the Library's complete source code as you receive it, in any medium, provided that you conspicuously and appropriately publish on each copy an appropriate copyright notice and disclaimer of warranty; keep intact all the notices that refer to this License and to the absence of any warranty; and distribute a copy of this License along with the Library.

   You may charge a fee for the physical act of transferring a copy, and you may at your option offer warranty protection in exchange for a fee.

2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

   a. The modified work must itself be a software library.

   b. You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

   c. You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

   d. If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply...
such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or
derivative of it, under Section 2) in object code or executable
form under the terms of Sections 1 and 2 above provided that you
accompany it with the complete corresponding machine-readable
source code, which must be distributed under the terms of Sections
1 and 2 above on a medium customarily used for software
interchange.

If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy
the source code from the same place satisfies the requirement to
distribute the source code, even though third parties are not
compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the
Library, but is designed to work with the Library by being
compiled or linked with it, is called a "work that uses the
Library". Such a work, in isolation, is not a derivative work of
the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because
it contains portions of the Library), rather than a "work that
uses the library". The executable is therefore covered by this
License. Section 6 states terms for distribution of such
executables.

When a "work that uses the Library" uses material from a header
file that is part of the Library, the object code for the work may
be a derivative work of the Library even though the source code is
not. Whether this is true is especially significant if the work
can be linked without the Library, or if the work is itself a
library. The threshold for this to be true is not precisely
defined by law.

If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a
derivative work. (Executables containing this object code plus
portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section
6. Any executables containing that work also fall under Section 6,
whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also compile or
link a "work that uses the Library" with the Library to produce a
work containing portions of the Library, and distribute that work
under terms of your choice, provided that the terms permit
modification of the work for the customer's own use and reverse
engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the
Library is used in it and that the Library and its use are covered
by this License. You must supply a copy of this License. If the
work during execution displays copyright notices, you must include
the copyright notice for the Library among them, as well as a
reference directing the user to the copy of this License. Also,
you must do one of these things:

a. Accompany the work with the complete corresponding
machine-readable source code for the Library including
whatever changes were used in the work (which must be
distributed under Sections 1 and 2 above); and, if the work
is an executable linked with the Library, with the complete
machine-readable "work that uses the Library", as object code
and/or source code, so that the user can modify the Library
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the modified Library. (It is understood that the user who
changes the contents of definitions files in the Library will
not necessarily be able to recompile the application to use
the modified definitions.)

b. Accompany the work with a written offer, valid for at least
three years, to give the same user the materials specified in
Subsection 6a, above, for a charge no more than the cost of
performing this distribution.

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from a designated place, offer equivalent access to copy the
above specified materials from the same place.

d. Verify that the user has already received a copy of these
materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the
Library" must include any data and utility programs needed for
reproducing the executable from it. However, as a special
exception, the source code distributed need not include anything
that is normally distributed (in either source or binary form)
with the major components (compiler, kernel, and so on) of the
operating system on which the executable runs, unless that
component itself accompanies the executable.

It may happen that this requirement contradicts the license
restrictions of other proprietary libraries that do not normally accompany the operating system. Such a contradiction means you cannot use both them and the Library together in an executable that you distribute.

7. You may place library facilities that are a work based on the Library side-by-side in a single library together with other library facilities not covered by this License, and distribute such a combined library, provided that the separate distribution of the work based on the Library and of the other library facilities is otherwise permitted, and provided that you do these two things:

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Ty Coon, President of Vice

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Preamble

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To protect your rights, we need to make restrictions that forbid distributors to deny you these rights or to ask you to surrender these rights. These restrictions translate to certain responsibilities for you if you distribute copies of the library or if you modify it.

For example, if you distribute copies of the library, whether gratis or for a fee, you must give the recipients all the rights that we gave you. You must make sure that they, too, receive or can get the source code. If you link other code with the library, you must provide complete object files to the recipients, so that they can relink them with the library after making changes to the library and recompiling it. And you must show them these terms so they know their rights.

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that what they have is not the original version, so that the original
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introduced by others.

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any free program. We wish to make sure that a company cannot
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is quite different from the ordinary General Public License. We use
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libraries into non-free programs.

When a program is linked with a library, whether statically or using
a shared library, the combination of the two is legally speaking a
combined work, a derivative of the original library. The ordinary
General Public License therefore permits such linking only if the
entire combination fits its criteria of freedom. The Lesser General
Public License permits more lax criteria for linking other code with
the library.

We call this license the "Lesser" General Public License because it
does Less to protect the user's freedom than the ordinary General
Public License. It also provides other free software developers Less
of an advantage over competing non-free programs. These disadvantages
are the reason we use the ordinary General Public License for many
libraries. However, the Lesser license provides advantages in certain
special circumstances.

For example, on rare occasions, there may be a special need to
courage the widest possible use of a certain library, so that it
becomes a de-facto standard. To achieve this, non-free programs must
be allowed to use the library. A more frequent case is that a free
library does the same job as widely used non-free libraries. In this
case, there is little to gain by limiting the free library to free
software only, so we use the Lesser General Public License.

In other cases, permission to use a particular library in non-free
programs enables a greater number of people to use a large body of
free software. For example, permission to use the GNU C Library in
non-free programs enables many more people to use the whole GNU
operating system, as well as its variant, the GNU/Linux operating
Although the Lesser General Public License is less protective of the users' freedom, it does ensure that the user of a program that is linked with the Library has the freedom and the wherewithal to run that program using a modified version of the Library.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, whereas the latter must be combined with the library in order to run.

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A "library" means a collection of software functions and/or data prepared so as to be conveniently linked with application programs (which use some of those functions and data) to form executables.

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"Source code" for a work means the preferred form of the work for making modifications to it. For a library, complete source code means all the source code for all modules it contains, plus any associated interface definition files, plus the scripts used to control compilation and installation of the library.

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2. You may modify your copy or copies of the Library or any portion of it, thus forming a work based on the Library, and copy and distribute such modifications or work under the terms of Section 1 above, provided that you also meet all of these conditions:

a) The modified work must itself be a software library.

b) You must cause the files modified to carry prominent notices stating that you changed the files and the date of any change.

c) You must cause the whole of the work to be licensed at no charge to all third parties under the terms of this License.

d) If a facility in the modified Library refers to a function or a table of data to be supplied by an application program that uses the facility, other than as an argument passed when the facility is invoked, then you must make a good faith effort to ensure that, in the event an application does not supply such function or table, the facility still operates, and performs whatever part of its purpose remains meaningful.

(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the
entire whole, and thus to each and every part regardless of who wrote it.

Thus, it is not the intent of this section to claim rights or contest your rights to work written entirely by you; rather, the intent is to exercise the right to control the distribution of derivative or collective works based on the Library.

In addition, mere aggregation of another work not based on the Library with the Library (or with a work based on the Library) on a volume of a storage or distribution medium does not bring the other work under the scope of this License.

3. You may opt to apply the terms of the ordinary GNU General Public License instead of this License to a given copy of the Library. To do this, you must alter all the notices that refer to this License, so that they refer to the ordinary GNU General Public License, version 2, instead of to this License. (If a newer version than version 2 of the ordinary GNU General Public License has appeared, then you can specify that version instead if you wish.) Do not make any other change in these notices.

Once this change is made in a given copy, it is irreversible for that copy, so the ordinary GNU General Public License applies to all subsequent copies and derivative works made from that copy.

This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.
However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

6. As an exception to the Sections above, you may also combine or link a "work that uses the Library" with the Library to produce a work containing portions of the Library, and distribute that work under terms of your choice, provided that the terms permit modification of the work for the customer's own use and reverse engineering for debugging such modifications.

You must give prominent notice with each copy of the work that the Library is used in it and that the Library and its use are covered by this License. You must supply a copy of this License. If the work during execution displays copyright notices, you must include the copyright notice for the Library among them, as well as a reference directing the user to the copy of this License. Also, you must do one of these things:

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1.90 libcgroup 0.41 :r0.3

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#!/bin/sh

find . -type f ! -name *~ ! -exec grep -q Begin-Header \{\} \; -print \\ | grep -v ^./build

This is the Debian GNU/Linux prepackaged version of the Common Error Description library. It is currently distributed together with the EXT2 file system utilities, which are otherwise packaged as "e2fsprogs".

This package was put together by Yann Dirson <dirson@debian.org>, from sources obtained from a mirror of: tsx-11.mit.edu/pub/linux/packages/ext2fs/

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This is the Debian GNU/Linux prepackaged version of the static EXT2 file system consistency checker (e2fsck.static). The EXT2 utilities were written by Theodore Ts'o <tytso@mit.edu> and Remy Card <card@masi.ibp.fr>.

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Theodore Ts'o
23-June-2007

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This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.

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<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice

That's all there is to it!
#
# This is a Makefile stub which handles the creation of BSD shared libraries.
#
# In order to use this stub, the following makefile variables must be defined.
#
# BSDLIB_VERSION = 1.0
# BSDLIB_IMAGE = libce
# BSDLIB_MYDIR = et
# BSDLIB_INSTALL_DIR = $(SHLIBDIR)
#
all:: image

real-subdirs:: Makefile
@echo "MKDIR pic"
@mkdir -p pic

BSD_LIB = $(BSDLIB_IMAGE).so.$(BSDLIB_VERSION)
BSDLIB_PIC_FLAG = -fpic
image::$(BSD_LIB)$

$(BSD_LIB): $(OBJ)

(cd pic; ld -Bshareable -o $(BSD_LIB) $(OBJ))

$(MV) pic/$(BSD_LIB) .

$(RM) -f ../$(BSD_LIB)

(cd ..; $(LN) $(LINK_BUILD_FLAGS) \`
  `echo $(my_dir) | sed -e 's;lib/;;'"/$(BSD_LIB) $(BSD_LIB))

install-shlibs install:: $(BSD_LIB)

@echo "INSTALL_PROGRAM $(BSDLIB_INSTALL_DIR)/$(BSD_LIB)"

@$(INSTALL_PROGRAM) $(BSD_LIB)

$(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

install-strip: install

install-shlibs-strip: install-shlibs

uninstall-shlibs uninstall::

$(RM) -f $(DESTDIR)$(BSDLIB_INSTALL_DIR)/$(BSD_LIB)

clean::

$(RM) -rf pic

$(RM) -f $(BSD_LIB)

$(RM) -f ../$(BSD_LIB)

Index: tdbsa/tdb.c

===================================================================
--- tdbsa.orig/tdb.c
+++ tdbsa/tdb.c
@@ -4,11 +4,11 @@ Rev: 23371

Last Changed Date: 2007-06-06 20:14:06 -0400 (Wed, 06 Jun 2007)

* /

- Unix SMB/CIFS implementation.
+ trivial database library - standalone version

- trivial database library - private includes
-
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1.92 libdbus-1-3 1.4.0-0ubuntu1

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in src/lib/gssapi, including the following files:

lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c

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lib/krb5/os/hst_realm.c

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@smaller
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lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
lib/gssapi/mechglue/gssd_pname_to_uid.c
lib/gssapi/mechglue/mglueP.h
lib/gssapi/mechglue/oid_ops.c
lib/gssapi/spnego/gssapiP_spnego.h
lib/gssapi/spnego/spnego_mech.c
@end smallexample

and the initial implementation of incremental propagation, including
the following new or changed files:

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include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c
@end example

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in src/lib/gssapi, including the following files:

lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
lib/gssapi/mechglue/g_imp_sec_context.c
lib/gssapi/mechglue/g_init_sec_context.c
lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
lib/gssapi/mechglue/g_utils.c
lib/gssapi/mechglue/g_verify.c
and the initial implementation of incremental propagation, including the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c

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cmd/krb5/kadmin/server/ipropd_svc.c
cmd/krb5/kproplog/kproplog.c
cmd/krb5/krb5d/krb5d_svc.c
lib/gss_mechs/mech_krb5/etc/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
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lib/libgss/g_dup_name.c
lib/libgss/g_dsp_name.c
lib/libgss/g_dsp_status.c
lib/libgss/g_exp_name.c
lib/libgss/g_exp_sec_context.c
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lib/libgss/g_imp_sec_context.c
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lib/libgss/g_process_context.c
lib/libgss/g_rel_buffer.c
lib/libgss/g_rel_cred.c
lib/libgss/g_rel_name.c
lib/libgss/g_rel_oid_set.c
lib/libgss/g_seal.c
lib/libgss/g_sign.c
lib/libgss/g_store_cred.c
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lib/libgss/g_userok.c
lib/libgss/g_utils.c
lib/libgss/g_verify.c
lib/libgss/gssd_pname_to_uid.c
uts/common/gssapi/include/gssapi_err_generic.h
uts/common/gssapi/include/mechglueP.h

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1.106 libklibc 1.5.20-1

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The implementations of GSSAPI mechglue in GSSAPI-SPNEGO in src/lib/gssapi, including the following files:

- lib/gssapi/generic/gssapi_err_generic.et
- lib/gssapi/mechglue/g_accept_sec_context.c
- lib/gssapi/mechglue/g_acquire_cred.c
- lib/gssapi/mechglue/g_canon_name.c
- lib/gssapi/mechglue/g_compare_name.c
- lib/gssapi/mechglue/g_context_time.c
- lib/gssapi/mechglue/g_delete_sec_context.c
- lib/gssapi/mechglue/g_dsp_name.c
- lib/gssapi/mechglue/g_dsp_status.c
- lib/gssapi/mechglue/g_dup_name.c
- lib/gssapi/mechglue/g_exp_sec_context.c
- lib/gssapi/mechglue/g_export_name.c
- lib/gssapi/mechglue/g_glue.c
- lib/gssapi/mechglue/g_imp_name.c
- lib/gssapi/mechglue/g_imp_sec_context.c
- lib/gssapi/mechglue/g_init_sec_context.c
- lib/gssapi/mechglue/g_initialize.c
- lib/gssapi/mechglue/g_inquire_context.c
- lib/gssapi/mechglue/g_inquire_cred.c
- lib/gssapi/mechglue/g_inquire_names.c
- lib/gssapi/mechglue/g_process_context.c
- lib/gssapi/mechglue/g_rel_buffer.c
- lib/gssapi/mechglue/g_rel_cred.c
- lib/gssapi/mechglue/g_rel_name.c
- lib/gssapi/mechglue/g_rel_oid_set.c
- lib/gssapi/mechglue/g_seal.c
- lib/gssapi/mechglue/g_sign.c
- lib/gssapi/mechglue/g_store_cred.c
- lib/gssapi/mechglue/g_unseal.c
- lib/gssapi/mechglue/g_userok.c
- lib/gssapi/mechglue/g_utils.c
- lib/gssapi/mechglue/g_verify.c
- lib/gssapi/mechglue/gssd_pname_to_uid.c
- lib/gssapi/mechglue/mglueP.h
- lib/gssapi/mechglue/oid_ops.c
- lib/gssapi/spnego/gssapiP_spnego.h
- lib/gssapi/spnego/spnego_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:
include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kproplog.c

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lib/krb5/os/hst.realm.c

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lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
\end{verbatim}
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the following new or changed files:

@smallexample
include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/prop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
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slave/kproplog.c
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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mech/gssapiP_spnego.h
lib/gss_mechs/mech_spnego/mech/spnego_mech.c
lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
lib/krb5/kdb/kdb_hdr.h
lib/krb5/kdb/kdb_log.c
lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
lib/libgss/g_acquire_cred.c
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lib/gssapi/generic/gssapi_err_generic.et
lib/gssapi/mechglue/g_accept_sec_context.c
lib/gssapi/mechglue/g_acquire_cred.c
lib/gssapi/mechglue/g_canon_name.c
lib/gssapi/mechglue/g_compare_name.c
lib/gssapi/mechglue/g_context_time.c
lib/gssapi/mechglue/g_delete_sec_context.c
lib/gssapi/mechglue/g_dsp_name.c
lib/gssapi/mechglue/g_dsp_status.c
lib/gssapi/mechglue/g_dup_name.c
lib/gssapi/mechglue/g_exp_sec_context.c
lib/gssapi/mechglue/g_export_name.c
lib/gssapi/mechglue/g_glue.c
lib/gssapi/mechglue/g_imp_name.c
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lib/gssapi/mechglue/g_initialize.c
lib/gssapi/mechglue/g_inquire_context.c
lib/gssapi/mechglue/g_inquire_cred.c
lib/gssapi/mechglue/g_inquire_names.c
lib/gssapi/mechglue/g_process_context.c
lib/gssapi/mechglue/g_rel_buffer.c
lib/gssapi/mechglue/g_rel_cred.c
lib/gssapi/mechglue/g_rel_name.c
lib/gssapi/mechglue/g_rel_oid_set.c
lib/gssapi/mechglue/g_seal.c
lib/gssapi/mechglue/g_sign.c
lib/gssapi/mechglue/g_store_cred.c
lib/gssapi/mechglue/g_unseal.c
lib/gssapi/mechglue/g_userok.c
and the initial implementation of incremental propagation, including
the following new or changed files:

include/iprop_hdr.h
kadmin/server/ipropd_svc.c
lib/kdb/iprop.x
lib/kdb/kdb_convert.c
lib/kdb/kdb_log.c
lib/kdb/kdb_log.h
lib/krb5/error_tables/kdb5_err.et
slave/kpropd_rpc.c
slave/kpropolog.c

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cmd/krb5/slave/kpropd_rpc.c
lib/gss_mechs/mech_krb5/et/kdb5_err.c
lib/gss_mechs/mech_spnego/mach/gssapiP_spnego.h
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lib/krb5/kadm5/kadm_host_srv_names.c
lib/krb5/kdb/kdb_convert.c
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lib/krb5/kdb/kdb_log.h
lib/libgss/g_accept_sec_context.c
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It was downloaded from http://www.cpan.org/modules/by-module/Locale/gettext-1.05.tar.gz

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1.110 liblzma2 4.999.9beta+20100527-1
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Name: XZ Utils

Maintainer: Lasse Collin <lasse.collin@tukaani.org>

Source: http://tukaani.org/xz
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  .
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1.121 libpci3 1:3.1.7-4ubuntu2
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1.124 libpcre3 8.02-1

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Written by: Philip Hazel
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1.125 libplymouth2 0.8.2-2ubuntu5

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built on Fri Jun 5 12:42:23 EST 1998
options:bn(64,32) md2(char) rc4(idx,int) des(idx,cisc,16,long) idea(int) blowfish(idx)
C flags:gcc -DTERMIOS -O3 -fomit-frame-pointer -Wall -DB_ENDIAN
The 'numbers' are in 1000s of bytes per second processed.

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<th>256 bytes</th>
<th>1024 bytes</th>
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rsa 1024 bits 4.3967s 0.2615s 0.2 3.8
rsa 2048 bits 29.5200s 0.9664s 0.0 1.0

dsa 512 bits 0.7862s 0.9709s 1.3 1.0
dsa 1024 bits 2.5375s 3.1625s 0.4 0.3
dsa 2048 bits 9.2150s 11.8200s 0.1 0.1

Pentium Pro 200mhz
FreeBSD 2.1.5
gcc 2.7.2.2

SSLay 0.7.0 30-Jan-1997
built on Tue Apr 22 12:14:36 EST 1997
options:bn(64,32) md2(int) rc4(idx,int) des(ptr,risc1,16,long) idea(int) blowfish(ptr2)
C flags:gcc -DTERMIOS -D_ANSI_SOURCE -fomit-frame-pointer -O3 -m486 -Wall
The 'numbers' are in 1000s of bytes per second processed.

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1.137 libstdc++6 4.5.1-7ubuntu2

1.137.1 Available under license:

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Version 3, 29 June 2007


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    size: 8388608, sector size: 512, PT: dos, offset: 446
    ---
    #1: 32 7648 0x83
    #2: 7680 8704 0xa5
    #5: 7936 4864 0x7 (freebsd)
    #6: 12544 3584 0x7 (freebsd)
    size: 4456448, sector size: 512, PT: bsd, offset: 512
    ---
    #1: 7936 4864 0x7
    #2: 12544 3584 0x7

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1.149 libxau 1.0.8 :r0

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1.154 libyaml-perl 0.71-1

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1.155 linux-firmware 1.38

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Firmware provided as part of an XC5000 Linux developers kit by Brian
Mathews <bmathews@xceive.com> to Devin Heitmueller <dheitmueller@linuxtv.org>
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agere_sta_fw.bin -- 9.48 Hermes I
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The Linux driver architecture was based on two modules, the MSF (Module
specific functions) and the HCF (Hardware Control Functions). Included
in the HCF is run-time firmware (binary format) which is downloaded into
the RAM of the Hermes 1/2/2.5 WMAC.

This hex coded firmware is not based on any open source software and
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Hope this helps.

Sincerely,

Viren Pathare
Intellectual Property Licensing Manager
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---
Firmware provided by Patrick Boettcher <pboettcher@dibcom.fr> to Devin Heitmueller <dheitmueller@kernellabs.com> on October 8, 2009.

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(If you think you should be in this list, but you are not, drop a
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Temple Place - Suite 330, Boston, MA 02111-1307, USA.
/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com) 
* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk) 
* */ 
* Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc. 
* http://www.hypermall.com/ 
* 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY 
* interrupts us (except possibly for removal/insertion of the cable?) 
* 10/4/97 - began heavy inline documentation of the code. Corrected typos 
* and spelling mistakes. 
* 10/5/97 - added code to handle PHY interrupts, disable PHY on 
* loss of link, and correctly re-enable PHY when link is 
* re-established. (put back CFG_PHYIE) 
* 
* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only. 
* 
* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997 
* 
* Linux driver for the IDT77201 NICStAR PCI ATM controller. 
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155; 
* see init_nicstar() for PHY initialization to change this. This driver 
* expects the Linux ATM stack to support scatter-gather lists 
* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push. 
* 
* Implementing minimal-copy of received data: 
* IDT always receives data into a small buffer, then large buffers 
* as needed. This means that data must always be copied to create 
* the linear buffer needed by most non-ATM protocol stacks (e.g. IP) 
* Fix is simple: make large buffers large enough to hold entire 
* SDU, and leave <small_buffer_data> bytes empty at the start. Then 
* copy small buffer contents to head of large buffer. 
* Trick is to avoid fragmenting Linux, due to need for a lot of large 
* buffers. This is done by 2 things: 
* 1) skb->destructor / skb->atm.recycle_buffer 
* combined, allow nicstar_free_rx_skb to be called to 
* recycle large data buffers 
* 2) skb_clone of received buffers 
* See nicstar_free_rx_skb and linearize_buffer for implementation 
* details. 
* 
*
This is the Ubuntu prepackaged version of the Linux kernel. Linux was written by Linus Torvalds <Linus.Torvalds@cs.Helsinki.FI> and others.

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Version 2, June 1991

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[This is the first released version of the library GPL. It is numbered 2 because it goes with version 2 of the ordinary GPL.]

Preamble

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The reason we have a separate public license for some libraries is that they blur the distinction we usually make between modifying or adding to a
program and simply using it. Linking a program with a library, without changing the library, is in some sense simply using the library, and is analogous to running a utility program or application program. However, in a textual and legal sense, the linked executable is a combined work, a derivative of the original library, and the ordinary General Public License treats it as such.

Because of this blurred distinction, using the ordinary General Public License for libraries did not effectively promote software sharing, because most developers did not use the libraries. We concluded that weaker conditions might promote sharing better.

However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

The precise terms and conditions for copying, distribution and modification follow. Pay close attention to the difference between a "work based on the library" and a "work that uses the library". The former contains code derived from the library, while the latter only works together with the library.

Note that it is possible for a library to be covered by the ordinary General Public License rather than by this special one.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

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threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data
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<signature of Ty Coon>, 1 April 1990
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for their continuing work on the DVB driver

Michael Holzt <kju@debian.org>
for his contributions to the dvb-net driver

Diego Picciani <d.picciani@novacomp.it>
for CyberLogin for Linux which allows logging onto EON
(in case you are wondering where CyberLogin is, EON changed its login
procedure and CyberLogin is no longer used.)

Martin Schaller <martin@smurf.franken.de>
for patching the cable card decoder driver

Klaus Schmidinger <Klaus.Schmidinger@cadsoft.de>
for various fixes regarding tuning, OSD and CI stuff and his work on VDR

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for his AFC kernel thread

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Wolfram Joost <dbox2@frokaschwei.de>
...and all the other dbox2 people
for many bugfixes in the generic DVB Core, frontend drivers and
their work on the dbox2 port of the DVB driver

Oliver Endriss <o.endriss@gmx.de>
for many bugfixes

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Augusto Cardoso <augusto@carhil.net>
for all the work for the FlexCopII chipset by B2C2,Inc.

Davor Emard <emard@softhome.net>
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TechnoTrend/Hauppauge DEC driver firmware

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for maintaining v4l/dvb inter-tree dependencies

Taylor Jacob <rtjacob@earthlink.net>
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Jean-Francois Thibert <jeanfrancois@sagetv.com>
for the nxt2004 frontend driver

Kirk Lapray <kirk.lapray@gmail.com>
for the or51211 and or51132 frontend drivers, and
for merging the nxt2002 and nxt2004 modules into a
single nxt200x frontend driver.

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/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)
  * nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)
  *
  * Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
  * http://www.hypermall.com/
  * 10/1/97 - commented out CFG_PHYIE bit - we don't care when the PHY
  * interrupts us (except possibly for removal/insertion of the cable?)
  * 10/4/97 - began heavy inline documentation of the code. Corrected typos
  * and spelling mistakes.
  * 10/5/97 - added code to handle PHY interrupts, disable PHY on
  * loss of link, and correctly re-enable PHY when link is
  * re-established. (put back CFG_PHYIE)
  *
  * Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
  *
  * R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
  *
  * Linux driver for the IDT77201 NICStar PCI ATM controller.
  * PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
  * see init_nicstar() for PHY initialization to change this. This driver
  * expects the Linux ATM stack to support scatter-gather lists
  * (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.
  *
  * Implementing minimal-copy of received data:
  * IDT always receives data into a small buffer, then large buffers
  * as needed. This means that data must always be copied to create
  * the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
  * Fix is simple: make large buffers large enough to hold entire
  * SDU, and leave <small_buffer_data> bytes empty at the start. Then
  * copy small buffer contents to head of large buffer.
  * Trick is to avoid fragmenting Linux, due to need for a lot of large
  * buffers. This is done by 2 things:
* 1) skb->destructor / skb->atm.recycle_buffer
  * combined, allow nicstar_free_rx_skb to be called to
  *    recycle large data buffers
* 2) skb_clone of received buffers
*    See nicstar_free_rx_skb and linearize_buffer for implementation
*    details.

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* M. Welsh, 6 July 1996
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Here is a list of the main contributors to lm-sensors version 3.

* Frodo Looijaard
  Original author of libsensors, sensors-detect, sensors and isadump.

* Merlin Hughes
  Original author of sensord.

* Bob Schlaermann
  Dynamic chip feature detection (a.k.a. generic chip support) in
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* Mark M. Hoffman
  Many improvements to the libsensors configuration file scanner.

* Jean Delvare
  New libsensors API, and migration of sensors and sensord thereto.
  Many optimizations in libsensors and sensors.
  Configuration file converter.
  Rewrite of sensors-detect.
  Support for multiple configuration files in libsensors.

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* GSS-API Programming Guide
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Network Working Group  H. Krawczyk
Request for Comments: 2104  IBM
HMAC: Keyed-Hashing for Message Authentication

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==========================================================================

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Subject: Re: makedev license?

> I maintain the makedev package for Debian GNU/Linux. I am about to move > from the ill-fated makedev-1.6 to the makedev-2.3.1 derived from your work > by the folks at Redhat. I don't see any evidence of a copyright assertion > or explicit license statement in the source. Your name appears to be the > earliest attached to the current sourcecode. Am I correct in assuming the > GPL? We try to be meticulous about having our base system be compliant > with our Debian Free Software Guidelines, so I'd like an explicit statement.

It was never explictly released as GPL, as that would have required including the file COPYING which would have been much larger than the actual MAKEDEV script (I was also too lazy to find out what incantations needed to be made). However, it is intended to be used as anyone sees fit, and the statement under "Copying Policy" is "Freely Redistributable" (see MAKEDEV.lsm from any of the releases I made).

The more recent modifications were done by Michael K. Johnson at Redhat. I think the understanding was that he would be taking over the
maintenance of MAKEDEV (our discussion took place last September).

The only previous history was (according to an old posting to comp.os.linux) that I started with Jim Winstead’s script.

So, as far as I am concerned, it is consistent with the Debian FSG.

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// ^\ \ | Work: Nick.Holloway@parallax.co.uk

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1.167 mawk 1.3.3-15ubuntu2
1.167.1 Available under license:

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This package was put together by Chris Fearnley <cjf@netaxs.com>, from sources obtained from:
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1.169 modutils-initscripts 1.0 :r7.1
1.169.1 Available under license:
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1.170 mount 2.17.2-0ubuntu1
1.170.1 Available under license:

size: 8388608, sector size: 512, PT: dos, offset: 446
---
#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
size: 4456448, sector size: 512, PT: bsd, offset: 512
---
#1: 7936 4864 0x7
#2: 12544 3584 0x7

1/5
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1.173 ncurses-base 5.7+20100626-0ubuntu1

1.174 ncurses-bin 5.7+20100626-0ubuntu1

1.175 net-tools 1.60-23ubuntu3

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4. [5]Michael Barone <michael.barone@lmco.com> GPSVME fixes
5. [6]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
9. [10]Piete Brooks <Piete.Brooks@el.cam.ac.uk> MSF clock driver, Trimble PARSE support
10. [11]Reg Clemens <reg@dfw.com> Oncore driver (Current maintainer)
11. [12]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
12. [13]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
15. [16]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
16. [17]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
17. [18]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
18. [19]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
19. [20]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
20. [21]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
21. [22]Jeff Johnson <sjb@chatham.usdesign.com> massive prototyping overhaul
22. [23]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or [24]<H.Lambermont@chello.nl> ntpswep
23. [25]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
24. [26] Frank Kardel [27]<kardel (at) ntp (dot) org> PARSE <GENERIC>
driver (>14 reference clocks), STREAMS modules for PARSE, support
scripts, syslog cleanup, dynamic interface handling
25. [28] William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX
modifications, HPUX modifications
26. [29] Dave Katz <dkatz@cisco.com> RS/6000 AIX port
27. [30] Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox
GPS clock driver
28. [31] George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
29. [32] Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
30. [33] Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation
code for Version 3 as specified in RFC-1305
31. [34] Danny Mayer <mayer@ntp.org> Network I/O, Windows Port, Code
Maintenance
32. [35] David L. Mills <mills@udel.edu> Version 4 foundation: clock
discipline, authentication, precision kernel; clock drivers:
Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics;
audio clock drivers: CHU, WWVH, IRIG
33. [36] Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
34. [37] Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
35. [38] Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port
36. [39] Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
37. [40] Derek Mulcahy <derek@toybox.demon.co.uk> and [41] Damon
Hart-Davis <d@hd.org> ARCRON MSF clock driver
38. [42] Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de>
monitoring/trap scripts, statistics file handling
39. [43] Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
40. [44] Wilfredo Sanchez <wsanchez@apple.com> added support for
NetInfo
41. [45] Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
42. [46] Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of
space on the stuff in the html/pic/ subdirectory
43. [47] Ray Schnitzler <schnitz@unipress.com> Unixware1 port
44. [48] Michael Shields <shields@tembel.org> USNO clock driver
45. [49] Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock
driver
46. [50] Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure
makeover, various other bits (see the ChangeLog)
47. [51] Kenneth Stone <ken@sdd.hp.com> HP-UX port
48. [52] Ajit Thyagarajan <ajit@ee.udel.edu> IP multicast/anycast
support
49. [53] Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp> TRAK clock
driver
50. [54] Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic
TrueTime clock driver
51. [55] Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and
validated HTML documents according to the HTML DTD
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1.182 openssh-client 1:5.5p1-4ubuntu4

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1.183 openssh-server 1:5.5p1-4ubuntu4

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1.186 openssl 0.9.7j

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### 1.187 OpenSSL 0.9.8j

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- Pentium Pro 200mhz
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- gcc 2.7.2.2

SSLeay 0.7.0 30-Jan-1997

built on Tue Apr 22 12:14:36 EST 1997

options:bn(64,32) md2(int) rc4(idx,int) des(ptr,risc1,16,long) idea(int) blowfish(ptr2)

C flags:gcc -DTERMIOS -D_ANSI_SOURCE -fomit-frame-pointer -O3 -m486 -Wall

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<td>6182.43k</td>
<td>6248.11k</td>
<td>6226.09k</td>
</tr>
<tr>
<td>rsa 512 bits</td>
<td>0.010s</td>
<td>0.045s</td>
<td>0.260s</td>
<td>1.690s</td>
<td></td>
</tr>
<tr>
<td>rsa 1024 bits</td>
<td>0.045s</td>
<td>0.260s</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>rsa 2048 bits</td>
<td>0.260s</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>rsa 4096 bits</td>
<td>1.690s</td>
<td></td>
<td></td>
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</table>

Motorola 68020 20mhz, NetBSD

SSLeay 0.9.0t 29-May-1998

built on Fri Jun 5 12:42:23 EST 1998

options:bn(64,32) md2(char) rc4(idx,int) des(idx,cisc,16,long) idea(int) blowfish(idx)

C flags:gcc -DTERMIOS -O3 -fomit-frame-pointer -Wall -DB_ENDIAN

The 'numbers' are in 1000s of bytes per second processed.

<table>
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<tr>
<th>type</th>
<th>8 bytes</th>
<th>64 bytes</th>
<th>256 bytes</th>
<th>1024 bytes</th>
<th>8192 bytes</th>
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<tr>
<td>md2</td>
<td>2176.00</td>
<td>5994.67</td>
<td>8079.73</td>
<td>8845.18</td>
<td>9077.01</td>
</tr>
<tr>
<td>mdc2</td>
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<td>6122.67</td>
<td>6167.66</td>
<td>6176.51</td>
<td>6174.87</td>
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<tr>
<td>md5</td>
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<td>127.31k</td>
<td>209.66k</td>
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<td>263.99k</td>
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<tr>
<td>hmac(md5)</td>
<td>12.33k</td>
<td>73.02k</td>
<td>160.17k</td>
<td>228.04k</td>
<td>261.15k</td>
</tr>
<tr>
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<td>84.31k</td>
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<td>48.62k</td>
<td>78.76k</td>
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</tr>
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<td>rc4</td>
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<td>148.94k</td>
<td>152.57k</td>
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</tr>
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<td>des cbc</td>
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<td>30.06k</td>
<td>30.38k</td>
<td>30.38k</td>
<td>30.53k</td>
</tr>
<tr>
<td>des ede3</td>
<td>10.51k</td>
<td>10.94k</td>
<td>11.01k</td>
<td>11.01k</td>
<td>11.01k</td>
</tr>
</tbody>
</table>
idea cbc 26.74k 29.23k 29.45k 29.60k 29.74k
rc2 cbc 34.27k 39.39k 40.03k 40.07k 40.16k
rc5-32/12 cbc 64.31k 83.18k 85.70k 86.70k 87.09k
blowfish cbc 48.86k 59.18k 60.07k 60.42k 60.78k
cast cbc 42.67k 50.01k 50.86k 51.20k 51.37k
    sign verify sign/s verify/s
rsa 512 bits 0.7738s 0.0774s 1.3 12.9
rsa 1024 bits 4.3967s 0.2615s 0.2 3.8
rsa 2048 bits 29.5200s 0.9664s 0.0 1.0
    sign verify sign/s verify/s
dsa 512 bits 0.7862s 0.9709s 1.3 1.0
dsa 1024 bits 2.5375s 3.1625s 0.4 0.3
dsa 2048 bits 9.2150s 11.8200s 0.1 0.1

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ABSTRACT

OSSP uuid is a ISO-C:1999 application programming interface (API)
and corresponding command line interface (CLI) for the generation of DCE 1.1, ISO/IEC 11578:1996 and IETF RFC-4122 compliant Universally Unique Identifier (UUID). It supports DCE 1.1 variant UUIDs of version 1 (time and node based), version 3 (name based, MD5), version 4 (random number based) and version 5 (name based, SHA-1). Additional API bindings are provided for the languages ISO-C++:1998, Perl:5 and PHP:4/5. Optional backward compatibility exists for the ISO-C DCE-1.1 and Perl Data::UUID APIs.

UUIDs are 128 bit numbers which are intended to have a high likelihood of uniqueness over space and time and are computationally difficult to guess. They are globally unique identifiers which can be locally generated without contacting a global registration authority. UUIDs are intended as unique identifiers for both mass tagging objects with an extremely short lifetime and to reliably identifying very persistent objects across a network.

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This file is part of OSSP uuid, a library for the generation of UUIDs which can found at http://www.ossp.org/pkg/lib/uuid/

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HOME AND DOCUMENTATION

The documentation and latest release can be found on

 o http://www.ossp.org/pkg/lib/uuid/
/*
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**
** uuid_md5.c: MD5 API implementation
*/

/* own headers (part 1/2) */
#include "uuid_ac.h"

/* system headers */
#include <stdlib.h>
#include <string.h>

/* own headers (part 2/2) */
#include "uuid_md5.h"

/*
* This is a RFC 1321 compliant Message Digest 5 (MD5) algorithm implementation. It is directly derived from the RSA code published in RFC 1321 with just the following functionality preserving changes:
* - converted function definitions from K&R to ANSI C
* - included contents of the "global.h" and "md5.h" headers
* - moved the SXX defines into the MD5Transform function
*/
* - replaced MD5_memcpy() with memcpy(3) and MD5_memset() with memset(3)
* - renamed "index" variables to "idx" to avoid namespace conflicts
* - reformatted C style to conform with OSSP C style
* - added own OSSP style frontend API
*/

/** ===== BEGIN RFC 1321 CODE =====
*/

/*
 * RSA Data Security, Inc., MD5 message-digest algorithm
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 *
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 * is identified as the "RSA Data Security, Inc. MD5 Message-Digest
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 * documentation and/or software.
 */

/* POINTER defines a generic pointer type */
typedef unsigned char *POINTER;

/* UINT4 defines a four byte word */
#if SIZEOF_UNSIGNED_SHORT == 4
typedef unsigned short int UINT4;
#elif SIZEOF_UNSIGNED_INT == 4
typedef unsigned int UINT4;
#elif SIZEOF_UNSIGNED_LONG == 4
typedef unsigned long int UINT4;
#elif SIZEOF_UNSIGNED_LONG_LONG == 4
typedef unsigned long long int UINT4;
#else
#error ERROR: unable to determine UINT4 type (four byte word)
typedef struct {
    UINT4 state[4]; /* state (ABCD) */
    UINT4 count[2]; /* number of bits, modulo 2^64 (lsb first) */
    unsigned char buffer[64]; /* input buffer */
} MD5_CTX;

/* prototypes for internal functions */
static void MD5Init     (MD5_CTX *ctx);
static void MD5Update    (MD5_CTX *ctx, unsigned char *, unsigned int);
static void MD5Final     (unsigned char [], MD5_CTX *);
static void MD5Transform (UINT4 [], unsigned char [], unsigned int);
static void Encode       (unsigned char *, UINT4 *, unsigned int);
static void Decode       (UINT4 *, unsigned char *, unsigned int);

/* finalization padding */
static unsigned char PADDING[64] = {
    0x80, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0
};

/* F, G, H and I are basic MD5 functions. */
#define F(x, y, z) (((x) & (y)) | ((~x) & (z)))
#define G(x, y, z) (((x) & (z)) | ((y) & (~z)))
#define H(x, y, z) ((x) ^ (y) ^ (z))
#define I(x, y, z) ((y) ^ ((x) | (~z)))

/* ROTATE_LEFT rotates x left n bits. */
#define ROTATE_LEFT(x, n) (((x) << (n)) | ((x) >> (32-(n))))

/* FF, GG, HH, and II transformations for rounds 1, 2, 3, and 4.
   Rotation is separate from addition to prevent recomputation. */
#define FF(a, b, c, d, x, s, ac) { 
    (a) += F ((b), (c), (d)) + (x) + (UINT4)(ac); 
    (a) = ROTATE_LEFT ((a), (s)); 
    (a) += (b); 
}
#define GG(a, b, c, d, x, s, ac) { 
    (a) += G ((b), (c), (d)) + (x) + (UINT4)(ac); 
    (a) = ROTATE_LEFT ((a), (s)); 
    (a) += (b); 
}
#define HH(a, b, c, d, x, s, ac) { 
    (a) += H ((b), (c), (d)) + (x) + (UINT4)(ac); 
    (a) = ROTATE_LEFT ((a), (s)); 
}
#define II(a, b, c, d, x, s, ac) { 
  (a) += I ((b), (c), (d)) + (x) + (UINT4)(ac); 
  (a) = ROTATE_LEFT ((a), (s)); 
  (a) += (b); 
}

/* MD5 initialization. Begins an MD5 operation, writing a new context. */
static void MD5Init(
  MD5_CTX *context)
{
  context->count[0] = context->count[1] = 0;

  /* Load magic initialization constants. */
  context->state[0] = 0x67452301;
  context->state[1] = 0xefcdab89;
  context->state[2] = 0x98badcfe;
  context->state[3] = 0x10325476;
  return;
}

/* MD5 block update operation. Continues an MD5 message-digest
 operation, processing another message block, and updating the
 context. */
static void MD5Update(
  MD5_CTX *context, /* context */
  unsigned char *input, /* input block */
  unsigned int inputLen) /* length of input block */
{
  unsigned int i, idx, partLen;
  /* Compute number of bytes mod 64 */
  idx = (unsigned int)((context->count[0] >> 3) & 0x3F);

  /* Update number of bits */
  if (((context->count[0] += ((UINT4)inputLen << 3)) < ((UINT4)inputLen << 3))
      context->count[1]++;
  context->count[1] += ((UINT4)inputLen >> 29);
  partLen = (unsigned int)64 - idx;

  /* Transform as many times as possible. */
  if (inputLen >= partLen) {
    memcpy((POINTER)&context->buffer[idx], (POINTER)input, (size_t)partLen);
    MD5Transform(context->state, context->buffer);
    for (i = partLen; i + 63 < inputLen; i += 64)
      MD5Transform(context->state, (input+i));
idx = 0;
}
else
i = 0;

/* Buffer remaining input */
memcpy((POINTER)&context->buffer[idx], (POINTER)&input[i], (size_t)(inputLen - i));
}

/* MD5 finalization. Ends an MD5 message-digest operation, writing the
the message digest and zeroizing the context. */
static void MD5Final(
unsigned char digest[],          /* message digest */
MD5_CTX *context)               /* context */
{
unsigned char bits[8];
unsigned int idx, padLen;

/* Save number of bits */
Encode(bits, context->count, 8);

/* Pad out to 56 mod 64. */
idx = (unsigned int)((context->count[0] >> 3) & 0x3f);
padLen = (idx < 56) ? ((unsigned int)56 - idx) : ((unsigned int)120 - idx);
MD5Update(context, PADDING, padLen);

/* Append length (before padding) */
MD5Update(context, bits, 8);

/* Store state in digest */
Encode(digest, context->state, 16);

/* Zeroize sensitive information. */
memset((POINTER)context, 0, sizeof(*context));
}

/* MD5 basic transformation. Transforms state based on block. */
static void MD5Transform(
    UINT4 state[],
    unsigned char block[])
{
    UINT4 a = state[0], b = state[1], c = state[2], d = state[3], x[16];

    Decode(x, block, 64);

    /* Round 1 */
#define S11 7
#define S12 12
#define S13 17
#define S14 22

FF (a, b, c, d, pc[0], S11, 0xd76aa478); /* 1 */
FF (d, a, b, c, xc[1], S12, 0x8e8c7b756); /* 2 */
FF (c, d, a, b, pc[2], S13, 0x242070db); /* 3 */
FF (b, c, d, a, xc[3], S14, 0xc1bdeceee); /* 4 */
FF (a, b, c, d, pc[4], S11, 0xf57c0fa0f); /* 5 */
FF (d, a, b, c, xc[5], S12, 0x4787c62a); /* 6 */
FF (c, d, a, b, pc[6], S13, 0xa8304613); /* 7 */
FF (b, c, d, a, xc[7], S14, 0xfd469501); /* 8 */
FF (a, b, c, d, pc[8], S11, 0x698098d8); /* 9 */
FF (d, a, b, c, xc[9], S12, 0x8b44f7af); /* 10 */
FF (c, d, a, b, pc[10], S13, 0xffff5bb1); /* 11 */
FF (b, c, d, a, xc[11], S14, 0x895cd7be); /* 12 */
FF (a, b, c, d, pc[12], S11, 0x6b901122); /* 13 */
FF (d, a, b, c, xc[13], S12, 0xf9d87193); /* 14 */
FF (c, d, a, b, pc[14], S13, 0xa679438e); /* 15 */
FF (b, c, d, a, xc[15], S14, 0x49b40821); /* 16 */

/* Round 2 */
#define S21 5
#define S22 9
#define S23 14
#define S24 20

GG (a, b, c, d, pc[1], S21, 0xf61e2562); /* 17 */
GG (d, a, b, c, xc[6], S22, 0xc040b340); /* 18 */
GG (c, d, a, b, pc[11], S23, 0x265e5a51); /* 19 */
GG (b, c, d, a, xc[0], S24, 0xe9b6c7aa); /* 20 */
GG (a, b, c, d, pc[5], S21, 0xd62f105d); /* 21 */
GG (d, a, b, c, xc[10], S22, 0x2441453); /* 22 */
GG (c, d, a, b, pc[12], S23, 0xd8a1e681); /* 23 */
GG (b, c, d, a, xc[4], S24, 0xe7d3fbc8); /* 24 */
GG (a, b, c, d, pc[9], S21, 0x21e1cde6); /* 25 */
GG (d, a, b, c, pc[14], S22, 0xc33707d6); /* 26 */
GG (c, d, a, b, xc[3], S23, 0x0f4d50d87); /* 27 */
GG (b, c, d, a, xc[8], S24, 0x455a14ed); /* 28 */
GG (a, b, c, d, pc[13], S21, 0xa9e3e905); /* 29 */
GG (d, a, b, c, pc[2], S22, 0xf6f3a2b8); /* 30 */
GG (c, d, a, b, pc[7], S23, 0x676f02d9); /* 31 */
GG (b, c, d, a, pc[12], S24, 0x88d2a4cc8a); /* 32 */

/* Round 3 */
#define S31 4
#define S32 11
#define S33 16
#define S34 23

HH (a, b, c, d, pc[5], S31, 0xfffa3942); /* 33 */
HH (d, a, b, c, pc[8], S32, 0x8771f681); /* 34 */
HH (c, d, a, b, x[11], S33, 0x6d9d6122); /* 35 */
HH (b, c, d, a, x[14], S34, 0xfa45b369); /* 36 */
HH (a, b, c, d, x[ 1], S31, 0xa4b4ee44); /* 37 */
HH (d, a, b, c, x[ 4], S32, 0xb9d3e8a9); /* 38 */
HH (c, d, a, b, x[ 7], S33, 0x66b4b60); /* 39 */
HH (b, c, d, a, x[10], S34, 0xbebfbc70); /* 40 */
HH (a, b, c, d, x[13], S31, 0x289b7ec6); /* 41 */
HH (d, a, b, c, x[ 0], S32, 0xeaa127f9); /* 42 */
HH (c, d, a, b, x[ 3], S33, 0xd4ef3085); /* 43 */
HH (b, c, d, a, x[ 6], S34, 0x4881d05); /* 44 */
HH (a, b, c, d, x[ 9], S31, 0xe6db99e5); /* 45 */
HH (d, a, b, c, x[12], S32, 0xf6bb4b60); /* 46 */
HH (c, d, a, b, x[15], S33, 0x1fa27cf8); /* 47 */
HH (b, c, d, a, x[ 2], S34, 0xc4ac5665); /* 48 */

/* Round 4 */
define S41 6
#define S42 10
#define S43 15
#define S44 21
II (a, b, c, d, x[ 0], S41, 0xf4292244); /* 49 */
II (d, a, b, c, x[ 7], S42, 0x432aff97); /* 50 */
II (c, d, a, b, x[14], S43, 0xb9423a7); /* 51 */
II (b, c, d, a, x[ 5], S44, 0xfcd93a039); /* 52 */
II (a, b, c, d, x[12], S41, 0x655b59c3); /* 53 */
II (d, a, b, c, x[ 3], S42, 0x80fccc92); /* 54 */
II (c, d, a, b, x[10], S43, 0xffeff47d); /* 55 */
II (b, c, d, a, x[ 1], S44, 0x85845dd1); /* 56 */
II (a, b, c, d, x[ 8], S41, 0x6fa87e4f); /* 57 */
II (d, a, b, c, x[15], S42, 0x2e2cece0); /* 58 */
II (c, d, a, b, x[ 6], S43, 0xa3014314); /* 59 */
II (b, c, d, a, x[13], S44, 0xf0811a1e); /* 60 */
II (a, b, c, d, x[ 4], S41, 0xf7537e82); /* 61 */
II (d, a, b, c, x[11], S42, 0xb3e3af235); /* 62 */
II (c, d, a, b, x[ 2], S43, 0x2ad7d2bb); /* 63 */
II (b, c, d, a, x[ 9], S44, 0xeb86d391); /* 64 */

state[0] += a;
state[1] += b;
state[2] += c;
state[3] += d;

/* Zeroize sensitive information. */
memset((POINTER)x, 0, sizeof(x));
}

/* Encodes input (UINT4) into output (unsigned char).
Assumes len is a multiple of 4. */
static void Encode(
  unsigned char *output,
  UINT4 *input,
  unsigned int len)
{
  unsigned int i, j;

  for (i = 0, j = 0; j < len; i++, j += 4) {
    output[j] = (unsigned char)(input[i] & 0xff);
    output[j+1] = (unsigned char)((input[i] >> 8) & 0xff);
    output[j+2] = (unsigned char)((input[i] >> 16) & 0xff);
    output[j+3] = (unsigned char)((input[i] >> 24) & 0xff);
  }
  return;
}

static void Decode(
  UINT4 *output,
  unsigned char *input,
  unsigned int len)
{
  unsigned int i, j;

  for (i = 0, j = 0; j < len; i++, j += 4)
    output[i] =  ((UINT4)input[j])
                | ((UINT4)input[j+1]) << 8
                | ((UINT4)input[j+2]) << 16
                | ((UINT4)input[j+3]) << 24;
  return;
}

/*
 * Decodes input (unsigned char) into output (UINT4).
 * Assumes len is a multiple of 4. */
static void Decode(
  UINT4 *output,
  unsigned char *input,
  unsigned int len)
{
  unsigned int i, j;

  for (i = 0, j = 0; j < len; i++, j += 4)
    output[i] =  ((UINT4)input[j])
                | ((UINT4)input[j+1]) << 8
                | ((UINT4)input[j+2]) << 16
                | ((UINT4)input[j+3]) << 24;
  return;
}

*/

** ===== END RFC 1321 CODE ===== *

struct md5_st {
  MD5_CTX ctx;
};

md5_rc_t md5_create(md5_t **md5)
{
  if (md5 == NULL)
    return MD5_RC_ARG;
  if (("*md5 = (md5_t *)malloc(sizeof(md5_t))")) == NULL)
    return MD5_RC_MEM;
  MD5Init(&("*md5)->ctx);
}
return MD5_RC_OK;
}

md5_rc_t md5_init(md5_t *md5)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    MD5Init(&(md5->ctx));
    return MD5_RC_OK;
}

md5_rc_t md5_update(md5_t *md5, const void *data_ptr, size_t data_len)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    MD5Update(&(md5->ctx), (unsigned char *)data_ptr, (unsigned int)data_len);
    return MD5_RC_OK;
}

md5_rc_t md5_store(md5_t *md5, void **data_ptr, size_t *data_len)
{
    MD5_CTX ctx;

    if (md5 == NULL || data_ptr == NULL)
        return MD5_RC_ARG;
    if (*data_ptr == NULL) {
        if (*data_ptr = malloc(MD5_LEN_BIN)) == NULL)
            return MD5_RC_MEM;
        if (data_len != NULL)
            *data_len = MD5_LEN_BIN;
    } else {
        if (data_len != NULL) {
            if (*data_len < MD5_LEN_BIN)
                return MD5_RC_MEM;
            *data_len = MD5_LEN_BIN;
        }
    }
    memcpy((void *)(&ctx), (void *)(&(md5->ctx)), sizeof(MD5_CTX));
    MD5Final((unsigned char *)(*data_ptr), &(ctx));
    return MD5_RC_OK;
}

md5_rc_t md5_format(md5_t *md5, char **data_ptr, size_t *data_len)
{
    static const char hex[] = "0123456789abcdef";
    unsigned char buf[MD5_LEN_BIN];
    unsigned char *bufptr;
size_t buflen;
md5_rc_t rc;
int i;

if (md5 == NULL || data_ptr == NULL)
    return MD5_RC_ARG;
if (*data_ptr == NULL) {
    if ((*data_ptr = (char *)malloc(MD5_LEN_STR+1)) == NULL)
        return MD5_RC_MEM;
    if (data_len != NULL)
        *data_len = MD5_LEN_STR+1;
}
else {
    if (data_len != NULL) {
        if (*data_len < MD5_LEN_STR+1)
            return MD5_RC_MEM;
        *data_len = MD5_LEN_STR+1;
    }
}

bufptr = buf;
 buflen = sizeof(buf);
if ((rc = md5_store(md5, (void **)((void *)&bufptr), &buflen)) != MD5_RC_OK)
    return rc;
for (i = 0; i < (int)buflen; i++) {
    (*data_ptr)[(i*2)+0] = hex[(int)(bufptr[i] >> 4)];
    (*data_ptr)[(i*2)+1] = hex[(int)(bufptr[i] & 0x0f)];
}
    (*data_ptr)[(i*2)] = '\0';
    return MD5_RC_OK;

md5_rc_t md5_destroy(md5_t *md5)
{
    if (md5 == NULL)
        return MD5_RC_ARG;
    free(md5);
    return MD5_RC_OK;
}
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1.193 perl 5.10.1-12ubuntu2

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It was downloaded from:

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

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=head1 NAME

copyright.t

=head1 DESCRIPTION

Tests that the latest copyright years in the top-level README file and the
C<perl -v> output match each other.

If the test fails, update at least one of README and perl.c so that they match
reality.

Optionally you can pass the C<--now> option to check they are at the current
year. This isn't checked by default, so that it doesn't fail for people
working on older releases. It should be run before making a new release.

=cut

use TestInit;
use strict;
use Config;
BEGIN { require 'test.pl' }

if ( $Config{usecrosscompile} ) {
  skip_all( "Not all files are available during cross-compilation" );
}

my ($opt) = @ARGV;

my $readme_year = readme_year();
my $v_year = v_year();

# Check that both copyright dates are up-to-date, but only if requested, so
# that tests still pass for people intentionally working on older versions:
if ($opt eq '--now') {
  my $current_year = (gmtime)[5] + 1900;
  is $v_year, $current_year, 'perl -v copyright includes current year';
  is $readme_year, $current_year, 'README copyright includes current year';
}
# Otherwise simply check that the two copyright dates match each other:
else
{
   is $readme_year, $v_year, 'README and perl -v copyright dates match';
}

done_testing;

sub readme_year
# returns the latest copyright year from the top-level README file
{

   open my $readme, '<', '../README' or die "Opening README failed: $!"
;

   # The copyright message is the first paragraph:
   local $/ = '';
   my $copyright_msg = <$readme>
;
   my ($year) = $copyright_msg =~ /.*(\d{4,})/s
      or die "Year not found in README copyright message '$copyright_msg'";

   $year;
}

sub v_year
# returns the latest copyright year shown in perl -v
{

   my $output = runperl switches => ['-v']
;
   my ($year) = $output =~ /copyright 1987.*(\d{4,})/i
      or die "Copyright statement not found in perl -v output '$output'";

   $year;
}

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   <signature of Ty Coon>, 1 April 1989
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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.6 of 6 September 2010

---
abstract: 'Build and install Perl modules'
author:
- 'Ken Williams <kwilliams@cpan.org>'
- "Development questions, bug reports, and patches should be sent to the\nModule-Build mailing list at <module-
build@perl.org>.'"
build_requires:
File::Temp: 0.15
Test::Harness: 3.16
Test::More: 0.49
generated_by: 'Module::Build version 0.3608'
license: gpl
meta-spec:
  url: http://module-build.sourceforge.net/META-spec-v1.4.html
  version: 1.4
name: Module-Build
resources:
  MailingList: mailto:module-build@perl.org
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  version: 3
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1.195 perl-base 5.10.1-12ubuntu2

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bzip2/libbzip2 version 1.0.5 of 10 December 2007

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bzip2/libbzip2 version 1.0.5 of 10 December 2007

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bzip2/libbzip2 version 1.0.5 of 10 December 2007

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That's all there is to it!

--------------------------------------------------------------------------

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Julian Seward, jseward@bzip.org
bzip2/libbzip2 version 1.0.5 of 10 December 2007

1.197 pixman 0.32.6 :r0

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*
* Author: Siarhei Siamashka (siarhei.siamashka@nokia.com)
*/

/*
* This file contains a macro ('generate_composite_function') which can
* construct 2D image processing functions, based on a common template.
* Any combinations of source, destination and mask images with 8bpp,
* 16bpp, 24bpp, 32bpp color formats are supported.
*
* This macro takes care of:
* - handling of leading and trailing unaligned pixels
* - doing most of the work related to L2 cache preload
* - encourages the use of software pipelining for better instructions
*
* The user of this macro has to provide some configuration parameters
* (bit depths for the images, prefetch distance, etc.) and a set of
* macros, which should implement basic code chunks responsible for
* pixels processing. See 'pixman-arm-neon-asm.S' file for the usage
* examples.
*
* TODO:
* - try overlapped pixel method (from Ian Rickards) when processing
*   exactly two blocks of pixels
* - maybe add an option to do reverse scanline processing
*/

/*
* Bit flags for 'generate_composite_function' macro which are used
* to tune generated functions behavior.
*/
.set FLAG_DST_WRITEONLY, 0
.set FLAG_DST_READWRITE, 1
.set FLAG_DEINTERLEAVE_32BPP, 2

/*
 * Offset in stack where mask and source pointer/stride can be accessed
 * from 'init' macro. This is useful for doing special handling for solid mask.
 */
.set ARGSS_STACK_OFFSET, 40

/*
 * Constants for selecting preferable prefetch type.
 */
.set PREFETCH_TYPE_NONE, 0 /* No prefetch at all */
.set PREFETCH_TYPE_SIMPLE, 1 /* A simple, fixed-distance-ahead prefetch */
.set PREFETCH_TYPE_ADVANCED, 2 /* Advanced fine-grained prefetch */

/*
 * Definitions of supplementary pixld/pixst macros (for partial load/store of
 * pixel data).
 */

.macro pixldst1 op, elem_size, reg1, mem_operand, abits
    .if abits > 0
        op&.&elem_size {d&reg1}, [&mem_operand&,:&abits&!]
    .else
        op&.&elem_size {d&reg1}, [&mem_operand&!]
    .endif
.endm

.macro pixldst2 op, elem_size, reg1, reg2, mem_operand, abits
    .if abits > 0
        op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&,:&abits&!]
    .else
        op&.&elem_size {d&reg1, d&reg2}, [&mem_operand&!]
    .endif
.endm

.macro pixldst4 op, elem_size, reg1, reg2, reg3, reg4, mem_operand, abits
    .if abits > 0
        op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&,:&abits&!]
    .else
        op&.&elem_size {d&reg1, d&reg2, d&reg3, d&reg4}, [&mem_operand&!]
    .endif
.endm

.macro pixldst0 op, elem_size, reg1, idx, mem_operand, abits
    op&.&elem_size {d&reg1[idx]}, [&mem_operand&!]
.endm
.macro pixldst3 op, elem_size, reg1, reg2, reg3, mem_operand
    op&&.elem_size {d&reg1, d&reg2, d&reg3}, &[mem_operand&]!
.endm

.macro pixldst30 op, elem_size, reg1, reg2, reg3, idx, mem_operand
    op&&.elem_size {d&reg1[idx], d&reg2[idx], d&reg3[idx]}, &[mem_operand&]!
.endm

.macro pixldst numbytes, op, elem_size, basereg, mem_operand, abits
    .if numbytes == 32
        pixldst4 op, elem_size, %basereg+4, %basereg+5, %basereg+6, %basereg+7, mem_operand, abits
    .elseif numbytes == 16
        pixldst2 op, elem_size, %basereg+2, %basereg+3, mem_operand, abits
    .elseif numbytes == 8
        pixldst1 op, elem_size, %basereg+1, mem_operand, abits
    .elseif numbytes == 4
        .if !RESPECT_STRICT_ALIGNMENT || (elem_size == 32)
            pixldst0 op, 32, %basereg+0, 1, mem_operand, abits
        .else elem_size == 16
            pixldst0 op, 16, %basereg+0, 2, mem_operand, abits
            pixldst0 op, 16, %basereg+0, 3, mem_operand, abits
        .else
            pixldst0 op, 8, %basereg+0, 4, mem_operand, abits
            pixldst0 op, 8, %basereg+0, 5, mem_operand, abits
            pixldst0 op, 8, %basereg+0, 6, mem_operand, abits
            pixldst0 op, 8, %basereg+0, 7, mem_operand, abits
        .endif
    .elseif numbytes == 2
        .if !RESPECT_STRICT_ALIGNMENT || (elem_size == 16)
            pixldst0 op, 16, %basereg+0, 1, mem_operand, abits
        .else
            pixldst0 op, 8, %basereg+0, 2, mem_operand, abits
            pixldst0 op, 8, %basereg+0, 3, mem_operand, abits
        .endif
    .elseif numbytes == 1
        pixldst0 op, 8, %basereg+0, 1, mem_operand, abits
    .else
        .error "unsupported size: numbytes"
    .endif
.endm

.macro pixld numpix, bpp, basereg, mem_operand, abits=0
    .if bpp > 0
        .if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE_32BPP_ENABLED != 0)
            pixldst4 vld4, 8, %basereg+4, %basereg+5, %basereg+6, %basereg+7, mem_operand, abits
        .endif
    .endif
.endm
.elseif (bpp == 24) && (numpix == 8)
  pixldst3 vld3, 8, %(basereg+3), %(basereg+4), %(basereg+5), mem_operand
.elseif (bpp == 24) && (numpix == 4)
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 4, mem_operand
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 5, mem_operand
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 6, mem_operand
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 7, mem_operand
.elseif (bpp == 24) && (numpix == 2)
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 2, mem_operand
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 3, mem_operand
.elseif (bpp == 24) && (numpix == 1)
  pixldst30 vld3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 1, mem_operand
.else
  pixldst %(numpix * bpp / 8), vld1, %(bpp), basereg, mem_operand, abits
endif
endif
.endm

.macro pixst numpix, bpp, basereg, mem_operand, abits=0
  .if bpp > 0
  .if (bpp == 32) && (numpix == 8) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    pixldst4 vst4, 8, %(basereg+4), %(basereg+5),
               %(basereg+6), %(basereg+7), mem_operand, abits
  .elseif (bpp == 24) && (numpix == 8)
    pixldst3 vst3, 8, %(basereg+3), %(basereg+4), %(basereg+5), mem_operand
  .elseif (bpp == 24) && (numpix == 4)
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 4, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 5, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 6, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 7, mem_operand
  .elseif (bpp == 24) && (numpix == 2)
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 2, mem_operand
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 3, mem_operand
  .elseif (bpp == 24) && (numpix == 1)
    pixldst30 vst3, 8, %(basereg+0), %(basereg+1), %(basereg+2), 1, mem_operand
  .else
    pixldst %(numpix * bpp / 8), vst1, %(bpp), basereg, mem_operand, abits
  endif
  endif
.endm

.macro pixld_a numpix, bpp, basereg, mem_operand
  .if (bpp * numpix) <= 128
    pixld numpix, bpp, basereg, mem_operand, %(bpp * numpix)
  .else
    pixld numpix, bpp, basereg, mem_operand, 128
  endif
.endm
.macro pixst_a numpix, bpp, basereg, mem_operand
.if (bpp * numpix) <= 128
   pixst numpix, bpp, basereg, mem_operand, %(bpp * numpix)
.else
   pixst numpix, bpp, basereg, mem_operand, 128
.endif
.endm

 Marino pixst_a numpix, bpp, basereg, mem_operand
 .if (bpp * numpix) <= 128
   pixst numpix, bpp, basereg, mem_operand, %(bpp * numpix)
 .else
   pixst numpix, bpp, basereg, mem_operand, 128
 .endif
 .endm

 /*
 * Pixel fetcher for nearest scaling (needs TMP1, TMP2, VX, UNIT_X register
 * aliases to be defined)
 */

 .macro pixld1_s elem_size, reg1, mem_operand
 .if elem_size == 16
   mov     TMP1, VX, asr #16
   adds    VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP1, mem_operand, TMP1, asl #1
      mov    TMP2, VX, asr #16
      adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP2, mem_operand, TMP2, asl #1
      vld1.16 {d&reg1&[0]}, [TMP1, :16]
      mov    TMP1, VX, asr #16
      adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP1, mem_operand, TMP1, asl #1
      vld1.16 {d&reg1&[1]}, [TMP1, :16]
      mov    TMP2, VX, asr #16
      adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP1, mem_operand, TMP1, asl #1
      vld1.16 {d&reg1&[2]}, [TMP1, :16]
      vld1.16 {d&reg1&[3]}, [TMP2, :16]
 .elseif elem_size == 32
   mov     TMP1, VX, asr #16
   adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP1, mem_operand, TMP1, asl #1
      vld1.16 {d&reg1&[0]}, [TMP1, :16]
      mov    TMP2, VX, asr #16
      adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP2, mem_operand, TMP2, asl #2
      vld1.16 {d&reg1&[2]}, [TMP2, :16]
      vld1.16 {d&reg1&[3]}, [TMP2, :16]
 .elseif elem_size == 64
   mov     TMP1, VX, asr #16
   adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
      bpl  5b
      add    TMP1, mem_operand, TMP1, asl #2
      vld1.16 {d&reg1&[0]}, [TMP1, :16]
      mov    TMP2, VX, asr #16
      adds   VX, VX, UNIT_X
   5: subpls VX, VX, SRC_WIDTH_FIXED
 .endif
bpl 5b
add TMP2, mem_operand, TMP2, asl #2
vld1.32 {d&reg1&[0]}, [TMP1, :32]
vld1.32 {d&reg1&[1]}, [TMP2, :32]
.else
  .error "unsupported"
.endif
.endm

.macro pixld2_s elem_size, reg1, reg2, mem_operand
  .if 0 /* elem_size == 32 */
    mov TMP1, VX, asr #16
    add VX, VX, UNIT_X, asl #1
    add TMP1, mem_operand, TMP1, asl #2
    mov TMP2, VX, asr #16
    sub VX, VX, UNIT_X
    add TMP2, mem_operand, TMP2, asl #2
    vld1.32 {d&reg1&[0]}, [TMP1, :32]
    mov TMP1, VX, asr #16
    adds VX, VX, UNIT_X
    add TMP1, mem_operand, TMP1, asl #2
    vld1.32 {d&reg1&[0]}, [TMP1, :32]
    mov TMP2, VX, asr #16
    add VX, VX, UNIT_X
    add TMP2, mem_operand, TMP2, asl #2
    vld1.32 {d&reg1&[1]}, [TMP1, :32]
    vld1.32 {d&reg2&[1]}, [TMP2, :32]
  .else
    pixld1_s elem_size, reg1, mem_operand
    pixld1_s elem_size, reg2, mem_operand
  .endif
.endm

.macro pixld0_s elem_size, reg1, idx, mem_operand
  .if elem_size == 16
    mov TMP1, VX, asr #16
    adds VX, VX, UNIT_X
  5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl 5b
    add TMP1, mem_operand, TMP1, asl #1
    vldl.16 {d&reg1&[idx]}, [TMP1, :16]
  .elseif elem_size == 32
    mov TMP1, VX, asr #16
    adds VX, VX, UNIT_X
  5: subpls VX, VX, SRC_WIDTH_FIXED
    bpl 5b
    add TMP1, mem_operand, TMP1, asl #2
    vld1.32 {d&reg1&[idx]}, [TMP1, :32]
.macro pixld_s_internal numbytes, elem_size, basereg, mem_operand
    .if numbytes == 32
        pixld2_s elem_size, %(basereg+4), %(basereg+5), mem_operand
        pixld2_s elem_size, %(basereg+6), %(basereg+7), mem_operand
        pixdeinterleave elem_size, %(basereg+4)
    .elseif numbytes == 16
        pixld2_s elem_size, %(basereg+2), %(basereg+3), mem_operand
    .elseif numbytes == 8
        pixld1_s elem_size, %(basereg+1), mem_operand
    .elseif numbytes == 4
        .if elem_size == 32
            pixld0_s elem_size, %(basereg+0), 1, mem_operand
        .elseif elem_size == 16
            pixld0_s elem_size, %(basereg+0), 2, mem_operand
            pixld0_s elem_size, %(basereg+0), 3, mem_operand
        .else
            pixld0_s elem_size, %(basereg+0), 4, mem_operand
            pixld0_s elem_size, %(basereg+0), 5, mem_operand
            pixld0_s elem_size, %(basereg+0), 6, mem_operand
            pixld0_s elem_size, %(basereg+0), 7, mem_operand
        .endif
    .elseif numbytes == 2
        .if elem_size == 16
            pixld0_s elem_size, %(basereg+0), 1, mem_operand
        .else
            pixld0_s elem_size, %(basereg+0), 2, mem_operand
            pixld0_s elem_size, %(basereg+0), 3, mem_operand
        .endif
    .elseif numbytes == 1
        pixld0_s elem_size, %(basereg+0), 1, mem_operand
    .else
        .error "unsupported size: numbytes"
    .endif
.endm

.macro pixld_s numpix, bpp, basereg, mem_operand
    .if bpp > 0
        pixld_s_internal %(numpix * bpp / 8), %(bpp), basereg, mem_operand
    .endif
.endm

.macro vuzp8 reg1, reg2
    vuzp.8 d&reg1, d&reg2
.endm
/* deinterleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixdeinterleave bpp, basereg
  .if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    vuzp8 %(%basereg+0), %(%basereg+1)
    vuzp8 %(%basereg+2), %(%basereg+3)
    vuzp8 %(%basereg+1), %(%basereg+3)
    vuzp8 %(%basereg+0), %(%basereg+2)
  .endif
.endm
.endm

/* interleave B, G, R, A channels for eight 32bpp pixels in 4 registers */
.macro pixinterleave bpp, basereg
  .if (bpp == 32) && (DEINTERLEAVE_32BPP_ENABLED != 0)
    vzip8 %(%basereg+0), %(%basereg+2)
    vzip8 %(%basereg+1), %(%basereg+3)
    vzip8 %(%basereg+2), %(%basereg+3)
    vzip8 %(%basereg+0), %(%basereg+1)
  .endif
.endm
.endm

/*
 * This is a macro for implementing cache preload. The main idea is that
 * cache preload logic is mostly independent from the rest of pixels
 * processing code. It starts at the top left pixel and moves forward
 * across pixels and can jump across scanlines. Prefetch distance is
 * handled in an 'incremental' way: it starts from 0 and advances to the
 * optimal distance over time. After reaching optimal prefetch distance,
 * it is kept constant. There are some checks which prevent prefetching
 * unneeded pixel lines below the image (but it still can prefetch a bit
 * more data on the right side of the image - not a big issue and may
 * be actually helpful when rendering text glyphs). Additional trick is
 * the use of LDR instruction for prefetch instead of PLD when moving to
 * the next line, the point is that we have a high chance of getting TLB
 * miss in this case, and PLD would be useless.
 *
 * This sounds like it may introduce a noticeable overhead (when working with
 * fully cached data). But in reality, due to having a separate pipeline and
 * instruction queue for NEON unit in ARM Cortex-A8, normal ARM code can
 * execute simultaneously with NEON and be completely shadowed by it. Thus
 * we get no performance overhead at all (*). This looks like a very nice
 * feature of Cortex-A8, if used wisely. We don't have a hardware prefetcher,
 * but still can implement some rather advanced prefetch logic in software
 * for almost zero cost!
 *

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* (*) The overhead of the prefetcher is visible when running some trivial
* pixels processing like simple copy. Anyway, having prefetch is a must
* when working with the graphics data.
*/

```
.macro PF a, x:vararg
  .if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_ADVANCED)
    a x
  .endif
.endm

.macro cache_preload std_increment, boost_increment
  .if (src_bpp_shift >= 0) || (dst_r_bpp != 0) || (mask_bpp_shift >= 0)
    .if regs_shortage
      PF ldr ORIG_W, [sp] /* If we are short on regs, ORIG_W is kept on stack */
    .endif
  .endif
  .if std_increment != 0
    PF add PF_X, PF_X, #std_increment
  .endif
  PF tst PF_CTL, #0xF
  PF addne PF_X, PF_X, #boost_increment
  PF subne PF_CTL, PF_CTL, #1
  PF cmp PF_X, ORIG_W
  .if src_bpp_shift >= 0
    PF pld, [PF_SRC, PF_X, lsl #src_bpp_shift]
  .endif
  .if dst_r_bpp != 0
    PF pld, [PF_DST, PF_X, lsl #dst_bpp_shift]
  .endif
  .if mask_bpp_shift >= 0
    PF pld, [PF_MASK, PF_X, lsl #mask_bpp_shift]
  .endif
  PF subge PF_X, PF_X, ORIG_W
  PF subges PF_CTL, PF_CTL, #0x10
  .if src_bpp_shift >= 0
    PF ldrgeb DUMMY, [PF_SRC, SRC_STRIDE, lsl #src_bpp_shift]!
  .endif
  .if dst_r_bpp != 0
    PF ldrgeb DUMMY, [PF_DST, DST_STRIDE, lsl #dst_bpp_shift]!
  .endif
  .if mask_bpp_shift >= 0
    PF ldrgeb DUMMY, [PF_MASK, MASK_STRIDE, lsl #mask_bpp_shift]!
  .endif
  .endif
.endm

.macro cache_preload_simple
  .if (PREFETCH_TYPE_CURRENT == PREFETCH_TYPE_SIMPLE)
    .if src_bpp > 0
      .if (src_bpp_shift >= 0) || (dst_r_bpp != 0) || (mask_bpp_shift >= 0)
        .if regs_shortage
          PF ldr ORIG_W, [sp] /* If we are short on regs, ORIG_W is kept on stack */
        .endif
      .endif
      .if std_increment != 0
        PF add PF_X, PF_X, #std_increment
      .endif
      PF cmp PF_X, ORIG_W
      .if src_bpp_shift >= 0
        PF pld, [PF_SRC, PF_X, lsl #src_bpp_shift]
      .endif
      .if dst_r_bpp != 0
        PF pld, [PF_DST, PF_X, lsl #dst_bpp_shift]
      .endif
      .if mask_bpp_shift >= 0
        PF pld, [PF_MASK, PF_X, lsl #mask_bpp_shift]
      .endif
      PF cmp PF_X, ORIG_W
      .if src_bpp_shift >= 0
        PF ldrgeb DUMMY, [PF_SRC, SRC_STRIDE, lsl #src_bpp_shift]!
      .endif
      .if dst_r_bpp != 0
        PF ldrgeb DUMMY, [PF_DST, DST_STRIDE, lsl #dst_bpp_shift]!
      .endif
      .if mask_bpp_shift >= 0
        PF ldrgeb DUMMY, [PF_MASK, MASK_STRIDE, lsl #mask_bpp_shift]!
      .endif
      .endif
    .endif
  .endif
.endm
```
pld [SRC, #(PREFETCH_DISTANCE_SIMPLE * src_bpp / 8)]
.endif
.if dst_r_bpp > 0
  pld [DST_R, #(PREFETCH_DISTANCE_SIMPLE * dst_r_bpp / 8)]
.endif
.if mask_bpp > 0
  pld [MASK, #(PREFETCH_DISTANCE_SIMPLE * mask_bpp / 8)]
.endif
.endif
.endm

.macro fetch_mask_pixblock
  pixld       pixblock_size, mask_bpp, 
          (mask_basereg - pixblock_size * mask_bpp / 64), MASK
.endm

/ *
* Macro which is used to process leading pixels until destination
* pointer is properly aligned (at 16 bytes boundary). When destination
* buffer uses 16bpp format, this is unnecessary, or even pointless.
*/
.macro ensure_destination_ptr_alignment process_pixblock_head, 
process_pixblock_tail, 
process_pixblock_tail_head
  .if dst_w_bpp != 24
    tst     DST_R, #0xF
    beq     2f
    .irp lowbit, 1, 2, 4, 8, 16
    local skip1
    .if (dst_w_bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst_w_bpp))
      .if lowbit < 16 /* we don't need more than 16-byte alignment */
        tst     DST_R, #lowbit
        beq     1f
      .endif
      pixld_src  (lowbit * 8 / dst_w_bpp), src_bpp, src_basereg, SRC
      pixld  (lowbit * 8 / dst_w_bpp), mask_bpp, mask_basereg, MASK
      .if dst_r_bpp > 0
        pixld_a  (lowbit * 8 / dst_r_bpp), dst_r_bpp, dst_r_basereg, DST_R
      .else
        add     DST_R, DST_R, #lowbit
      .endif
      PF add   PF_X, PF_X, #(lowbit * 8 / dst_w_bpp)
      sub      W, W, #(lowbit * 8 / dst_w_bpp)
    1:
    .endif
    .endr
  pixdeinterleave src_bpp, src_basereg

pixdeinterleave mask bpp, mask basereg
pixdeinterleave dst_r bpp, dst_r basereg

process_pixblock_head
  cache_preload 0, pixblock_size
  cache_preload_simple
  process_pixblock_tail

  pixinterleave dst w bpp, dst w basereg
  .irp lowbit, 1, 2, 4, 8, 16
  .if (dst w bpp <= (lowbit * 8)) && ((lowbit * 8) < (pixblock_size * dst w bpp))
  .if lowbit < 16 /* we don't need more than 16-byte alignment */
    tst DST W, #lowbit
    beq 1f
  .endif
  .endif
  .endr
  .endif
  2:
  .endm

/*
 * Special code for processing up to (pixblock_size - 1) remaining
 * trailing pixels. As SIMD processing performs operation on
 * pixblock_size pixels, anything smaller than this has to be loaded
 * and stored in a special way. Loading and storing of pixel data is
 * performed in such a way that we fill some 'slots' in the NEON
 * registers (some slots naturally are unused), then perform compositing
 * operation as usual. In the end, the data is taken from these 'slots'
 * and saved to memory.
 *
 * cache_preload_flag - allows to suppress prefetch if
 * set to 0
 * dst_aligned_flag - selects whether destination buffer
 * is aligned
 */

.macro process_trailing_pixels cache_preload_flag, \\
    dst_aligned_flag, \\
    process_pixblock_head, \\
    process_pixblock_tail, \\
    process_pixblock_tail_head
  tst W, #(pixblock_size - 1)
  beq 2f
  .irp chunk_size, 16, 8, 4, 2, 1
  .if pixblock_size > chunk_size
    tst W, #chunk_size
beq 1f
pixld_src chunk_size, src_bpp, src_basereg, SRC
pixld chunk_size, mask_bpp, mask_basereg, MASK
.if dst_aligned_flag != 0
pixld_a chunk_size, dst_r_bpp, dst_r_basereg, DST_R
.else
pixld chunk_size, dst_r_bpp, dst_r_basereg, DST_R
.endif
.if cache_preload_flag != 0
PF add PF_X, PF_X, #chunk_size
.endif
1:
.endif
.endr
pixdeinterleave src_bpp, src_basereg
pixdeinterleave mask_bpp, mask_basereg
pixdeinterleave dst_r_bpp, dst_r_basereg

process_pixblock_head
.if cache_preload_flag != 0
cache_preload 0, pixblock_size
.cache_preload_simple
.endif
.process_pixblock_tail
.pixinterleave dst_w_bpp, dst_w_basereg
.irp chunk_size, 16, 8, 4, 2, 1
.if pixblock_size > chunk_size
.tst W, #chunk_size
.beq 1f
.if dst_aligned_flag != 0
.pixst_a chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.else
.pixst chunk_size, dst_w_bpp, dst_w_basereg, DST_W
.endif
1:
.endif
.endr
2:
.endm

/*
 * Macro, which performs all the needed operations to switch to the next
 * scanline and start the next loop iteration unless all the scanlines
 * are already processed.
 */
.macro advance_to_next_scanline start_of_loop_label
.if regs_shortage
ldrd W, [sp] /* load W and H (width and height) from stack */
/*
.else
   mov        W, ORIG_W
.endif
.
.add        DST_W, DST_W, DST_STRIDE, lsl #dst_bpp_shift
.
.if src_bpp != 0
   add        SRC, SRC, SRC_STRIDE, lsl #src_bpp_shift
.endif
.
.if mask_bpp != 0
   add        MASK, MASK, MASK_STRIDE, lsl #mask_bpp_shift
.endif
.
.if (dst_w_bpp != 24)
   sub        DST_W, DST_W, W, lsl #dst_bpp_shift
.endif
.
.if (src_bpp != 24) && (src_bpp != 0)
   sub        SRC, SRC, W, lsl #src_bpp_shift
.endif
.
.if (mask_bpp != 24) && (mask_bpp != 0)
   sub        MASK, MASK, W, lsl #mask_bpp_shift
.endif
.
.subs       H, H, #1
.
.mov        DST_R, DST_W
.
.if regs_shortage
   str        H, [sp, #4] /* save updated height to stack */
.endif
.
.bge        start_of_loop_label
.
.endm

/*
 * Registers are allocated in the following way by default:
 * d0, d1, d2, d3     - reserved for loading source pixel data
 * d4, d5, d6, d7     - reserved for loading destination pixel data
 * d24, d25, d26, d27 - reserved for loading mask pixel data
 * d28, d29, d30, d31 - final destination pixel data for writeback to memory
 */

.macro generate_composite_function fname, 
   src_bpp_, 
   mask_bpp_, 
   dst_w_bpp_, 
   flags, 
   pixblock_size_, 
   prefetch_distance, 
   init, 
   cleanup, 
   process_pixblock_head, 
   process_pixblock_tail, 
   process_pixblock_tail_head, 
   dst_w_basereg_ = 28, 
   dst_r_basereg_ = 4, 


.func fname
.globl fname
/* For ELF format also set function visibility to hidden */
#if defined __ELF__
.hidden fname
.type fname, %function
#endif
fname:
push {r4-r12, lr} /* save all registers */

/ *
* Select prefetch type for this function. If prefetch distance is
* set to 0 or one of the color formats is 24bpp, SIMPLE prefetch
* has to be used instead of ADVANCED.
*/
.set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_DEFAULT
.if prefetch_distance == 0
.set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE
.elseif (PREFETCH_TYPE_CURRENT > PREFETCH_TYPE_SIMPLE) && 
    ((src_bpp_ == 24) || (mask_bpp_ == 24) || (dst_w_bpp_ == 24))
.set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_SIMPLE
.endif

/ *
* Make some macro arguments globally visible and accessible
* from other macros
*/
.set src_bpp, src_bpp_
.set mask_bpp, mask_bpp_
.set dst_w_bpp, dst_w_bpp_
.set pixblock_size, pixblock_size_
.set dst_w_basereg, dst_w_basereg_
.set dst_r_basereg, dst_r_basereg_
.set src_basereg, src_basereg_
.set mask_basereg, mask_basereg_

.macro pixld_src x:vararg
    pixld x
.endm
.macro fetch_src_pixblock
    pixld_src pixblock_size, src_bpp, \ 
    (src_basereg - pixblock_size * src_bpp / 64), SRC
.endm
/*
* Assign symbolic names to registers
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W .req r0 /* width (is updated during processing) */

H .req r1 /* height (is updated during processing) */

DST_W .req r2 /* destination buffer pointer for writes */

DST_STRIDE .req r3 /* destination image stride */

SRC .req r4 /* source buffer pointer */

SRC_STRIDE .req r5 /* source image stride */

DST_R .req r6 /* destination buffer pointer for reads */

MASK .req r7 /* mask pointer */

MASK_STRIDE .req r8 /* mask stride */

PF_CTL .req r9 /* combined lines counter and prefetch */

/* distance increment counter */

PF_X .req r10 /* pixel index in a scanline for current */

/* for prefetch purposes */

PF_SRC .req r11 /* pointer to source scanline start */

/* for prefetch purposes */

PF_DST .req r12 /* pointer to destination scanline start */

/* for prefetch purposes */

PF_MASK .req r14 /* pointer to mask scanline start */

/* for prefetch purposes */

*/*

* Check whether we have enough registers for all the local variables.
* If we don't have enough registers, original width and height are
* kept on top of stack (and 'regs_shortage' variable is set to indicate
* this for the rest of code). Even if there are enough registers, the
* allocation scheme may be a bit different depending on whether source
* or mask is not used.
*/*

.if (PREFETCH_TYPE_CURRENT < PREFETCH_TYPE_ADVANCED)

ORIG_W .req r10 /* saved original width */

DUMMY .req r12 /* temporary register */

.set regs_shortage, 0

.elseif mask_bpp == 0

ORIG_W .req r7 /* saved original width */

DUMMY .req r8 /* temporary register */

.set regs_shortage, 0

.elseif src_bpp == 0

ORIG_W .req r4 /* saved original width */

DUMMY .req r5 /* temporary register */

.set regs_shortage, 0

.else

ORIG_W .req r1 /* saved original width */

DUMMY .req r1 /* temporary register */

.set regs_shortage, 1

.endif
.set mask_bpp_shift, -1
.if src_bpp == 32
    .set src_bpp_shift, 2
.elseif src_bpp == 24
    .set src_bpp_shift, 0
.elseif src_bpp == 16
    .set src_bpp_shift, 1
.elseif src_bpp == 8
    .set src_bpp_shift, 0
.elseif src_bpp == 0
    .set src_bpp_shift, -1
.else
    .error "requested src bpp (src_bpp) is not supported"
.endif
.if mask_bpp == 32
    .set mask_bpp_shift, 2
.elseif mask_bpp == 24
    .set mask_bpp_shift, 0
.elseif mask_bpp == 8
    .set mask_bpp_shift, 0
.elseif mask_bpp == 0
    .set mask_bpp_shift, -1
.else
    .error "requested mask bpp (mask_bpp) is not supported"
.endif
.if dst_w_bpp == 32
    .set dst_bpp_shift, 2
.elseif dst_w_bpp == 24
    .set dst_bpp_shift, 0
.elseif dst_w_bpp == 16
    .set dst_bpp_shift, 1
.elseif dst_w_bpp == 8
    .set dst_bpp_shift, 0
.else
    .error "requested dst bpp (dst_w_bpp) is not supported"
.endif
.if (((flags) & FLAG_DST_READWRITE) != 0)
    .set dst_r_bpp, dst_w_bpp
.else
    .set dst_r_bpp, 0
.endif
.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
    .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
    .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif
.if prefetch_distance < 0 || prefetch_distance > 15
  .error "invalid prefetch distance (prefetch_distance)"
.endif

.if src_bpp > 0
  ldr SRC, [sp, #40]
.endif
.if mask_bpp > 0
  ldr MASK, [sp, #48]
.endif
  PF mov PF_X, #0
.if src_bpp > 0
  ldr SRC_STRIDE, [sp, #44]
.endif
.if mask_bpp > 0
  ldr MASK_STRIDE, [sp, #52]
.endif
  mov DST_R, DST_W

.if src_bpp == 24
  sub SRC_STRIDE, SRC_STRIDE, W
  sub SRC_STRIDE, SRC_STRIDE, W, lsl #1
.endif
.if mask_bpp == 24
  sub MASK_STRIDE, MASK_STRIDE, W
  sub MASK_STRIDE, MASK_STRIDE, W, lsl #1
.endif
.if dst_w_bpp == 24
  sub DST_STRIDE, DST_STRIDE, W
  sub DST_STRIDE, DST_STRIDE, W, lsl #1
.endif

/*
 * Setup advanced prefetcher initial state
 */
  PF mov PF_SRC, SRC
  PF mov PF_DST, DST_R
  PF mov PF_MASK, MASK
  PF mov PF_CTL, H, lsl #4
  PF add PF_CTL, #(prefetch_distance - 0x10)

.init
.if regs_shortage
  push {r0, r1}
.endif
  subs H, H, #1
.if regs_shortage
str   H, [sp, #4] /* save updated height to stack */
.else
   mov   ORIG_W, W
.endif
blt    9f
cmp    W, #(pixblock_size * 2)
blt    8f
/*
 * This is the start of the pipelined loop, which if optimized for
 * long scanlines
 */
0:
   ensure_destination_ptr_alignment process_pixblock_head,
       process_pixblock_tail,
       process_pixblock_tail_head

/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
pixld_a pixblock_size, dst_r_bpp, 
         (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld   pixblock_size, mask_bpp, 
        (mask_basereg - pixblock_size * mask_bpp / 64), MASK
PF add   PF_X, PF_X, #pixblock_size
process_pixblock_head
cache_preload 0, pixblock_size
cache_preload_simple
subs    W, W, #(pixblock_size * 2)
blt    2f
1:
   process_pixblock_tail_head
   cache_preload_simple
   subs    W, W, #pixblock_size
   bge    1b
2:
   process_pixblock_tail
   pixst_a pixblock_size, dst_w_bpp, 
            (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W

/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 1, 1, 
   process_pixblock_head,
   process_pixblock_tail,
   process_pixblock_tail_head
advance_to_next_scanline 0b
.if regs_shortage
   pop   {r0, r1}
.endif
/* This is the start of the loop, designed to process images with small width */
* (less than pixblock_size * 2 pixels). In this case neither pipelining
* nor prefetch are used.
*/

8:
/* Process exactly pixblock_size pixels if needed */
tst    W, #pixblock_size
beq    1f
pixld  pixblock_size, dst_r_bpp, \  
(dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
fetch_src_pixblock
pixld  pixblock_size, mask_bpp, \  
(mask_basereg - pixblock_size * mask_bpp / 64), MASK
process_pixblock_head
process_pixblock_tail
pixst  pixblock_size, dst_w_bpp, \  
(dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W

1:
/* Process the remaining trailing pixels in the scanline */
process_trailing_pixels 0, 0, \  
    process_pixblock_head, \  
    process_pixblock_tail, \  
    process_pixblock_tail_head
advance_to_next_scanline 8b

9:
.if regs_shortage
    pop    {r0, r1}
.endif

cleanup
    pop    {r4-r12, pc} /* exit */

.purgem fetch_src_pixblock
.purgem pixld_src

.unreq SRC
.unreq MASK
.unreq DST_R
.unreq DST_W
.unreq ORIG_W
.unreq W
.unreq H
.unreq SRC_STRIDE
.unreq DST_STRIDE
.unreq MASK_STRIDE
.unreq PF_CTL
/* A simplified variant of function generation template for a single scanline processing (for implementing pixman combine functions) */

.macro generate_composite_function_scanline        use_nearest_scaling, \
   fname, \ 
   src_bpp_, \ 
   mask_bpp_, \ 
   dst_w_bpp_, \ 
   flags, \ 
   pixblock_size_, \ 
   init, \ 
   cleanup, \ 
   process_pixblock_head, \ 
   process_pixblock_tail, \ 
   process_pixblock_tail_head, \ 
   dst_w_basereg_ = 28, \ 
   dst_r_basereg_ = 4, \ 
   src_basereg_  = 0, \ 
   mask_basereg_ = 24

   .func fname
   .global fname
   /* For ELF format also set function visibility to hidden */
   #ifdef __ELF__
   .hidden fname
   .type fname, %function
   #endif

   fname:
   .set PREFETCH_TYPE_CURRENT, PREFETCH_TYPE_NONE

   /* * Make some macro arguments globally visible and accessible * from other macros */
   #/ 
   .set src_bpp, src_bpp_ 
   .set mask_bpp, mask_bpp_ 
   .set dst_w_bpp, dst_w_bpp_ 
   .set pixblock_size, pixblock_size_ 
   .set dst_w_basereg, dst_w_basereg_ 
   .set dst_r_basereg, dst_r_basereg_ 

"
.set src_basereg, src_basereg_
.set mask_basereg, mask_basereg_

.if use_nearest_scaling != 0
/*
 * Assign symbolic names to registers for nearest scaling
*/
W .req r0
DST_W .req r1
SRC .req r2
VX .req r3
UNIT_X .req ip
MASK .req lr
TMP1 .req r4
TMP2 .req r5
DST_R .req r6
SRC_WIDTH_FIXED .req r7

.macro pixld_src x:vararg
    pixld_s x
.endm

ldr UNIT_X, [sp]
push {r4-r8, lr}
ldr SRC_WIDTH_FIXED, [sp, #(24 + 4)]
.if mask_bpp != 0
    ldr MASK, [sp, #(24 + 8)]
.endif
.else
/*
 * Assign symbolic names to registers
*/
W .req r0  /* width (is updated during processing) */
DST_W .req r1  /* destination buffer pointer for writes */
SRC .req r2  /* source buffer pointer */
DST_R .req ip  /* destination buffer pointer for reads */
MASK .req r3  /* mask pointer */

.macro pixld_src x:vararg
    pixld x
.endm
.endif

.if ((((flags) & FLAG_DST_READWRITE) != 0)
    .set dst_r_bpp, dst_w_bpp
.else
    .set dst_r_bpp, 0
.endif
.if (((flags) & FLAG_DEINTERLEAVE_32BPP) != 0)
  .set DEINTERLEAVE_32BPP_ENABLED, 1
.else
  .set DEINTERLEAVE_32BPP_ENABLED, 0
.endif

.macro fetch_src_pixblock
  pixld_src  pixblock_size, src_bpp, \n    (src_basereg - pixblock_size * src_bpp / 64), SRC
.endm

.init
mov         DST_R, DST_W

cmp         W, #pixblock_size
blt         8f

.ensure_destination_ptr_alignment process_pixblock_head, \n  process_pixblock_tail, \n  process_pixblock_tail_head

subs        W, W, #pixblock_size
blt         7f

/* Implement "head (tail_head) ... (tail_head) tail" loop pattern */
.pixld_a  pixblock_size, dst_r_bpp, \n        (dst_r_basereg - pixblock_size * dst_r_bpp / 64), DST_R
.fetch_src_pixblock
.pixld  pixblock_size, mask_bpp, \n        (mask_basereg - pixblock_size * mask_bpp / 64), MASK
.process_pixblock_head
subs        W, W, #pixblock_size
blt         2f
1:
  .process_pixblock_tail_head
subs        W, W, #pixblock_size
bge         1b
2:
  .process_pixblock_tail
.pixst_a  pixblock_size, dst_w_bpp, \n        (dst_w_basereg - pixblock_size * dst_w_bpp / 64), DST_W
7:
/* Process the remaining trailing pixels in the scanline (dst aligned) */
.process_trailing_pixels 0, 1, \n  .process_pixblock_head, \n  .process_pixblock_tail, \n  .process_pixblock_tail_head
cleanup
.if use_nearest_scaling != 0
  pop  {r4-r8, pc} /* exit */
.else
  bx   lr /* exit */
.endif
8:
/* Process the remaining trailing pixels in the scanline (dst unaligned) */
process_trailing_pixels 0, 0, \
  process_pixblock_head, \
  process_pixblock_tail, \
  process_pixblock_tail_head

.cleanup
.if use_nearest_scaling != 0
  pop  {r4-r8, pc} /* exit */

.unreq  DST_R
.unreq  SRC
.unreq  W
.unreq  VX
.unreq  UNIT_X
.unreq  TMP1
.unreq  TMP2
.unreq  DST_W
.unreq  MASK
.unreq  SRC_WIDTH_FIXED

.else
  bx   lr /* exit */

.unreq  SRC
.unreq  MASK
.unreq  DST_R
.unreq  DST_W
.unreq  W
.endif

.purgem  fetch_src_pixblock
.purgem  pixld_src

.endfunc
.endm

.macro generate_composite_function_single_scanline x:vararg
  generate_composite_function_scanline 0, x
.endm
.macro generate_composite_function_nearest_scanline x:vararg
  generate_composite_function_scanline 1, x
.endm

/* Default prologue/epilogue, nothing special needs to be done */

.macro default_init
.endm

.macro default_cleanup
.endm

/ *
* Prologue/epilogue variant which additionally saves/restores d8-d15
* registers (they need to be saved/restored by callee according to ABI).
* This is required if the code needs to use all the NEON registers.
*/

.macro default_init_need_all_regs
  vpush       {d8-d15}
.endm

.macro default_cleanup_need_all_regs
  vpop        {d8-d15}
.endm

/******************************************************************************/

/*
* Conversion of 8 r5g6b6 pixels packed in 128-bit register (in)
* into a planar a8r8g8b8 format (with a, r, g, b color components
* stored into 64-bit registers out_a, out_r, out_g, out_b respectively).
* Warning: the conversion is destructive and the original
* value (in) is lost.
*/

.macro convert_0565_to_8888 in, out_a, out_r, out_g, out_b
  vshrn.u16   out_r, in,    #8
  vshrn.u16   out_g, in,    #3
  vsli.u16    in,    in,    #5
  vmov.u8     out_a, #255
  vsri.u8     out_r, out_r, #5
  vsri.u8     out_g, out_g, #6
  vshrn.u16   out_b, in,    #2
.endm

.macro convert_0565_to_x888 in, out_r, out_g, out_b
  vshrn.u16   out_r, in,    #8
  vshrn.u16   out_g, in,    #3
  vsli.u16    in,    in,    #5
  vmov.u8     out_a, #255
  vsri.u8     out_r, out_r, #5
  vsri.u8     out_g, out_g, #6
  vshrn.u16   out_b, in,    #2
.endm
vshrn.u16  out_r, in,  #8
vshrn.u16  out_g, in,  #3
vsl.i.u16   in, in,  #5
vsri.u8    out_r, out_r, #5
vsri.u8    out_g, out_g, #6
vshrn.u16  out_b, in,  #2
.endm

/*
 * Conversion from planar a8r8g8b8 format (with a, r, g, b color components
 * in 64-bit registers in_a, in_r, in_g, in_b respectively) into 8 r5g6b6
 * pixels packed in 128-bit register (out). Requires two temporary 128-bit
 * registers (tmp1, tmp2)
 */
.macro convert_8888_to_0565 in_r, in_g, in_b, out, tmp1, tmp2
  vshll.u8    tmp1, in_g, #8
  vshll.u8    out, in_r, #8
  vshll.u8    tmp2, in_b, #8
  vsri.u16    out, tmp1, #5
  vsri.u16    out, tmp2, #11
.endm

/*
 * Conversion of four r5g6b5 pixels (in) to four x8r8g8b8 pixels
 * returned in (out0, out1) registers pair. Requires one temporary
 * 64-bit register (tmp). 'out1' and 'in' may overlap, the original
 * value from 'in' is lost
 */
.macro convert_four_0565_to_x888_packed in, out0, out1, tmp
  vshl.u16    out0, in,   #5  /* G top 6 bits */
  vshl.u16    tmp,  in,   #11 /* B top 5 bits */
  vsri.u16    in,   in,   #5  /* R is ready in top bits */
  vsri.u16    out0, out0, #6  /* G is ready in top bits */
  vsri.u16    tmp,  tmp,  #5  /* B is ready in top bits */
  vshr.u16    out1, in,   #8  /* R is in place */
  vsri.u16    out0, tmp,  #8  /* G & B is in place */
  vzip.u16    out0, out1      /* everything is in place */
.endm

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1.199 pm-utils 1.4.1 :r1

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That's all there is to it!

1.202 python 2.7.9 :r1.4
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Python was created in the early 1990s by Guido van Rossum at Stichting Mathematisch Centrum (CWI, see http://www.cwi.nl) in the Netherlands as a successor of a language called ABC. Guido remains Python’s principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for National Research Initiatives (CNRI, see http://www.cnri.reston.va.us) in Reston, Virginia where he released several versions of the software.
In May 2000, Guido and the Python core development team moved to BeOpen.com to form the BeOpen PythonLabs team. In October of the same year, the PythonLabs team moved to Digital Creations (now Zope Corporation, see http://www.zope.com). In 2001, the Python Software Foundation (PSF, see http://www.python.org/psf/) was formed, a non-profit organization created specifically to own Python-related Intellectual Property. Zope Corporation is a sponsoring member of the PSF.

All Python releases are Open Source (see http://www.opensource.org for the Open Source Definition). Historically, most, but not all, Python releases have also been GPL-compatible; the table below summarizes the various releases.

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<td>PSF</td>
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from setuptools import setup, find_packages
import sys, os

version = '0.1.4'

setup(name='pam',
      version=version,
      description="PAM interface using ctypes",
      long_description="An interface to the Pluggable Authentication Modules (PAM) library on linux, written in pure python (using ctypes)",
      classifiers=["Development Status :: 3 - Alpha",
                  "Intended Audience :: Developers",
                  "License :: OSI Approved :: MIT License",
                  "Operating System :: POSIX :: Linux",
                  "Operating System :: MacOS :: MacOS X",
                  "Programming Language :: Python",
                  "Topic :: Software Development :: Libraries :: Python Modules",
                  "Topic :: System :: Systems Administration :: Authentication/Directory"
                  ],
      keywords='',
      author='Chris AtLee',
      author_email='chris@atlee.ca',
      url='http://atlee.ca/software/pam',
      download_url = "http://atlee.ca/software/pam/dist/%s" % version,
      license="MIT",
      py_modules=['pam'],
      zip_safe=True,
      install_requires=[],
      entry_points="""
      "-*- Entry points: -*-
      """
      )
1.206 python-psutil 2.2.1 :r0
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1.207 python-pyudev 0.16.1 :r0
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 * Major rewrite to support fully async operation
 *
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 *
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 *
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Index: debug.c

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/debug.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- debug.c	7 Sep 2007 10:01:21 -0000	1.1
+++ debug.c	15 Jan 2008 13:49:25 -0000	1.3
@@ -52,7 +52,11 @@
 void X86EMU_trace_regs (void)
 {
     if (DEBUG_TRACE()) {
-        x86emu_dump_regs();
+        if (M.x86.mode & (SYSMODE_PREFIX_DATA | SYSMODE_PREFIX_ADDR)) {
+            x86emu_dump_xregs();
+        } else {
+            x86emu_dump_regs();
+        }
     }
     if (DEBUG_DECODE() && ! DEBUG_DECODE_NOPRINT()) {
         printk("%04x:%04x ",M.x86.saved_cs, M.x86.saved_ip);
@@ -185,7 +189,7 @@
 for (i=0; i< M.x86.enc_pos; i++) {
     printf(buf1+2*i,"%02x", fetch_data_byte_abs(s,o+i));
 }
-    printk("%-20s",buf1);
+    printk("%-20s ",buf1);
 }

static void print_decoded_instruction (void)
Index: ops2.c

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops2.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- ops2.c7 Sep 2007 10:01:21 -00001.1
+++ ops2.c20 Mar 2008 15:48:34 -00001.3
@@ -149,8 +149,69 @@
target += (s16) M.x86.R_IP;
DECODE_PRINTF2("%04x\n", target);
TRACE_AND_STEP();
- if (cond)
+ if (cond) {
M.x86.R_IP = (u16)target;
+JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " LONG COND ");
+ }
+ DECODE_CLEAR_SEGOVR();
+ END_OF_INSTR();
+}
+
+/****************************************************************************
+REMARKS:
+Handles opcode 0x0f,0xC8-0xCF
+****************************************************************************/
+s32 x86emu_bswap(s32 reg)
+{
+ // perform the byte swap
+ s32 temp = reg;
+ reg = (temp & 0xFF000000) >> 24;
+ reg |= (temp & 0xFF0000) >> 8;
+ reg |= (temp & 0xFF00) << 8;
+ reg |= (temp & 0xFF) << 24;
+ return reg;
+}
+
+void x86emuOp2_bswap(u8 op2)
+{
+ /* byte swap 32 bit register */
+ START_OF_INSTR();
+ DECODE_PRINTF("BSWAP\t");
+ switch (op2) {
+
case 0xc8:
+
DECODE_PRINTF("EAX\n");
+
M.x86.R_EAX = x86emu_bswap(M.x86.R_EAX);
+
break;
+
case 0xc9:
+
DECODE_PRINTF("ECX\n");
+
M.x86.R_ECX = x86emu_bswap(M.x86.R_ECX);
+
break;
+
case 0xca:
+
DECODE_PRINTF("EDX\n");
+
M.x86.R_EDX = x86emu_bswap(M.x86.R_EDX);
+
break;
+
case 0xcb:

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2448


DECODE_PRINTF("EBX\n");
M.x86.R_EBX = x86emu_bswap(M.x86.R_EBX);
break;
case 0xcc:
    DECODE_PRINTF("ESP\n");
    M.x86.R_ESP = x86emu_bswap(M.x86.R_ESP);
    break;
case 0xcd:
    DECODE_PRINTF("EBP\n");
    M.x86.R_EBP = x86emu_bswap(M.x86.R_EBP);
    break;
case 0xce:
    DECODE_PRINTF("ESI\n");
    M.x86.R_ESI = x86emu_bswap(M.x86.R_ESI);
    break;
case 0xcf:
    DECODE_PRINTF("EDI\n");
    M.x86.R_EDI = x86emu_bswap(M.x86.R_EDI);
    break;
}
TRACE_AND_STEP();
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();

/* 0xc5 */ x86emuOp2_illegal_op,
/* 0xc6 */ x86emuOp2_illegal_op,
/* 0xc7 */ x86emuOp2_illegal_op,
/* 0xc8 */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xc9 */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xca */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xcb */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xcc */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xcd */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xce */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xcf */ x86emuOp2_illegal_op /* TODO: bswap */
/* 0xd0 */ x86emuOp2_illegal_op,
/* 0xd1 */ x86emuOp2_illegal_op,
Index: ops.c
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/ops.c,v
retrieving revision 1.1
diff -u -u -r1.1 ops.c
--- ops.c 1 Sep 2007 10:01:21 -0000
+++ ops.c 20 Mar 2008 16:52:00 -0000
@@ -1061,7 +1061,11 @@
     imm = (s8)fetch_byte_imm();
     DECODE_PRINTF2("PUSH\t%d\n", imm);
     TRACE_AND_STEP();
-    push_word(imm);
+    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+        push_long(imm);
+    } else {
+        push_word(imm);
+    }
     DECODE_CLEAR_SEGOVR();
     END_OF_INSTR();
}
@@ -1256,8 +1260,10 @@
     target = (u16)(M.x86.R_IP + (s16)offset);
     DECODE_PRINTF2("%x\n", target);
     TRACE_AND_STEP();
-    if (cond)
+    if (cond) {
         M.x86.R_IP = target;
+        JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " NEAR COND ");
+    }
     DECODE_CLEAR_SEGOVR();
     END_OF_INSTR();
}
@@ -2516,9 +2522,11 @@
     count = 1;
     if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
-        /* dont care whether REPE or REPNE */
-        /* move them until CX is ZERO. */
-        count = M.x86.R_CX;
+        /* move them until (E)CX is ZERO. */
+        count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
+        M.x86.R_CX = 0;
+        if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX = 0;
            M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
     }
     while (count--) {
- @ @ -2526,8 +2534,8 @@
+        store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, val);
+        M.x86.R_SI += inc;
M.x86.R_DI += inc;
+    if (M.x86.intr & INTR_HALT)
+        break;
    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -2559,9 +2569,11 @@
count = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
-    /* move them until CX is ZERO. */
-    count = M.x86.R_CX;
-    /* move them until (E)CX is ZERO. */
+    /* move them until (E)CX is ZERO. */
+    count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
    M.x86.R_CX = 0;
+    if (M.x86.mode & SYSMODE_32BIT_REP)
+        M.x86.R_ECX = 0;
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} while (count--) {
@@ -2574,6 +2586,8 @@
    }
    M.x86.R_SI += inc;
    M.x86.R_DI += inc;
+    if (M.x86.intr & INTR_HALT)
+        break;
    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
@@ -2598,16 +2612,21 @@
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* REPE */
-    /* move them until CX is ZERO. */
-    while (M.x86.R_CX != 0) {
+    /* move them until (E)CX is ZERO. */
+    while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        val1 = fetch_data_byte(M.x86.R_SI);
        val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
        cmp_byte(val1, val2);
-        M.x86.R_CX -= 1;
+        if (M.x86.mode & SYSMODE_32BIT_REP)
+            M.x86.R_ECX -= 1;
+            else
+                M.x86.R_CX -= 1;
        M.x86.R_SI += inc;
        M.x86.R_DI += inc;
        if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && (ACCESS_FLAG(F_ZF) == 0) ) break;
    }
if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
+ if (M.x86.intr & INTR_HALTED)
+    break;
}
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
    TRACE_AND_STEP();
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* REPE */
        - /* move them until CX is ZERO. */
        - while (M.x86.R_CX != 0) {
-            M.x86.R_CX -= 1;
        + /* move them until (E)CX is ZERO. */
        +    while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        +        val1 = fetch_data_long(M.x86.R_SI);
        +        val2 = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
-            M.x86.R_CX -= 1;
+            if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+            else
+                M.x86.R_CX -= 1;
        M.x86.R_SI += inc;
        M.x86.R_DI += inc;
        if ( (M.x86.mode & SYSMODE_PREFIX_REPE) && ACCESS_FLAG(F_ZF) ) break;
        if ( (M.x86.mode & SYSMODE_PREFIX_REPNE) && ACCESS_FLAG(F_ZF) ) break;
+            if (M.x86.intr & INTR_HALTED)
+                break;
    }
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
    TRACE_AND_STEP();
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
-        /* move them until CX is ZERO. */
-        while (M.x86.R_CX != 0) {
+        /* move them until (E)CX is ZERO. */
+            while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
                store_data_byte_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AL);
-                M.x86.R_CX -= 1;
+                if (M.x86.mode & SYSMODE_32BIT_REP)
+                    M.x86.R_ECX -= 1;
+                else
+                    M.x86.R_CX -= 1;
}
M.x86.R_CX -= 1;
M.x86.R_DI += inc;
if (M.x86.intr & INTR_HALTED)
    break;
}
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
    count = 1;
    if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
        /* dont care whether REPE or REPNE */
        /* move them until CX is ZERO. */
        count = M.x86.R_CX;
        /* move them until (E)CX is ZERO. */
        count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
        M.x86.R_CX = 0;
    }
    while (count--) {
        store_data_word_abs(M.x86.R_ES, M.x86.R_DI, M.x86.R_AX);
    }
M.x86.R_DI += inc;
if (M.x86.intr & INTR_HALTED)
    break;
}
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
inc = 1;
if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    /* move them until CX is ZERO. */
    while (M.x86.R_CX != 0) {
        if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX -= 1;
        else
            M.x86.R_CX -= 1;
        M.x86.R_SI += inc;
        if (M.x86.intr & INTR_HALTED)
            break;
    }
    while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        M.x86.R_AL = fetch_data_byte(M.x86.R_SI);
        M.x86.R_CX -= 1;
        if (M.x86.mode & SYSMODE_32BIT_REP)
            M.x86.R_ECX -= 1;
        else
            M.x86.R_CX -= 1;
        M.x86.R_SI += inc;
        if (M.x86.intr & INTR_HALTED)
            break;
    }
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
} else {
  count = 1;
  if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    /* move them until CX is ZERO. */
    - count = M.x86.R_CX;
    + /* move them until (E)CX is ZERO. */
    + count = (M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX;
    M.x86.R_CX = 0;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+    M.x86.R_ECX = 0;
+    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
  }
  while (count--) {
    M.x86.R_AX = fetch_data_word(M.x86.R_SI);
    M.x86.R_SI += inc;
+     if (M.x86.intr & INTR_HALTED)
+       break;
    }
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
    inc = 1;
    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
      /* REPE */
      - /* move them until CX is ZERO. */
      - while (M.x86.R_CX != 0) {
      + /* move them until (E)CX is ZERO. */
      + while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
+         cmp_byte(M.x86.R_AL, val2);
+         if (M.x86.mode & SYSMODE_32BIT_REP)
+             M.x86.R_ECX -= 1;
+         else
+             M.x86.R_CX -= 1;
+             M.x86.R_DI += inc;
+             if (ACCESS_FLAG(F_ZF) == 0)
+                 break;
+             if (M.x86.intr & INTR_HALTED)
+                 break;
      }
      M.x86.mode &= ~SYSMODE_PREFIX_REPE;
    } else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {

/* REPNE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
    val2 = fetch_data_byte_abs(M.x86.R_ES, M.x86.R_DI);
    cmp_byte(M.x86.R_AL, val2);
    - M.x86.R_CX -= 1;
+ if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+ else
+                M.x86.R_CX -= 1;
    M.x86.R_DI += inc;
    if (ACCESS_FLAG(F_ZF))
    break;  /* zero flag set means equal */
+ if (M.x86.intr & INTR_HALTED)
+    break;
    }
    M.x86.mode &= ~SYSMODE_PREFIX_REPNE;
} else {
    @ @ -2951.8 +3003.8 @@
    TRACE_AND_STEP();
    if (M.x86.mode & SYSMODE_PREFIX_REPE) {
        /* REPE */
        - /* move them until CX is ZERO. */
        - while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
            if (M.x86.mode & SYSMODE_PREFIX_DATA) {
                val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
                cmp_long(M.x86.R_EAX, val);
            }
+            if (M.x86.intr & INTR_HALTED)
+                break;
            if (M.x86.mode & SYSMODE_32BIT_REP)
+                M.x86.R_ECX -= 1;
+ else
+                M.x86.R_CX -= 1;
+ M.x86.R_DI += inc;
            if (ACCESS_FLAG(F_ZF) == 0)
            break;
+ if (M.x86.intr & INTR_HALTED)
+    break;
        }
        M.x86.mode &= ~SYSMODE_PREFIX_REPE;
} else if (M.x86.mode & SYSMODE_PREFIX_REPNE) {
/ * REPNE */
- /* move them until CX is ZERO. */
- while (M.x86.R_CX != 0) {
+ /* move them until (E)CX is ZERO. */
+ while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
        /* move them until (E)CX is ZERO. */
+        while (((M.x86.mode & SYSMODE_32BIT_REP) ? M.x86.R_ECX : M.x86.R_CX) != 0) {
            if (M.x86.mode & SYSMODE_PREFIX_DATA) {
                val = fetch_data_long_abs(M.x86.R_ES, M.x86.R_DI);
                cmp_long(M.x86.R_EAX, val);
            } else {
                M.x86.R_CX -= 1;
                if (M.x86.mode & SYSMODE_32BIT_REP)
                    M.x86.R_ECX -= 1;
                else
                    M.x86.R_CX -= 1;
                M.x86.R_DI += inc;
                if (ACCESS_FLAG(F_ZF))
                    break; /* zero flag set means equal */
                if (M.x86.intr & INTR_HALTED)
                    break;
            }
-            M.x86.R_CX -= 1;
+            if (M.x86.mode & SYSMODE_32BIT_REP)
DECODE_PRINTF("RETF\t");
imm = fetch_word_imm();
DECODE_PRINTF2("%x\n", imm);
-RETURN_TRACE("RETF",M.x86.saved_cs,M.x86saved_ip);

TRACE_AND_STEP();
- M.x86.R_IP = pop_word();
- M.x86.R_CS = pop_word();
+RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
  M.x86.R_SP += imm;
  DECODE_CLEAR_SEGoiceVR();
  END_OF_INSTR();
@@ -3488,10 +3550,10 @@
{
  START_OF_INSTR();
  DECODE_PRINTF("RETF\n");
-RETURN_TRACE("RETF",M.x86.saved_cs,M.x86.saved_ip);

TRACE_AND_STEP();
- M.x86.R_IP = pop_word();
- M.x86.R_CS = pop_word();
+RETURN_TRACE(M.x86.saved_cs,M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, "FAR");
  DECODE_CLEAR_SEGoiceVR();
  END_OF_INSTR();
}@@ -4020,8 +4082,11 @@
  ip += (s16) M.x86.R_IP;
  DECODE_PRINTF2("%04x\n", ip);
-  M.x86.R_CX -= 1;
-  if (M.x86.R_CX != 0 && !ACCESS_FLAG(F_ZF)) /* CX != 0 and !ZF */
+  if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+    M.x86.R_ECX -= 1;
+  else
+    M.x86.R_CX -= 1;
+  if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
+    !ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and !ZF */
    M.x86.R_IP = ip;
  DECODE_CLEAR_SEGoiceVR();
  END_OF_INSTR();
@@ -4041,8 +4106,11 @@
  ip += (s16) M.x86.R_IP;
  DECODE_PRINTF2("%04x\n", ip);
-  M.x86.R_CX -= 1;
-  if (M.x86.R_CX != 0 && ACCESS_FLAG(F_ZF))       /* CX != 0 and ZF */
+  if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+    M.x86.R_ECX -= 1;
+  else
+    M.x86.R_CX -= 1;
if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0 &&
    ACCESS_FLAG(F_ZF)) /* (E)CX != 0 and ZF */
M.x86.R_IP = ip;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -4062,8 +4130,11 @@
ip += (s16) M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
    TRACE_AND_STEP();
-    M.x86.R_CX -= 1;
-    if (M.x86.R_CX != 0)
+    if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+        M.x86.R_ECX -= 1;
+    else
+        M.x86.R_CX -= 1;
+    if (((M.x86.mode & SYSMODE_PREFIX_ADDR) ? M.x86.R_ECX : M.x86.R_CX) != 0) /* (E)CX != 0 */
M.x86.R_IP = ip;
DECODE_CLEAR_SEGOVR();
END_OF_INSTR();
@@ -4085,8 +4156,10 @@
target = (u16)(M.x86.R_IP + offset);
    DECODE_PRINTF2("%x\n", target);
    TRACE_AND_STEP();
-    if (M.x86.R_CX == 0)
-        M.x86.R_IP = target;
+    if (M.x86.R_CX == 0) {
+        M.x86.R_IP = target;
+        JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " CXZ ");
+    }
+    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -4213,6 +4286,7 @@
ip = (s16)fetch_word_imm();
ip += (s16)M.x86.R_IP;
    DECODE_PRINTF2("%04x\n", ip);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, M.x86.R_IP, " NEAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = (u16)ip;
    DECODE_CLEAR_SEGOVR();
@@ -4233,6 +4307,7 @@
    cs = fetch_word_imm();
    DECODE_PRINTF2("%04x:\", cs);
    DECODE_PRINTF2("%04x\n", ip);
+    JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, cs, ip, " FAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = ip;
    M.x86.R_CS = cs;
@@ -4254,6 +4329,7 @@
offset = (s8)fetch_byte_imm();
target = (u16)(M.x86.R_IP + offset);
DECODE_PRINTF2("%x\n", target);
+ JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, target, " BYTE ");
TRACE_AND_STEP();
M.x86.R_IP = target;
DECODE_CLEAR_SEGOVR();
@@ -4357,6 +4433,8 @@
    DECODE_PRINTF("REPNE\n");
    TRACE_AND_STEP();
    M.x86.mode |= SYSMODE_PREFIX_REPNE;
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+    M.x86.mode |= SYSMODE_32BIT_REP;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -4371,6 +4449,8 @@
    DECODE_PRINTF("REPE\n");
    TRACE_AND_STEP();
    M.x86.mode |= SYSMODE_PREFIX_REPE;
+ if (M.x86.mode & SYSMODE_PREFIX_ADDR)
+    M.x86.mode |= SYSMODE_32BIT_REP;
    DECODE_CLEAR_SEGOVR();
    END_OF_INSTR();
}
@@ -5013,12 +5093,14 @@
    break;
    case 4: /* jmp word ptr ... */
    destval = fetch_data_word(destoffset);
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, M.x86.R_CS, destval, " WORD ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    break;
    case 5: /* jmp far ptr ... */
    destval = fetch_data_word(destoffset);
    destval2 = fetch_data_word(destoffset + 2);
+   JMP_TRACE(M.x86.saved_cs, M.x86.saved_ip, destval2, destval, " FAR ");
    TRACE_AND_STEP();
    M.x86.R_IP = destval;
    M.x86.R_CS = destval2;
Index: prim_ops.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/prim_ops.c,v
retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- prim_ops.c7 Sep 2007 10:01:21 -0000
+++ prim_ops.c16 Jan 2008 14:18:15 -0000
void imul_long_direct(u32 *res_lo, u32 *res_hi, u32 d, u32 s)
{
    #ifdef __HAS_LONG_LONG__
    -    s64 res = (s64)d * (s64)s;
    +    s64 res = (s64)(s32)d * (s64)(s32)s;
    *res_lo = (u32)res;
    *res_hi = (u32)(res >> 32);
    #endif
}

void mul_long(u32 s)
{
    #ifdef __HAS_LONG_LONG__
    -    u64 res = (u32)M.x86.R_EAX * (u32)s;
    +    u64 res = (u64)M.x86.R_EAX * s;
    M.x86.R_EAX = (u32)res;
    M.x86.R_EDX = (u32)(res >> 32);
    #endif
}

if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    -    /* in until CX is ZERO. */
    -    u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
    +    /* in until (E)CX is ZERO. */
    +    u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
        M.x86.R_ECX : M.x86.R_CX);
    -
    while (count--) {
    single_in(size);
    M.x86.R_DI += inc;
    }
    M.x86.R_CX = 0;
    -    if (M.x86.mode & SYSMODE_PREFIX_DATA) {
    +    if (M.x86.mode & SYSMODE_32BIT_REP) {
        M.x86.R_ECX = 0;
    }
    M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
    }
    @ @ -2355,15 +2354,15 @@
}

if (M.x86.mode & (SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE)) {
    /* dont care whether REPE or REPNE */
    -    /* out until CX is ZERO. */
    -    u32 count = ((M.x86.mode & SYSMODE_PREFIX_DATA) ?
    +    /* out until (E)CX is ZERO. */
    +    u32 count = ((M.x86.mode & SYSMODE_32BIT_REP) ?
        M.x86.R_ECX : M.x86.R_CX);
    while (count--) {

single_out(size);
M.x86.R_SI += inc;
}
M.x86.R_CX = 0;
- if (M.x86.mode & SYSMODE_PREFIX_DATA) {
+ if (M.x86.mode & SYSMODE_32BIT_REP) {
   M.x86.R_ECX = 0;
}
M.x86.mode &= ~(SYSMODE_PREFIX_REPE | SYSMODE_PREFIX_REPNE);
Index: sys.c
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/sys.c,v
retrieving revision 1.1
retrieving revision 1.2
diff -u -u -r1.1 -r1.2
--- sys.c 7 Sep 2007 10:01:21 -0000 1.1
+++ sys.c 7 Sep 2007 10:03:13 -0000 1.2
@@ -45,11 +45,6 @@
#include <x86emu/regs.h>
#include "debug.h"
#include "prim_ops.h"
-#ifdef LINUXBIOS_VERSION
-#include "io.h"
-#else
-#include <sys/io.h>
-#endif

-#ifdef IN_MODULE
+#ifdef IN_MODULE
 #include "xf86_ansic.h"
@@ -220,7 +215,7 @@
{
 DB(if (DEBUG_IO_TRACE()))
 printk("inb %#04x \n", addr);
- return inb(addr);
+ return 0;
}
/****************************************************************************
@@ -235,7 +230,7 @@
{
 DB(if (DEBUG_IO_TRACE()))
 printk("inw %#04x \n", addr);
- return inw(addr);
+ return 0;
}
/****************************************************************************
@@ -250,7 +245,7 @@


{ 
DB(if (DEBUG_IO_TRACE()))
printk("inl %#04x \n", addr);
-return inl(addr);
+return 0;
}

/****************************************************************************
@@ -264,7 +259,6 @@
{ 
DB(if (DEBUG_IO_TRACE()))
printk("outb %#02x -> %#04x \n", val, addr);
-outb(val, addr);
return;
}
@@ -279,7 +273,6 @@
{ 
DB(if (DEBUG_IO_TRACE()))
printk("outw %#04x -> %#04x \n", val, addr);
-outw(val, addr);
return;
}
[@ @ -295,7 +288,6 @@
DB(if (DEBUG_IO_TRACE()))
printk("outl %#08x -> %#04x \n", val, addr);
-outl(val, addr);
return;
}
@@ -405,6 +397,6 @@
void X86EMU_setMemBase(void *base, size_t size)
{ 
-M.mem_base = (int) base;
+M.mem_base = (unsigned long) base;
M.mem_size = size;
}
Index: include/x86emu/debug.h
===================================================================
RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/debug.h,v
retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/debug.h7 Sep 2007 10:01:21 -0000
+++ include/x86emu/debug.h20 Mar 2008 15:25:27 -0000
@@ -40,8 +40,6 @@
#ifndef __X86EMU_DEBUG_H
#define __X86EMU_DEBUG_H
/*---------------------- Macros and type definitions ----------------------*/

/* checks to be enabled for "runtime" */
@@ -78,6 +76,8 @@
#define DEBUG_SYSINT()        (M.x86.debug & DEBUG_SYSINT_F)
#define DEBUG_TRACECALL()     (M.x86.debug & DEBUG_TRACECALL_F)
#define DEBUG_TRACECALLREGS() (M.x86.debug & DEBUG_TRACECALL_REGS_F)
+#define DEBUG_TRACEJMP()       (M.x86.debug & DEBUG_TRACEJMP_F)
+#define DEBUG_TRACEJMPREGS()   (M.x86.debug & DEBUG_TRACEJMP_REGS_F)
#define DEBUG_SYS()           (M.x86.debug & DEBUG_SYS_F)
#define DEBUG_MEM_TRACE()     (M.x86.debug & DEBUG_MEM_TRACE_F)
#define DEBUG_IO_TRACE()      (M.x86.debug & DEBUG_IO_TRACE_F)
@@ -96,6 +96,8 @@
#define DEBUG_SYSINT()        0
#define DEBUG_TRACECALL()     0
#define DEBUG_TRACECALLREGS() 0
+#define DEBUG_TRACEJMP()       0
+#define DEBUG_TRACEJMPREGS()   0
#define DEBUG_SYS()           0
#define DEBUG_MEM_TRACE()     0
#define DEBUG_IO_TRACE()      0
@@ -169,14 +171,20 @@
x86emu_dump_regs();                                     \
   if (DEBUG_TRACECALL())                                     \
      printk("%04x:%04x: CALL %s%04x:%04x\n", u , v, s, w, x);
-# define RETURN_TRACE(n,u,v)
-  + if (DEBUG_TRACECALLREGS())
+   + if (DEBUG_TRACEJMPREGS())
+      x86emu_dump_regs();
+   + if (DEBUG_TRACEJMP())
+      printk("%04x:%04x: JMP %s%04x:%04x\n", u , v, s, w, x);
   else
   # define CALL_TRACE(u,v,w,x,s)
- # define RETURN_TRACE(n,u,v)
+   + if (DEBUG_TRACEJMP())
++ define RETURN_TRACE(u,v,w,x,s)
++ define JMP_TRACE(u,v,w,x,s)
#ifndef DEBUG

Index: include/x86emu/regs.h

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/regs.h,v

retrieving revision 1.1
retrieving revision 1.4
diff -u -u -r1.1 -r1.4
--- include/x86emu/regs.h 7 Sep 2007 10:01:21 -0000 1.1
+++ include/x86emu/regs.h 15 Jan 2008 13:46:40 -0000 1.4
@@ -231,6 +231,9 @@
#define SYSMODE_PREFIX_REPNE 0x00000100
#define SYSMODE_PREFIX_DATA 0x00000200
#define SYSMODE_PREFIX_ADDR 0x00000400
+//phueper: for REP(E|NE) Instructions, we need to decide whether it should be using
+//the 32bit ECX register as or the 16bit CX register as count register
+#define SYSMODE_32BIT REP 0x00000800
#define SYSMODE_INTR_PENDING 0x10000000
#define SYSMODE_EXTRN_INTR 0x20000000
#define SYSMODE_HALTED 0x40000000
@@ -250,7 +253,8 @@
SYSMODE_SEGOVR_GS | \nSYSMODE_SEGOVR_SS | \nSYSMODE_PREFIX_DATA | \n- SYSMODE_PREFIX_ADDR)
+ SYSMODE_PREFIX_ADDR    | \n+ SYSMODE_32BIT REP)

#define INTR_SYNCH 0x1
#define INTR_ASYNCH 0x2
@@ -274,9 +278,9 @@
 */
 u32 mode;
 volatile int intr; /* mask of pending interrupts */
-int debug;
+ volatile int debug;
#endif DEBUG

Index: include/x86emu/x86emu.h

RCS file: /cvs/osdf/cvs/host/other-licence/x86emu/include/x86emu/x86emu.h,v

retrieving revision 1.1
retrieving revision 1.3
diff -u -u -r1.1 -r1.3
--- include/x86emu/x86emu.h 7 Sep 2007 10:01:21 -0000 1.1
+++ include/x86emu/x86emu.h 19 Oct 2007 08:42:15 -0000 1.3
@@ -47,6 +47,7 @@
 #include <console.h>
 #define printk(x...) printk(BIOS_DEBUG, x)
 #else
+  include <stdio.h>
 #define printk printf
 #endif
@@ -189,6 +181,8 @@
 #define DEBUG_TRACECALL_REGS_F  0x004000
 #define DEBUG_DECODE_NOPPRINT_F  0x008000
 #define DEBUG_SAVE_IP_CS_F  0x010000
+  #define DEBUG_TRACEJMP_F       0x020000
+  #define DEBUG_TRACEJMP_REGS_F   0x040000
 #define DEBUG_SYS_F  (DEBUG_SVC_F|DEBUG_FS_F|DEBUG_PROC_F)

 void X86EMU_trace_regs(void);
@@ -200,5 +194,4 @@
 ifdef __cplusplus
 }                       /* End of "C" linkage for C++ */
 endif
-#endif /* __X86EMU_X86EMU_H */
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Stefan Reinauer

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However, unrestricted linking of non-free programs would deprive the users of those programs of all benefit from the free status of the libraries themselves. This Library General Public License is intended to permit developers of non-free programs to use free libraries, while preserving your freedom as a user of such programs to change the free libraries that are incorporated in them. (We have not seen how to achieve this as regards changes in header files, but we have achieved it as regards changes in the actual functions of the Library.) The hope is that this will lead to faster development of free libraries.

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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b) Use a suitable shared library mechanism for linking with the
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-- Wolfgang Denk

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ftp.gnu.org:/pub/gnu/readline/readline-6.0.tar.gz.

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I would like to thank the following people (in alphabetical order):

- Seth Arnold, for contributing to the documentation.
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If I've missed anyone, it's due to oversight. Drop me a line and I'll rectify the situation as quickly as possible.

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Test 1.1:101

Testing argument parsing
First type
e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_2
e1_l1_3
e1_l1_3
e1_l1_4
e1_l1_4
e1_l1_5
e1_l1_5
e1_l1_6
e1_l1_6
e1_l1_7
e1_l1_7
e1_l1_8
e1_l1_8
e1_l1_9
e1_l1_9
e1_l1_10
e1_l1_10
e1_l1_11
e1_l1_11
e1_l1_12
e1_l1_12
e1_l1_13
e1_l1_13
e1_l1_14
e1_l1_14

============
Test 1.2:102
============
e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
e1_l1_5
e1_l1_6
e1_l1_7
e1_l1_8
e1_l1_9
e1_l1_10
e1_l1_11
e1_l1_12
e1_l1_13
e1_l1_14
Test 1.3:103

e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
e1_l1_5
e1_l1_6
e1_l1_7
e1_l1_8
e1_l1_9
e1_l1_10
e1_l1_11
e1_l1_12
e1_l1_13
e1_l1_14

Test 1.4:104

e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
e1_l1_5
e1_l1_6
e1_l1_7
e1_l1_8
e1_l1_9
e1_l1_10
e1_l1_11
e1_l1_12
e1_l1_13

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2684
<table>
<thead>
<tr>
<th>Second type</th>
<th>Test 1.4.1:105</th>
</tr>
</thead>
<tbody>
<tr>
<td>11_1</td>
<td>l1_1</td>
</tr>
<tr>
<td>11_2</td>
<td>l1_2</td>
</tr>
<tr>
<td>11_3</td>
<td>l1_3</td>
</tr>
<tr>
<td>11_4</td>
<td>l1_4</td>
</tr>
<tr>
<td>11_5</td>
<td>l1_5</td>
</tr>
<tr>
<td>11_6</td>
<td>l1_6</td>
</tr>
<tr>
<td>11_7</td>
<td>l1_7</td>
</tr>
<tr>
<td>11_8</td>
<td>l1_8</td>
</tr>
<tr>
<td>11_9</td>
<td>l1_9</td>
</tr>
<tr>
<td>11_10</td>
<td>l1_10</td>
</tr>
<tr>
<td>11_11</td>
<td>l1_11</td>
</tr>
<tr>
<td>11_12</td>
<td>l1_12</td>
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<tr>
<td>11_13</td>
<td>l1_13</td>
</tr>
<tr>
<td>11_14</td>
<td>l1_14</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Second type</th>
<th>Test 1.5:106</th>
</tr>
</thead>
<tbody>
<tr>
<td>s1_11_1</td>
<td>s1_l1_1</td>
</tr>
<tr>
<td>s1_11_1</td>
<td>s1_l1_1</td>
</tr>
<tr>
<td>s1_11_2</td>
<td>s1_l1_2</td>
</tr>
<tr>
<td>s1_11_2</td>
<td>s1_l1_2</td>
</tr>
<tr>
<td>s1_11_3</td>
<td>s1_l1_3</td>
</tr>
<tr>
<td>s1_11_3</td>
<td>s1_l1_3</td>
</tr>
<tr>
<td>s1_11_4</td>
<td>s1_l1_4</td>
</tr>
<tr>
<td>s1_11_4</td>
<td>s1_l1_4</td>
</tr>
<tr>
<td>s1_11_5</td>
<td>s1_l1_5</td>
</tr>
<tr>
<td>s1_11_5</td>
<td>s1_l1_5</td>
</tr>
<tr>
<td>s1_11_6</td>
<td>s1_l1_6</td>
</tr>
<tr>
<td>s1_11_6</td>
<td>s1_l1_6</td>
</tr>
<tr>
<td>s1_11_7</td>
<td>s1_l1_7</td>
</tr>
<tr>
<td>s1_11_7</td>
<td>s1_l1_7</td>
</tr>
<tr>
<td>s1_11_8</td>
<td>s1_l1_8</td>
</tr>
<tr>
<td>s1_11_8</td>
<td>s1_l1_8</td>
</tr>
<tr>
<td>s1_11_9</td>
<td>s1_l1_9</td>
</tr>
<tr>
<td>s1_11_9</td>
<td>s1_l1_9</td>
</tr>
<tr>
<td>s1_11_10</td>
<td>s1_l1_10</td>
</tr>
<tr>
<td>s1_11_10</td>
<td>s1_l1_10</td>
</tr>
<tr>
<td>s1_11_11</td>
<td>s1_l1_11</td>
</tr>
<tr>
<td>s1_11_11</td>
<td>s1_l1_11</td>
</tr>
<tr>
<td>s1_11_12</td>
<td>s1_l1_12</td>
</tr>
<tr>
<td>s1_11_12</td>
<td>s1_l1_12</td>
</tr>
</tbody>
</table>
Test 1.6: 107

Test 1.7: 108
e1_l1_13
e1_l1_13
e1_l1_14
e1_l1_14

================
Test 1.9:110
================
s1_l1_1
s1_l1_2
s1_l1_3
s1_l1_4
s1_l1_5
s1_l1_6
s1_l1_7
s1_l1_8
s1_l1_9
s1_l1_10
s1_l1_11
s1_l1_12
s1_l1_13
s1_l1_14

================
Test 1.10:111
================
s1_l1_1
s1_l1_2
s1_l1_3
s1_l1_4
s1_l1_5
s1_l1_6
s1_l1_7
s1_l1_8
s1_l1_9
s1_l1_10
s1_l1_11
s1_l1_12
s1_l1_13
s1_l1_14

================
Test 1.11:112
================
e1_l1_1
e1_l1_2
e1_l1_3
e1_l1_4
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e1_l1_12
e1_l1_12
e1_l1_13
e1_l1_13
e1_l1_14
e1_l1_14
e1_l1_1
e1_l1_1
e1_l1_2
e1_l1_2
e1_l1_3
e1_l1_3
e1_l1_4
e1_l1_4
e1_l1_5
e1_l1_5
e1_l1_6
e1_l1_6
e1_l1_7
e1_l1_7
e1_l1_8
e1_l1_8
e1_l1_9
e1_l1_9
e1_l1_10
e1_l1_10
e1_l1_11
e1_l1_11
e1_l1_12
e1_l1_12
e1_l1_13
e1_l1_13
e1_l1_14
e1_l1_14

=============
Test 1.17:118
==============

l1_1
l1_1
l1_2
l1_2
l1_3
l1_3
l1_4
l1_4
l1_5
l1_5
11_6
11_6
11_7
11_7
11_8
11_8
11_9
11_9
11_10
11_10
11_11
11_11
11_12
11_12
11_13
11_13
11_14
11_14

=============
Test 1.18:119
=============  
11_1
11_2
11_3
11_4
11_5
11_6
11_7
11_8
11_9
11_10
11_11
11_12
11_13
11_14
Testing address ranges

=============  
Test 2.1:120
=============  
11_4

=============  
Test 2.2:121
=============  
l2_6
Test 2.3:122
11_14

Test 2.4:123
12_9

Test 2.5:124

Test 2.6:125
12_9

Test 2.7:126

Test 2.9:127
11_7

Test 2.10:128
11_7

Test 2.11:129
11_7

Test 2.12:130
11_1
11_2
11_3
11_4

Test 2.13:131
Test 2.14:132
l2_7
l2_8
l2_9

==============
Test 2.15:133
==============
l1_4
l1_5
l1_6
l1_7
l1_8
l1_9
l1_10
l1_11
l1_12
l1_13
l1_14
l2_1
l2_2
l2_3
l2_4
l2_5
l2_6
l2_7
l2_8
l2_9

==============
Test 2.16:134
==============
l1_4
l1_5
l1_6
l1_7
l1_8
l1_9
l1_10
l1_11
l1_12
l1_13
l1_14
l2_1
l2_2
l2_3
l2_4
l2_5
l2_6
Test 2.17:135

11_4
11_5
11_6
11_7
11_8
11_9
11_10
11_14
12_1
12_2
12_3
12_4
12_5
12_6
12_7
12_8
12_9

Test 2.18:136

12_3
12_4
12_5
12_6
12_7
12_8
12_9

Test 2.19:137

11_12

Test 2.20:138

11_7
Brace and other grouping

Test 3.1:139

11_1
Test 3.4:142

Testing a c d and i commands

Test 4.1:143

before_i1
after_i before_i1
before_i2
after_i before_i2
before_i3
after_i before_i3
before_i4
after_i before_i4
before_i5
after_i before_i5
before_i6
after_i before_i6
before_i7
after_i before_i7
before_i8
after_i before_i8
before_i9
after_i before_i9
before_i10
after_i before_i10
before_i11
after_i before_i11
before_i12
after_i before_i12
before_il1_13
after_ibefore_il1_13
before_il1_14
after_ibefore_il1_14
before_il2_1
after_ibefore_il2_1
before_il2_2
after_ibefore_il2_2
before_il2_3
after_ibefore_il2_3
before_il2_4
after_ibefore_il2_4
before_il2_5
after_ibefore_il2_5
before_il2_6
inserted
after_ibefore_il2_6
before_il2_7
after_ibefore_il2_7
before_il2_8
after_ibefore_il2_8
before_il2_9
after_ibefore_il2_9

============
Test 4.2:144
============
before_al1_1
after_abefore_al1_1
before_al1_2
after_abefore_al1_2
before_al1_3
after_abefore_al1_3
before_al1_4
after_abefore_al1_4
before_a5-12l1_5
after_abefore_a5-12l1_5
appended
before_a5-12l1_6
after_abefore_a5-12l1_6
appended
before_a5-12l1_7
after_abefore_a5-12l1_7
appended
before_a5-12l1_8
after_abefore_a5-12l1_8
appended
before_a5-12l1_9
after_abefore_a5-1211_9
appended
before_a5-1211_10
after_abefore_a5-1211_10
appended
before_a5-1211_11
after_abefore_a5-1211_11
appended
before_a5-1211_12
after_abefore_a5-1211_12
appended
before_al1_13
after_abefore_al1_13
before_al1_14
after_abefore_al1_14
before_al2_1
after_abefore_al2_1
before_al2_2
after_abefore_al2_2
before_al2_3
after_abefore_al2_3
before_al2_4
after_abefore_al2_4
before_al2_5
after_abefore_al2_5
before_al2_6
after_abefore_al2_6
before_al2_7
after_abefore_al2_7
before_al2_8
after_abefore_al2_8
before_al2_9
after_abefore_al2_9

===============
Test 4.3:145
===============
^l1_1
^l1_1$
appended
^l1_2
^l1_2$
appended
^l1_3
^l1_3$
appended
^l1_4
^l1_4$
Test 4.4:146
Test 4.5:147

Test 4.6:148

Test 4.7:149

Test 4.8:150

Testing labels and branching

Test 5.1:151
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2705

label1_l1_9
label1_l1_10
label1_l1_11
label1_l1_12
label2_l1_13
label3_label2_l1_13
label2_l1_14
label3_label2_l1_14

============
Test 5.2:152
============
tested l2_1
tested l2_2
tested l2_3
tested l2_4
tested l2_5
tested l2_6
tested l2_7
tested l2_8
tested l2_9
tested l2_10
tested l2_11
tested l2_12
tested l2_13
tested l2_14

============
Test 5.3:153
============
^l1_1
^l1_1$
^l1_2
^l1_2$
^l1_3
^l1_3$
^l1_4
^l1_4$
l_5$
l_5$
l_6$
l_7$
l_8$

============
Test 5.4:154
============
^l1_1$
^l1_2$
m1_5
m1_6
m1_7
m1_8
m1_9
m1_10
m1_11
m1_12
m1_13
m1_14

Pattern space commands

==============
Test 6.1:159
==============
changed
changed
changed
changed
changed
changed
changed
changed
changed
changed
changed
changed
changed
changed

==============
Test 6.2:160
==============
l1_1
l1_2
l1_3
l1_5
l1_6
l1_7
l1_8
l1_9
l1_10
l1_11
l1_12
l1_13
l1_14

==============
Test 6.3:161

11_5
11_6
11_7
11_8
11_9
11_10
11_11
11_12
11_13
11_14

Test 6.4:162

11_1
11_2
11_3
11_2
11_3
11_5
11_2
11_3
11_2
11_3
11_6
11_6
11_7
11_8
11_9
11_10
11_11
11_12
11_13
11_14

Test 6.5:163

11_1
11_2
11_3
11_4
11_5
11_6
11_7
11_8
Test 6.6:164

Testing print and file routines

Test 7.1:165

Test 7.2:166
16
12.2
17
12.3
18
12.4
19
12.5
20
12.6
21
12.7
22
12.8
23
12.9

============
Test 7.3:167
============
11.1
11.2
11.3
11.4
11.5
11.6
11.7
11.8
11.9
11.10
11.11
11.12
11.13
11.14
w results
11.3
11.4
11.5
11.6
11.7
11.8
11.9
11.10
11.11
11.12

============
Test 7.4:168
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11_3
11_4
11_5
11_6
11_7
11_8
11_9
11_10
11_11
11_12
11_13
11_14

============
Test 7.8:171
============

Testing substitution commands

============
Test 8.1:172
============

XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXX
XXX
XXX
XXX
XXX

============
Test 8.2:173
============

XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
XXXX
Open Source Used In Open Source Used in the IR800 Series 15.8(3)M1

Test 8.6:177

Test 8.7:178
Test 8.8:179

Test 8.9:180

---------
x_x1xl1
x_x1xl2
x_x1xl3
x_x1xl4
x_x1xl5
x_x1xl6
x_x1xl7
x_x1xl8
x_x1xl9
x_x1xl10
x_x1xl11
x_x1xl12
x_x1xl13
x_x1xl14

---------
 l1u0
  u1
  u21
 l1u0
  u1
  u22
 l1u0
  u1
  u23
 l1u0
  u1
  u24
 l1u0
  u1
  u25
 l1u0
  u1
  u26
 l1u0
  u1
  u27
 l1u0
  u1
  u28
 l1u0
  u1
Test 8.10: 181

-------------
X0 X1 X2 X3 X4
-------------
Test 8.11: 182

-------------
X1 X2 X3 X4
-------------

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IX_11
IX_12
IX_13
IX_14
s while results
IX_1
IX_2
IX_3
IX_4
IX_5
IX_6
IX_7
IX_8
IX_9
IX_10
IX_11
IX_12
IX_13
IX_14

============
Test 8.12:183
============
IX_X
IX_X
IX_X
IX_4
IX_5
IX_6
IX_7
IX_8
IX_9
IX_X0
IX_X
IX_X
IX_X
IX_X4

============
Test 8.13:184
============
I8_8
I8_7
I8_6
I8_5
I8_4
I8_3
I8_2
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**1.217 sensible-utils 0.0.4ubuntu1**

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1.222 ssh 1:5.5p1-4ubuntu4

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This package contains a Gnome based implementation of ssh-askpass written by Damien Miller.

It is split out from the main package to isolate the dependency on the Gnome and X11 libraries.

It was packaged for Debian by Philip Hands <phil@hands.com>.

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It was downloaded from here: 
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1.224 sysv-rc 2.87dsf-4ubuntu18

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Primary-Site: ftp.cistron.nl/pub/people/miquels/software
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Send patches to sysvinit-devel@nongnu.org

The of the start-stop-daemon

* A rewrite of the original Debian's start-stop-daemon Perl script
* in C (faster - it is executed many times during system startup).

* Written by Marek Michalkiewicz <marekm@i17linuxb.ists.pwr.wroc.pl>,
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This is the Debian GNU/Linux prepackaged version of System V Init.

Init was written by Miquel van Smoorenburg <miquels@cistron.nl>.

This package was first put together by Bruce Perens <Bruce@Pixar.com> from pre-distribution sources. Ian Murdock <imurdock@debian.org> integrated it into the base system maintained it until the end of 1995. Miquel van Smoorenburg <miquels@cistron.nl> implemented Debian support. In version 2.85 the package was split into three (sysvinit, sysv-rc, and initscripts) in order to make room for alternative rc mechanisms.

The upstream source is available at:

Primary-Site: ftp.cistron.nl/pub/people/miquels/software
92K syssvinit-2.86.tar.gz
Alternate-Site: sunsite.unc.edu/pub/Linux/system/daemons/init
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1.231 tunctl 1.5 :r0

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/* Copyright 2002 Jeff Dike
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 */

#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include <errno.h>
#include <fcntl.h>
#include <unistd.h>
#include <pwd.h>
#include <grp.h>
#include <net/if.h>
#include <sys/ioctl.h>
#include <linux/if_tun.h>

/* TUNSETGROUP appeared in 2.6.23 */
#ifndef TUNSETGROUP
#define TUNSETGROUP   _IOW('T', 206, int)
#endif

static void Usage(char *name)
{
    fprintf(stderr, "Create: %s [-b] [-u owner] [-g group] [-t device-name] \
    ""[-p|-n] [-f tun-clone-device]"n", name);
    fprintf(stderr, "Delete: %s -d device-name [-f tun-clone-device]"n", name);
    fprintf(stderr, "The default tun clone device is /dev/net/tun - some systems"
    " use\n/dev/misc/net/tun instead\n"");
    fprintf(stderr, "b will result in brief output (just the device name)\n"");
    fprintf(stderr, "-n will result in a point-to-point tun device. Default is a tap,\n"");
    fprintf(stderr, " except the device contains \"tun\" in the name.\n"");
    exit(1);
}

int main(int argc, char **argv)
{
    struct ifreq ifr;
    struct passwd *pwd;
    struct group *gr;

    ...
uid_t owner = -1;
gid_t group = -1;
int tap_fd, opt, delete = 0, brief = 0, type = 0;
char *tun = "", *file = "/dev/net/tun", *name = argv[0], *end;

while((opt = getopt(argc, argv, "bd:f:npt:u:g:h")) > 0){
    switch(opt) {
    case 'b':
        brief = 1;
        break;
    case 'd':
        delete = 1;
        tun = optarg;
        break;
    case 'f':
        file = optarg;
        break;
    case 'p':
        if(type != 0)
            Usage(name);
        type = IFF_TAP;
        break;
    case 'n':
        if(type != 0)
            Usage(name);
        type = IFF_TUN;
        break;
    case 'u':
        pw = getpwnam(optarg);
        if(pw != NULL){
            owner = pw->pw_uid;
            break;
        }
        owner = strtol(optarg, &end, 0);
        if(*end != '0'){
            fprintf(stderr, """%s" is neither a username nor a numeric uid.\n", optarg);
            Usage(name);
            } else
            break;
    case 'g':
        gr = getgrnam(optarg);
        if(gr != NULL){
            group = gr->gr_gid;
            break;
        }
        group = strtol(optarg, &end, 0);
        if(*end != '0'){
            fprintf(stderr, """%s" is neither a groupname nor a numeric gid.\n", optarg);
            Usage(name);
            } else
            break;
fprintf(stderr, "%s' is neither a groupname nor a numeric group.
", optarg);
Usage(name);
}
break;

case 't':
    tun = optarg;
    break;

case '?':
case 'h':
default:
    Usage(name);
}
}

argv += optind;
argc -= optind;

if(argc > 0)
    Usage(name);

if((tap_fd = open(file, O_RDWR)) < 0){
    fprintf(stderr, "Failed to open '%s' : ", file);
    perror(""");
    exit(1);
}

if(type == 0) {
    type = strstr(tun, "tun") ? IFF_TUN : IFF_TAP;
}

memset(&ifr, 0, sizeof(ifr));

ifr.ifr_flags = type | IFF_NO_PI;
strcpy(ifr.ifr_name, tun, sizeof(ifr.ifr_name) - 1);
if(iocctl(tap_fd, TUNSETIFF, (void *) &ifr) < 0){
    perror("TUNSETIFF");
    exit(1);
}

if(delete){
    if(iocctl(tap_fd, TUNSETPERSIST, 0) < 0){
        perror("disabling TUNSETPERSIST");
        exit(1);
    }

    printf("Set '%s' nonpersistent\n", ifr.ifr_name);
}
else {
    /* emulate behaviour prior to TUNSETGROUP */
    if(owner == -1 && group == -1) {
        owner = geteuid();
    }

    if(owner != -1) {
        if(ioctl(tap_fd, TUNSETOWNER, owner) < 0)
            perror("TUNSETOWNER");
        exit(1);
    }

    if(group != -1) {
        if(ioctl(tap_fd, TUNSETGROUP, group) < 0)
            perror("TUNSETGROUP");
        exit(1);
    }

    if(ioctl(tap_fd, TUNSETPERSIST, 1) < 0)
        perror("enabling TUNSETPERSIST");
    exit(1);
}

if(brief)
    printf("%s\n", ifr.ifr_name);
else {
    printf("Set '%s' persistent and owned by", ifr.ifr_name);
    if(owner != -1)
        printf(" uid %d", owner);
    if(group != -1)
        printf(" gid %d", group);
    printf("\n");
}

return(0);

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1.235 update-rc.d 0.7 :r5
1.235.1 Available under license:

#!/bin/sh
#
# update-rc.d Update the links in /etc/rc[0-9S].d/
#
# (c) 2003, 2004 Phil Blundell <pb@handhelds.org>
#
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initd="/etc/init.d"
etcd="/etc/rc"
notreally=0
force=0
dostart=0
verbose=0

usage()
{
cat >&2 <<EOF
    update-rc.d [-n] [-r <root>] [-s] <basename> defaults [NN | sNN kNN]
    -n: not really
    -f: force
    -v: verbose
    -r: alternate root path (default is /)
    -s: invoke start methods if appropriate to current runlevel
EOF
}

checklinks()
{
local i dn fn remove=0
if [ "x$1" = "xremove" ]; then
    echo " Removing any system startup links for $bn ..."
    remove=1
fi

for i in 0 1 2 3 4 5 6 7 8 9 $S; do
dn="$etc$d$i.d"
if [ ! -d $dn ]; then
    continue;
fi
for f in ${dn}/[SK]??$bn; do
    if [ -L $f ]; then
        if [ $remove -eq 0 ]; then
            return 1
        fi
        echo "$f"
    fi
    rm $f
fi
done
dolink()
{
    startstop=$1
    lev=`echo $2 | cut -d/ -f1`
    nn=`echo $2 | cut -d/ -f2`
    fn="${etcd}${lev}.d/${startstop}${nn}${bn}"
    [ $verbose -eq 1 ] && echo "$fn -> ../init.d/$bn"
    if [ $notreally -eq 0 ]; then
        mkdir -p `dirname $fn`
        ln -s ../init.d/$bn $fn
    fi
    if [ $dostart -eq 1 ] && [ $startstop = "S" ] && [ $lev = $RUNLEVEL ]; then
        $fn start || true
    fi
}

makelinks()
{
    if ! checklinks; then
        echo " System startup links for $initd/$bn already exist."
        if [ $dostart -eq 1 ] && [ $notreally -eq 0 ] && [ -L ${etcd}${RUNLEVEL}.d/S??${bn} ]; then
            ${etcd}${RUNLEVEL}.d/S??${bn} restart || true
        fi
        exit 0
    fi
    echo " Adding system startup for $initd/$bn."
    for i in $startlinks; do
dolink S $i
done
    for i in $stoplinks; do
dolink K $i
done
}

while [ $# -gt 0 ]; do
    case $1 in
        -n)
            notreally=1
            shift
            continue
            ;;
        -v)
            verbose=1
            shift
    esac
    :
shift continue
;; -f) force=1
shift continue
;; -s) dostart=1
shift continue
;; -r) shift
root=$1
initd="${root}${initd}"
etcd="${root}${etcd}"
shift
;; -h | --help)
usage
exit 0
;; *
)*
break
;; esac
done

if [ $# -lt 2 ]; then
usage
exit 1
fi

bn=$1
shift

sn=${initd}/$bn
if [ -L "$sn" -a -n "$root" ]; then
if which readlink >/dev/null; then
while true; do
linksn="$(readlink "$sn")"
if [ -z "$linksn" ]; then
break
fi
fi

Open Source Used In Open Source Used in the IR800 Series 15.8(3)M1
2878
if [ $1 != "remove" ]; then
if [ ! -f "$sn" ]; then
    echo "update-rc.d: Sinitd/$bn: file does not exist" >&2
    exit 1
fi
else
    if [ -f "$sn" ]; then
        if [ $force -eq 1 ]; then
            echo "update-rc.d: Sinitd/$bn exists during rc.d purge (continuing)" >&2
        else
            echo "update-rc.d: Sinitd/$bn exists during rc.d purge (use -f to force)" >&2
        fi
        exit 1
    fi
else
    if [ $dostart -eq 1 ]; then
        #RUNLEVEL=`sed 's/-[^{]*\(/\1/\)/' < /proc/1/cmdline`
        RUNLEVEL=`runlevel | cut -d" " -f2`
        if [ "x$RUNLEVEL" = "x" ]; then
            echo "Unable to determine current runlevel" >&2
            exit 1
        fi
    fi
    if [ $1 = "remove" ]; then
        checklinks "remove"
        ;;
    defaults)
    if [ $# -gt 3 ]; then
        echo "defaults takes only one or two arguments" >&2
        usage
        exit 1
    fi
start=20
stop=20
if [ $# -gt 1 ]; then
start=$2
stop=$2
fi
if [ $# -gt 2 ]; then
stop=$3
fi
start=`printf %02d $start`
stop=`printf %02d $stop`
stoplinks="0/$stop 1/$stop 6/$stop"
startlinks="2/$start 3/$start 4/$start 5/$start"
makelinks
::

start | stop)
while [ $# -gt 0 ]; do
if [ $1 = "start" ]; then
letter=S
elif [ $1 = "stop" ]; then
letter=K
else
echo "expected start or stop" >&2
usage
exit 1
fi
shift
NN=`printf %02d $(expr $1 + 0)`
shift
while [ "x$1" != "x." ]; do
if [ $# -eq 0 ]; then
echo "action with list of runlevels not terminated by \." >&2
exit 1
fi
level=$1
shift
case $letter in
S) startlinks="$startlinks $level/$NN" ;;
K) stoplinks="$stoplinks $level/$NN" ;;
esac
done
shift
done
makelinks
::

*)
usage
exit 1
;

esac

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Version 2, June 1991

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```plaintext
size: 8388608, sector size: 512, PT: dos, offset: 446
---
#1: 32 7648 0x83
#2: 7680 8704 0xa5
#5: 7936 4864 0x7 (freebsd)
#6: 12544 3584 0x7 (freebsd)
size: 4456448, sector size: 512, PT: bsd, offset: 512
---
#1: 7936 4864 0x7
#2: 12544 3584 0x7
```

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Initialize empty image
f1c9645dbc14efddc7d8a322685f26eb bsd.img
Create new DOS partition table
57e721e38d1266c2df055067c18f2cf9 bsd.img

---layout----------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Create 1st primary partition
ada64ace122978d00d1d1c0e5ee45d26 bsd.img

---layout----------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type
<removed>1   2048  4095  2048  1M 83 Linux

Create 2st primary partition
1bebf87248e05d6e4e62b749da65d023 bsd.img
Set 2nd partition type
2d8e8d2ff51a88a045db233418dd73f3e bsd.img

---layout----------
Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: dos
Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type
<removed>1   2048  4095  2048  1M 83 Linux
<removed>2   4096  20479  16384  8M a5 FreeBSD

-----------
Create default BSD
2e1cee529cb59c9341afe0443f196a1 bsd.img

---layout----------

Welcome to fdisk <removed>.
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors
Geometry: 255 heads, 63 sectors/track, 1 cylinders
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: bsd
partitions: 4

Slice Start   End Sectors  Size Type     Fsize Bsize Cpg
  c      4096 20479   16384    8M unused       0     0   0
d     0 16064   16065  7.9M unused       0     0   0

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):

---layout----------

Welcome to fdisk <removed>.
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors
Geometry: 255 heads, 63 sectors/track, 1 cylinders
Units: sectors of 1 * 512 = 512 bytes
Sector size (logical/physical): 512 bytes / 512 bytes
I/O size (minimum/optimal): 512 bytes / 512 bytes
Disklabel type: bsd
partitions: 4

<table>
<thead>
<tr>
<th>Slice</th>
<th>Start</th>
<th>End</th>
<th>Sectors</th>
<th>Size</th>
<th>Type</th>
<th>Fsize</th>
<th>Bsize</th>
<th>Cpg</th>
</tr>
</thead>
<tbody>
<tr>
<td>a</td>
<td>4096</td>
<td>6144</td>
<td>2049</td>
<td>1M</td>
<td>4.2BSD</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>c</td>
<td>4096</td>
<td>20479</td>
<td>16384</td>
<td>8M</td>
<td>unused</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>d</td>
<td>0</td>
<td>16064</td>
<td>16065</td>
<td>7.9M</td>
<td>unused</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):

-------------------
Changes will remain in memory only, until you decide to write them.
Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help):

0 unused 5 4.1BSD 9 4.4LFS d boot
1 swap 6 Eighth Edition a unknown e ADOS
2 Version 6 7 4.2BSD b HPFS f HFS
3 Version 7 8 MS-DOS c ISO-9660 10 AdvFS
4 System V

Command (m for help):

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Maintainer: Lasse Collin <lasse.collin@tukaani.org>
Source: http://tukaani.org/xz
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Subject: Re: XZ utils for Debian
Date: Sun, 19 Jul 2009 13:28:23 +0300
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1.245 yajl 2.1.0 :r0.1
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1.246 zlib 1.2.8 :r0
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1.247 zlib1g 1:1.2.3.4.dfsg-3ubuntu1

1.247.1 Available under license:

This is the pre-packaged Debian Linux version of the zlib compression
library. It was packaged by Michael Alan Dorman <mdorman@debian.org>
from sources originally retrieved from ftp.uu.net in the directory
/pub/archiving/zip/zlib as the file zlib-1.0.4.tar.gz.

There is a homepage at http://www.gzip.org/zlib/

Acknowledgments:

The deflate format used by zlib was defined by Phil Katz. The deflate
and zlib specifications were written by Peter Deutsch. Thanks to all the
people who reported problems and suggested various improvements in zlib;
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