

# SPEC<sup>®</sup> CFP2006 Result

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## Cisco Systems

Cisco UCS B460 M4 (Intel Xeon E7-8890 v4, 2.20 GHz)

SPECfp<sup>®</sup>\_rate2006 = Not Run

SPECfp\_rate\_base2006 = 2380

CPU2006 license: 9019

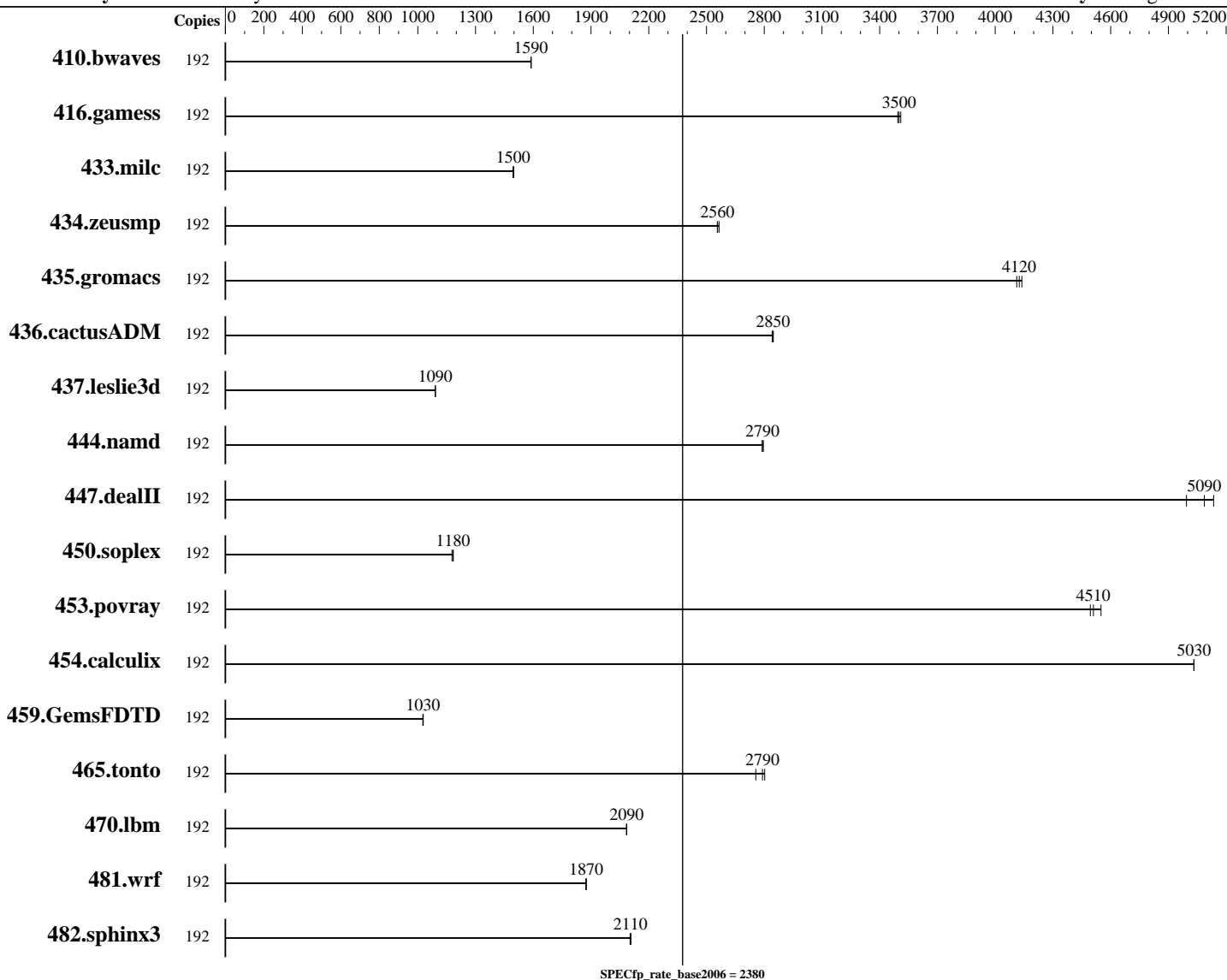
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: May-2016

Hardware Availability: Jul-2016

Software Availability: Aug-2015



### Hardware

CPU Name: Intel Xeon E7-8890 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 96 cores, 4 chips, 24 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 Chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64)  
 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 60 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)  
Disk Subsystem: 1 X 400 GB SAS SSD, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	192	1642	1590	<b><u>1643</u></b>	<b><u>1590</u></b>	1644	1590							
416.gamess	192	<b><u>1074</u></b>	<b><u>3500</u></b>	1076	3490	1071	3510							
433.milc	192	1176	1500	<b><u>1179</u></b>	<b><u>1500</u></b>	1179	1490							
434.zeusmp	192	684	2560	681	2570	<b><u>683</u></b>	<b><u>2560</u></b>							
435.gromacs	192	331	4140	<b><u>332</u></b>	<b><u>4120</u></b>	333	4110							
436.cactusADM	192	806	2850	808	2840	<b><u>806</u></b>	<b><u>2850</u></b>							
437.leslie3d	192	1654	1090	<b><u>1654</u></b>	<b><u>1090</u></b>	1656	1090							
444.namd	192	551	2800	552	2790	<b><u>552</u></b>	<b><u>2790</u></b>							
447.dealII	192	440	4990	<b><u>432</u></b>	<b><u>5090</u></b>	428	5140							
450.soplex	192	1352	1180	<b><u>1354</u></b>	<b><u>1180</u></b>	1360	1180							
453.povray	192	227	4490	224	4550	<b><u>226</u></b>	<b><u>4510</u></b>							
454.calculix	192	315	5030	<b><u>315</u></b>	<b><u>5030</u></b>	315	5030							
459.GemsFDTD	192	1985	1030	<b><u>1984</u></b>	<b><u>1030</u></b>	1981	1030							
465.tonto	192	686	2760	<b><u>677</u></b>	<b><u>2790</u></b>	674	2800							
470.lbm	192	1265	2090	1266	2080	<b><u>1265</u></b>	<b><u>2090</u></b>							
481.wrf	192	1145	1870	<b><u>1144</u></b>	<b><u>1870</u></b>	1144	1880							
482.sphinx3	192	<b><u>1778</u></b>	<b><u>2110</u></b>	1779	2100	1776	2110							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

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## Platform Notes

CPU performance set to Enterprise  
Power Technology set to Performance  
Energy Performance BIAS setting set to Balanced Performance  
Memory RAS configuration set to Maximum Performance  
Memory Power Saving Mode set to Disabled  
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-snnk Tue Jan 5 19:17:21 2010

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8890 v4 @ 2.20GHz
 4 "physical id"s (chips)
 192 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 24
  siblings  : 48
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
 27 28 29
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
 27 28 29
  physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
 27 28 29
  physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
 27 28 29
cache size : 30720 KB
```

```
From /proc/meminfo
MemTotal:      528963636 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
```

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## Platform Notes (Continued)

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

```
uname -a:
Linux linux-snk 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 5 19:16
```

```
SPEC is set to: /opt/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        xfs   372G  109G  263G  30% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. EXM4.3.1.1.3.042620161123 04/26/2016

Memory:  
32x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 1600 MHz  
64x NO DIMM NO DIMM

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

Submitted\_by: "Vijay Durairaj (viyd)" <viyd@cisco.com>  
Submitted: Thu May 19 03:12:54 EDT 2016  
Submission: cpu2006-20160516-41332.sub

## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

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The flags files that were used to format this result can be browsed at

<https://pro.spec.org/private/osg/submit/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<https://pro.spec.org/private/osg/submit/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<https://pro.spec.org/private/osg/submit/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<https://pro.spec.org/private/osg/submit/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

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