

ASR9000 Software / Firmware & Hardware Compatibility Matrix

			Firmware Software Version										
Hardware Type	FPD Description	Sub-Type	IOS XR 3.7.2	IOS XR 3.7.3	IOS XR 3.9.0	IOS XR 3.9.1	IOS XR 3.9.2	IOS XR 4.0.0	IOS XR 4.0.1	IOS XR 4.0.2	IOS XR 4.0.3	IOS XR 4.1.0	IOS XR 4.1.1
A9K-BPID2-10-SLOT	Can Bus Ctrl (CBC) BP2	cbc	x	x	x	7.00	7.00	7.00	7.00	7.00	7.00	7.00	7.103
A9K-BPID2-6-SLOT	Can Bus Ctrl (CBC) BP2	cbc	x	x	x	7.00	7.00	7.00	7.00	7.00	7.00	7.00	7.103
Fan Trays													
ASR-9010-FAN	Can Bus Ctrl (CBC) FAN	cbc	4.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00
ASR-9006-FAN	Can Bus Ctrl (CBC) FAN	cbc	5.00	5.00	5.00	5.00	5.00	5.00	5.00	5.00	5.00	5.00	5.00
RSPs													
A9K-RSP-4G	Can Bus Ctrl (CBC) RSP2	cbc	1.10	1.20	1.02	1.02	1.02	1.02	1.02	1.02	1.02	1.02	1.02
	CPUCtrl RSP2	cpld2	1.17	1.17	1.17	1.17	1.17	1.17	1.17	1.17	1.17	1.17	1.17
	IntCtrl RSP2	fpga2	1.14	1.14	1.15	1.15	1.15	1.15	1.15	1.15	1.15	1.15	1.15
	ClkCtrl RSP2	fpga3	1.13	1.13	1.17	1.18	1.18	1.18	1.18	1.18	1.18	1.23	1.23
	UTI RSP2	fpga4	1.60	1.60	3.08	3.08	3.08	3.08	3.08	3.08	3.08	3.08	3.08
	PUNT RSP2	fpga1	1.40	1.40	1.05	1.05	1.05	1.05	1.05	1.05	1.05	1.05	1.05
	HSBI RSP2	hsbi	x	1.00	3.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00
	ROMMONA RSP2	rommonA	1.00	1.00	1.01	x	x	1.04	1.05	1.05	1.05	1.05	1.05
	ROMMONB RSP2	rommon	1.00	1.00	1.02	1.04	1.04	1.04	1.05	1.05	1.05	1.05	1.05
A9K-RSP-8G	Can Bus Ctrl (CBC) RSP2	cbc	x	x	1.02	1.02	1.02	1.02	1.02	1.02	1.02	1.02	1.02
	CPUCtrl RSP2	cpld2	x	x	1.17	1.17	1.17	1.17	1.17	1.17	1.17	1.17	1.17
	IntCtrl RSP2	fpga2	x	x	1.15	1.15	1.15	1.15	1.15	1.15	1.15	1.15	1.15
	ClkCtrl RSP2	fpga3	x	x	1.17	1.18	1.18	1.18	1.18	1.18	1.18	1.23	1.23
	UTI RSP2	fpga4	x	x	3.08	3.08	3.08	3.08	3.08	3.08	3.08	3.08	3.08
	PUNT RSP2	fpga1	x	x	1.05	1.05	1.05	1.05	1.05	1.05	1.05	1.05	1.05
	HSBI RSP2	hsbi	x	x	3.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00	4.00
	ROMMONA RSP2	rommonA	x	x	1.01	x	x	1.04	1.05	1.05	1.05	1.05	1.05
	ROMMONB RSP2	rommon	x	x	1.02	1.04	1.04	1.04	1.05	1.05	1.05	1.05	1.05
Line Cards													
A9K-40GE-B	Can Bus Ctrl (CBC) LC2	cbc	2.10	2.20	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02
	CPUCtrl LC2	cpld1	0.15	0.15	0.19	0.19	0.19	0.19	0.19	0.19	0.19	1.00	1.00
	PHYCtrl LC2	cpld2	0.60	0.60	0.06	0.06	0.06	0.06	0.06	0.06	0.06	0.06	0.06
	PortCtrl LC2	fpga2	0.80	0.80	0.09	0.09	0.09	0.09	0.10	0.10	0.10	0.10	0.10
	Bridge LC2	fpga[1]	0.38	0.38	0.41	0.42	0.42	0.42	0.43	0.43	0.43	0.43	0.43
	HSBI LC2	hsbi	x	1.00	3.00	x	x	x	x	x	x	x	x
	ROMMONA LC2	rommonA	1.00	1.00	1.01	x	x	1.03	1.05	1.05	1.05	1.05	1.05
	ROMMONB LC2	rommon	1.00	1.00	1.02	1.03	1.03	1.03	1.05	1.05	1.05	1.05	1.05
A9K-40GE-E	Can Bus Ctrl (CBC) LC2	cbc	2.10	2.20	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02
	CPUCtrl LC2	cpld1	0.15	0.15	0.19	0.19	0.19	0.19	0.19	0.19	0.19	1.00	1.00
	PHYCtrl LC2	cpld2	0.60	0.60	0.06	0.06	0.06	0.06	0.06	0.06	0.06	0.06	0.06
	PortCtrl LC2	fpga2	0.80	0.80	0.09	0.09	0.09	0.09	0.10	0.10	0.10	0.10	0.10
	Bridge LC2	fpga[1]	0.38	0.38	0.41	0.42	0.42	0.42	0.43	0.43	0.43	0.43	0.43
	HSBI LC2	hsbi	x	1.00	3.00	x	x	x	x	x	x	x	x
	ROMMONA LC2	rommonA	1.00	1.00	1.01	x	x	1.03	1.05	1.05	1.05	1.05	1.05
	ROMMONB LC2	rommon	1.00	1.00	1.02	1.03	1.03	1.03	1.05	1.05	1.05	1.05	1.05
A9K-40GE-L	Can Bus Ctrl (CBC) LC2	cbc	x	x	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02
	CPUCtrl LC2	cpld1	x	x	0.19	0.19	0.19	0.19	0.19	0.19	0.19	1.00	1.00
	PHYCtrl LC2	cpld2	x	x	0.06	0.06	0.06	0.06	0.06	0.06	0.06	0.06	0.06
	PortCtrl LC2	fpga2	x	x	0.09	0.09	0.09	0.09	0.10	0.10	0.10	0.10	0.10

	Serdes Upgrade LC2	fpga3	x	x	14.42	14.42	14.42	14.42	14.44	14.44	14.44	14.44	14.44
	Bridge LC2	fpga1	x	x	0.41	0.42	0.42	0.42	0.43	0.43	0.43	0.43	0.43
	HSBI LC2	hsbi	x	x	3.00	x	x	x	x	x	x	x	x
	ROMMONB LC2	rommon	x	x	1.02	1.03	1.03	1.03	1.05	1.05	1.05	1.05	1.05
A9K-8T-B	Can Bus Ctrl (CBC) LC3	cbc	x	x	x	6.02	6.02	6.02	6.02	6.02	6.02	6.02	6.02
	CPUCtrl LC3	cpld1	x	x	x	1.00	1.00	1.00	1.00	1.00	1.00	1.00	1.02
	PHYCtrl LC3	cpld2	x	x	x	0.08	0.08	0.08	0.08	0.08	0.08	0.08	0.08
	LCClkCtrl LC3	cpld3	x	x	x	0.03	0.03	0.03	0.03	0.03	0.03	0.03	0.03
	DB CPUCtrl LC3	cpld4	x	x	x	1.00	1.00	1.00	1.00	1.00	1.00	1.03	1.03
	PortCtrl LC3	fpga2	x	x	x	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11
	Serdes Upgrade LC3	fpga3	x										
	Raven LC3	fpga1	x	x	x	1.00	1.00	1.00	1.00	1.00	1.00	1.02	1.02
	ROMMONB LC3	rommon	x	x	x	1.03	1.03	1.03	1.03	1.03	1.03	1.03	1.03
A9K-8T-E	Can Bus Ctrl (CBC) LC3	cbc	x	x	6.02	6.02	6.02	6.02	6.02	6.02	6.02	6.02	6.02
	CPUCtrl LC3	cpld1	x	x	0.07	1.00	1.00	1.00	1.00	1.00	1.00	1.02	1.02
	PHYCtrl LC3	cpld2	x	x	0.08	0.08	0.08	0.08	0.08	0.08	0.08	0.08	0.08
	LCClkCtrl LC3	cpld3	x	x	0.03	0.03	0.03	0.03	0.03	0.03	0.03	0.03	0.03
	[DB] CPUCtrl LC3	cpld4	x	x	0.05	1.00	1.00	1.00	1.00	1.00	1.00	1.03	1.03
	PortCtrl LC3	fpga2	x	x	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11
	Raven LC3	fpga1	x	x	0.05	1.00	1.00	1.00	1.00	1.00	1.00	1.02	1.02
	HSBI LC3	hsbi	x	x	3.00	x	x	x	x	x	x	x	x
	ROMMONB LC3	rommon	x	x	1.02	1.03	1.03	1.03	1.03	1.03	1.03	1.03	1.03
A9K-8T-L	Can Bus Ctrl (CBC) LC3	cbc	x	x	6.02	6.02	6.02	6.02	6.02	6.02	6.02	6.02	6.02
	CPUCtrl LC3	cpld1	x	x	0.07	1.00	1.00	1.00	1.00	1.00	1.00	1.02	1.02
	PHYCtrl LC3	cpld2	x	x	0.08	0.08	0.08	0.08	0.08	0.08	0.08	0.08	0.08
	LCClkCtrl LC3	cpld3	x	x	0.03	0.03	0.03	0.03	0.03	0.03	0.03	0.03	0.03
	CPUCtrl LC3	cpld4	x	x	0.05	1.00	1.00	1.00	1.00	1.00	1.00	1.03	1.03
	PortCtrl LC3	fpga2	x	x	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11
	Raven LC3	fpga1	x	x	0.05	1.00	1.00	1.00	1.00	1.00	1.00	1.02	1.02
	HSBI LC3	hsbi	x	x	3.00	x	x	x	x	x	x	x	x
	ROMMONB LC3	rommon	x	x	1.02	1.03	1.03	1.03	1.03	1.03	1.03	1.03	1.03
A9K-2T20GE-B	Can Bus Ctrl (CBC) LC2	cbc	x	x	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02
	CPUCtrl LC2	cpld1	x	x	0.19	0.19	0.19	0.19	0.19	0.19	0.19	1.00	1.00
	PHYCtrl LC2	cpld2	x	x	0.09	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11
	LCClkCtrl LC2	cpld3	x	x	0.09	0.09	0.09	0.09	0.09	0.09	0.09	0.09	0.09
	PortCtrl LC2	fpga2	x	x	0.16	0.16	0.16	0.16	0.16	0.16	0.16	0.16	0.16
	Bridge LC2	fpga1	x	x	0.41	0.42	0.42	0.42	0.43	0.43	0.43	0.43	0.43
	HSBI LC2	hsbi	x	x	3.00	x	x	x	x	x	x	x	x
	ROMMONB LC2	rommon	x	x	1.02	1.03	1.03	1.03	1.05	1.05	1.05	1.05	1.05
A9K-2T20GE-E	Can Bus Ctrl (CBC) LC2	cbc	x	x	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02
	CPUCtrl LC2	cpld1	x	x	0.19	0.19	0.19	0.19	0.19	0.19	0.19	1.00	1.00
	PHYCtrl LC2	cpld2	x	x	0.09	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11
	LCClkCtrl LC2	cpld3	x	x	0.09	0.09	0.09	0.09	0.09	0.09	0.09	0.09	0.09
	PortCtrl LC2	fpga2	x	x	0.16	0.16	0.16	0.16	0.16	0.16	0.16	0.16	0.16
	Bridge LC2	fpga1	x	x	0.41	0.42	0.42	0.42	0.43	0.43	0.43	0.43	0.43
	HSBI LC2	hsbi	x	x	3.00	x	x	x	x	x	x	x	x
	ROMMONB LC2	rommon	x	x	1.02	1.03	1.03	1.03	1.05	1.05	1.05	1.05	1.05
A9K-2T20GE-L	Can Bus Ctrl (CBC) LC2	cbc	x	x	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02	2.02
	CPUCtrl LC2	cpld1	x	x	0.19	0.19	0.19	0.19	0.19	0.19	0.19	1.00	1.00
	PHYCtrl LC2	cpld2	x	x	0.08	0.11	0.11	0.11	0.11	0.11	0.11	0.11	0.11
	LCClkCtrl LC2	cpld3	x	x	0.09	0.09	0.09	0.09	0.09	0.09	0.09	0.09	0.09
	Tomcat LC2	fpga2	x	x	0.16	0.16	0.16	0.16	0.16	0.16	0.16	0.16	0.16
	Bridge LC2	fpga1	x	x	0.41	0.42	0.42	0.42	0.43	0.43	0.43	0.43	0.43

A9K-MPA-4x10GE	EP I/O FPGA	fpga6	x	x	x	x	x	x	x	x	x	x	x
A9K-MPA-20x1GE	EP I/O FPGA	fpga3	x	x	x	x	x	x	x	x	x	x	x

Legend:
 'Value' = The recommended frmware version for the particular XR Release
 'X' = Not applicable or relevant to particular XR Release

Notes:
 1. Shipped versions of ROMMON from manufacturing should supersede any previously released versions in software. Always use the latest version of ROMMON. Specifically, do not force downgrade ROMMON versions.
 2. All FPGA, PLD, and ROMMON versions are fully backward compatible. There is no requirement to force downgrade firmware when downgrading software.



IOS XR 4.2.0



7.103
7.103



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1.18
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2.02
1.00
0.06
0.10
0.43
3.00
1.05
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2.02
1.00
0.06
0.10
0.43
3.00
1.05
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2.02
1.00
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0.10

14.44
0.43
3.00
1.05
6.02
1.02
0.08
0.03
1.03
0.11
14.44
1.02
1.03
6.02
1.02
0.08
0.03
1.03
0.11
1.02
3.00
1.03
6.02
1.02
0.08
0.03
1.03
0.11
1.02
3.00
1.03
2.02
1.00
0.11
0.10
0.16
0.43
3.00
1.05
2.02
1.00
0.11
0.10
0.16
0.43
3.00
1.05
2.02
1.00
0.11
0.10
0.16
0.43

3.00
1.05
6.02
1.02
0.04
0.01
1.03
0.01
14.44
1.02
1.03
18.05
0.01
2.00
0.29
1.02
1.02
19.108
1.01
1.00
1.01
1.21
19.108
1.01
1.00
1.01
1.21
20.113
0.05
0.06
1.21
20.113
0.05
0.06
1.21
20.113
0.05
0.06
1.21
20.113
0.05
0.06
1.21
21.107
1.01
1.02
1.01
1.00
1.01
23.00

1.22
21.107
1.01
1.02
1.01
1.00
1.01
23.00
1.22

3.05
0.15
5.14
0.23
3.00
1.03
1.03

1.00
1.36
2.02
1.00
1.00
1.00
1.36
2.02
1.00
1.02
1.04
1.04
1.01
2.12
1.04
1.04
1.01
2.12
1.04
1.08
2.12
1.00
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0.11
1.04
2.08
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