

Cisco 3640 Router-PBX Interoperability: Lucent/Avaya Definity G3si V7 PBX with T1 CAS to an MGCP Gateway

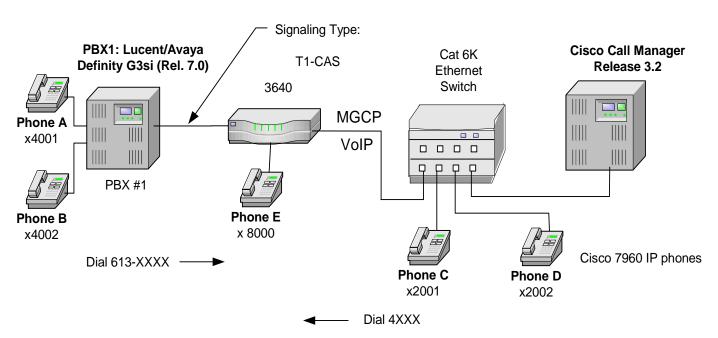
Introduction

- This note describes interoperability of Lucent/Avaya Definity G3si Version 9 PBX with Cisco CallManager using Cisco 3640 with T1 CAS as an MGCP Gateway.
- The Network Topology diagram shows the test set-up for end-to-end interoperability between the Cisco CallManager connected to the PBX via Cisco 3640 with T1 CAS link as an MGCP Gateway.
- Connectivity is achieved by using the T1-CAS signaling protocol type on the MGCP gateway and the Lucent/Avaya PBX.
- Wink-Start signaling is best choice for reliability.

Network Topology

Figure 1. Network Topology or Test Setup

Basic Call Setup End-to-End Configuration





Limitations

E&M-Immediate-Start Signaling

• E&M-Immediate-start Signaling type is not supported on CCM Gateway configuration GUI.

E&M-Delay-Dial Signaling

- E&M-Delay-Dial signaling is not functional when the default timer setting (300 msec) for the Delay-Start parameter is used on the Gateway. Since this timer parameter can't be changed by the CCM Gateway configuration GUI, the Wink Timer trunk parameter on the Lucent/Avaya PBX needs to be changed from the default of 300 msec. to 400 msec. in order for the Delay-Dial signaling type to work properly. Note that the Delay-Dial signaling type is a delayed version of a Wink-Start Signaling, therefore the Wink Timer on the PBX needs to be set to a greater value than the Delay-Start timer on the Gateway.
- Wink-Start signaling is best choice for reliability.

System Components

Hardware Requirements

- Cisco 3640 Gateway with 2MFT T1 Port
- Cisco Cat6K switch
- Cisco CallManager 3.2
- Lucent/Avaya Definity G3si V9 PBX, TN464F, DS1 INTFC 24/32.

Software Requirements

- Cisco IOS Software Release "c3640-is-mz.122-10.8.PI5".
- PBX Software Release 7 Version G3V9i.02.0.033.2
- Cisco CallManager Release 3.2

Configuration

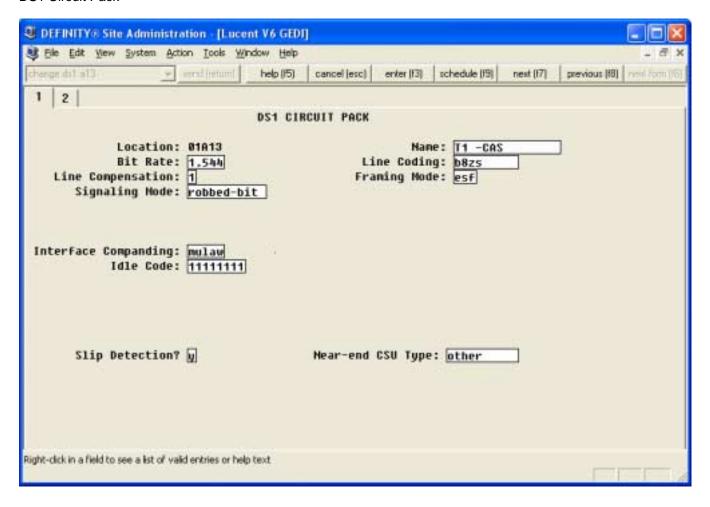
Configuring the Lucent/Anaya Definity G3si PBX

Configure in the following sequence:

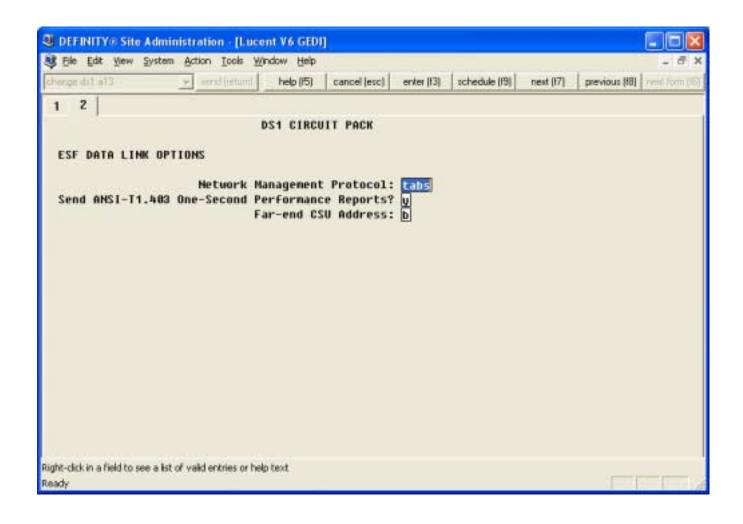
- Add the new circuit pack.
- Add the new Trunk group.
- Configure Dialing Plan.



DS1 Circuit Pack

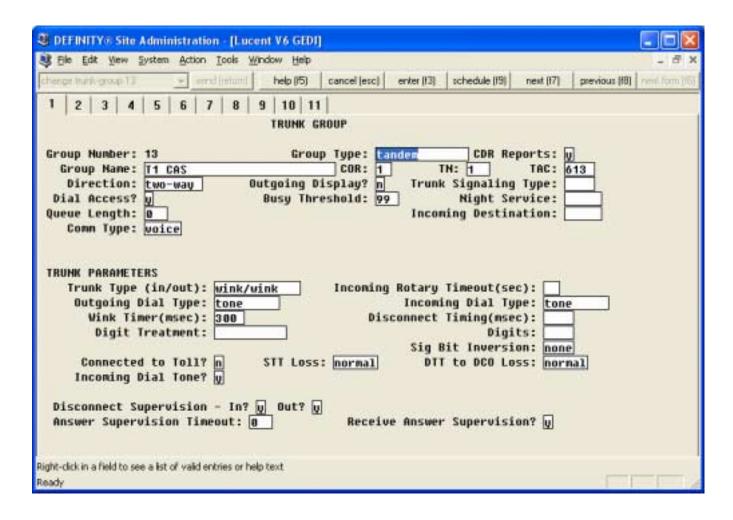








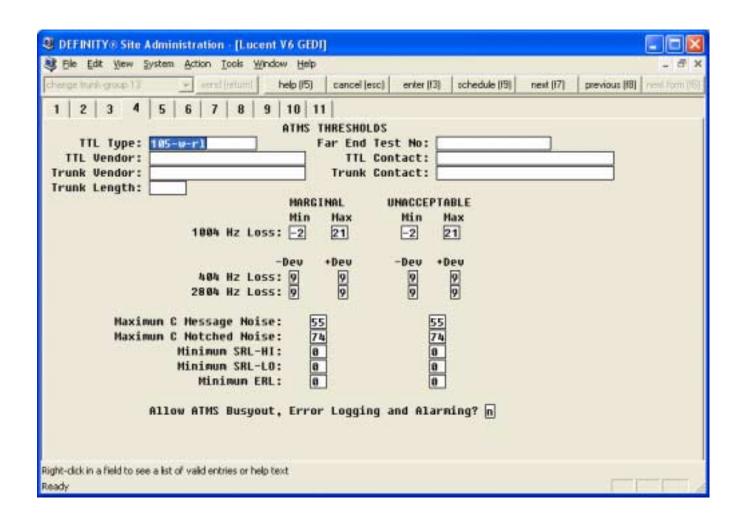
Trunk Group (Wink-Start signaling)



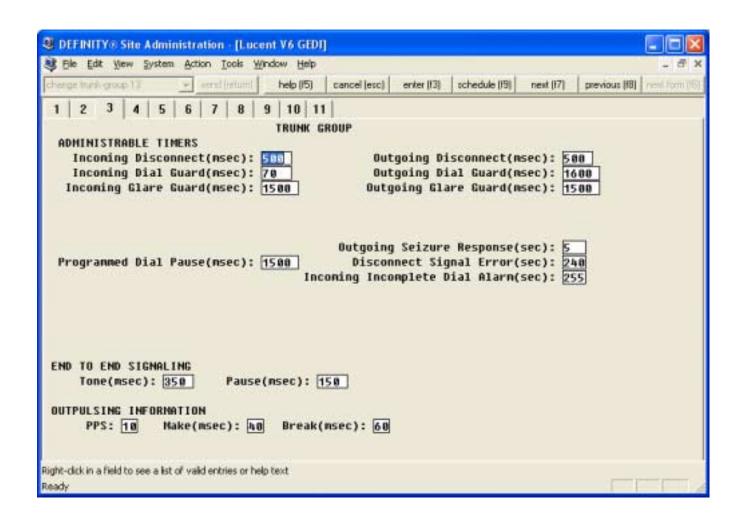


② DEFINITY® Site Administration - [Lucent V6 GEDI]	
Ble Edit Yew System Action Tools Window Help	- 6 X
change hunk-group 13 y annul return help (/5) cancel (esc.) enter (/3) schedule (/9) next (/7) previous (/8) n	ent from (fo)
1 2 3 4 5 6 7 8 9 10 11 TRUNK FEATURES	
ACA Assignment? Measured: none Maintenance Tests? Maintenance Tests	
Incoming Tone (DTMF) ANI: no Per Call CPN Blocking Code: Per Call CPN Unblocking Code:	
Right-click in a field to see a list of valid entries or help text Ready	

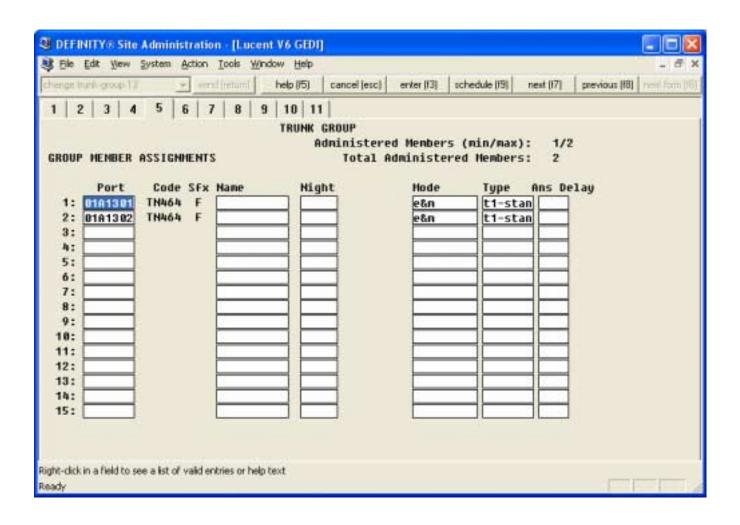






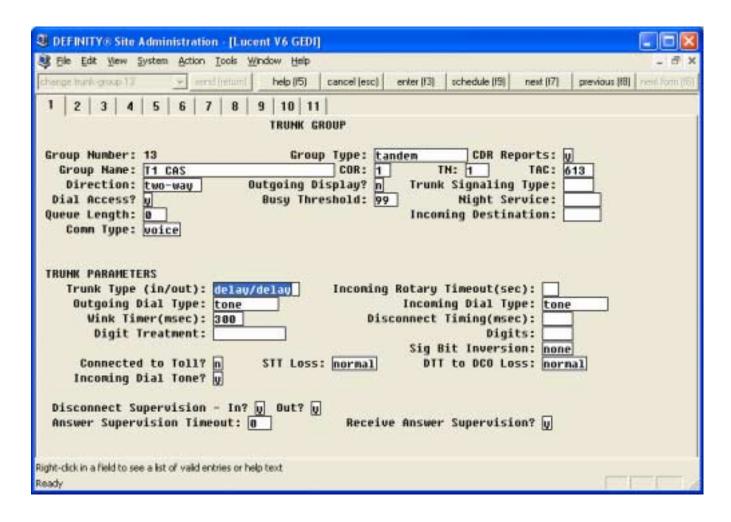






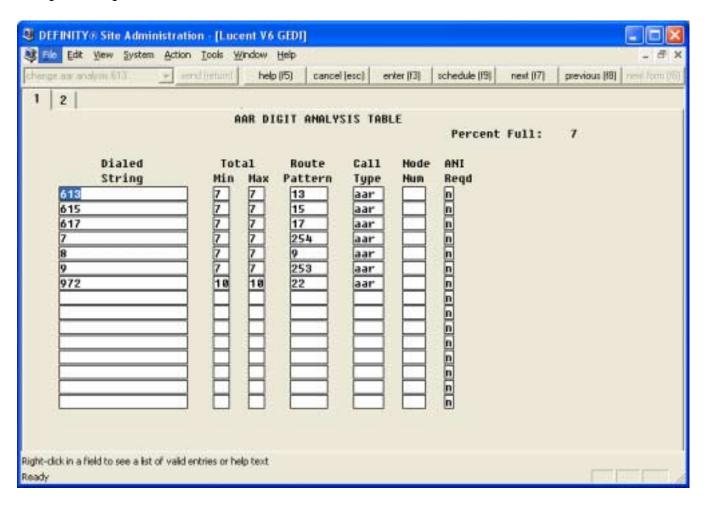


Trunk Group for Delay-Dial Signaling

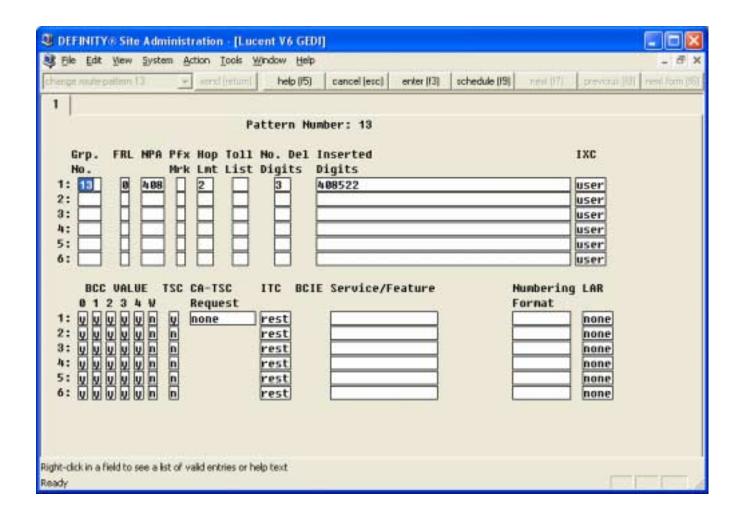




Configure Dialing Plan



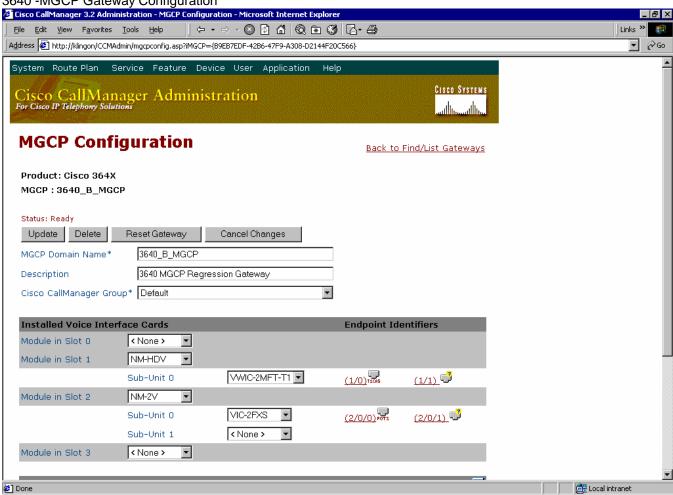




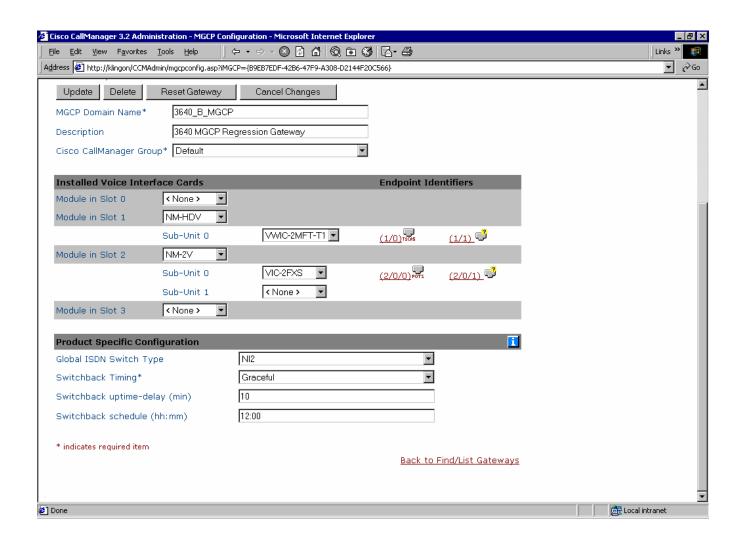


Configuring Cisco CallManager

3640 -MGCP Gateway Configuration



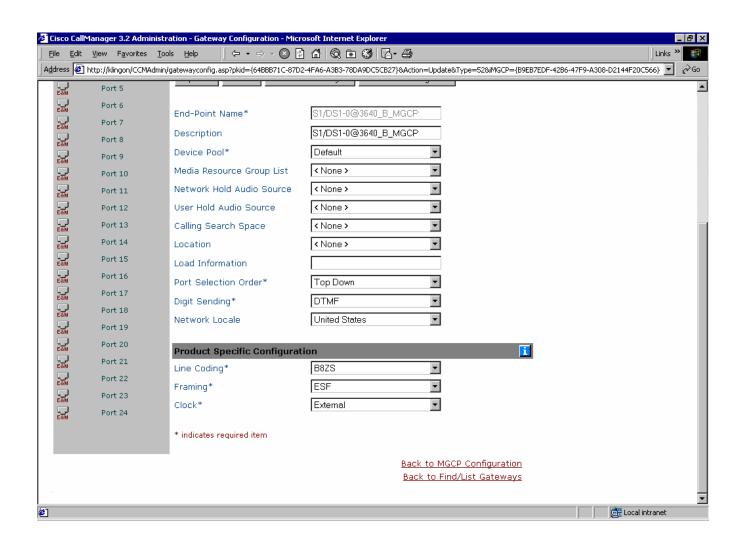






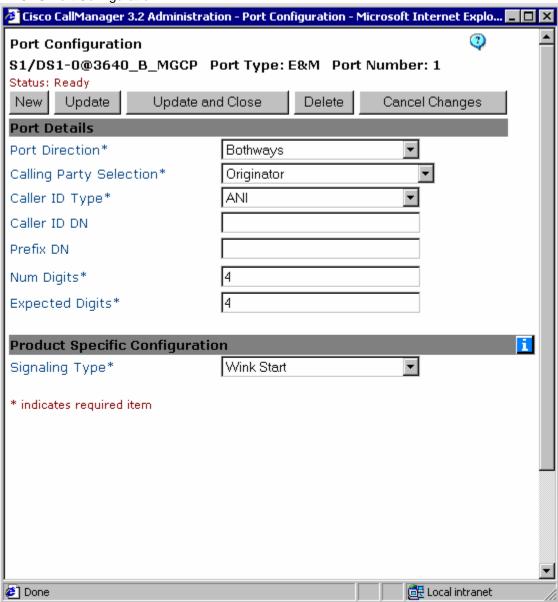
Digital Access T1-CAS Configuration Cisco CallManager 3.2 Administration - Gateway Configuration - Microsoft Internet Explorer _ B × Links » <u>File Edit View Favorites Tools Help</u> Address bttp://klingon/CCMAdmin/gatewayconfig.asp?pkid={64BB871C-87D2-4FA6-A3B3-78DA9DC5CB27}&Action=Update&Type=52&MGCP={B9EB7EDF-42B6-47F9-A308-D2144F20C566} System Route Plan Service Feature Device User Application Help CISCO SYSTEMS Cisco CallManager Administration For Cisco IP Telephony Solutions **Gateway Configuration** Back to MGCP Configuration Back to Find/List Gateways Product : Cisco 364X <<u>Add a New Port</u>> Gateway: \$1/D\$1-0@3640_B_MGCP Device Protocol: Digital Access T1 E&M Port 1 Registration: Not Registered E&M Port 2 IP Address: 10.1.1.73 E&M Port 3 Status: Ready Port 4 Update Delete Reset Gateway Cancel Changes Port 5 Port 6 End-Point Name* S1/DS1-0@3640_B_MGCP Port 7 Description S1/DS1-0@3640_B_MGCP Port 8 Default • Device Pool* Port 9 • Media Resource Group List < None > E&M Port 10 • Network Hold Audio Source < None > Port 11 ▼ User Hold Audio Source < None > Port 12 Port 13 • Calling Search Space < None > E&M • < None > Location E&M Port 15 Load Information E&M Port 16 Top Down Port Selection Order* • Port 17 Бтис Done





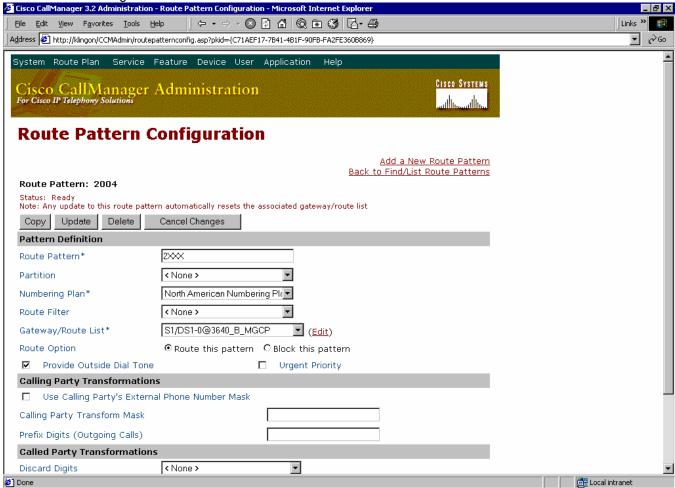


T1-CAS Port Configuration





Route Pattern Configuration





Configuring the Cisco 3640 Router

```
3640 B mgcp#sh ver
Cisco Internetwork Operating System Software
IOS (tm) 3600 Software (C3640-IS-M), Version 12.2(10.8)PI5, MAINTENANCE INTERIM
SOFTWARE
Synched to technology version 12.2(10.8u)T
TAC Support: http://www.cisco.com/tac
Copyright (c) 1986-2002 by cisco Systems, Inc.
Compiled Fri 07-Jun-02 17:40 by cmong
Image text-base: 0x60008930, data-base: 0x61924000
ROM: System Bootstrap, Version 11.1(17)AA, EARLY DEPLOYMENT RELEASE SOFTWARE (fc
ROM: 3600 Software (C3640-IS-M), Version 12.2(10.8)PI5, MAINTENANCE INTERIM SOF
TWARE
3640_B_mgcp uptime is 1 week, 1 day, 21 hours, 31 minutes
System returned to ROM by reload
System image file is "slot0:c3640-is-mz.122-10.8.PI5"
cisco 3640 (R4700) processor (revision 0x00) with 59392K/6144K bytes of memory.
Processor board ID 07658970
R4700 CPU at 100Mhz, Implementation 33, Rev 1.0
Bridging software.
X.25 software, Version 3.0.0.
SuperLAT software (copyright 1990 by Meridian Technology Corp).
Primary Rate ISDN software, Version 1.1.
2 Ethernet/IEEE 802.3 interface(s)
2 Channelized T1/PRI port(s)
2 Voice FXS interface(s)
DRAM configuration is 64 bits wide with parity disabled.
125K bytes of non-volatile configuration memory.
8192K bytes of processor board System flash (Read/Write)
16384K bytes of processor board PCMCIA Slot1 flash (Read/Write)
Configuration register is 0x0
3640_B_mgcp#sh run
Building configuration...
Current configuration: 1580 bytes
version 12.2
no parser cache
service timestamps debug uptime
service timestamps log uptime
no service password-encryption
no service dhcp
hostname 3640_B_mgcp
voice-card 1
ip subnet-zero
no ip domain lookup
voice call carrier capacity active
!
!
```



```
fax interface-type fax-mail
mta receive maximum-recipients 0
ccm-manager mgcp
ccm-manager music-on-hold
ccm-manager config server 10.1.1.2
ccm-manager config
controller T1 1/0
 framing esf
 linecode b8zs
 ds0-group 1 timeslots 1-24 type e&m-wink-start
controller T1 1/1
 framing sf
 linecode ami
interface Ethernet0/0
 ip address 10.1.1.78 255.255.255.0
 half-duplex
 no cdp enable
interface Ethernet0/1
no ip address
 shutdown
half-duplex
no cdp enable
ip classless
ip http server
ip pim bidir-enable
call rsvp-sync
voice-port 1/0:1
voice-port 2/0/0
voice-port 2/0/1
mgcp call-agent 10.1.1.2 2427 service-type mgcp version 0.1
mgcp dtmf-relay voip codec all mode out-of-band mgcp rtp unreachable timeout 1000 action notify
mgcp modem passthrough voip mode nse
mgcp package-capability rtp-package mgcp package-capability sst-package
no mgcp timer receive-rtcp
mgcp sdp simple
mgcp profile default
dial-peer cor custom
dial-peer voice 201 pots
 application mgcpapp
port 2/0/1
dial-peer voice 200 pots
 application mgcpapp
 port 2/0/0
```



```
dial-peer voice 300 pots
application mgcpapp
dial-peer voice 101 pots
application mgcpapp
dial-peer voice 999101 pots
application mgcpapp
port 1/0:1
line con 0
line aux 0
line vty 0 4
login
!
end
3640_B_mgcp#sh diag
Slot 0:
      Combo 2E, 2W Port adapter, 4 ports
      Port adapter is analyzed
      Port adapter insertion time unknown
      EEPROM contents at hardware discovery:
                            Board revision A0
      Hardware revision 1.2
                     6986074
      Serial number
                                 Part number 800-01171-04
      FRU Part Number: NM-2E2W=
      Test history
                                 RMA number
                                              00-00-00
      EEPROM format version 1
      EEPROM contents (hex):
        0x20: 01 1E 01 02 00 6A 99 5A 50 04 93 04 00 00 00
        0x30: 50 00 00 00 98 02 14 17 FF FF FF FF FF FF FF FF
Slot 1:
      High Density Voice Port adapter
      Port adapter is analyzed
      Port adapter insertion time unknown
      EEPROM contents at hardware discovery:
                          : 1.1
      Hardware Revision
      Top Assy. Part Number
                           : 800-03567-01
                    : E0
: 0-0
      Board Revision
      Deviation Number
                           : 02
      Fab Version
      PCB Serial Number
                           : JAB043409F7
      RMA Test History
                          : 00
      RMA Number
                           : 0-0-0-0
      RMA History
                           : 00
      EEPROM format version 4
      EEPROM contents (hex):
        0x00: 04 FF 40 00 CC 41 01 01 CO 46 03 20 00 0D EF 01
        0x10: 42 45 30 80 00 00 00 00 02 02 C1 8B 4A 41 42 30
        0x20: 34 33 34 30 39 46 37 03 00 81 00 00 00 00 04 00
        WIC Slot 0:
      T1 (2 Port) Multi-Flex Trunk (Drop&Insert) WAN Daughter Card
                             Board revision B0
      Hardware revision 1.0
      Serial number
                     28355670
                                 Part number
                                              800-04614-03
      Test history
                                 RMA number
      Connector type
                     PCI
      EEPROM format version 1
      EEPROM contents (hex):
        0x20: 01 24 01 00 01 B0 AC 56 50 12 06 03 00 00 00
        0x30: 58 00 00 00 02 05 31 00 FF FF FF FF FF FF FF FF
```



```
HDV firmware: Compiled Thu 27-Dec-01 13:17 by miriyala
       HDV memory size 524280 heap free 197225
Slot 2:
       4 PORT Voice PM for MARs Port adapter
       Port adapter is analyzed
       Port adapter insertion time unknown
       EEPROM contents at hardware discovery:
       Hardware revision 1.0
                                       Board revision BO
                         7968450
                                                     800-02491-01
       Serial number
                                       Part number
       FRU Part Number: NM-2V=
       Test history
                         0x0
                                       RMA number
                                                     00-00-00
       EEPROM format version 1
       EEPROM contents (hex):
          0x20: 01 65 01 00 00 79 96 C2 50 09 BB 01 00 00 00
         0x30: 58 00 00 00 98 04 01 17 FF FF FF FF FF FF FF FF
       WIC Slot 0:
       FXS Voice daughter card (2 port)
                                   Board revision B0
       Hardware revision 1.1
                         24298176
                                       Part number 800-02493-02
       Serial number
                                                      00 - 00 - 00
       Test history
                         0x0
                                       RMA number
       Connector type
                         Wan Module
       EEPROM format version 1
       EEPROM contents (hex):
          0x20: 01 0E 01 01 01 72 C2 C0 50 09 BD 02 00 00 00 00
          0x30: 58 00 00 00 01 02 05 01 FF FF FF FF FF FF FF FF
3640_B_mgcp#sh controller t1 1/0
T1 1/0 is up.
  Applique type is Channelized T1
  Cablelength is long gain36 0db
 No alarms detected.
 alarm-trigger is not set
 Version info Firmware: 20020306, FPGA: 11
  Framing is ESF, Line Code is B8ZS, Clock Source is Line.
 Data in current interval (7 seconds elapsed):
     O Line Code Violations, O Path Code Violations
     O Slip Secs, O Fr Loss Secs, O Line Err Secs, O Degraded Mins
     O Errored Secs, O Bursty Err Secs, O Severely Err Secs, O Unavail Secs
3640_B_mgcp#
```

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