



Access Identifiers

This chapter describes the access identifiers (AIDs) of TL1 commands and autonomous messages for the Cisco ONS 15454, Cisco ONS 15454 M2, and Cisco ONS 15454 M6.

The AID code directs an input command to its intended physical or data entity inside the network element (NE). Equipment modules and facilities are typical examples of entities addressed by the access code. The AIDs in this section apply to the SONET ONS 15454, ONS 15454 M2, and ONS 15454 M6 except where noted.



Note

Access identifiers supported on the Cisco ONS 15454 platform are also supported on Cisco ONS 15454 M2 and Cisco ONS 15454 M6 platforms.

26.1 ALL

AID for CXP port on 100G-LC-C card: AGGR-[SLOT]-[APPM]-[PORT].

AID for fixed trunk on 100G-LC-C card: CHAN-[SLOT]-[PORT].

Table 26-1 **ALL**

AID	Pattern	
AidUnion Id	FACILITY STS VT	
AidUnion Id1	BLSR	
ACHAN	ACHAN[-{1-50}]-{2-7}-{1}-{1}	
AGGR	AGGR-[SLOT]-[APPM]-[PORT] AGGR[-{1-50}]-{2-7}-{1}-{1}	
BAND	ALL BAND[-{1-50}]-{1-6,12-17}-{1-4}-ALL BAND[-{1-50}]-{1-6,12-17}-{1-4}-{RX,TX} BAND[-{1-50}]-{1-6,12-17}-{1}-ALL BAND[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}	

Table 26-1 ALL (continued)

AID	Pattern	
BANDWL	BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-1530.33	
	BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-ALL	
	BANDWL-[{1-50}]-{1-6,12-17}-{1-32}-{RX,TX,PT}<WLEN>	
	BANDWL-[{1-50}]-{1-6,12-17}-{1-32}-{RX,TX,PT}-ALL	
BITS	ALL	
	BITS-ALL	
	BITS[<SHELFID>]-ALL	
	BITS[<SHELFID>]-{1,2}	
	SYNC[<SHELFID>]-BITS{1,2}	
BLSR	BLSR-RINGID	
BWP	BWP-ALL	
	BWP-{1-10000}	
CHANNE L	ALL	CHAN[-{1-50}]-{1-6,12-17}-{5}
	CHAN[-{1-50}]-{1-6,12-17}-ALL	CHAN[-{1-50}]-{1-6,12-17}-{9}
	CHAN[-{1-50}]-{1-6,12-17}-{1-32}-ALL	CHAN[-{1-50}]-{1-6,12-17}-{9,10}
	CHAN[-{1-50}]-{1-6,12-17}-{1-32}-{RX,TX}	CHAN[-{1-50}]-{1-6,12-17}-{3,4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-32}-{RX,PT}	CHAN[-{1-50}]-{1-6,12-17}-{21,22}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-40}-ALL	CHAN[-{1-50}]-{1-6,12-17}-{19}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-40}-{RX,TX}	CHAN[-{1-50}]-{1-6,12-17}-{17,18}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-40}-{RX,PT}	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-ALL	CHAN[-{1-50}]-{1-6,12-17}-{1,2}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{RX,TX}	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{RX,PT}	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-ALL	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{RX,TX}	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{RX,PT}	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-ALL	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{RX,TX}	CHAN[-{1-50}]-{1-6,12-17}-{1-4}-{1}
COM	Common	
CTRL	CTRL-ALL	
	CTRL-{1-4}	

Table 26-1 ALL (continued)

AID	Pattern	
CrossConnectId	FACILITY	
	STS	
CrossConnectId1	VCM	
	FACILITY	
	STS	
	VT	
DS1	ALL	
	DS1-{1-6,12-17}-{1-50,13,15,17,19,21,23,25,27,29,31,33,35}-{1-28}	
	DS1-{1-6,12-17}-{1-50,13,15,17,19,21,23}-{1-28}	
	DS1-{1-6,12-17}-{1-6}-{1-28}	
ENV	ALL	ENV-IN[-{1-50}]{1-6}
	ENV-IN-ALL	ENV-OUT-ALL
	ENV-IN[-{1-50}]-ALL	ENV-OUT[-{1-50}]-ALL
	ENV-IN[-{1-50}]{1-20}	ENV-OUT[-{1-50}]{1-16}
	ENV-IN[-{1-50}]{1-32}	ENV-OUT[-{1-50}]{1-2}
	ENV-IN[-{1-50}]{1-3}	ENV-OUT[-{1-50}]{1-4}
	ENV-IN[-{1-50}]{1-4}	ENV-{IN,OUT}[-{1-50}]{1-16}

Table 26-1 ALL (continued)

AID	Pattern	
EQPT	ALL	PPM[-{1-50}]-{1-4,11-14}-{1-4}-{1-4}
	AIP-ALL	
	AIP[-{1-50}]	PPM[-{1-50}]-{1-6,12-17}-{1-4}
	BIC-ALL	
	BIC[-{1-50}]-ALL	PPM[-{1-50}]-{1-6,12-17}-{1-8}
	BIC[-{1-50}]-{A,B}	PWR-ALL
	BP-ALL	PWR[-{1-50}]-ALL
	BP[-{1-50}]	PWR[-{1-50}]-{A,B}
	FAN-ALL	SLOT-ALL
	FAN[-{1-50}]	SLOT[-{1-50}]-ALL
	PIM[-{1-50}]-{1-4,11-14}-ALL	SLOT[-{1-50}]-{1-14}
	PIM[-{1-50}]-{1-4,11-14}-{1-4}	SLOT[-{1-50}]-{1-17}
	PPM[-{1-50}]-1-{1,2}	SLOT[-{1-50}]-{1-2}
	PPM[-{1-50}]-2-{1,2}	SLOT[-{1-50}]-{1-4,11-14}
	PPM[-{1-50}]-{1-4,11-14}-{1-4}-ALL	SLOT[-{1-50}]-{1-6,12-17}
		SLOT[-{1-50}]-{1-8}
		SHELF-ALL
		SHELF-{1-50}
ETHERNET	ALL	
	ETH[-{1-50}]-{1-5,12-16}-{1-22}-1	
	ETH[-{1-50}]-{1-6,12-17}-{1-4}-1	
ETH	ETH-{1,2,5,6}-{1-6}	
	ETH-{1}-{1-6}	
ETHID	ALL	
	ETHID[-{1-50}]-{1-5,12-16}-{1-22}-1-{1-20}	
	ETHID[-{1-50}]-{1-6,12-17}-{1-4}-1-{1-20}	

Table 26-1 ALL (continued)

AID	Pattern	
FACILIT Y	ALL	FAC[-{1-50}]-{5-6}-{1-3}
	E1-{1,2,5,6}-{1-21}	FAC[-{1-50}]-{8,10}-{1}
	E1-{1,2,5,6}-{1-63}	FSTE-{1,2,5,6}-{0-7}
	E3-{1,2,5,6}-{1-3}	FSTE-{1,2,5,6}-{1-8}
	EC1-{1,2,5,6}-{1-3}	FSTE-{1}-{0-7}
	EC1-{2}-{1-3}	FSTE-{1}-{1-8}
	ETH-{1,2,5,6}-{1-6}	OC12-{2}-{1-2}-{1}
	ETH-{1}-{1-6}	OC12-{3,4}-{1-2}-{1}
	FAC[-{1-50}]-{1-4,11-14}-ALL	OC3-{2}-{1-2}-{1}
	FAC[-{1-50}]-{1-4,11-14}-{1-16}	OC3-{3,4}-{1-2}-{1}
	FAC[-{1-50}]-{1-4,11-14}-{1-4}	OC48-{3,4}-{1-2}-{1}
	FAC[-{1-50}]-{1-4,11-14}-{1-4}-{1-4}-{1}	STM1-{3,4}-{1,2}-{1}
	FAC[-{1-50}]-{1-4,14-17}-{1-8}	STM4-{3,4}-{1,2}-{1}
	FAC[-{1-50}]-{1-4}-1	STM16-{3,4}-{1,2}-{1}
	FAC[-{1-50}]-{1-4}-{1-4}	T1-{1,2,5,6}-{1-28}
	FAC[-{1-50}]-{1-6,12-17}-1	T1-{1,2,5,6}-{1-84}
	FAC[-{1-50}]-{1-6,12-17}-ALL	T1-{2}-{1-21}
	FAC[-{1-50}]-{1-6,12-17}-{0-11}	T3-{1,2,5,6}-{1-3}
	FAC[-{1-50}]-{1-6,12-17}-{0-1}	T3-{2}-{1-3}
	FAC[-{1-50}]-{1-6,12-17}-{1-50,14,16,18,20,22, 24,26,28,30,32,34,36}	VFAC-{1,2,5,6}-{0-1}
	FAC[-{1-50}]-{1-6,12-17}-{1-50,14,16,18,20,22, 24}	VFAC-{1,2,5,6}-{1-6}
	FAC[-{1-50}]-{1-6,12-17}-{1-12}	VFAC-{1,2,5,6}-{1-8}
	FAC[-{1-50}]-{1-6,12-17}-{1-4}	VFAC[-{1-50}]-{1-4,11-14}-{ 1-4}-{1-4}-1
	FAC[-{1-50}]-{1-6,12-17}-{1-6}	VFAC[-{1-50}]-{1-6,12-17}-{ 0-1}
	FAC[-{1-50}]-{1-6,12-17}-{1}	VFAC[-{1-50}]-{1-6,12-17}-{ 1,2}
	FAC[-{1-50}]-{1-6}-ALL	VFAC[-{1-50}]-{1-6,12-17}-{ 1,2}-{1,8}
	FAC[-{1-50}]-{5,6,12,13}-{1}	VFAC[-{1-50}]-{1}-{0-1}
	FAC[-{1-50}]-{5-6}-{1-28}	VFAC[-{1-50}]-{1}-{1-8}
IPADDR	111.222.333.444	
	"[3ffe:0501:0008:0000:0260:97ff:fe40:efab]"	
IPCC	ALL	
	CC-{1-16}	

Table 26-1 ALL (continued)

AID	Pattern
LINE	LINE[-{1-50}]{1-6,12-17}{1-2}-ALL
	LINE[-{1-50}]{1-6,12-17}{1-2}{RX,TX}
	LINE[-{1-50}]{1-6,12-17}{1-3}-ALL
	LINE[-{1-50}]{1-6,12-17}{1-3}{RX,TX}
	LINE[-{1-50}]{8,10}{1}-ALL
	LINE[-{1-50}]{8,10}{1}{RX,TX}
	LINE-[[{1-50}]{1-6,12-17}{1}{RX,TX}(COM)
	LINE-[[{1-50}]{1-6,12-17}{2}{RX,TX}(OSC)
	LINE-[[{1-50}]{1-6,12-17}{3}{RX,TX}(LINE)
	LINE-[[{1-50}]{1-6,12-17}{1-3}-ALL
	LINE-[[{1-50}]{1-6,12-17}{1}{RX,TX}(LINE)
	LINE-[[{1-50}]{1-6,12-17}{2}{RX,TX}(COM)
	LINE-[[{1-50}]{1-6,12-17}{3}{RX,TX}(OSC)
	LINE-[[{1-50}]{1-6,12-17}{4}{RX,TX}(DC)
	LINE-[[{1-50}]{1-6,12-17}{1-4}-ALL
	LINE-[[{1-50}]{1-6,12-17}-1-RX (For input OTS)
	LINE-[[{1-50}]{1-6,12-17}-1-ALL
	CHAN-[[{1-50}]{1-6,12-17}{1-32}-TX (For drop OCH)
	CHAN-[[{1-50}]{1-6,12-17}{1-32}-ALL
	LINE-[[{1-50}]{1-5,12-16}{1}{RX,TX} (EXP)
	LINE-[[{1-50}]{1-5,12-16}{2}{RX,TX} (COM)
	LINE-[[{1-50}]{1-5,12-16}{3}{TX} (DROP)
	LINE-[[{1-50}]{1-5,12-16}{1-3}-ALL
	CHAN-[[{1-50}]{1-5,12-16}{1-32}{RX} (ADD)
	CHAN-[[{1-50}]{1-5,12-16}{1-32}{PT} (PT)
	CHAN-[[{1-50}]{1-5,12-16}{1-32}-ALL
	LINE-[[{1-50}]{1-6,12-17}{1}{RX,TX} (EXP)
	LINE-[[{1-50}]{1-6,12-17}{2}{RX,TX} (COM)
	LINE-[[{1-50}]{1-6,12-17}{3}{RX,TX} (EXP to other ring)
	LINE-[[{1-50}]{1-6,12-17}{1-3}-ALL

Table 26-1 ALL (continued)

AID	Pattern	
LINEWL	LINEWL[-{1-50}]-{1-6,8,10,12-17}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}-<WLEN> LINEWL[-{1-50}]-{1-6,12-17}-{1-3}-{RX,TX}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1-3}-{RX,TX}-<WLEN> LINEWL[-{1-50}]-{1-6,12-17}-{1-2}-{RX,TX}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1-2}-{RX,TX}-<WLEN> LINEWL[-{1-50}]-{1-6,12-17}-{1-2}-{RX,TX}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1-2}-{RX,TX}-<WLEN> LINEWL[-{1-50}]-{1-6,12-17}-{1,3,4}-{RX,TX}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1,3,4}-{RX,TX}-<WLEN>	LINEWL[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}-ALL LINEWL[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}-<WLEN> LINEWL[-{1-50}]-{1-5,12-16}-{1-8}-{RX}-<WLEN> LINEWL[-{1-50}]-{1-5,12-16}-{9}-{RX,TX}-<WLEN> LINEWL[-{1-50}]-{1-5,12-16}-{10}-{TX}-<WLEN> LINEWL[-{1-50}]-{1-5,12-16}-{11}-{RX,TX}-<WLEN>
LNKTERM	ALL LNKTERM-ALL LNKTERM-{1-65535}	
ODU0	ODU0[-{1-50}]-{1-6,12-17}-{1-10}-{1}-{1-4}-{1-2}	
OSC	OSC-RINGID	
OPM	ALL OPM[-{1-50}]-{1-5,12-16}-<WLEN> OPM[-{1-50}]-{1-5,12-16}-ALL	
PR SLOT	NULL SLOT-1 SLOT-13 SLOT-15 SLOT-17 SLOT-3 SLOT-5	

Table 26-1 ALL (continued)

AID	Pattern	
RPRIF	ALL RPRIF-{1-6,12-17}-0	PPM[-{1-50}]-{1-4,11-14}-{1-4}-{1-4} RPRIF-<SLOT>-<RPR INTERFACE>. Currently, only interface 0 is supported.
RFILE	RFILE-DB RFILE-LOG RFILE-PKG RFILE-COMPDB	
SHELF	SHELF-ALL SHELF-{1-50}	
STS	FAC-{1-4,11-14}-{1-4}-{1-4}-{1} FAC-{1-6,12-17}-{1-4} STS-{1,2,5,6}-1 STS-{1,2,5,6}-{1-3} STS-{1,2,5,6}-{1-3}-1 STS-{1-4,11-14}-{1-16}-1 STS-{1-4,11-14}-{1-16}-ALL STS-{1-4,11-14}-{1-16}-{1,13,25,37} STS-{1-4,11-14}-{1-16}-{1,25} STS-{1-4,11-14}-{1-16}-{1,4,7,10,-,46} STS-{1-4,11-14}-{1-4}-1 STS-{1-4,11-14}-{1-4}-ALL STS-{1-4,11-14}-{1-4}-{1,13,25,37,-,181} STS-{1-4,11-14}-{1-4}-{1,25,49,73,-,169} STS-{1-4,11-14}-{1-4}-{1,4,7,10,-,190} STS-{1-4,11-14}-{1-4}-{1,49,97,145} STS-{1-4,11-14}-{1-4}-{1,4}-{1} STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,13,25,37}	

Table 26-1 ALL (continued)

AID	Pattern
STS (continued)	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,25}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,13,16,25,28,37,40}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7,10,13,16,19,22,25,28,31,34,37,40,43,46}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-12}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-3}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-48}
	STS-{1-4,11-14}-{1-4}-{1-192}
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-ALL
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-{1,4,7,13,16,19,25,28,31,43}
	STS-{1-4,14-17}-{1-16}-{1-48}
	STS-{1-4,14-17}-{1-4}-1
	STS-{1-4,14-17}-{1-4}-ALL
	STS-{1-4,14-17}-{1-4}-{1,4,7,10}
	STS-{1-4,14-17}-{1-4}-{1,4,7}
	STS-{1-4,14-17}-{1-4}-{1-3}
	STS-{1-4,14-17}-{1-8}-1
	STS-{1-4,14-17}-{1-8}-ALL
	STS-{1-4,14-17}-{1-8}-{1-3}
	STS-{1-4}-1-1
	STS-{1-4}-1-ALL
	STS-{1-4}-1-{1,13,25,37}
	STS-{1-4}-1-{1,4,7,10,-,46}
	STS-{1-4}-1-{1,4,7,10}
	STS-{1-4}-1-{1,7,13,19,-,43}
	STS-{1-4}-1-{1,7}
	STS-{1-4}-1-{1-12}
	STS-{1-4}-1-{1-48}
	STS-{1-6,12-17}-1
	STS-{1-6,12-17}-1-1
	STS-{1-6,12-17}-1-ALL
	STS-{1-6,12-17}-1-{1,13,25,37}
	STS-{1-6,12-17}-1-{1,4,10,13,16,19,25,28,37,40}
	STS-{1-6,12-17}-1-{1,4,7,10,13,16,19,22,25}

Table 26-1 ALL (continued)

AID	Pattern
STS (continued)	STS-{1-6,12-17}-1-{1,4,7,10-46}
	STS-{1-6,12-17}-1-{1,4,7,10}
	STS-{1-6,12-17}-1-{1,4,7,13,16,19,25,28,37,40,43}
	STS-{1-6,12-17}-1-{1,4,7}
	STS-{1-6,12-17}-1-{1,4}
	STS-{1-6,12-17}-1-{1-12}
	STS-{1-6,12-17}-1-{1-48}
	STS-{1-6,12-17}-ALL
	STS-{1-6,12-17}-{1-12}-1
	STS-{1-6,12-17}-{1-24}-1
	STS-{1-6,12-17}-{1-36}-1
	STS-{1-6,12-17}-{1-4}-1
	STS-{1-6,12-17}-{1-4}-ALL
	STS-{1-6,12-17}-{1-4}-{1,4,7,10-46}
	STS-{1-6,12-17}-{1-4}-{1,4,7}
	STS-{1-6,12-17}-{1-4}-{1,4}
	STS-{1-6,12-17}-{1-4}-{1-12}
	STS-{1-6,12-17}-{1-6}
	STS-{2}-{1-2}-{1}-{1,4,7,10}
	STS-{2}-{1-2}-{1}-{1,7}
	STS-{2}-{1-2}-{1}-{1-12}
	STS-{2}-{1-2}-{1}-{1-3}
	STS-{2}-{1-2}-{1}-{1}
	STS-{2}-{1-3}-{1}
	STS-{2}-{1}
	STS-{3,4}-{1,2}-1-{1-12}
	STS-{3,4}-{1,2}-1-{1-3}
	STS-{3,4}-{1,2}-1-{1-48}
	STS-{5,6,12,13}-1-1
	STS-{5,6,12,13}-1-{1,13,25,37-180}
	STS-{5,6,12,13}-1-{1,13,25,37}
	STS-{5,6,12,13}-1-{1,4,7,10,13,16,19,22,25}
	STS-{5,6,12,13}-1-{1,4,7,10-190}
	STS-{5,6,12,13}-1-{1,4,7,10-46}
	STS-{5,6,12,13}-1-{1,4,7,13,16,19,25,28,37,40,43}

Table 26-1 ALL (continued)

AID	Pattern	
STS (continued)	STS-{5,6,12,13}-1-{1,49,97,145}	
	STS-{5,6,12,13}-1-{1-192}	
	STS-{5,6,12,13}-1-{1-48}	
	STS-{5,6}-1	
	STS-{5,6}-{1-4}-1	
	STS-{5-6}-ALL	
	VFAC-{1-6,12-17}-{0-1}	
SYN	SYNC-NE	
SYN_SR C	BITS-1	FAC-{5,6,12,13}-{1}
	BITS-2	INTERNAL
	FAC-{1-4,11-14}-{1-16}	NONE
	FAC-{1-4,11-14}-{1-4}	OC12-{2}-{1-2}-{1}
	FAC-{1-4}-1	OC3-{2}-{1-2}-{1}
	FAC-{1-4}-{1-4}	SYNC-NE
	FAC-{1-6,12-17}-{1-4}	T1-{2}-{1-21}
	FAC-{1-6,12-17}-{1}	
SYNC_R EF	SYNC-ALL	
	SYNC-NE	
	SYNC-{BITS1,BITS2}	
SYNCSW	INT	
	PRI	
	SEC	
UDC	UDC-{F,DCC}-{A,B}	
VCFAC	VCFAC[-{1-50}]-{2-7}-{1-2}	
VFAC	VFAC[-{1-50}]-{2-7}-{1-10}-1	
VCHAN	VCHAN[-{1-50}]-{2-7}-{1-2}	
VLINE	VLINE[-{1-50}]-{2-7}-{1-2}-{1}-{1-12}	

Table 26-1 ALL (continued)

AID	Pattern	
VT	VT1-{1,2,5,6}-1-{1-7}-{1-4}	VT1-{2}-{1-2}-{1}-{1-3}-{1-7}-{1-4}
	VT1-{1,2,5,6}-{1-3}-1-{1-7}-{1-4}	VT1-{2}-{1-3}-{1}-{1-7}-{1-4}
	VT1-{1,2,5,6}-{1-3}-{1-7}-{1-4}	VT1-{2}-{1}-{1-7}-{1-3}
	VT1-{1-4,14-17}-{1-8}-{1-3}-{1-7}-{1-4}	VT1-{3,4}-{1-2}-{1}-{1-12}-{1-7}-{1-4}
	VT1-{1-4}-1-{1-12}-{1-7}-{1-4}	VT1-{3,4}-{1-2}-{1}-{1-3}-{1-7}-{1-4}
	VT1-{1-4}-1-{1-48}-{1-7}-{1-4}	VT1-{3,4}-{1-2}-{1}-{1-48}-{1-7}-{1-4}
	VT1-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}	VT1-{5,6,12,13}-1-{1-192}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-12}-{1-7}-{1-4}	VT1-{5,6,12,13}-1-{1-48}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-48}-{1-7}-{1-4}	VT1-{5-6}-1-{1-7}-{1-2}
	VT1-{1-6,12-17}-1-{1-7}-{1-2}	VT1-{5-6}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-12}-1-{1-7}-{1-4}	VT2-{1-4,14-17}-{1-8}-{1-3}-{1-7}-{1-3}
	VT1-{1-6,12-17}-{1-24}-1-{1-7}-{1-4}	VT2-{1-6,12-17}-1-{1-12}-{1-7}-{1-3}
	VT1-{1-6,12-17}-{1-36}-1-{1-7}-{1-4}	VT2-{1-6,12-17}-1-{1-48}-{1-7}-{1-3}
	VT1-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-4}	VT2-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-3}
	VT1-{1-6,12-17}-{1-4}-{1-3}-{1-7}-{1-4}	VT2-{1-6,12-17}-{1-4}-{1-3}-{1-7}-{1-3}
	VT1-{1-6,12-17}-{1-6}-{1-7}-{1-4}	VT2-{5,6,12,13}-1-{1-192}-{1-7}-{1-3}
	VT1-{2}-{1-2}-{1}-{1-12}-{1-7}-{1-4}	VT2-{5,6,12,13}-1-{1-48}-{1-7}-{1-3}
WDMAN S	WDMSIDE-{UNKNOWN,A,B,C,D,E,F,G,H} WDMNODE	
WLEN	WLEN-{E,W}-{ADD,DROP,EXP}-{1530.33,1531.12,1531.90,1532.68,1534.25,1535.04,1535.82,1536.61,1538.19,1538.98,1539.77,1540.56,1542.14,1542.94,1543.73,1544.53,1546.12,1546.92,1547.72,1548.51,1550.12,1550.92,1551.72,1552.52,1554.13,1554.94,1555.75,1556.55,1558.17,1558.98,1559.79,1560.61}	

26.2 AidUnionId

Table 26-2 AidUnionId

AID	Pattern
Facility	ALL
	FAC-{5-6}-{1-3}
	ACHAN[-{1-50}]{2-7}{1}{1}
	FAC-{8,10}{1}
	AGGR[-{1-50}]{2-7}{1}{1}
	FSTE-{1,2,5,6}{0-7}
	CHAN[-{1-50}]{2-7}{2}
	FSTE-{1,2,5,6}{1-8}
	CHAN[-{1-50}]{2-7}{1-10}{1}
	FSTE-{1}{0-7}
	EC1-{1,2,5,6}{1-3}
	FSTE-{1}{1-8}
	EC1-{2}{1-3}
	OC12-{2}{1-2}{1}
	FAC-{1-4,11-14}-ALL
	OC12-{3,4}{1-2}{1}
	FAC-{1-4,11-14}{1-16}
	OC3-{2}{1-2}{1}
	FAC-{1-4,11-14}{1-4}
	OC3-{3,4}{1-2}{1}
	FAC-{1-4,11-14}{1-4}{1-4}{1}
	T1-{1,2,5,6}{1-28}
	FAC-{1-4,14-17}{1-8}
	T1-{1,2,5,6}{1-84}
	FAC-{1-4}-1
	T1-{2}{1-21}
	FAC-{1-4}{1-4}
	T3-{1,2,5,6}{1-3}
	FAC-{1-6,12-17}-1
	T3-{2}{1-3}
	FAC-{1-6,12-17}-ALL
	VFAC-{1,2,5,6}{0-1}
	FAC-{1-6,12-17}{0-11}
	VFAC-{1,2,5,6}{1-8}
	FAC-{1-6,12-17}{0-1}
	VFAC-{1-4,11-14}{1-4}{1-4}-1
	FAC-{1-6,12-17}{1-50,14,16,18,20,22,24,26,28,30,32,34,36}
	VFAC-{1-6,12-17}{0-1}
	VFAC-{1-6,12-17}{1,2}
	FAC-{1-6,12-17}{1-50,14,16,18,20,22,24}
	VFAC-{1-6,12-17}{1,2}{1,8}
	FAC-{1-6,12-17}{1-12}
	VFAC-{1}{0-1}
	FAC-{1-6,12-17}{1-4}
	VFAC-{1}{1-8}
	FAC-{1-6,12-17}{1-6}
	VFAC[-{1-50}]{2-7}{1-10}{1}
	FAC-{1-6,12-17}{1}
	VLINE[-{1-50}]{2-7}{1-2}{1}{1-12}
	FAC-{1-6}-ALL
	FAC-{5,6,12,13}{1}
	FAC-{5-6}{1-28}

Table 26-2 *AidUnionId (continued)*

AID	Pattern
STS	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}
	FAC-{1-6,12-17}-{1-4}
	STS-{1,2,5,6}-1
	STS-{1,2,5,6}-{1-3}
	STS-{1,2,5,6}-{1-3}-1
	STS-{1-4,11-14}-{1-16}-1
	STS-{1-4,11-14}-{1-16}-ALL
	STS-{1-4,11-14}-{1-16}-{1,13,25,37}
	STS-{1-4,11-14}-{1-16}-{1,25}

Table 26-2 AidUnionId (continued)

AID	Pattern
STS (continued)	STS-{1-4,11-14}-{1-16}-{1,4,7,10,-,46}
	STS-{1-4,11-14}-{1-4}-1
	STS-{1-4,11-14}-{1-4}-ALL
	STS-{1-4,11-14}-{1-4}-{1,13,25,37,-,181}
	STS-{1-4,11-14}-{1-4}-{1,25,49,73,-,169}
	STS-{1-4,11-14}-{1-4}-{1,4,7,10,-,190}
	STS-{1-4,11-14}-{1-4}-{1,49,97,145}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,13,25,37}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,25}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,13,16,25,28,37,40}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7,10,13,16,19,22,25,28,31,34,37,40,43,46}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-12}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-3}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-48}
	STS-{1-4,11-14}-{1-4}-{1-192}
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-ALL
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-{1,4,7,13,16,19,25,28,31,43}

Table 26-2 *AidUnionId (continued)*

AID	Pattern
STS (continued)	STS-{1-4,14-17}-{1-16}-{1-48}
	STS-{1-4,14-17}-{1-4}-1
	STS-{1-4,14-17}-{1-4}-ALL
	STS-{1-4,14-17}-{1-4}-{1,4,7,10}
	STS-{1-4,14-17}-{1-4}-{1,4,7}
	STS-{1-4,14-17}-{1-4}-{1-3}
	STS-{1-4,14-17}-{1-8}-1
	STS-{1-4,14-17}-{1-8}-ALL
	STS-{1-4,14-17}-{1-8}-{1-3}
	STS-{1-4}-1-1
	STS-{1-4}-1-ALL
	STS-{1-4}-1-{1,13,25,37}
	STS-{1-4}-1-{1,4,7,10,-,46}
	STS-{1-4}-1-{1,4,7,10}
	STS-{1-4}-1-{1,7,13,19,-,43}
	STS-{1-4}-1-{1,7}
	STS-{1-4}-1-{1-12}
	STS-{1-4}-1-{1-48}
	STS-{1-6,12-17}-1
	STS-{1-6,12-17}-1-1
	STS-{1-6,12-17}-1-ALL
	STS-{1-6,12-17}-1-{1,13,25,37}
	STS-{1-6,12-17}-1-{1,4,10,13,16,19,25,28,37,40}
	STS-{1-6,12-17}-1-{1,4,7,10,13,16,19,22,25}
	STS-{1-6,12-17}-1-{1,4,7,10-46}
	STS-{1-6,12-17}-1-{1,4,7,10}
	STS-{1-6,12-17}-1-{1,4,7,13,16,19,25,28,37,40,43}
	STS-{1-6,12-17}-1-{1,4,7}
	STS-{1-6,12-17}-1-{1,4}
	STS-{1-6,12-17}-1-{1-12}
	STS-{1-6,12-17}-1-{1-48}
	STS-{1-6,12-17}-ALL
	STS-{1-6,12-17}-{1-12}-1
	STS-{1-6,12-17}-{1-24}-1

Table 26-2 AidUnionId (continued)

AID	Pattern
STS (continued)	STS-{1-6,12-17}-{1-36}-1
	STS-{1-6,12-17}-{1-4}-1
	STS-{1-6,12-17}-{1-4}-ALL
	STS-{1-6,12-17}-{1-4}-{1,4,7,10-46}
	STS-{1-6,12-17}-{1-4}-{1,4,7}
	STS-{1-6,12-17}-{1-4}-{1,4}
	STS-{1-6,12-17}-{1-4}-{1-12}
	STS-{1-6,12-17}-{1-6}
	STS-{2}-{1-2}-{1}-{1,4,7,10}
	STS-{2}-{1-2}-{1}-{1,7}
	STS-{2}-{1-2}-{1}-{1-12}
	STS-{2}-{1-2}-{1}-{1-3}
	STS-{2}-{1-2}-{1}-{1}
	STS-{2}-{1-3}-{1}
	STS-{2}-{1}
	STS-{3,4}-{1,2}-1-{1-12}
	STS-{3,4}-{1,2}-1-{1-3}
	STS-{3,4}-{1,2}-1-{1-48}
	STS-{5,6,12,13}-1-1
	STS-{5,6,12,13}-1-{1,13,25,37-180}
	STS-{5,6,12,13}-1-{1,13,25,37}
	STS-{5,6,12,13}-1-{1,4,7,10,13,16,19,22,25}
	STS-{5,6,12,13}-1-{1,4,7,10-190}
	STS-{5,6,12,13}-1-{1,4,7,10-46}
	STS-{5,6,12,13}-1-{1,4,7,13,16,19,25,28,37,40,43}
	STS-{5,6,12,13}-1-{1,49,97,145}
	STS-{5,6,12,13}-1-{1-192}
	STS-{5,6,12,13}-1-{1-48}
	STS-{5,6}-1
	STS-{5,6}-{1-4}-1
	STS-{5-6}-ALL
	VFAC-{1-6,12-17}-{0-1}

Table 26-2 *AidUnionId (continued)*

AID	Pattern
VT	ALL
	VT1-{1,2,5,6}-1-{1-7}-{1-4}
	VT1-{1,2,5,6}-{1-3}-1-{1-7}-{1-4}
	VT1-{1,2,5,6}-{1-3}-{1-7}-{1-4}
	VT1-{1-4,14-17}-{1-8}-{1-3}-{1-7}-{1-4}
	VT1-{1-4}-1-{1-12}-{1-7}-{1-4}
	VT1-{1-4}-1-{1-48}-{1-7}-{1-4}
	VT1-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-12}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-48}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-7}-{1-2}
	VT1-{1-6,12-17}-{1-12}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-24}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-36}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-4}-{1-3}-{1-7}-{1-4}

Table 26-2 AidUnionId (continued)

AID	Pattern
VT (continued)	VT1-{1-6,12-17}-{1-6}-{1-7}-{1-4}
	VT1-{2}-{1-2}-{1}-{1-12}-{1-7}-{1-4}
	VT1-{2}-{1-2}-{1}-{1-3}-{1-7}-{1-4}
	VT1-{2}-{1-3}-{1}-{1-7}-{1-4}
	VT1-{2}-{1}-{1-7}-{1-3}
	VT1-{3,4}-{1-2}-{1}-{1-12}-{1-7}-{1-4}
	VT1-{3,4}-{1-2}-{1}-{1-3}-{1-7}-{1-4}
	VT1-{3,4}-{1-2}-{1}-{1-48}-{1-7}-{1-4}
	VT1-{5,6,12,13}-1-{1-192}-{1-7}-{1-4}
	VT1-{5,6,12,13}-1-{1-48}-{1-7}-{1-4}
	VT1-{5-6}-1-{1-7}-{1-2}
	VT1-{5-6}-1-{1-7}-{1-4}
	VT2-{1-4,14-17}-{1-8}-{1-3}-{1-7}-{1-3}
	VT2-{1-6,12-17}-1-{1-12}-{1-7}-{1-3}
	VT2-{1-6,12-17}-1-{1-48}-{1-7}-{1-3}
	VT2-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-3}
	VT2-{1-6,12-17}-{1-4}-{1-3}-{1-7}-{1-3}
	VT2-{5,6,12,13}-1-{1-192}-{1-7}-{1-3}
	VT2-{5,6,12,13}-1-{1-48}-{1-7}-{1-3}

26.3 AidUnionId1

Table 26-3 *AidUnionId1*

AID	Patterns
BLSR	ALL BLSR-RINGID

26.4 BAND

(Cisco ONS 15454 only) The BAND AID is used to access Optical Multiplex Section (OMS) layer of optical networking units.

AID for OCH interface of CXP port on 100G-LC-C card: ACHAN-[SLOT]-[PPM]-[PORT].

AID for fixed trunk on 100G-LC-C card: CHAN-[SLOT]-[PORT].

Table 26-4 *BAND*

Pattern	Description
ALL	All of the OMSs of the NE. The ALL AID is applicable for retrieve-only commands.
BAND[-{1-50}]-{1-6,12-17}-{1-4}-ALL	All the channels in a Band OADM (1Bn, 4Bn) unit.
BAND[-{1-50}]-{1-6,12-17}-{1-4}-{RX,TX}	The Receive/Transmit channels in a Band OADM (1Bn, 4Bn) unit.
BAND[-{1-50}]-{1-6,12-17}-{1}-ALL	All the channels in an Optical Multiplexer/Demultiplexer (4Ch) unit.
BAND[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}	The Receive/Transmit channels in an Optical Multiplexer/Demultiplexer (4Ch) unit.

26.5 BANDWL

(Cisco ONS 15454) Band wavelength. Identifies a wavelength channel included in any of the lower layer OMS facilities.

Table 26-5 *BANDWL*

Pattern	Description
BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-1530.33	Identifies an allocated wavelength channel included in any of the lower layer OMS facilities.
BANDWL-{1-6,12-17}-{1-32}-{RX,TX}-ALL	Identifies all of the allocated wavelength channels included in any of the lower layer OMS facilities.

Table 26-5 BANDWL (continued)

Pattern	Description
BANDWL-[[1-50]-][1-6,12-17]-[1-32]-{RX,TX,PT}-<WLEN>	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH (in the form of 15xx.yy)]
BANDWL-[[1-50]-][1-6,12-17]-[1-32]-{RX,TX,PT}-ALL	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH (in the form of 15xx.yy)]

26.6 BITS

AID for building integrated timing supply (BITS).

Table 26-6 BITS

Pattern	Description
ALL	The ALL AID is applicable to RTRV commands only (RTRV-BITS and RTRV-ALM/COND-BITS). The All AID is equivalent to BITS-ALL for these commands. For RTRV-ALM/COND-SYCN, the ALL AID translates to BITS-ALL, SYNC-BITS1, and SYNC-BITS2.
BITS-ALL	BITS AIDs of both BITS-1 and BITS-2 in the RTRV-BITS command.
BITS[<SHELFID>]-ALL	BITS shelf ID of both BITS-1 and BITS-2 in the RTRV-BITS command.
BITS[<SHELFID>]-{1,2}	Individual BITS AID.
SYNC[<SHELFID>]-BITS{1,2}	BITS-OUT AIDs of BITS-1 and BITS-2. These AIDs are applicable only in ED/RTRV-BITS commands and are used for setting and retrieving the BITS-OUT parameters.

26.7 BLSR

(ONS 15454) BLSR AIDs are used to access the specific BLSR of the NE.

Table 26-7 *BLSR*

Pattern	Description
ALL	All the BLSRs in the NE. The ALL AID is applicable for retrieve-only commands like RTRV-<MOD_RING> (BLSR).
BLSR-RINGID	RINGID is a string of up to six characters. Valid characters are [A–Z,0–9] (case insensitive).

26.8 CHANNEL

(Cisco ONS 15454 only) Accesses the optical channels (OCH) layer of optical networking units.

Table 26-8 *CHANNEL*

CHANNEL Values	Description
ALL	ALL OCHs of the NE. The ALL AID is applicable for retrieve-only commands.
ACHAN[-{1-50}]-{2-7}-{1}-{1}	Identifies the OCH Facility of Aggregate port of 100G-LC-C Card.
CHAN[-{1-50}]-{2-7}-{2}	Identifies the Fixed Trunk or the Coherent Trunk on 100G-LC-C Card.
CHAN[-{1-50}]-{1-6,12-17}-ALL	All the channels of an Optical Transponder/Muxponder. The format is CHAN-[SLOT]-ALL
CHAN[-{1-50}]-{1-6,12-17}-{1-32}-ALL	All the channels in an Optical Multiplexer/Demultiplexer 32WSS unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL.
CHAN[-{1-50}]-{1-6,12-17}-{1-32}-{RX,TX}	The Receive/Transmit channels in an Optical Multiplexer/Demultiplexer (32DMX, 32MUX) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-50}]-{1-6,12-17}-{1-32}-{RX,PT}	The Receive/Pass-through channels in an Optical 32WSS unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-50}]-{1-5,12-16}-{1-40}-ALL	All the channels in Optical Multiplexer/Demultiplexer/WSS (40Ch) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL.
LL	
CHAN[-{1-50}]-{1-5,12-16}-{1-40}-{RX,TX}	The receive/transmit channels in an Optical Multiplexer/Demultiplexer (40Ch) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-50}]-{1-5,12-16}-{1-40}-{RX,PT}	The Receive/Pass-through channels in an Optical WSS (40Ch) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].

Table 26-8 CHANNEL (continued)

CHANNEL Values	Description
CHAN[-{1-50}]-{1-6,12-17} }-{1-4}-ALL	All the Channels in an OADM (AD-1C-xx.x, AD-2C-xx.x, AD-4C-xx.x) unit or Optical Multiplexer/Demultiplexer (4MD-xx.x) units. The format is CHAN-[SHELF]-[SLOT]-[PORT]-ALL.
CHAN[-{1-50}]-{1-6,12-17} }-{1-4}- {RX,TX}	The Receive/Transmit Channels in an OADM (AD-1C-xx.x, AD-2C-xx.x, AD-4C-xx.x) unit or Optical Multiplexer/Demultiplexer (4MD-xx.x) unit. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[DIRECTION].
CHAN[-{1-50}]-{1-6,12-17} }-{2,3}	A single channel of an Optical Transponder/Muxponder. The TXP_MR_10G, TXP_MR_10E, and TXP_MR_2.5G cards use CHAN-[SHELF]-SLOT-2 for the one dense wavelength division multiplexing (DWDM) Facility. The TXPP_MR_2.5G card uses CHAN-[SHELF]-SLOT-{2,3} for the two DWDM Facilities. The format is CHAN-[SHELF]-[SLOT]-[PORT].
CHAN[-{1-50}]-{1-6,12-17} }-{5}	A single channel of an Optical Muxponder. The TXP_MR_10G and TXP_MR_10E cards use CHAN-[SHELF]-SLOT-5 for the one DWDM facility. The TXP_MR_10G uses the CHAN-[SHELF]-SLOT-5 for the one DWDM facility. The format is CHAN-[SHELF]-[SLOT]-[PORT].
CHAN[-{1-50}]-{1-6,12-17} }-{9,10}	A single channel of an Optical Muxponder. The MXP_MR_10DME_C and MXP_2.5G_10G cards use CHAN-[SHELF]-SLOT-9 for the one DWDM Facility. The MXPP_2.5G_10G card uses CHAN-[SHELF]-SLOT-{9,10} for the two DWDM Facilities. The format is CHAN-[SHELF]-[SLOT]-[PORT].
CHAN[-{1-50}]-{1-6,12-17} }-{3,4}-{1}	A single channel of an Optical Muxponder. The 10GE-XP uses CHAN-[SHELF]-SLOT-{3,4}-1 for the two DWDM facilities. The format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN[-{1-50}]-{1-6,12-17} }-{21,22}-{ 1}	A single channel of an Optical Muxponder. The GE-XP uses CHAN-[SHELF]-SLOT-{11,12}-1 for the two DWDM facilities. The format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN-[{1-50}]-{1-6,12-17} }-{1-32}- {RX,TX,PT}	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION].
CHAN-[{1-50}]-{1-6,12-17} }-{1-32}- ALL	OCH is the termination point of OCHNC connections and it is also used to report alarms and conditions eventually notified by the node connection anomalies. Because the end points of an OCHNC connection can be both at the OCH filter level and at the node LINE OTS single wavelength level, this logical termination is useful to represent univocally a wavelength termination independently by the physical port it is contained in. The format is CHAN-[SHELF]-[SLOT]-[PORT]-[PORTDIRECTION]-[WAVELENGTH] (in the form of 15xx.yy).

Table 26-8 CHANNEL (continued)

CHANNEL Values	Description
CHAN[-{1-50}]-{1-6,12-17}-{19}-{1}	A single channel of an ADM-10G card. ADM-10G uses CHAN-[SHELF]-SLOT-19 for one DWDM Facility. Format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN[-{1-50}]-{1-6,12-17}-{17,18}-{1}	The DWDM interlinks between two peer ADM-10G cards. ADM-10G uses CHAN-[SHELF]-SLOT-{17,18} for the two DWDM interlinks Facilities. Format is CHAN-[SHELF]-[SLOT]-[PPM]-[PORT].
CHAN[-{1-50}]-{1-6,12-17}-{19}-{1}	A single channel of an ADM-10G card. ADM-10G uses CHAN-[shelf]-slot-19 for the 1 DWDM Facility. Format is CHAN-[shelf]-[slot]-[ppm]-[port].
CHAN[-{1-50}]-{2-7}-{1-10}-{1}	Identifies OCH interface of SFP+ ports on 10x10G-LC and CFP ports on CFP-LC.
CHAN[-{1--0}]-{1-6,12-17}-{17,18}-{1}	The DWDM Interlinks between 2 peer ADM-10G cards. ADM-10G uses CHAN-[shelf]-slot-{17,18} for the 2 DWDM Interlinks Facilities. Where format is CHAN-[shelf]-[slot]-[ppm]-[port]. In Release 9.0 or later, Port Number 18 can be used as an optical channel in the single card mode, the AID and the format remain the same.
PCHAN-ALL	All the Channels in all the Passive patch panel module where format is PCHAN-ALL. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
PCHAN-{1-96}-ALL	All the Channels in a Passive patch panel module where format is PCHAN-[id]-ALL. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
PCHAN-{1-96}-{1-4}-ALL	All the TX and RX Channels in a port of 15216-FLD-xx-x, passive modules where format is PCHAN-[id]-[port]-ALL. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
PCHAN-{1-96}-{1-4}-{RX-TX}	All the TX and RX Channels in a port of 15216-FLD-xx-x, passive modules where format is PCHAN-[id]-[port]-[direction]. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
PCHAN-{1-96}-{1-40}-ALL	All the TX and RX Channels in a port of 15216-MD-ODD, 15216-MD-EVEN passive modules where format is PCHAN-[id]-[port]-ALL. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.

Table 26-8 CHANNEL (continued)

CHANNEL Values	Description
PCHAN-{1-96}-{1-40}-{RX,TX}	The Receive/Transmit Channels in a 15216-MD-ODD, 15216-MD-EVEN passive modules where format is PCHAN-[id]-[port]-[direction]. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
PCHAN-{1-96}-{1-40}-ALL	All the TX and RX Channels in a port of 15216-MD-ODD, 15216-MD-EVEN passive modules where format is PCHAN-[id]-[port]-ALL. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
PCHAN-{1-96}-{1-40}-{RX,TX}	The Receive/Transmit Channels in a 15216-MD-ODD, 15216-MD-EVEN passive modules where format is PCHAN-[id]-[port]-[direction]. This AID is applicable to OCH commands like ENT/DLT/RTRV-LNK, ENT/DLT/ED/RTRV-OCHNC, RTRV-OCH, ENT/ED/RTRV/OPR/RLS/DLT-CPS, SET/RTRV-HOP-CPS.
VCHAN[-{1-50}]{-2-7}{1-2}	Identifies the OCH Facility of the Backplane Virtual ports on 100G-LC-C card.

26.9 CHGRP

AID for the Channel Group (CHGRP). [Table 26-9](#) lists the CHGRP AIDs and their descriptions.

Table 26-9 CHGRP

Pattern	Description
ALL	The ALL aid is applicable for RTRV-only commands, basically the RTRV-rr type of commands. For example, RTRV-CHGRP with ALL aid returns all CHGRP ports on the GE_XP and 10GE_XP cards.
CHGRP-[[{1-50}]{-1-6,12-17}{-1-11}	Channel group AID on GE_XP and 10GE_XP cards. The format is CHGRP-[SHELF]-[SLOT]-[CHGRP]

26.10 COM

Common

Table 26-10 COM

Pattern	Description
COM	Common

26.11 CrossConnectId

AID for CXP port on 100G-LC-C: AGGR-[SLOT]-[APPM]-[PORT].

AID for fixed trunk on 100G-LC-C: CHAN-[SLOT]-[PORT] .

Table 26-11 *CrossConnect Id*

Pattern	Description
FACILITY	ALL FAC-{5-6}-{1-3}
	EC1-{1,2,5,6}-{1-3} FAC-{8,10}-{1}
	EC1-{2}-{1-3} FSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-ALL FSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,11-14}-{1-16} FSTE-{1}-{0-7}
	FAC-{1-4,11-14}-{1-4} FSTE-{1}-{1-8}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1} OC12-{2}-{1-2}-{1}
	FAC-{1-4,14-17}-{1-8} OC12-{3,4}-{1-2}-{1}
	FAC-{1-4}-1 OC3-{2}-{1-2}-{1}
	FAC-{1-4}-{1-4} OC3-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-1 T1-{1,2,5,6}-{1-28}
	FAC-{1-6,12-17}-ALL T1-{1,2,5,6}-{1-84}
	FAC-{1-6,12-17}-{0-11} T1-{2}-{1-21}
	FAC-{1-6,12-17}-{0-1} T3-{1,2,5,6}-{1-3}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36} T3-{2}-{1-3}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24} VFAC-{1,2,5,6}-{0-1}
	FAC-{1-6,12-17}-{1-12} VFAC-{1,2,5,6}-{1-8}
	FAC-{1-6,12-17}-{1-4} VFAC-{1-4,11-14}-{1-4}-{1-4}-1
	FAC-{1-6,12-17}-{1-4} VFAC-{1-6,12-17}-{0-1}
	FAC-{1-6,12-17}-{1-6} VFAC-{1-6,12-17}-{1,2}
	FAC-{1-6,12-17}-{1} VFAC-{1-6,12-17}-{1,2}-{1,8}
	FAC-{1-6}-ALL VFAC-{1}-{0-1}
	FAC-{5,6,12,13}-{1} VFAC-{1}-{1-8}
	FAC-{5-6}-{1-28}
STS	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}

Table 26-11 CrossConnect Id (continued)

Pattern	Description
STS (continued)	FAC-{1-6,12-17}-{1-4}
	STS-{1,2,5,6}-1
	STS-{1,2,5,6}-{1-3}
	STS-{1,2,5,6}-{1-3}-1
	STS-{1-4,11-14}-{1-16}-1
	STS-{1-4,11-14}-{1-16}-ALL
	STS-{1-4,11-14}-{1-16}-{1,13,25,37}
	STS-{1-4,11-14}-{1-16}-{1,25}
	STS-{1-4,11-14}-{1-16}-{1,4,7,10,-,46}
	STS-{1-4,11-14}-{1-4}-1
	STS-{1-4,11-14}-{1-4}-ALL
	STS-{1-4,11-14}-{1-4}-{1,13,25,37,-,181}
	STS-{1-4,11-14}-{1-4}-{1,25,49,73,-,169}
	STS-{1-4,11-14}-{1-4}-{1,4,7,10,-,190}
	STS-{1-4,11-14}-{1-4}-{1,49,97,145}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,13,25,37}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,25}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,13,16,25,28,37,40}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7,10,13,16,19,22,25,28,31,34,37,40,43,46}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-12}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-3}

Table 26-11 CrossConnect Id (continued)

Pattern	Description
STS (continued)	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-48}
	STS-{1-4,11-14}-{1-4}-{1-192}
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-ALL
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-{1,4,7,13,16,19,25,28,31,43}
	STS-{1-4,14-17}-{1-16}-{1-48}
	STS-{1-4,14-17}-{1-4}-1
	STS-{1-4,14-17}-{1-4}-ALL
	STS-{1-4,14-17}-{1-4}-{1,4,7,10}
	STS-{1-4,14-17}-{1-4}-{1,4,7}
	STS-{1-4,14-17}-{1-4}-{1-3}
	STS-{1-4,14-17}-{1-8}-1
	STS-{1-4,14-17}-{1-8}-ALL
	STS-{1-4,14-17}-{1-8}-{1-3}
	STS-{1-4}-1-1
	STS-{1-4}-1-ALL
	STS-{1-4}-1-{1,13,25,37}
	STS-{1-4}-1-{1,4,7,10,-,46}
	STS-{1-4}-1-{1,4,7,10}
	STS-{1-4}-1-{1,7,13,19,-,43}
	STS-{1-4}-1-{1,7}
	STS-{1-4}-1-{1-12}
	STS-{1-4}-1-{1-48}
	STS-{1-6,12-17}-1
	STS-{1-6,12-17}-1-1
	STS-{1-6,12-17}-1-ALL
	STS-{1-6,12-17}-1-{1,13,25,37}
	STS-{1-6,12-17}-1-{1,4,10,13,16,19,25,28,37,40}
	STS-{1-6,12-17}-1-{1,4,7,10,13,16,19,22,25}
	STS-{1-6,12-17}-1-{1,4,7,10-46}
	STS-{1-6,12-17}-1-{1,4,7,10}
	STS-{1-6,12-17}-1-{1,4,7,13,16,19,25,28,37,40,43}
	STS-{1-6,12-17}-1-{1,4,7}
	STS-{1-6,12-17}-1-{1,4}

Table 26-11 CrossConnect Id (continued)

Pattern	Description
STS (continued)	STS-{1-6,12-17}-1-{1-12}
	STS-{1-6,12-17}-1-{1-48}
	STS-{1-6,12-17}-ALL
	STS-{1-6,12-17}-{1-12}-1
	STS-{1-6,12-17}-{1-24}-1
	STS-{1-6,12-17}-{1-36}-1
	STS-{1-6,12-17}-{1-4}-1
	STS-{1-6,12-17}-{1-4}-ALL
	STS-{1-6,12-17}-{1-4}-{1,4,7,10-46}
	STS-{1-6,12-17}-{1-4}-{1,4,7}
	STS-{1-6,12-17}-{1-4}-{1,4}
	STS-{1-6,12-17}-{1-4}-{1-12}
	STS-{1-6,12-17}-{1-6}
	STS-{2}-{1-2}-{1}-{1,4,7,10}
	STS-{2}-{1-2}-{1}-{1,7}
	STS-{2}-{1-2}-{1}-{1-12}
	STS-{2}-{1-2}-{1}-{1-3}
	STS-{2}-{1-2}-{1}-{1}
	STS-{2}-{1-3}-{1}
	STS-{2}-{1}
	STS-{3,4}-{1,2}-1-{1-12}
	STS-{3,4}-{1,2}-1-{1-3}
	STS-{3,4}-{1,2}-1-{1-48}
	STS-{5,6,12,13}-1-1
	STS-{5,6,12,13}-1-{1,13,25,37-180}
	STS-{5,6,12,13}-1-{1,13,25,37}
	STS-{5,6,12,13}-1-{1,4,7,10,13,16,19,22,25}
	STS-{5,6,12,13}-1-{1,4,7,10-190}
	STS-{5,6,12,13}-1-{1,4,7,10-46}
	STS-{5,6,12,13}-1-{1,4,7,13,16,19,25,28,37,40,43}
	STS-{5,6,12,13}-1-{1,49,97,145}
	STS-{5,6,12,13}-1-{1-192}
	STS-{5,6,12,13}-1-{1-48}

Table 26-11 CrossConnect Id (continued)

Pattern	Description
STS (continued)	STS-{5,6}-1
	STS-{5,6}-{1-4}-1
	STS-{5-6}-ALL
	VFAC-{1-6,12-17}-{0-1}

26.12 CrossConnectId1

AID for CXP port on 100G-LC-C: AGGR[-{1-50}]-{2-7}-{1}-{1}.

AID for fixed trunk on 100G-LC-C: CHAN-[SLOT]-[PORT].

Table 26-12 CrossConnectId1

AID	Pattern
EQPT	ALL PPM[-{1-50}]-{1-4,11-14}-{1-4}-{1-4}
	AIP-ALL PPM[-{1-50}]-{1-6,12-17}-{1-4}
	AIP[-{1-50}] PPM[-{1-50}]-{1-6,12-17}-{1-8}
	BIC-ALL PWR-ALL
	BIC[-{1-50}]-ALL PWR[-{1-50}]-ALL
	BIC[-{1-50}]-{A,B} PWR[-{1-50}]-{A,B}
	BP-ALL SLOT-ALL
	BP[-{1-50}] SLOT[-{1-50}]-ALL
	FAN-ALL SLOT[-{1-50}]-{1-14}
	FAN[-{1-50}] SLOT[-{1-50}]-{1-17}
	PIM[-{1-50}]-{1-4,11-14}-ALL SLOT[-{1-50}]-{1-2}
	PIM[-{1-50}]-{1-4,11-14}-{1-4} SLOT[-{1-50}]-{1-4,11-14}
	PPM[-{1-50}]-1-{1,2} SLOT[-{1-50}]-{1-6,12-17}
	PPM[-{1-50}]-2-{1,2} SLOT[-{1-50}]-{1-8}
	PPM[-{1-50}]-{1-4,11-14}-{1-4}-ALL
VCM	VCM-{1,2,5,6}-{0-1}-{1-256}
	VCM-{1,2,5,6}-{1-8}-{1-256}
	VCM-{1-6,12-17}-{0-1}-ALL
	VCM-{1-6,12-17}-{0-1}-{1-256}
	VCM-{1-6,12-17}-{1-4}-ALL
	VCM-{1-6,12-17}-{1-4}-{1-256}

Table 26-12 CrossConnectId1 (continued)

AID	Pattern
FACILITY	ALL FAC-{5-6}-{1-3}
	EC1-{1,2,5,6}-{1-3} FAC-{8,10}-{1}
	EC1-{2}-{1-3} FSTE-{1,2,5,6}-{0-7}
	FAC-{1-4,11-14}-ALL FSTE-{1,2,5,6}-{1-8}
	FAC-{1-4,11-14}-{1-16} FSTE-{1}-{0-7}
	FAC-{1-4,11-14}-{1-4} FSTE-{1}-{1-8}
	FAC-{1-4,11-14}-{1-4}-{1-4}-{1} OC12-{2}-{1-2}-{1}
	FAC-{1-4,14-17}-{1-8} OC12-{3,4}-{1-2}-{1}
	FAC-{1-4}-1 OC3-{2}-{1-2}-{1}
	FAC-{1-4}-{1-4} OC3-{3,4}-{1-2}-{1}
	FAC-{1-6,12-17}-1 T1-{1,2,5,6}-{1-28}
	FAC-{1-6,12-17}-ALL T1-{1,2,5,6}-{1-84}
	FAC-{1-6,12-17}-{0-11} T1-{2}-{1-21}
	FAC-{1-6,12-17}-{0-1} T3-{1,2,5,6}-{1-3}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24,26,28,30,32,34,36} T3-{2}-{1-3}
	FAC-{1-6,12-17}-{1-12,14,16,18,20,22,24} VFAC-{1,2,5,6}-{0-1}
	FAC-{1-6,12-17}-{1-12} VFAC-{1,2,5,6}-{1-8}
	FAC-{1-6,12-17}-{1-4} VFAC-{1-4,11-14}-{1-4}-{1-4}-1
	FAC-{1-6,12-17}-{1-4} VFAC-{1-6,12-17}-{0-1}
	FAC-{1-6,12-17}-{1-6} VFAC-{1-6,12-17}-{1,2}
	FAC-{1-6,12-17}-{1} VFAC-{1-6,12-17}-{1,2}-{1,8}
	FAC-{1-6}-ALL VFAC-{1}-{0-1}
	FAC-{5,6,12,13}-{1} VFAC-{1}-{1-8}
	FAC-{5-6}-{1-28}

Table 26-12 CrossConnectId1 (continued)

AID	Pattern
STS	FAC-{1-4,11-14}-{1-4}-{1-4}-{1}
	FAC-{1-6,12-17}-{1-4}
	STS-{1,2,5,6}-1
	STS-{1,2,5,6}-{1-3}
	STS-{1,2,5,6}-{1-3}-1
	STS-{1-4,11-14}-{1-16}-1
	STS-{1-4,11-14}-{1-16}-ALL
	STS-{1-4,11-14}-{1-16}-{1,13,25,37}
	STS-{1-4,11-14}-{1-16}-{1,25}
	STS-{1-4,11-14}-{1-16}-{1,4,7,10,-,46}
	STS-{1-4,11-14}-{1-4}-1
	STS-{1-4,11-14}-{1-4}-ALL
	STS-{1-4,11-14}-{1-4}-{1,13,25,37,-,181}
	STS-{1-4,11-14}-{1-4}-{1,25,49,73,-,169}
	STS-{1-4,11-14}-{1-4}-{1,4,7,10,-,190}
	STS-{1-4,11-14}-{1-4}-{1,49,97,145}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,13,25,37}

Table 26-12 CrossConnectId1 (continued)

AID	Pattern
STS (continued)	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,25}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,13,16,25,28,37,40}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7,10,13,16,19,22,25,28,31,34,37,40,43,46}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-12}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-3}
	STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-48}
	STS-{1-4,11-14}-{1-4}-{1-192}
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-ALL
	STS-{1-4,11-14}-{1-4}-{1-4}-{1}-{1,4,7,13,16,19,25,28,31,43}
	STS-{1-4,14-17}-{1-16}-{1-48}
	STS-{1-4,14-17}-{1-4}-1
	STS-{1-4,14-17}-{1-4}-ALL
	STS-{1-4,14-17}-{1-4}-{1,4,7,10}
	STS-{1-4,14-17}-{1-4}-{1,4,7}
	STS-{1-4,14-17}-{1-4}-{1-3}
	STS-{1-4,14-17}-{1-8}-1
	STS-{1-4,14-17}-{1-8}-ALL
	STS-{1-4,14-17}-{1-8}-{1-3}
	STS-{1-4}-1-1
	STS-{1-4}-1-ALL
	STS-{1-4}-1-{1,13,25,37}
	STS-{1-4}-1-{1,4,7,10,-,46}
	STS-{1-4}-1-{1,4,7,10}
	STS-{1-4}-1-{1,7,13,19,-,43}
	STS-{1-4}-1-{1,7}
	STS-{1-4}-1-{1-12}
	STS-{1-4}-1-{1-48}
	STS-{1-6,12-17}-1
	STS-{1-6,12-17}-1-1
	STS-{1-6,12-17}-1-ALL
	STS-{1-6,12-17}-1-{1,13,25,37}
	STS-{1-6,12-17}-1-{1,4,10,13,16,19,25,28,37,40}
	STS-{1-6,12-17}-1-{1,4,7,10,13,16,19,22,25}

Table 26-12 CrossConnectId1 (continued)

AID	Pattern
STS (continued)	STS-{1-6,12-17}-1-{1,4,7,10-46}
	STS-{1-6,12-17}-1-{1,4,7,10}
	STS-{1-6,12-17}-1-{1,4,7,13,16,19,25,28,37,40,43}
	STS-{1-6,12-17}-1-{1,4,7}
	STS-{1-6,12-17}-1-{1,4}
	STS-{1-6,12-17}-1-{1-12}
	STS-{1-6,12-17}-1-{1-48}
	STS-{1-6,12-17}-ALL
	STS-{1-6,12-17}-{1-12}-1
	STS-{1-6,12-17}-{1-24}-1
	STS-{1-6,12-17}-{1-36}-1
	STS-{1-6,12-17}-{1-4}-1
	STS-{1-6,12-17}-{1-4}-ALL
	STS-{1-6,12-17}-{1-4}-{1,4,7,10-46}
	STS-{1-6,12-17}-{1-4}-{1,4,7}
	STS-{1-6,12-17}-{1-4}-{1,4}
	STS-{1-6,12-17}-{1-4}-{1-12}
	STS-{1-6,12-17}-{1-6}
	STS-{2}-{1-2}-{1}-{1,4,7,10}
	STS-{2}-{1-2}-{1}-{1,7}
	STS-{2}-{1-2}-{1}-{1-12}
	STS-{2}-{1-2}-{1}-{1-3}
	STS-{2}-{1-2}-{1}-{1}
	STS-{2}-{1-3}-{1}
	STS-{2}-{1}
	STS-{3,4}-{1,2}-1-{1-12}
	STS-{3,4}-{1,2}-1-{1-3}
	STS-{3,4}-{1,2}-1-{1-48}
	STS-{5,6,12,13}-1-1
	STS-{5,6,12,13}-1-{1,13,25,37-180}
	STS-{5,6,12,13}-1-{1,13,25,37}
	STS-{5,6,12,13}-1-{1,4,7,10,13,16,19,22,25}
	STS-{5,6,12,13}-1-{1,4,7,10-190}
	STS-{5,6,12,13}-1-{1,4,7,10-46}
	STS-{5,6,12,13}-1-{1,4,7,13,16,19,25,28,37,40,43}

Table 26-12 *CrossConnectId1 (continued)*

AID	Pattern
STS (continued)	STS-{5,6,12,13}-1-{1,49,97,145}
	STS-{5,6,12,13}-1-{1-192}
	STS-{5,6,12,13}-1-{1-48}
	STS-{5,6}-1
	STS-{5,6}-{1-4}-1
	STS-{5-6}-ALL
	STS[-{1-50}]-{1-5,12-16}-{1-17,19} }-{1}-{1-48}
	VFAC-{1-6,12-17}-{0-1}

Table 26-12 CrossConnectId1 (continued)

AID	Pattern
VT	ALL
	VT1-{1,2,5,6}-1-{1-7}-{1-4}
	VT1-{1,2,5,6}-{1-3}-1-{1-7}-{1-4}
	VT1-{1,2,5,6}-{1-3}-{1-7}-{1-4}
	VT1-{1-4,14-17}-{1-8}-{1-3}-{1-7}
	}-{1-4}
	VT1-{1-4}-1-{1-12}-{1-7}-{1-4}
	VT1-{1-4}-1-{1-48}-{1-7}-{1-4}
	VT1-{1-4}-{1-4}-{1-3}-{1-7}-{1-4}
	}
	VT1-{1-6,12-17}-1-{1-12}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-48}-{1-7}-{1-4}
	VT1-{1-6,12-17}-1-{1-7}-{1-2}
	VT1-{1-6,12-17}-{1-12}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-24}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-36}-1-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-4}
	VT1-{1-6,12-17}-{1-4}-{1-3}-{1-7}
	}-{1-4}
	VT1-{1-6,12-17}-{1-6}-{1-7}-{1-4}
	}
	VT1-{2}-{1-2}-{1}-{1-12}-{1-7}-{1-4}
	VT1-{2}-{1-2}-{1}-{1-3}-{1-7}-{1-4}
	VT1-{2}-{1-3}-{1}-{1-7}-{1-4}
	VT1-{2}-{1}-{1-7}-{1-3}

Table 26-12 CrossConnectId1 (continued)

AID	Pattern
VT (continued)	VT1-{3,4}-{1-2}-{1}-{1-12}-{1-7} -{1-4}
	VT1-{3,4}-{1-2}-{1}-{1-3}-{1-7}- {1-4}
	VT1-{3,4}-{1-2}-{1}-{1-48}-{1-7} -{1-4}
	VT1-{5,6,12,13}-1-{1-192}-{1-7}- {1-4}
	VT1-{5,6,12,13}-1-{1-48}-{1-7}-{ 1-4}

26.13 DS1

(Cisco ONS 15454 only) Used to access the DS-1 frame layer of the DS3XM. The format is DS1-[SLOT]-[DS3PORT]-[DS1PORT].

Table 26-13 DS1

Pattern	Description
ALL	The ALL AID applies to RTRV-DS1 and RTRV-ALM/COND-DS1 commands only to retrieve all DS1 facilities and DS1-level alarms/conditions on the NE.
DS1-{1-6,12-17}-{1-12,13,15,17,19,21,23,25,27,29,31,33,35}-{1-28}	DS1 AIDs for the DS3XM-12 card STS48 backplane rate. The format is DS1-[SLOT]-[DS3PORT]-[DS1PORT]. Ports 1–12 are always available and odd ports after 12 are available. Note The DS3XM-12 even DS3Ports (after 12) are valid DS3XM-12 DS1 AIDs, which are applied on DS3XM-12 TCA autonomous events (REPT EVT DS1) only. Note The DS3XM-12 even DS3Ports (after 12) are not valid DS3XM-12 DS1 AIDs for all DS3XM-12 DS1 provisioning/retrieval non-TCA event commands.
DS1-{1-6,12-17}-{1-12,13,15,17,19,21,23}-{1-28}	DS1 AIDs for DS3XM-12 card STS48 backplane rate. The format is DS1-[SLOT]-[DS3PORT]-[DS1PORT]. Ports 1–12 are always available and odd ports after 12 are available. Note The DS3XM-12 even DS3Ports (after 12) are valid DS3XM-12 DS1 AIDs, which are applied on DS3XM-12 TCA autonomous events (REPT EVT DS1) only. Note The DS3XM-12 even DS3Ports (after 12) are not valid DS3XM-12 DS1 AIDs for all DS3XM-12 DS1 provisioning/retrieval non-TCA event commands.
DS1-{1-6,12-17}-{1-6}-{1-28}	DS1 AIDs for DS3XM-6 cards. Format is DS1-[SLOT]-[DS3PORT]-[DS1PORT].

26.14 ENV

The environmental AID for the AIC-I cards. “IN” is used for environmental alarms. “OUT” is used for environmental controls.

Table 26-14 ENV

Pattern	Description
ALL	The ALL AID applies to retrieve-only commands: RTRV-ALM/COND-ENV, RTRV-ATTR-CONT, and RTRV-ATTR-ENV.
ENV-IN-ALL	Environmental AID for ALL environmental alarms on the Cisco ONS 15454.
ENV-IN[-{1-50}]-ALL	Environmental AID for ALL multishelf environmental alarms.
ENV-IN[-{1-50}]-{1-4}	(ONS 15454 only) Environmental alarm AID for the AIC-I card on the ONS 15454.
ENV-IN[-{1-50}]-{1-16}	(ONS 15454 only) Environmental alarm AID on the ONS 15454.
ENV-IN[-{1-50}]-{1-20}	(ONS 15454 only) Environmental alarm AID for the AIC-I card on the ONS 15454.
ENV-IN[-{1-50}]-{1-32}	(ONS 15454) Environmental alarm AID for AIC-I card extensions on the ONS 15454.
ENV-{IN,OUT}[-{1-50}]-{1-16}	ENV-IN-{1-16} is used for environmental alarm AIDs. ENV-OUT-{1-16} is used for external control AIDs.
ENV-OUT-ALL	All environmental control output contacts.
ENV-OUT[-{1-50}]-ALL	Environmental AID for AIC-I cards. “OUT” is used for environmental controls
ENV-OUT[-{1-50}]-{1-4}	(ONS 15454 only) Environmental control AID for the AIC-I cards on the ONS 15454.
ENV-OUT[-{1-50}]-{1-16}	(ONS 15454 only) Environmental control AID for AIC-I card extensions on the ONS 15454.

26.15 EQPT

Equipment AIDs are used to access specific cards.

In the ONS 15454, the OC-48/OC-192 cards are only used in Slots 5, 6, 12, and 13.

AID for CXP port on 100G-LC-C and 10x10G-LC: APPM-[SLOT]-[PPM]-[PORT]

¹
Table 26-15 EQPT

Pattern	Description
AIP[-{1-50}]	(ONS 15454 only) The AID for the alarm interface panel (AIP). It is used for RTRV-INV output only.
AIP-ALL	(ONS 15454 only) The AID for all of the AIPs in any shelf of the node. It is used for RTRV-INV output only.
ALL	Only used for RTRV-INV, RTRV-EQPT, and RTRV-ALM/COND-EQPT commands. RTRV-INV returns all the inventory information for the NE. The ONS 15454 includes the multiservice cards, common control cards, and the AIP, BP, and FAN.
BIC-ALL	AIDs for the backplane interface connectors (BICs), BIC-A and BIC-B. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BIC[-{1-50}]-ALL	AIDs for the backplane interface connectors (BICs), BIC-A and BIC-B of a single shelf in a multishelf node. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BIC[-{1-50}]-{A,B}	AIDs for the BICs. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
BP[-{1-50}]	(ONS 15454) The AID for the backplane. It is used for RTRV-INV output only.
BP-ALL	(ONS 15454) The AID for all of the backplanes in any shelf on the multishelf node. It is used for RTRV-INV output only.
FAN[-{1-50}]	The AID for the fan tray. It is used for RTRV-INV, RTRV-ALM, RTRV-COND, RTRV-ALM-EQPT, and RTRV-COND-EQPT output only.
FAN-ALL	The AID for all of the fan tray in all the shelves of a multishelf node. It is used for RTRV-INV, RTRV-ALM, RTRV-COND, RTRV-ALM-EQPT, and RTRV-COND-EQPT output only.
PPM-{1-6,12-17}-{1-4}	Pluggable Port Module for MRC-2.5G-4. Format is PPM-[SLOT]-[PPM]. It is not applied on CHG-EQPT, and RMV/RST/ED-EQPT commands.
PPM-{1-6,12-17}-{1-12}	Pluggable Port Module for MRC-2.5G-12. Format is PPM-[SLOT]-[PPM]. It is not applied on CHG-EQPT, and RMV/RST/ED-EQPT commands.
PPM[-{1-50}]-{1-4,11-14}-{1-4}-ALL	PPM AID. Applicable for the RTRV-EQPT and RTRV-INV commands pertaining to the ASAP card. Format is PPM-[SLOT]-[PIM]-[PPM].
PPM[-{1-50}]-{1-4,11-14}-{1-4}-{1-4}	PPM AID. Applicable for the ENT/ED/RTRV/DLT-EQPT and RTRV-INV commands pertaining to the ASAP card. Format is PPM-[SLOT]-[PIM]-[PPM].
PPM-{1-6,12-17}-1	PPM for the OC192-XFP card. Format is PPM-[SLOT]-[PPM].
PPM-{1-6,12-17}-{1-12}	PPM for the MRC-12 card. Format is PPM-[SLOT]-[PPM].
PPM[-{1-50}]-{1-6,12-17}-{1-4}	PPM AID for dense wavelength division multiplexing (DWDM) MXP_2.5G_10G, TXP_MR_10G, TXP_MR_2.5G, TXPP_MR_2.5G, MXP_2.5G_10E, and TXP_MR_10E cards. Format of AID is PPM-[SLOT]-[PPM].
PPM[-{1-50}]-{1-6,12-17}-{1-8}	(ONS 15454 only) PPM AID for the MXP_MR_2.5G and MXPP_MR_2.5G cards. Format of AID is PPM-[SLOT]-[PPM].

Table 26-15 EQPT (continued)

Pattern	Description
PPM[-{1-50}]-{1-6,12-17}-{1-22}	PPM AID for XP-GE Cards. Format of AID is PPM-[SHELF]-[SLOT]-[PPM]
PUNIT-ALL	The whole NE Passive patch panel equipment AIDs. These AIDs are applicable to EQPT commands like ENT/DLT/RTRV/ED-EQPT commands.
PUNIT-{1-96}	Individual equipment AID of the Passive patch panel units in a 15454 where format is PUNIT-[id]. These AIDs are applicable to EQPT commands like ENT/DLT/RTRV/ED-EQPT commands.
PWR-ALL	AIDs for the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
PWR[-{1-50}]-{A,B}	AIDs for the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
PWR[-{1-50}]-ALL	AIDs for all of the power supply sources. These AIDs are valid only for the RTRV-ALM-EQPT and RTRV-COND-EQPT commands.
SLOT-ALL	All of the NE equipment AIDs.
SLOT[-{1-50}]-{1-17}	(ONS 15454 only) EQPT AID where the format is SLOT-[SLOT].
SLOT[-{1-50}]-{1-6,12-17}	(ONS 15454 only) Individual equipment AID of the multiservice card units or slots where the format is SLOT-[SLOT].
SPPM[-{36-55}]-{1-44}	PPM for the CPT 50 panel GIGE ports card. The format is SPPM-[PTSA]-[PPM].
SFOPPM[-{36-55}]-{45-48}	PPM for the CPT 50 panel 10GIGE ports card. The format is SFOPPM-[PTSA]-[PPM].
PTSYS[-{1}]-ALL	PT System AID for the CPT system. The format is PTSYS-{PTSYSID}.
FOG[-{1}]-{36-55}-ALL	Fan-Out-Group AID for connecting the CPT 50 panel to fabric and line cards. The format is FOG-{PTSYSID}-{FOGID}.

26.16 ETH

Ethernet (ETH) AIDs are used to access the ethernet port. [Table 26-9](#) lists the ETH AIDs and their descriptions.

Table 26-16 ETH

Pattern	Description
ETH-{1,2,5,6}-{1-6}	Format is ETH-slot-port. Port numbering is 1-based.

Table 26-16 ETH

Pattern	Description
ETH[-{1-50}]-{1-6,12-17}-{1-22}-{1}	Ethernet AID for ethernet port on GE_XP and 10GE_XP cards. The format is ETH-[SHELF]-[SLOT]-[PPM]-[PORT].
ETH[-{1-50}]-{1-6,12-17}-{1-22}-{1}-{1-4096}	Ethernet CEPID AID for ethernet port CEP on GE_XP and 10GE_XP cards. The format is ETH-[SHELF]-[SLOT]-[PPM]-[PORT]-[CEPID], where [CEPID] is the connection end point identifier, that identifies a single 1 Gbit/sec circuit inside an ethernet port.

26.17 FACILITY

(Cisco ONS 15454, ONS 15454 M2, and ONS 15454 M6) Facility AIDs are used to access specific ports.

ONS 15454 Facility AID format:

- Format for optical and EC1 facilities without PPM: FAC-[SLOT]-[PORT]
- Format for optical facilities with PPM: FAC-[SLOT]-[PPM]-[PORT]
- Format for optical facilities with PPM and PIM: FAC-[SLOT]-[PIM]-[PPM]-[PORT]
- Format for DS1-flavored electrical facilities: FAC-[SLOT]-[PORT]
- Format for DS3-flavored (including DS3i) electrical facilities: FAC-[SLOT]-[PORT]
- Format for packet-over-SONET (POS) ports: VFAC-[SLOT]-[PORT]
- Format for POS ports with PIMs and PPMs: VFAC-[SLOT]-[PIM]-[PPM]-[PORT]
- Format for 100G-LC-C, 10x10G-LC, and CFP-LC cards:
VLINE-[SLOT]-[PPM]-[PORT]-[SUBLANE-NO] Sublane Aid for all sublanes
- Format for CXP port on 100G-LC-C card: AGGR-[SLOT]-[APPM]-[PORT].
- Format for SFP+ ports on for 10x10G-LC and CFP-LC cards: VFAC-[SLOT]-[PPM]-[PORT]

Table 26-17 FACILITY

Pattern	Description
ALL	The ALL AID is applicable for RTRV-only commands (RTRV-rr type of commands), for example, RTRV-OC48 with the ALL AID returns all OC48 facilities on the node. RTRV-T1 with the ALL AID returns all T1 facilities on the node.
AGGR[-{1-50}]-{2-7}-{1}-{1}	Facility AID for CXP port on 100G-LC-C card.
ETH[-{1-50}]-{1-5,12-16}-{1-22}-1	Facility AID for GE_XP card. Format is FAC-[SHELF]-[SLOT]-[PPM]-[PORT]
ETH[-{1-50}]-{1-6,12-17}-{1-4}-1	Facility AID for 10GE_XP card. Format is FAC-[SHELF]-[SLOT]-[PPM]-[PORT]
FAC-{1-6,12-17}-{1-10}	(ONS 15454) Facility AID for CE-MR-10 (454) cards that can plug into Slots 1-6 and 12-17 and there are 10 Facility ports, where format is FAC-[SLOT]-[PORT]

Table 26-17 FACILITY (continued)

Pattern	Description
FAC[-{1-50}]{1-4,11-14}{1-4}{1-4}{1-4}{1}	Facility AID for the ASAP card with PIM and PPM. The format is FAC-[SLOT]-[PIM]-[PPM]-[PORT].
FAC[-{1-50}]{1-4,14-17}{1-8}	(ONS 15454 only) Facility AID for an OC3-8 card, where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]{1-5,12-16}{1-20}-1	Facility AID for GE-XP card, where format is: FAC-[SHELF]-[SLOT]-[PPM]-[PORT]
FAC[-{1-50}]{1-6,12-17}{1-2}-1	Facility AID for 10GE-XP card, where format is: FAC-[SHELF]-[SLOT]-[PPM]-[PORT]
FAC[-{1-50}]{1-6,12-17}-1	(ONS 15454 only) Facility AID for the 1 Client (CLNT) port on a TXP_MR_10G, TXP_MR_2.5G, TXP_MR_2.5G, or TXPP_MR_2.5G card, where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]{1-6,12-17}-ALL	(ONS 15454 only) All the facilities of an multiservice unit or slot, where the format is FAC-[SLOT]-[ALL].
FAC[-{1-50}]{1-6,12-17}{0-11}	(ONS 15454 only) Facility AID for the Ethernet front-end ports on the ML-100T-12 card. Ports are numbered starting with 0. The first port is FAC-SLOT-0, the second port is FAC-SLOT-1, and so on. The last port is FAC-SLOT-11. The format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]{1-6,12-17}{0-1}	(ONS 15454 only) Facility AID for the Ethernet back-end ports on the ML1000-2 card. Ports are numbered starting with 0. The first port is FAC-SLOT-0 and the second port is FAC-SLOT-1. The format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}{1-8}	Facility AID for the Ethernet front-end ports on the CE-100T-8 card The format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}{0-11}	Facility AID for the Ethernet front-end ports on the MS-ISC-100T The first port is FAC-SLOT-0, the second port is FAC-SLOT-1, and so on. The last port is FAC-SLOT-11. The format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]{1-6,12-17}{1-12,14,16,18,20,22,24,26,28,30,32,34,36}	Facility AID for the DS3XM-12 STS48 backplane rate, where the format is FAC-[SLOT]-[PORT]. Ports 1 through 12 are always available, but only even ports greater than 12 are available.
FAC[-{1-50}]{1-6,12-17}{1-12,14,16,18,20,22,24}	Facility AID for DS3XM-12 STS12 backplane, rate where the format is FAC-[SLOT]-[PORT]. Ports 1 through 12 are always available, but only even ports greater than 12 are available.
FAC[-{1-50}]{1-6,12-17}{1-12}	(ONS 15454 only) Facilities AID for the EC1 and DS3 cards, where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]{1-6,12-17}{1-4}	(ONS 15454 only) Facility AID for the four Client (CLNT) facilities on the MXP_2.5G_10G card. Facility AID for 4-port G1000/FC_MR-4 card. Facility AID for creating/editing cross-connects (STS1/VC3, STS3C/VC4, STS6C/VC4-2C, STS9C/VC4-3C, STS12C/VC4-4C, and STS24C/VC4-8C) for the 4-port G1000/FC_MR-4 card, where the format is FAC-[SLOT]-[PORT].

Table 26-17 FACILITY (continued)

Pattern	Description
FAC[-{1-50}]-{1-6,12-17}-{1-6}	(ONS 15454 only) Facility AID for the DS3XM card, where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]-{1-6,12-17}-{1}	(ONS 15454 only) Facility AID for a single-port OC12, OC48AS and OC3 in OSC-CSM cards. Facility AID for the client ports on the muxponder (MXP) and transponder (TXP) cards, where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]-{5,6,12,13}-{1}	(ONS 15454 only) Facility AID for the OC48/OC192 cards. The OC48/OC192 cards can only use Slots 5, 6, 12, and 13), where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]-{8,10}-{1}	(ONS 15454 only) Facility AID for the OSCM card. The OSCM cards can use only the XC slots (Slot 8, Slot 10), where the format is FAC-[SLOT]-[PORT].
FAC[-{1-50}]-{1-6,12-17}-1-1	Facility AID for the TXP_MR_2.5G, TXPP_MR_2.5G, TXP_MR_10E, TXP_MR_10EX and TXPP_MR_10EX cards in the format of:FAC-[shelf]-[slot]-[ppm]-[port].
VFAC-{1-6,12-17}-{1-10}	(ONS 15454) Virtual Facility AID for CE-MR-10 cards that can plug into Slots 1-6 and 12-17 and there are 10 Virtual Facility ports. Format is VFAC-[SLOT]-[PORT]
VFAC-{1}-{1-6}	Virtual Facility AID for CE-MR-6 (310CL) cards that can plug into Slot 1 and there are 6 Virtual Facility ports. Format is VFAC-[SLOT]-[PORT]
VFAC[-{1-50}]-{1-5,12-16}-{21-22}-1	Facility AID for the trunk "higher layer" of GE-XP card in the format of:VFAC-[SHELF]-[SLOT]-[PPM]-[PORT]
VFAC[-{1-50}]-{1-6,12-17}-{3-4}-1	Facility AID for the trunk "higher layer" of 10GE-XP card in the format of:VFAC-[SHELF]-[SLOT]-[PPM]-[PORT]
VFAC[-{1-50}]-{1-6,12-17}-{0-1}	(ONS 15454 only) Facility AID for the back-end POS ports on the ML-Series cards. Port numbering is 0-based (first POS port is VFAC-SLOT-0, second POS port is VFAC-SLOT-1). VC4, VC4-2C, VC4-3C, VC4-4C, and VC4-8C for the ML1000 and ML100T cards. Format is VFAC-[SLOT]-[PORT].
VFAC[-{1-50}]-{1-6,12-17}-{1,2}	Generic framing procedure (GFP) facility AIDS on the MXP-MR-2.5G and MXPP-MR-2.5G cards.
VFAC[-{1-50}]-{1-6,12-17}-{1,2}-{1,8}	GFP client facility AIDS for MXP-MR-2.5G and MXPP-MR-2.5G cards.
FAC[-{1-50}]-{1-4,14-17}-{1-4}	Facility AID for four Ether Ports on the CE1000 card.
VFAC[-{1-50}]-{1-4,14-17}-{1-4}	Facility AID for the back-end ports on the CE1000 card.
VFAC[-{1-50}]-{2-7}-{1-10}-{1}	Facility AID for SFP+ ports on 10x10G-LC and CFP ports on Fumaiole cards.
VLINE[-{1-50}]-{2-7}-{1-2}-{1}-{1-12}	Facility AID for sublanes on 10x10G-LC card only.

Table 26-17 *FACILITY (continued)*

Pattern	Description
SFAC[-{36-55}]-{1-44}-1	GIGE facilities on the CPT 50 panel. The format is SFAC-[PTSAID]-[PPM]-[PORT].
SFOFAC[-{36-55}]-{45-48}-1	10GIGE facilities on the CPT 50 panel. The format is SFOFAC-[PTSAID]-[PPM]-[PORT].

26.18 IPADDR

IP Address

Table 26-18 *IPADDR*

Pattern	Description
111.222.333.444	Standard 4-part IP address notation
ALL	ALL

26.19 LINE

(Cisco ONS 15454 only) The LINE AID is used to access the Optical Transport Section (OTS) layer of optical network units. Applicable only to AD-1B-xx.x, AD-4B-xx.x, AD-1C-xx.x, AD-2C-xx.x, AB-4C-xx.x, OSC-CSM, OSCM, OPT-BST, OPT-PRE, 4MD-xx.x, 32MUX-O, 32DMX-O, 40-SMR1-C, 40-SMR2-C, and 80-WXC-C cards.

The format is LINE-[SLOT]-[PORT]-[DIRECTION].

Table 26-19 *LINE*

Values	Description
ALL	All of the OTSs of the NE. The ALL AID applies for retrieve-only commands.
LINE-{1-6,12-17}-{1-2}-ALL	All the lines in an OPT-PRE, OCS-CSM, AD-1B, AD-4B, AD-1C, AD-2C, or AD-4C unit.
LINE-{1-6,12-17}-{1-2}-{RX,TX}	The receive/transmit lines in an OPT-PRE, OCS-CSM, AD-1B, AD-4B, AD-1C, AD-2C, or AD-4C unit.
LINE-{1-6,12-17}-{1-3}-{RX,TX}	The receive/transmit lines in an OPT-BST unit.
LINE-{8,10}-{1}-ALL	All the lines in OSCM units.
LINE-{8,10}-{1}-{RX,TX}	The receive/transmit lines in OSCM units.

Table 26-19 LINE (continued)

Values	Description
LINE-[[1-50]-][1-6,12-17]-{1}- {RX,TX}	For Booster units, there is an input OTS for LINE(1) and an output OTS (Amplified) for amplification stage COM(2), 2 OTS for input line COM(2) and output LINE(1) and 2 OTS for OSC(3) Add & Drop service channel.
LINE-[[1-50]-][1-6,12-17]-{2}- {RX,TX}	
LINE-[[1-50]-][1-6,12-17]-{3}- {RX,TX}	
LINE-[[1-50]-][1-6,12-17]-{1-3}-ALL	
LINE-[[1-50]-][1-6,12-17]-{1}- {RX,TX}	Pre-Amplifier unit with 2 stages of amplification; input OTS for LINE(1) and an output OTS (Amplified) for amplification stage COM(2), 2 OTS for input line COM(2) and output LINE(1) and 2 OTS for OSC(3) Add & Drop service channel and output and an input OTS for the DCU(2).
LINE-[[1-50]-][1-6,12-17]-{2}- {RX,TX}	
LINE-[[1-50]-][1-6,12-17]-{3}- {RX,TX}	
LINE-[[1-50]-][1-6,12-17]-{4}- {RX,TX}	
LINE-[[1-50]-][1-6,12-17]-{1-4}-ALL	
LINE-[[1-50]-][1-6,12-17]-1-RX For input OTS	For demux units there is an OTS for input line, and up to 32 OCH for drop channel connectors. Demux is a unidirectional unit.
LINE-[[1-50]-][1-6,12-17]-1-ALL	
CHAN-[[1-50]-][1-6,12-17]-{1-32}-TXFor Drop OCH	
CHAN-[[1-50]-][1-6,12-17]-{1-32}- ALL	

Table 26-19 *LINE (continued)*

Values	Description
LINE-[[1-50]-]{1-5,12-16}-{1}- {RX,TX} (EXP)	Wavelength switch selector unit has input and output OTS for signal coming from amplifier units COM(2), input and output OTS for signal continuing to next WSS unit in the node EXP(1) and an output PT(3) drop port for the signal continuing to 32-DMX-L card. It also has 32 internal OCH pass-through channels (PT) and 32 external input channels (ADD). This unit is 2 slots sized.
LINE-[[1-50]-]{1-5,12-16}-{2}- {RX,TX} (COM)	
LINE-[[1-50]-]{1-5,12-16}-{3}-{TX} (DROP)	
LINE-[[1-50]-]{1-5,12-16}-{1-3}-ALL	
CHAN-[[1-50]-]{1-5,12-16}-{1-32}- {RX} (ADD)	
CHAN-[[1-50]-]{1-5,12-16}-{1-32}- {PT} (PT)	
CHAN-[[1-50]-]{1-5,12-16}-{1-32}- ALL	
LINE-[[1-50]-]{1-6,12-17}-{1}- {RX,TX} (EXP)	Multiring/mesh unit has 6 OTS connectors: 2 are the input and output OTS for signal coming from amplifier units COM(1), the other are for the split 50/50 signal continuing to AD, MUX/DEMUX, WSS unit. Because the incoming signal is split into 2 separate signals, there are two sets of input and output EXP port (2 and 3).
LINE-[[1-50]-]{1-6,12-17}-{2}- {RX,TX} (COM)	
LINE-[[1-50]-]{1-6,12-17}-{3}- {RX,TX} (EXP to other ring)	
LINE-[[1-50]-]{1-6,12-17}-{1-3}-ALL	
LINE-[[1-50]-]{1-6,12-17}-{4}-ALL	All the receive and transmit COM lines in a 40-SMR2-C card.
LINE-[[1-50]-]{1-6,12-17}-{4}-{RX,TX}	The receive/transmit COM lines in a 40-SMR2-C card.
LINE-[[1-50]-]{1-6,12-17}-{2}-ALL	All the receive and transmit OSC lines in a 40-SMR2-C card.
LINE-[[1-50]-]{1-6,12-17}-{2}-{RX,TX}	The receive/transmit OSC lines in a 40-SMR2-C card.
LINE-[[1-50]-]{1-6,12-17}-{1}-ALL	All the receive and transmit DC lines in a 40-SMR2-C card.
LINE-[[1-50]-]{1-6,12-17}-{1}-{RX,TX}	The receive/transmit DC lines in a 40-SMR2-C card.

Table 26-19 *LINE (continued)*

Values	Description
LINE[-{1-50}]-{1-6,12-17}-{3}-ALL	All the ADD-RX and DROP-TX lines in a 40-SMR2-C card.
LINE[-{1-50}]-{1-6,12-17}-{3}-{RX,TX}	The ADD-RX/DROP-TX lines in a 40-SMR2-C card.
LINE[-{1-50}]-{1-6,12-17}-{5}-TX	The transmit EXP-TX line in a 40-SMR2-C card.
LINE[-{1-50}]-{1-6,12-17}-{6-8}-RX	The receive EXP-RX lines in a 40-SMR2-C card.
LINE[-{1-50}]-{1-6,12-17}-{5}-ALL	All the receive and transmit COM lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{5}-{RX,TX}	The receive/transmit COM lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{3}-ALL	All the receive and transmit OSC lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{3}-{RX,TX}	The receive/transmit OSC lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{2}-ALL	All the receive and transmit DC lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{2}-{RX,TX}	The receive/transmit DC lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{4}-ALL	All the ADD-RX and DROP-TX lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{4}-{RX,TX}	The ADD-RX/DROP-TX lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{1}-ALL	All the receive and transmit EXP lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}	The receive/transmit EXP lines in a 40-SMR1-C card.
LINE[-{1-50}]-{1-5,12-16}-ALL	All the OTSs lines in a 80-WXC-C cards.
LINE[-{1-50}]-{1-5,12-16}-{10}	The receive or transmit COM line in a 80-WXC-C card.
LINE[-{1-50}]-{1-5,12-16}-{9}	The receive or transmit AD line in a 80-WXC-C card.
LINE[-{1-50}]-{1-5,12-16}-{1-8}	The receive or transmit EAD line in a 80-WXC-C card.
LINE[-{1-50}]-{1-5,12-16}-{11}-ALL	All the DROP-TX lines in a 80-WXC-C card.
LINE[-{1-50}]-{1-5,12-16}-{11}-{TX}	The transmit DROP-TX line in a 80-WXC-C card.
LINE[-{1-50}]-{1-5,12-16}-{12}-ALL	All the EXP-TX lines in a 80-WXC-C card.

Table 26-19 *LINE (continued)*

Values	Description
LINE[-{1-50}]{1-5,12-16}{12}{TX}	The transmit EXP-TX line in a 80-WXC-C card.
LINE[-{1-50}]{1-5,12-16}{13}-ALL	All the COM-RX lines in a 80-WXC-C card.
LINE[-{1-50}]{1-5,12-16}{13}{RX}	The receive COM-RX line in a 80-WXC-C card.
PLINE-ALL	All the EXP/COMM/DC lines in all the Passive patch panel module where format is PLINE-ALL.
PLINE-{1-96}-ALL	All the EXP/COM/DC lines in a Passive patch panel module where format is PLINE-[id]-ALL.
PLINE-{1-96}{1}-ALL	All the IN and OUT DC/COM lines in DCU,15216-MD-40-ODD/EVEN passive modules where format is PLINE-[id]-[port]-ALL
PLINE-{1-96}{1-3}-ALL	All the IN and OUT COM lines in 15216-MD-ID-50 passive module where format is PLINE-[id]-[port]-ALL.
PLINE-{1-96}{1-20}-ALL	All the IN COM and OUT EXP lines in PP-MESH-4 passive module where format is PLINE-[id]-[port]-ALL.
PLINE-{1-96}{1-72}-ALL	All the IN COM and OUT EXP lines in PP-MESH-8 passive module where format is PLINE-[id]-[port]-ALL.
PLINE-{1-96}{1}{RX-TX}	The Receive or Transmit DC/COM lines in DCU,15216-MD-40-ODD/EVEN passive modules where format is PLINE-[id]-[port]-[direction]
PLINE-{1-96}{1-3}{RX-TX}	The Recieve or Transmit COM line in a 15216-MD-ID-50 passive module where format is PLINE-[id]-[port]-[direction].
PLINE-{1-96}{1,6,11,16}-RX	The Recieve COM lines in a PP-MESH-4 passive module where format is PLINE-[id]-[port]-[direction].
PLINE-{1-96}{2-5,7-10,12-15,17-20}-TX	The Transmit EXP lines in a PP-MESH-4 passive module where format is PLINE-[id]-[port]-[direction].
PLINE-{1-96}{1,10,19,28,37,46,55,64}-RX	The Recieve COM line in a PP-MESH-8, passive modules where format is PLINE-[id]-[port]-[direction].
PLINE-{1-96}{2-9,11-18,20-27,29-36,38-45,47-54,56-63,65-72}-TX	The Transmit EXP line in a PP-MESH-8, passive modules where format is PLINE-[id]-[port]-[direction].
PLINE-{1-96}{1}{RX-TX}	The Recieve or Transmit COM line in a 15216-FLD-xx-x, passive modules where format is PLINE-[id]-[port]-[direction].
PLINE-{1-96}{2}{RX-TX}	The Recieve or Transmit EXP line in a 15216-FLD-xx-x, passive modules where format is PLINE-[id]-[port]-[direction].

26.20 LINEWL

Line wavelength. Identifies a wavelength channel included in any of the lower layer OTS facilities.

Table 26-20 LINEWL

Values	Description
LINEWL[-{1-50}]{1-6,8,10,12-17}-ALL	All the Optical Channels representing single wavelength inside OTS facility of a card configured in specified slot. The format is LINE-[SHELF]-[SLOT]-ALL
LINEWL[-{1-50}]{1-6,12-17}{1}-{RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS facility in a 32-DMX-O, 32-DMX, 32-DMX-L, 32-MUX-O, 40-DMX-C, 40-MUX-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-50}]{1-6,12-17}{1}-{RX,TX}<wlen>	The Optical Channel representing single wavelength inside OTS facility in a 32-DMX-O, 32-DMX, 32-DMX-L, 32-MUX-O, 40-DMX-C, 40-MUX-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-50}]{1-6,12-17}{1-3}{RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS facility in a 32-WSS, 40-WSS-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-50}]{1-6,12-17}{1-3}{RX,TX}<WLEN>	The Optical Channel representing single wavelength inside OTS facility in a 32-WSS, 40-WSS-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-50}]{1-6,12-17}{1-2}{RX,TX}-ALL	All the Optical Channel representing single wavelength inside OTS facility in a AD-1B, AD-4B, AD-1C, AD-2C, AD-4C units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL
LINEWL[-{1-50}]{1-6,12-17}{1-2}{RX,TX}<WLEN>	The Optical Channel representing single wavelength inside OTS facility in a AD-1B, AD-4B, AD-1C, AD-2C, AD-4C units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN]
LINEWL[-{1-50}]{1-6,12-17}{1-2}{RX,TX}-ALL	All the Optical Channel representing single wavelength inside OTS (COM=1, DC=2) facility in an OPT-PRE unit. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL
LINEWL[-{1-50}]{1-6,12-17}{1-2}{RX,TX}<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, DC=2) facility in a OPT-PRE units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]<WLEN>
LINEWL[-{1-50}]{1-6,12-17}{1,3}{RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C, and OPT-AMP-23-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-50}]{1-6,12-17}{1,3}{RX,TX}<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C, OPT-AMP-23-C, and OPT-AMP-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-50}]{1-6,12-17}{1,3,4}{RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3, DC=4) facility in a OPT-AMP-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.

Table 26-20 *LINEWL (continued)*

Values	Description
LINEWL[-{1-50}]-{1-6,12-17}-{1,3,4}-{RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3, DC=4) facility in a OPT-AMP-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}-ALL	All the Optical Channel representing single wavelength inside OTS (COM) facility in a OPT-PRE units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL
LINEWL[-{1-50}]-{1-6,12-17}-{1}-{RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM) facility in a OPT-PRE units. The format is LINE-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN]
LINEWL[-{1-50}]-{1-6,12-17}-{1,3}-{RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C and OPT-AMP-23-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-50}]-{1-6,12-17}-{1,3}-{RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=3) facility in a OPT-BST, OPT-BST-E, OPT-BST-L, OPT-AMP-L, OPT-AMP-17-C and OPT-AMP-23-C cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL[-{1-50}]-{1-6,12-17}-{1-2}-{RX,TX}-ALL	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=2) facility in a OSC-CSM cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-ALL.
LINEWL[-{1-50}]-{1-6,12-17}-{1-2}-{RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside OTS (COM=1, LINE=2) facility in a OSC-CSM cards. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN]-[WLEN].
LINEWL- [{1-50}]-{1-5,12-16}-{1-8}-{RX}-<WLEN>	The Optical Channel representing single wavelength inside EXP lines from all the other WXC cards in a 40-WX-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL- [{1-50}]-{1-5,12-16}-{9}-{RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside ADD/DROP line to/from MUX/DMX/WSS cards in a 40-WXC-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL- [{1-50}]-{1-5,12-16}-{10}-{TX}-<WLEN>	The Optical Channel representing single wavelength inside EXP line in a 40-WXC-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL- [{1-50}]-{1-5,12-16}-{11}-{RX,TX}-<WLEN>	The Optical Channel representing single wavelength inside COM line in a 40-WXC-C card. The format is LINEWL-[SHELF]-[SLOT]-[PORT]-[DIRN].
LINEWL[-{1-50}]-{1-6,12-17}-{4}-{TX}-ALL	All the Optical Channel representing single wavelength inside COM line TX in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL

Table 26-20 *LINEWL (continued)*

Values	Description
LINEWL[-{1-50}]-{1-6,12-17}-{4} -{TX}-<wlen>	The Optical Channel representing single wavelength inside COM line TX in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>
LINEWL[-{1-50}]-{1-6,12-17}-{3} -{RX,TX}-ALL	All the Optical Channel representing single wavelength inside ADD-RX/DROP-TX lines in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL
LINEWL[-{1-50}]-{1-6,12-17}-{3} -{RX,TX}-<wlen>	The Optical Channel representing single wavelength inside ADD-RX/DROP-TX line in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>.
LINEWL[-{1-50}]-{1-6,12-17}-{5} -{TX}-ALL	All the Optical Channel representing single wavelength inside EXP-TX line in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL.
LINEWL[-{1-50}]-{1-6,12-17}-{5} -{TX}-<wlen>	The Optical Channel representing single wavelength inside EXP-TX line in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>
LINEWL[-{1-50}]-{1-6,12-17}-{6-8}-{RX}-ALL	All the Optical Channel representing single wavelength inside EXP-RX lines in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL
LINEWL[-{1-50}]-{1-6,12-17}-{6-8}-{RX}-<wlen>	The Optical Channel representing single wavelength inside EXP-RX lines in a 40-SMR2-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>
LINEWL[-{1-50}]-{1-6,12-17}-{5} -{TX}-ALL	All the Optical Channel representing single wavelength inside COM line TX in a 40-SMR1-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL
LINEWL[-{1-50}]-{1-6,12-17}-{5} -{TX}-<wlen>	The Optical Channel representing single wavelength inside COM line TX in a 40-SMR1-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>
LINEWL[-{1-50}]-{1-6,12-17}-{4} -{RX,TX}-ALL	All the Optical Channel representing single wavelength inside ADD-RX/DROP-TX lines in a 40-SMR1-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL
LINEWL[-{1-50}]-{1-6,12-17}-{4} -{RX,TX}-<wlen>	The Optical Channel representing single wavelength inside ADD-RX/DROP-TX line in a 40-SMR1-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>
LINEWL[-{1-50}]-{1-6,12-17}-{1} -{RX,TX}-ALL	All the Optical Channel representing single wavelength inside EXP lines in a 40-SMR1-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL
LINEWL[-{1-50}]-{1-6,12-17}-{1} -{RX,TX}-<wlen>	The Optical Channel representing single wavelength inside EXP line in a 40-SMR1-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>
LINEWL[-{1-50}]-{1-5,12-16}-ALL	All the Optical Channels representing single wavelength inside OTS facility in a 80-WXC-C card configured in specified slot. The format is LINEWL-[shelf]-[slot]-ALL

Table 26-20 *LINEWL (continued)*

Values	Description
LINEWL[-{1-50}]-{1-5,12-16}-{10}-ALL	All the Optical Channel representing single wavelength inside COM line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-ALL
LINEWL[-{1-50}]-{1-5,12-16}-{10}-<wlen>	The Optical Channel representing single wavelength inside COM line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-<wlen>
LINEWL[-{1-50}]-{1-5,12-16}-{9}-ALL	All the Optical Channel representing single wavelength inside ADD/DROP line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-ALL
LINEWL[-{1-50}]-{1-5,12-16}-{9}-<wlen>	The Optical Channel representing single wavelength inside ADD/DROP line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-<wlen>
LINEWL[-{1-50}]-{1-5,12-16}-{1-8}-ALL	All the Optical Channel representing single wavelength inside EAD line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-ALL
LINEWL[-{1-50}]-{1-5,12-16}-{1-8}-<wlen>	The Optical Channel representing single wavelength inside EAD line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-<wlen>
LINEWL[-{1-50}]-{1-5,12-16}-{11}-{TX}-ALL	All the Optical Channel representing single wavelength inside DROP-TX line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-ALL
LINEWL[-{1-50}]-{1-5,12-16}-{11}-{TX}-<wlen>	The Optical Channel representing single wavelength inside DROP-TX line in a 80-WXC-C cards. The format is LINEWL-[shelf]-[slot]-[port]-[dirn]-<wlen>

26.21 LNKTERM

Link termination AIDs are used to access the termination points of a provisionable patchcord.

Table 26-21 *LNKTERM*

Pattern	Description
ALL	Indicates all the provisionable patchcord terminations on a node. Applicable only for the retrieve commands.
LNKTERM-ALL	Indicates all the provisionable patchcord terminations on a node. Applicable only for the retrieve commands.
LNKTERM-{1-65535}	Indicates a single provisionable patchcord termination point on a node, where the format is LNKTERM-.

26.22 OPM

OPM AIDs represent the single wavelength inside an optical power monitoring object.

Table 26-22 OPM

Values	Description
ALL	The first ID represents the shelf, the second ID represents the slot, and the last ID is the wavelength, represented in the form of 15xx.yy nanometers. The last index of the wavelength is the value of the wavelength as described in OPTICAL_WLEN.
OPM[-{1-50}]-{1-5,12-16}-ALL	
OPM[-{1-50}]-{1-5,12-16}-<wlen>	

26.23 OSC

(Cisco ONS 15454 only) OSC AIDs are used to access the OSC of the NE.

Table 26-23 OSC

Values	Description
ALL	All of the OSCs of the NE. The ALL AID applies to the retrieve-only commands.
OSC-RINGID	RINGID is a string of up to six characters. Valid characters are [A–Z,0–9] (case insensitive).

26.24 PRSLOT

(Cisco ONS 15454 only) Valid protection slots for the electrical cards.

Table 26-24 PRSLOT

Pattern	Description
NULL	Indicates there is no protection group. Used when trying to delete a protection group.
SLOT-1	The No.1 slot of an NE.
SLOT-3	The No.3 slot of an NE.
SLOT-5	The No.5 slot of an NE.
SLOT-13	The No.13 slot of an NE.
SLOT-15	The No.15 slot of an NE.
SLOT-17	The No.17 slot of an NE.

26.25 RFILE

(ONS 15454) File transfer type.

Table 26-25 RFILE

Pattern	Description
RFILE-DB	Transferring the provisioning database.
RFILE-LOG	Transferring a log file.

Table 26-25 *RFILE*

RFILE-PKG	Transferring a software package.
RFILE-COMPDB	Transferring the provisioning and system database.

26.26 SHELF

(ONS 15454) Identifies a shelf within a node. SHELF is applicable only to nodes that are set to MULTISHELF or MULTISHELFETH mode.

Table 26-26 *SHELF*

Pattern	Description
SHELF-ALL	All of the shelves in the node.
SHELF-{1-50}	A specific shelf in the node (shelf 1 through 30).

26.27 STS

SONET frame-level AID set:

- STS AID format for optical and EC1 facilities without PPM: STS-[SLOT]-[PORT]-[STS]
- STS AID format for optical facilities with PPMs: STS-[SLOT]-[PPM]-[PORT]-[STS]
- STS AID format for optical facilities with PIMs and PPMs:
STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS]
- STS AID format for DS1 electrical facilities: STS-[SLOT]-[STS]
- STS AID format for DS3 (except DS3i) electrical facilities: STS-[SLOT]-[PORT]-[STS]
- STS AID format for DS3i electrical facilities: STS-[SLOT]-[STS]
- STS AID format for G1K-4 card Gigabit Ethernet (GIGE) facilities: FAC-[SLOT]-[PORT]

Table 26-27 *STS*

Pattern	Description
ALL	The ALL AID applies to the RTRV-only commands: RTRV-STS with the ALL AID retrieves all STS interfaces on the NE. RTRV-STS1 with ALL AID retrieves all STS1 interfaces on the NE. RTRV-STS3c with ALL AID retrieves all STS3c interfaces on the NE.
FAC-{1-4,11-14}-{1-4}-{1-4}-{1}	Dynamically allocated STSs of all widths for the GIGE port on an ASAP card. Format is FAC-[SLOT]-[PIM]-[PPM]-[PORT].
FAC-{1-6,12-17}-{1-4}	(ONS 15454 only) Dynamically allocated STSs of all widths for the G1K-4 card. Format is FAC-[SLOT]-[PORT]

Table 26-27 STS (continued)

Pattern	Description
STS-{1-4,11-14}-{1-4}-{1,4}-{1}	StS3c AID for the ASAP card with an OC3 port provisioned. STS12C AID for the ASAP card with an OC12 port provisioned. STS48C AID for the ASAP card with OC48 port provisioned. Format of AID is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,13,25,37}	STS12C AID for the ASAP card with an OC48 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,25}	STS24C AID for the ASAP card with an OC48 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,13,16,25,28,37,40}	STS9C AID for the ASAP card with an OC48 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7,10,13,16,19,22,25,28,31,34,37,40,43,46}	STS3C AID for the ASAP card with an OC48 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7}	STS6C AID for the ASAP card with an OC12 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4}	STS9C AID for the ASAP card with an OC12 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-12}	STS1 AID for the ASAP card with an OC12 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-3}	STS1 AID for the ASAP card with an OC3 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1-48}	STS1 AID for the ASAP card with an OC48 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-ALL	STS1, STS3C, STS6C, STS9C, STS12C, STS24C, and STS48C AID for the ASAP card with an OCN port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,11-14}-{1-4}-{1,4}-{1}-{1,4,7,13,16,19,25,28,31,43}	STS6C AID for the ASAP card with an OC48 port provisioned. Format is STS-[SLOT]-[PIM]-[PPM]-[PORT]-[STS].
STS-{1-4,14-17}-{1-4}-1	(ONS 15454 only) STS12C AIDs for a 4-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-4,14-17}-{1-4}-ALL	(ONS 15454 only) All the STSs for a given 4-port OC12 card. Format is STS-[SLOT]-[PORT]-ALL.
STS-{1-4,14-17}-{1-4}-{1,4,7,10}	(ONS 15454 only) STS3C for the 4-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-4,14-17}-{1-4}-{1,4,7}	(ONS 15454 only) STS6C AIDs for a 4-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].

Table 26-27 STS (continued)

Pattern	Description
STS-{1-4,14-17}-{1-4}-{1-3}	(ONS 15454 only) STS1 AID for the 4-port OC3 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-4,14-17}-{1-8}-1	(ONS 15454 only) STS3C for the 8-port OC3 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-4,14-17}-{1-8}-ALL	(ONS 15454 only) All the STSs for a given 8-port OC3 card. Format is STS-[SLOT]-[PORT]-ALL.
STS-{1-4,14-17}-{1-8}-{1-3}	(ONS 15454 only) STS1 AID for the 8-port OC3 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1	(ONS 15454 only) STS1 AID for a DS1 card. Format is STS-[SLOT]-[STS]. There is only 1 STS for the DS1 card.
STS-{1-6,12-17}-1-1	(ONS 15454 only) STS12C AID for a single-port OC12 card STS48C AID for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-ALL	(ONS 15454 only) All the STSs of an STS bandwidth on a single port optical card. Format is STS-[SLOT]-[PORT]-ALL.
STS-{1-6,12-17}-1-{1,13,25,37}	(ONS 15454 only) STS12C AIDs for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1,4,10,13,16,19,25,28,37,40}	(ONS 15454 only) STS9C AID for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1,4,7,10,13,16,19,22,25}	(ONS 15454 only) STS24C AID for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1,4,7,10-46}	(ONS 15454 only) STS3C AID for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1,4,7,10}	(ONS 15454 only) STS3C AID for a single-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1,4,7,13,16,19,25,28,37,40,43}	(ONS 15454 only) STS6C AID for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1,4,7}	(ONS 15454 only) STS6C AID for an OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1,4}	(ONS 15454 only) STS9C AID for a single-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1-12}	(ONS 15454 only) STS1 AID for a single-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-1-{1-48}	(ONS 15454 only) STS1 AID for an OC48AS card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-ALL	(ONS 15454 only) STS ALL AID for the card in the given slot. Format is STS-[SLOT]-[ALL].
STS-{1-6,12-17}-{1-12}-1	(ONS 15454 only) STS1 AID for EC1 and DS3 cards. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-24}-1	STS1 AIDs for the DS3XM-12 STS12 backplane rate cards. Format is STS-[SLOT]-[PORT]-[STS].

Table 26-27 STS (continued)

Pattern	Description
STS-{1-6,12-17}-{1-36}-1	STS1 AIDs for the DS3XM-12 STS48 backplane rate cards. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-4}-1	(ONS 15454 only) STS3C AID for a 4-port OC3 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-4}-ALL	(ONS 15454 only) All the STSs for a 4-port OC3 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-4}-{1,4,7,10-46}	Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-4}-{1,4,7}	(ONS 15454 only) STS6c AID for a 4-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-4}-{1,4}	(ONS 15454 only) STS9C AID for a 4-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-4}-{1-12}	(ONS 15454 only) STS1 AID for a 4-port OC12 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{1-6,12-17}-{1-6}	(ONS 15454 only) STS1 AID for a DS3XM card. Format is STS-[SLOT]-[STS].
STS-{2}-{1-2}-{1}-{1-3}	STS1 AID for the OC3 port. Format is STS-[SLOT]-[PPM]-[PORT]-[STS].
STS-{5,6,12,13}-1-1	(ONS 15454 only) STS48c AID for an OC48 card, or STS192 AID for an OC192 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,13,25,37-180}	(ONS 15454 only) STS12c AID for an OC192 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,13,25,37}	(ONS 15454 only) STS12c AIDs for an OC48 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,4,7,10,13,16,19,22,25}	(ONS 15454 only) STS24c AID for an OC48 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,4,7,10-190}	(ONS 15454 only) STS3c for an OC192 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,4,7,10-46}	(ONS 15454 only) STS3c AID for an OC48 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,4,7,13,16,19,25,28,37,40,43}	(ONS 15454 only) STS6c AID for an OC48 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1,49,97,145}	(ONS 15454 only) STS48c AID for an OC192 card. Format is STS-[SLOT]-[PORT]-[STS].
STS-{5,6,12,13}-1-{1-192}	(ONS 15454 only) STS1 AID for an OC192 card. Format is STS-[SLOT]-[PORT]-[STS].

Table 26-27 STS (continued)

Pattern	Description
STS-{5,6,12,13}-1-{1-48}	(ONS 15454 only) STS1 AID for an OC48 card. Format is STS-[SLOT]-[PORT]-[STS].
VFAC-{1-6,12-17}-{0-1}	(ONS 15454 only) Virtual facility AID for the ML-Series card back-end POS ports. Both the ML1000-2 and ML100T-12 have two POS ports. Port numbering starts at 0. Format is VFAC-[SLOT]-[PORT].

26.28 SYN

Synchronization AIDs

Table 26-28 SYN

Pattern	Description
SYNC[-{1-50}]-ALL	ALL synchronization AID (values 1 to 8 is the shelf ID).
SYNC[-{1-50}]-NE	NE synchronization AID (values 1 to 8 is the shelf ID).
SYNC[-{1-50}]-{BITS1,BITS2}	BITS synchronization AID (values 1 to 8 is the shelf ID).

26.29 SYN_SRC

Synchronization source

Table 26-29 SYN_SRC

Pattern	Description
BITS-1	(ONS 15454) Synchronization source is BITS-1. Format is BITS-[PORT].
BITS-2	(ONS 15454) Synchronization source is BITS-2. Format is BITS-[PORT].
FAC-{1-4,11-14}-{1-4}	Synchronization source is 4-port OC192. Format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}-{1-4}	(ONS 15454 only) Synchronization source is the optical card (four-port OC3 and four-port OC12) facility. Format is FAC-[SLOT]-[PORT].
FAC-{1-6,12-17}-{1}	(ONS 15454 only) Synchronization source is the optical card (one-port OC12 and OC48AS) facility. Format is FAC-[SLOT]-[PORT].
FAC-{5,6,12,13}-{1}	Synchronization source is the optical card (OC48, OC192) facility. Format is FAC-[SLOT]-[PORT].
INTERNAL	Set the SYN_SRC to be the system default value. The INTERNAL value of the SYN_SRC is only applied for the SYNC-NE AID on the ED-SYNCN command.

Table 26-29 *SYN_SRC (continued)*

Pattern	Description
NONE	Set the SYNC_SRC value to the default value for BITS-OUT. The NONE value of SYNC_SRC only applies to the BITS-1 and BITS-2 AID of the ED-SYNCN command.
SYNC-NE	SYNC-NE source. It is only used in the alarm report or alarm retrieve commands.

26.30 SYNC_REF

(Cisco ONS 15454, ONS 15454 M2, and ONS 15454 M6) Synchronization AIDs.

Table 26-30 *SYNC_REF*

Pattern	Description
ALL	Equivalent to a combination of SYNC-ALL, BITS-1 and BITS-2. This AID is valid only for the commands RTRV-SYNCN, RTRV-ALM-SYNCN, and RTRV-COND-SYNCN.
SYNC-ALL	All synchronization references.
SYNC-{1-50}-NE	NE synchronization AID.
SYNC-{1-50}-{BITS1, BITS2}	BITS1 and BITS2 synchronization AIDs.

26.31 SYNC_SW

New synchronization reference that will be used.

Table 26-31 *SYNC_SW*

Pattern	Description
INT	Internal clock. The INT value is only applied for the SYNC-NE AID on the OPR-SYNC_SW command.
PRI	Primary timing reference.
SEC	Secondary timing reference.

26.32 UDC

(ONS 15454) UDC AIDs for F-UDC and DCC-UDC channels on the AIC-I card.

Table 26-32 UDC

Pattern	Description
ALL	Applicable to RTRV-only commands, for example, RTRV-ALM/COND-UDCF and RTRV-ALM/COND-UDCDCC. Corresponds to a superset of F-UDC and DCC-UDC AIDs.
UDC-{F,DCC}-{A,B}	F-UDC and DCC-UDC AIDs for A and B channels. DCC-UDC is supported on the ONS 15454 only. F-UDC is supported on the ONS 15454.

26.33 VT

(ONS 15454) Virtual tributary.

- VT1 AID format for optical and EC1 facilities without PPM:
VT1-[SLOT]-[PORT]-[STS]-[VTG]-[VTN]
- VT1 AID format for optical facilities with PPM:
VT1-[SLOT]-[PPM]-[PORT]-[STS]-[VTG]-[VTN]
- VT1 AID format for DS1 electrical facilities: VT1-[SLOT]-[STS]-[VTG]-[VTN]
- VT1 AID format for DS3 electrical facilities: VT1-[SLOT]-[PORT]-[STS]-[VTG]-[VTN]

Table 26-33 VT

Pattern	Description
ALL	Applies to RTRV-only commands. For example, the RTRV-VT and RTRV-VT1 with ALL AIDs return all VT1 interfaces on the node.
VT1-{1-4,14-17}-{1-8}-{1-3}-{1-7}-{1-4}	(ONS 15454 only) 8-port OC3 card
VT1-{1-6,12-17}-1-{1-12}-{1-7}-{1-4}	(ONS 15454 only) Single-port OC12 card
VT1-{1-6,12-17}-1-{1-48}-{1-7}-{1-4}	(ONS 15454 only) OC48AS card
VT1-{1-6,12-17}-1-{1-7}-{1-2}	(ONS 15454 only) DS1 card
VT1-{1-6,12-17}-{1-12}-1-{1-7}-{1-4}	(ONS 15454 only) EC1 card
VT1-{1-6,12-17}-{1-24}-1-{1-7}-{1-4}	VT1.5 AIDs for DS3XM-12 STS12 backplane rate cards
VT1-{1-6,12-17}-{1-36}-1-{1-7}-{1-4}	VT1.5 AIDs for DS3XM-12 STS48 backplane rate cards
VT1-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-4}	(ONS 15454 only) 4-port OC12 card
VT1-{1-6,12-17}-{1-4}-{1-3}-{1-7}-{1-4}	(ONS 15454 only) 4-port OC3 card
VT1-{1-6,12-17}-{1-6}-{1-7}-{1-4}	(ONS 15454 only) DS3XM-6 card
VT1-{5,6,12,13}-1-{1-192}-{1-7}-{1-4}	(ONS 15454 only) OC192 card
VT1-{5,6,12,13}-1-{1-48}-{1-7}-{1-4}	(ONS 15454 only) OC48 card
VT2-{1-4,14-17}-{1-8}-{1-3}-{1-7}-{1-3}	(ONS 15454 only) Eight-port OC3 card
VT2-{1-6,12-17}-1-{1-12}-{1-7}-{1-3}	(ONS 15454 only) Single-port OC12 card
VT2-{1-6,12-17}-1-{1-48}-{1-7}-{1-3}	(ONS 15454 only) OC48AS card

Table 26-33 VT (continued)

Pattern	Description
VT2-{1-6,12-17}-{1-4}-{1-12}-{1-7}-{1-3}	(ONS 15454 only) Four-port OC12 card
VT2-{1-6,12-17}-{1-4}-{1-3}-{1-7}-{1-3}	(ONS 15454 only) Four-port OC3 card
VT2-{5,6,12,13}-1-{1-192}-{1-7}-{1-3}	(ONS 15454 only) OC192 card
VT2-{5,6,12,13}-1-{1-48}-{1-7}-{1-3}	(ONS 15454 only) OC48 card

26.34 WDMANS

(Cisco ONS 15454 only) This AID is used to access the Wavelength Division Multiplexing (WDM) automatic optical node setup (AONS) application of the NE.

Table 26-34 WDMANS

Pattern	Description
WDMSIDE-{UNKNOWN,A,B,C,D,E,F,G,H}	Automatic MSTP node setup identifier
WDMNODE	Automatic MSTP node setup identifier

26.35 WLEN

(Cisco ONS 15454 only) This AID represents the single wavelength inside an external facility. If the facility is of type OTS (line), the wavelengths contained are all the available in the node (currently 32). If the facility is of type OCH (CHAN), the wavelength is just one and it is the same of the correspondent wavelength customized for that channel.

Table 26-35 WLEN

Pattern	Description
WLEN-{E,W}-{ADD,DROP,EXP}-{1530.33,1531.12,1531.90,1532.68,1534.25,1535.04,1535.82,1536.61,1538.19,1538.98,1539.77,1540.56,1542.14,1542.94,1543.73,1544.53,1546.12,1546.92,,1547.72,1548.51,1550.12,1550.92,1551.72,1552.52,1554.13,1554.94,1555.75,1556.55,1558.17,1558.98,1559.79,1560.61,1577.44,1577.86,1578.27,1578.69,1579.10,1579.52,1579.93,1580.35,1580.77,1581.18,1581.60,1582.02,1582.44,1582.85,1583.27,1583.69,1584.11,1584.53,1584.95,1585.36,1585.78,1586.20,1586.62,1587.04,1587.46,1587.88,1588.30,1588.73,1589.15,1589.57,1589.99,1590.41,1590.83,1591.26,1591.68,1592.10,1592.52,1592.95,1593.37,1593.79}	Wavelength identifier

26.36 CTC Port Numbers and TL1 Aids

Table 26-36 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
OPT-RAMP-C	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
	DC-RX	7	LINE-shelf-slot-4-RX
	DC-TX	8	LINE-shelf-slot-4-TX
	RAMAN-RX	9	LINE-shelf-slot-5-RX
	RAMAN-TX	10	LINE-shelf-slot-5-TX
OPT-AMP-17C	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-AMP-C	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	7	LINE-shelf-slot-4-RX
	DC-TX	8	LINE-shelf-slot-4-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OSC-CSM	COM-RX	2	LINE-shelf-slot-1-RX
	COM-TX	3	LINE-shelf-slot-1-TX
	LINE-RX	4	LINE-shelf-slot-2-RX
	LINE-TX	5	LINE-shelf-slot-2-TX
	OSC-RX	6	LINE-shelf-slot-3-RX
	OSC-TX	7	LINE-shelf-slot-3-TX
	OC3	1	FAC-shelf-slot-1

Table 26-36 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
40 WXC	EXP-i-RX	1..8	LINE-shelf-slot-i-RX (i=1..8)
	EXP-TX	11	LINE-shelf-slot-10-TX
	ADD-RX	9	LINE-shelf-slot-9-RX
	DROP-TX	10	LINE-shelf-slot-9-TX
	COM-RX	12	LINE-shelf-slot-11-RX
	COM-TX	13	LINE-shelf-slot-11-TX
80-WXC-C	EAD-i-R/T	1..8	LINE-shelf-slot-i (i=1..8)
	ADD R/T	9	LINE-shelf-slot-9
	COM R/T	10	LINE-shelf-slot-10
	DROP-TX	11	LINE-shelf-slot-11-TX
	COM-RX	13	LINE-shelf-slot-13-RX
	EXP-TX	12	LINE-shelf-slot-12-TX
32 DMX	CHAN TX	1-32	CHAN-shelf-slot-i-TX (i=1..32)
	COM-RX	33	LINE-shelf-slot-1-RX
OPT-PRE	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	3	LINE-shelf-slot-2-RX
	DC-TX	4	LINE-shelf-slot-2-TX
40 DMX	CHAN TX	1 - 40	CHAN-shelf-slot-i-TX (i=1..40)
	COM RX	41	LINE-shelf-slot-1-RX
4 MD	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	CHAN-RX	5	CHAN-shelf-slot-3-RX
	CHAN-TX	6	CHAN-shelf-slot-3-TX
	CHAN-RX	7	CHAN-shelf-slot-4-RX
	CHAN-TX	8	CHAN-shelf-slot-4-TX
	COM-RX	9	LINE-shelf-slot-1-RX
	COM-TX	10	LINE-shelf-slot-1-TX
40 MUX	CHAN-RX	1-40	CHAN-shelf-slot-i-RX (i=1..40)
	COM-TX	41	LINE-shelf-slot-1-TX
32 DMX L	CHAN TX	1 - 32	CHAN-shelf-slot-i-TX (i=1..32)
	COM RX	33	LINE-shelf-slot-1-RX

Table 26-36 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
32 WSS L	ADD-RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	PT	33-64	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	69	LINE-shelf-slot-3-TX
	EXP-RX	66	LINE-shelf-slot-2-RX
	EXP-TX	65	LINE-shelf-slot-2-TX
	COM-RX	68	LINE-shelf-slot-1-RX
	COM-TX	67	LINE-shelf-slot-1-TX
OPT-BST	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-BST E	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-AMP L	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-1-TX
	DC-RX	7	LINE-shelf-slot-4-RX
	DC-TX	8	LINE-shelf-slot-4-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-3-RX
	LINE-TX	6	LINE-shelf-slot-3-TX
OPT-BST L	COM-RX	1	LINE-shelf-slot-1-RX
	COM-TX	2	LINE-shelf-slot-2-TX
	OSC-RX	3	LINE-shelf-slot-1-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	LINE-RX	5	LINE-shelf-slot-1-RX
	LINE-TX	6	LINE-shelf-slot-2-TX

Table 26-36 *CTC Port Numbers and TL1 Aids*

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
MMU	EXPA-RX	5	LINE-shelf-slot-3-RX
	EXPA-TX	6	LINE-shelf-slot-3-TX
	EXP-RX	1	LINE-shelf-slot-1-RX
	EXP-TX	2	LINE-shelf-slot-1-TX
	COM-RX	3	LINE-shelf-slot-2-RX
	COM-TX	4	LINE-shelf-slot-2-TX
PSM	W-RX	1	LINE-shelf-slot-1-RX
	W-TX	2	LINE-shelf-slot-1-TX
	P-RX	3	LINE-shelf-slot-2-RX
	P-TX	4	LINE-shelf-slot-2-TX
	COM-RX	5	LINE-shelf-slot-3-RX
	COM-TX	6	LINE-shelf-slot-3-TX
AD 1B	BAND-RX	1	BAND-shelf-slot-1-RX
	BAND-TX	2	BAND-shelf-slot-1-TX
	EXP-RX	3	LINE-shelf-slot-1-RX
	EXP-TX	4	LINE-shelf-slot-1-TX
	COM-RX	5	LINE-shelf-slot-2-RX
	COM-TX	6	LINE-shelf-slot-2-TX
AD 1C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	EXP-RX	3	LINE-shelf-slot-1-RX
	EXP-TX	4	LINE-shelf-slot-1-TX
	COM-RX	5	LINE-shelf-slot-2-RX
	COM-TX	6	LINE-shelf-slot-2-TX
AD 2C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	EXP-RX	5	LINE-shelf-slot-1-RX
	EXP-TX	6	LINE-shelf-slot-1-TX
	COM-RX	7	LINE-shelf-slot-2-RX
	COM-TX	8	LINE-shelf-slot-2-TX

Table 26-36 *CTC Port Numbers and TL1 Aids*

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
AD 4C	CHAN-RX	1	CHAN-shelf-slot-1-RX
	CHAN-TX	2	CHAN-shelf-slot-1-TX
	CHAN-RX	3	CHAN-shelf-slot-2-RX
	CHAN-TX	4	CHAN-shelf-slot-2-TX
	CHAN-RX	5	CHAN-shelf-slot-3-RX
	CHAN-TX	6	CHAN-shelf-slot-3-TX
	CHAN-RX	7	CHAN-shelf-slot-4-RX
	CHAN-TX	8	CHAN-shelf-slot-4-TX
	EXP-RX	9	LINE-shelf-slot-1-RX
	EXP-TX	10	LINE-shelf-slot-1-TX
	COM-RX	11	LINE-shelf-slot-2-RX
	COM-TX	12	LINE-shelf-slot-2-TX
32 WSS	ADD-RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	PT	33-64	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	69	LINE-shelf-slot-3-TX
	EXP-RX	66	LINE-shelf-slot-2-RX
	RXP-TX	65	LINE-shelf-slot-2-TX
	COM-RX	68	LINE-shelf-slot-1-RX
	COM-TX	67	LINE-shelf-slot-1-TX
40 WSS CO	ADD-RX	1-40	CHAN-shelf-slot-i-RX (i=1..32)
	PT	41-80	CHAN-shelf-slot-i-PT (i=1..32)
	DROP-TX	85	LINE-shelf-slot-3-TX
	EXP-RX	82	LINE-shelf-slot-2-RX
	RXP-TX	81	LINE-shelf-slot-2-TX
	COM-RX	84	LINE-shelf-slot-1-RX
	COM-TX	83	LINE-shelf-slot-1-TX
32 DMXO	CHAN-TX	1-32	CHAN-shelf-slot-i-TX (i=1..32)
	COM-RX	33	LINE-shelf-slot-1-RX
32 MUXO	CHAN RX	1-32	CHAN-shelf-slot-i-RX (i=1..32)
	COM TX	33	LINE-shelf-slot-1-TX

Table 26-36 CTC Port Numbers and TL1 Aids

Equipment	CTC Port Name	CTC Port Number	TL1 Aid
SMR-1	EXP-RX	1	LINE-shelf-slot-1-RX
	EXP-TX	2	LINE-shelf-slot-1-TX
	DC-RX	3	LINE-shelf-slot-2-RX
	DC-TX	4	LINE-shelf-slot-2-TX
	OSC-RX	5	LINE-shelf-slot-3-RX
	OSC-TX	6	LINE-shelf-slot-3-TX
	ADD-RX	7	LINE-shelf-slot-4-RX
	DROP-TX	8	LINE-shelf-slot-4-TX
	LINE-RX	9	LINE-shelf-slot-5-RX
	LINE_TX	10	LINE-shelf-slot-5-TX
SMR-2	DC-RX	1	LINE-shelf-slot-1-RX
	DC-TX	2	LINE-shelf-slot-1-TX
	OSC-RX	3	LINE-shelf-slot-2-RX
	OSC-TX	4	LINE-shelf-slot-2-TX
	ADD-RX	5	LINE-shelf-slot-3-RX
	DROP-TX	6	LINE-shelf-slot-3-TX
	LINE-RX	7	LINE-shelf-slot-4-RX
	LINE_TX	8	LINE-shelf-slot-4-TX
	EXP-TX-1	9	LINE-shelf-slot-5-TX
	EXP-RX-2	10	LINE-shelf-slot-6-RX
	EXP-RX-3	11	LINE-shelf-slot-7-RX
	EXP-RX-4	12	LINE-shelf-slot-8-RX
10x10G-LC	CXP [port 11]	CXP 11-2...11 or 11-2...11	VLINE-SL-1-1-i (i=2...11)
100G-LC-C	CXP [port 1]	CXP 1-2...11 or 1-2...11	VLINE-SL-1-1-i (i=2...11)
CFP-LC	CFP-1	CFP 1-1...10 or 1-1...10	VLINE-SL-1-1-i (i=1...10)
	CFP-2	CFP 2-1...10 or 2-1...10	VLINE-SL-2-1-i (i=1...10)