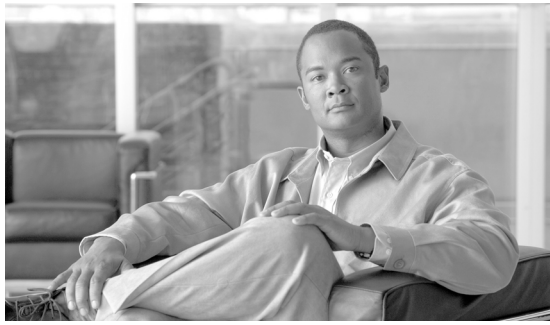




# CHAPTER 25

## TST Commands

This chapter provides test (TST) commands for the Cisco ONS 15454.

### 25.1 TST-INSERRBITS-<MOD2>

(CISCO ONS 15454) The test inserted error bits for DS1, T1, or T3 ( TST-INSERRBITS-<MOD2>) command tests the connectivity and error count on the traffic line, running on the DS1, T1, and T3 ports of the electrical card.

#### Usage Guidelines

Before issuing this command, bit error rate testing (BERT) must be enabled on the port and the SYNC STATUS of BERT on the port must be UP.



#### Note

This command is limited to DS1/E1-56 and DS3XM-12 electrical cards.

#### Category

Ports

#### Security

Provisioning

#### Input Format

TST-INSERRBITS-<MOD2>:[<TID>]:<AID>:<CTAG>:::BITERRS=<BITERRS>;

#### Input Example

TST-INSERRBITS-DS1::FAC-2-1:CTAG:::BITERRS=SINGLE;

#### Input Parameters

|           |                                                                                     |
|-----------|-------------------------------------------------------------------------------------|
| <AID>     | Access identifier from the <a href="#">“26.15 FACILITY” section on page 26-34</a> . |
| <BITERRS> | Specifies the error rate to be injected for BERT. The parameter type is BITERR.     |
| • STOP    | Stops inserting bit errors.                                                         |
| • SINGLE  | Inserts single bit errors.                                                          |

|        |                                      |
|--------|--------------------------------------|
| • 1E-3 | Inserts bit errors at the 1E-3 rate. |
| • 1E-4 | Inserts bit errors at the 1E-4 rate. |
| • 1E-5 | Inserts bit errors at the 1E-5 rate. |
| • 1E-6 | Inserts bit errors at the 1E-6 rate. |