

# NPE-200 Network Processing Engine for Cisco 7200 Series Routers

---

## Description

The NPE-200 network processing engine for Cisco 7200 series routers is now available. The network processing engine maintains and executes the system management functions for Cisco 7200 series routers. The network processing engine also shares the system memory and environmental monitoring function with the I/O controller. The NPE-200 consists of the following components:

- Reduced instruction set computing (RISC) microprocessor. The NPE-200 has an R5000 microprocessor that operates at an internal clock speed of 200 megahertz (MHz).
- System controller that uses direct memory access (DMA) to transfer data between DRAM and packet SRAM on the network processing engine.
- DRAM for storing routing tables, protocols, network accounting applications, packets of information in preparation for process switching, and packet buffering for SRAM overflow. The standard configuration is 32 MB, with up to 128 MB available through single in-line memory modules (SIMM) upgrades.
- Packet SRAM for storing packets of information in preparation for fast switching. The NPE-200 has 4 MB of SRAM.
- Unified cache SRAM that functions as the secondary cache for the microprocessor. (The primary cache is within the processor.)
- Two environmental sensors for monitoring the cooling air as it leaves the chassis.
- Erasable programmable read-only memory (EPROM) for storing sufficient code for booting the Cisco IOS software.

## Configuration Tasks

None

## Configuration Examples

None

## Command Reference

None

## What to Do Next

For more information on the NPE-200 network processing engine, refer to the *Cisco 7200 series Network Processing Engine Replacement Instructions* and *Cisco 72xx Installation and Configuration Guide* publications.