



BIOS Parameters by Server Model

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C22 and C24 Servers

Main BIOS Parameters for C22 and C24 Servers

Name	Description
TPM Support set TPMAdminCtrl	<p>TPM (Trusted Platform Module) is a microchip designed to provide basic security-related functions primarily involving encryption keys. This option allows you to control the TPM Security Device support for the system. It can be one of the following:</p> <ul style="list-style-type: none">• Disabled—The server does not use the TPM.• Enabled—The server uses the TPM. <p>Note We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Advanced BIOS Parameters for C22 and C24 Servers

Processor Configuration Parameters

Name	Description
Intel Hyper-Threading Technology set IntelHyperThread	<p>Whether the processor uses Intel Hyper-Threading Technology, which allows multithreaded software applications to execute threads in parallel within each processor. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit hyperthreading. • Enabled—The processor allows for the parallel execution of multiple threads. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Number of Enabled Cores set CoreMultiProcessing	<p>Allows you to disable one or more of the physical cores on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • All—Enables all physical cores. This also enables Hyper Threading on the associated logical processor cores. • 1 through <i>n</i>—Specifies the number of physical processor cores that can run on the server. Each physical core has an associated logical core. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Execute Disable set ExecuteDisable	<p>Classifies memory areas on the server to specify where application code can execute. As a result of this classification, the processor disables code execution if a malicious worm attempts to insert code in the buffer. This setting helps to prevent damage, worm propagation, and certain classes of malicious buffer overflow attacks. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not classify memory areas. • Enabled—The processor classifies memory areas. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Name	Description
Intel VT set IntelVT	<p>Whether the processor uses Intel Virtualization Technology (VT), which allows a platform to run multiple operating systems and applications in independent partitions. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit virtualization. • Enabled—The processor allows multiple operating systems in independent partitions. <p>Note If you change this option, you must power cycle the server before the setting takes effect.</p>
Intel VT-d set IntelVTD	<p>Whether the processor uses Intel Virtualization Technology for Directed I/O (VT-d). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not use virtualization technology. • Enabled—The processor uses virtualization technology.
Intel VT-d Coherency Support set CoherencySupport	<p>Whether the processor supports Intel VT-d Coherency. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not support coherency. • Enabled—The processor uses VT-d Coherency as required.
Intel VT-d ATS Support set ATS	<p>Whether the processor supports Intel VT-d Address Translation Services (ATS). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not support ATS. • Enabled—The processor uses VT-d ATS as required.

Name	Description
CPU Performance set CPUPerformance	Sets the CPU performance profile for the server. The performance profile consists of the following options: <ul style="list-style-type: none"> • DCU Streamer Prefetcher • DCU IP Prefetcher • Hardware Prefetcher • Adjacent Cache-Line Prefetch This can be one of the following: <ul style="list-style-type: none"> • Enterprise—All options are enabled. • High_Throughput—Only the DCU IP Prefetcher is enabled. The rest of the options are disabled. • HPC—All options are enabled. This setting is also known as high performance computing. • Custom—All performance profile options can be configured from the BIOS setup on the server. In addition, the Hardware Prefetcher and Adjacent Cache-Line Prefetch options can be configured in the fields below.
Hardware Prefetcher set HardwarePrefetch	Whether the processor allows the Intel hardware prefetcher to fetch streams of data and instruction from memory into the unified second-level cache when necessary. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The hardware prefetcher is not used. • Enabled—The processor uses the hardware prefetcher when cache issues are detected.
Adjacent Cache Line Prefetcher set AdjacentCacheLinePrefetch	Whether the processor fetches cache lines in even/odd pairs instead of fetching just the required line. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor only fetches the required line. • Enabled— The processor fetches both the required line and its paired line.

Name	Description
DCU Streamer Prefetch set DcuStreamerPrefetch	<p>Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not try to anticipate cache read requirements and only fetches explicitly requested lines. • Enabled—The DCU prefetcher analyzes the cache read pattern and prefetches the next line in the cache if it determines that it may be needed.
DCU IP Prefetcher set DcuIpPrefetch	<p>Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not preload any cache data. • Enabled—The DCU IP prefetcher preloads the L1 cache with the data it determines to be the most relevant.
Direct Cache Access Support set DirectCacheAccess	<p>Allows processors to increase I/O performance by placing data from I/O devices directly into the processor cache. This setting helps to reduce cache misses. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—Data from I/O devices is not placed directly into the processor cache. • Enabled—Data from I/O devices is placed directly into the processor cache.

Name	Description
<p>Power Technology set CPUPowerManagement</p>	<p>Enables you to configure the CPU power management settings for the following options:</p> <ul style="list-style-type: none"> • Enhanced Intel Speedstep Technology • Intel Turbo Boost Technology • Processor Power State C6 <p>Power Technology can be one of the following:</p> <ul style="list-style-type: none"> • Custom—The server uses the individual settings for the BIOS parameters mentioned above. You must select this option if you want to change any of these BIOS parameters. • Disabled—The server does not perform any CPU power management and any settings for the BIOS parameters mentioned above are ignored. • Energy_Efficient—The server determines the best settings for the BIOS parameters mentioned above and ignores the individual settings for these parameters.
<p>Enhanced Intel Speedstep Technology set EnhancedIntelSpeedStep</p>	<p>Whether the processor uses Enhanced Intel SpeedStep Technology, which allows the system to dynamically adjust processor voltage and core frequency. This technology can result in decreased average power consumption and decreased average heat production. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor never dynamically adjusts its voltage or frequency. • Enabled—The processor utilizes Enhanced Intel SpeedStep Technology and enables all supported processor sleep states to further conserve power. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p> <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>

Name	Description
Intel Turbo Boost Technology set IntelTurboBoostTech	<p>Whether the processor uses Intel Turbo Boost Technology, which allows the processor to automatically increase its frequency if it is running below power, temperature, or voltage specifications. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not increase its frequency automatically. • Enabled—The processor utilizes Turbo Boost Technology if required. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C6 set ProcessorC6Report	<p>Whether the BIOS sends the C6 report to the operating system. When the OS receives the report, it can transition the processor into the lower C6 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not send the C6 report. • Enabled—The BIOS sends the C6 report, allowing the OS to transition the processor to the C6 low power state. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C1 Enhanced set ProcessorC1EReport	<p>Whether the CPU transitions to its minimum frequency when entering the C1 state. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The CPU continues to run at its maximum frequency in C1 state. • Enabled—The CPU transitions to its minimum frequency. This option saves the maximum amount of power in C1 state.
Frequency Floor Override set CpuFreqFloor	<p>Whether the CPU is allowed to drop below the maximum non-turbo frequency when idle. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled— The CPU can drop below the maximum non-turbo frequency when idle. This option decreases power consumption but may reduce system performance. • Enabled— The CPU cannot drop below the maximum non-turbo frequency when idle. This option improves system performance but may increase power consumption.

Name	Description
<p>P-STATE Coordination set PsdCoordType</p>	<p>Allows you to define how BIOS communicates the P-state support model to the operating system. There are 3 models as defined by the Advanced Configuration and Power Interface (ACPI) specification.</p> <ul style="list-style-type: none"> • HW_ALL—The processor hardware is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package). • SW_ALL—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a physical package), and must initiate the transition on all of the logical processors. • SW_ANY—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package), and may initiate the transition on any of the logical processors in the domain. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
<p>Energy Performance set CpuEngPerfBias</p>	<p>Allows you to determine whether system performance or energy efficiency is more important on this server. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced_Energy • Balanced_Performance • Energy_Efficient • Performance

Memory Configuration Parameters

Name	Description
Select Memory RAS set SelectMemoryRAS	<p>How the memory reliability, availability, and serviceability (RAS) is configured for the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Maximum_Performance—System performance is optimized. • Mirroring—System reliability is optimized by using half the system memory as backup. • Lockstep—If the DIMM pairs in the server have an identical type, size, and organization and are populated across the SMI channels, you can enable lockstep mode to minimize memory access latency and provide better performance. This option offers better system performance than Mirroring and better reliability than Maximum Performance but lower reliability than Mirroring and lower system performance than Maximum Performance.
DRAM Clock Throttling set DRAMClockThrottling	<p>Allows you to tune the system settings between the memory bandwidth and power consumption. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced— DRAM clock throttling is reduced, providing a balance between performance and power. • Performance—DRAM clock throttling is disabled, providing increased memory bandwidth at the cost of additional power. • Energy_Efficient—DRAM clock throttling is increased to improve energy efficiency.
NUMA set NUMAOptimize	<p>Whether the BIOS supports Non-Uniform Memory Access (NUMA). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not support NUMA. • Enabled—The BIOS includes the ACPI tables that are required for NUMA-aware operating systems. If you enable this option, the system must disable Inter-Socket Memory interleaving on some platforms.

Name	Description
Low Voltage DDR Mode set LvDDRMode	Whether the system prioritizes low voltage or high frequency memory operations. This can be one of the following: <ul style="list-style-type: none"> • Power_Saving_Mode—The system prioritizes low voltage memory operations over high frequency memory operations. This mode may lower memory frequency in order to keep the voltage low. • Performance_Mode—The system prioritizes high frequency operations over low voltage operations.
DRAM Refresh rate set DramRefreshRate	Allows you to set the rate at which the DRAM cells are refreshed. This can be one of the following: <ul style="list-style-type: none"> • 1x—DRAM cells are refreshed every 64ms. • 2x—DRAM cells are refreshed every 32ms. • 3x—DRAM cells are refreshed every 21ms. • 4x—DRAM cells are refreshed every 16ms. • Auto—DRAM cells refresh rate is automatically chosen by the BIOS based on the system configuration. This is the recommended setting for this parameter.
Channel Interleaving set ChannelInterLeave	Whether the CPU divides memory blocks and spreads contiguous portions of data across interleaved channels to enable simultaneous read operations. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some channel interleaving is used. • 2_Way • 3_Way • 4_Way—The maximum amount of channel interleaving is used.

Name	Description
Rank Interleaving set RankInterLeave	<p>Whether the CPU interleaves physical ranks of memory so that one rank can be accessed while another is being refreshed. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some rank interleaving is used. • 2_Way • 4_Way • 8_Way—The maximum amount of rank interleaving is used.
Patrol Scrub set PatrolScrub	<p>Whether the system actively searches for, and corrects, single bit memory errors even in unused portions of the memory on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The system checks for memory ECC errors only when the CPU reads or writes a memory address. • Enabled—The system periodically reads and writes memory searching for ECC errors. If any errors are found, the system attempts to fix them. This option may correct single bit errors before they become multi-bit errors, but it may adversely affect performance when the patrol scrub is running.
Demand Scrub set DemandScrub	<p>Whether the system corrects single bit memory errors encountered when the CPU or I/O makes a demand read. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled— Single bit memory errors are not corrected. • Enabled— Single bit memory errors are corrected in memory and the corrected data is set in response to the demand read.

Name	Description
Altitude set Altitude	The approximate number of meters above sea level at which the physical server is installed. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines the physical elevation. • 300_M—The server is approximately 300 meters above sea level. • 900_M—The server is approximately 900 meters above sea level. • 1500_M—The server is approximately 1500 meters above sea level. • 3000_M—The server is approximately 3000 meters above sea level.

QPI Configuration Parameters

Name	Description
QPI Link Frequency Select set QPILinkFrequency	The Intel QuickPath Interconnect (QPI) link frequency, in gigatransfers per second (GT/s). This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines the QPI link frequency. • 6.4_GT/s • 7.2_GT/s • 8.0_GT/s

Name	Description
QPI Snoop Mode set QpiSnoopMode	<p>The Intel QuickPath Interconnect (QPI) snoop mode. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU automatically recognizes this as Early Snoop mode. • Early Snoop— The distributed cache ring stops can send a snoop probe or a request to another caching agent directly. This mode has lower latency and it is best for workloads that have shared data sets across threads and can benefit from a cache-to-cache transfer, or for workloads that are not NUMA optimized. • Home Snoop— The snoop is always spawned by the home agent (centralized ring stop) for the memory controller. This mode has a higher local latency than early snoop, but it provides extra resources for a larger number of outstanding transactions. • Home Directory Snoop— The home directory is an optional enabled feature that is implemented at both the HA and iMC logic in the processor. The goal of the directory is to filter snoops to the remote sockets and a node controller in scalable platforms and 2S and 4S configurations. • Home Directory Snoop with OSB— In the Opportunistic Snoop Broadcast (OSB) directory mode, the HA could choose to do speculative home snoop broadcast under very lightly loaded conditions even before the directory information has been collected and checked.

Onboard Storage Parameters

Name	Description
Onboard SCU Storage Support set DisableSCU	<p>Whether the onboard software RAID controller is available to the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The software RAID controller is not available. • Enabled—The software RAID controller is available.

USB Configuration Parameters

Name	Description
Legacy USB Support set LegacyUSBSupport	Whether the system supports legacy USB devices. This can be one of the following: <ul style="list-style-type: none"> • Disabled—USB devices are only available to EFI applications. • Enabled—Legacy USB support is always available. • Auto—Disables legacy USB support if no USB devices are connected.
Port 60/64 Emulation set UsbEmul6064	Whether the system supports 60h/64h emulation for complete USB keyboard legacy support. This can be one of the following: <ul style="list-style-type: none"> • Disabled—60h/64 emulation is not supported. • Enabled—60h/64 emulation is supported. You should select this option if you are using a non-USB aware operating system on the server.
All USB Devices set AllUsbDevices	Whether all physical and virtual USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—All USB devices are disabled. • Enabled—All USB devices are enabled.
USB Port: Rear set UsbPortRear	Whether the rear panel USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the rear panel USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the rear panel USB ports. Devices connected to these ports are detected by the BIOS and operating system.
USB Port: Front set UsbPortFront	Whether the front panel USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the front panel USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the front panel USB ports. Devices connected to these ports are detected by the BIOS and operating system.

Name	Description
USB Port: Internal set UsbPortInt	Whether the internal USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the internal USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the internal USB ports. Devices connected to these ports are detected by the BIOS and operating system.
USB Port: KVM set UsbPortKVM	Whether the KVM ports are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the KVM keyboard and/or mouse devices. Keyboard and/or mouse will not work in the KVM window. • Enabled—Enables the KVM keyboard and/or mouse devices.
USB Port: vMedia set UsbPortVMedia	Whether the virtual media devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the vMedia devices. • Enabled—Enables the vMedia devices.

PCI Configuration Parameters

Name	Description
MMIO Above 4GB set MemoryMappedIOAbove4GB	Whether to enable or disable MMIO above 4GB or not. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The server does not map I/O of 64-bit PCI devices to 4GB or greater address space. • Enabled—The server maps I/O of 64-bit PCI devices to 4GB or greater address space.
ASPM Support set ASPMSupport	Allows you to set the level of ASPM (Active Power State Management) support in the BIOS. This can be one of the following: <ul style="list-style-type: none"> • Disabled—ASPM support is disabled in the BIOS. • Force L0s—Force all links to L0 standby (L0s) state. • Auto—The CPU determines the power state.

Name	Description
VGA Priority set <code>VgaPriority</code>	Allows you to set the priority for VGA graphics devices if multiple VGA devices are found in the system. This can be one of the following: <ul style="list-style-type: none"> • Onboard—Priority is given to the onboard VGA device. BIOS post screen and OS boot are driven through the onboard VGA port. • Offboard—Priority is given to the PCIE Graphics adapter. BIOS post screen and OS boot are driven through the external graphics adapter port. • Onboard_VGA_Disabled—Priority is given to the PCIE Graphics adapter, and the onboard VGA device is disabled. <p>Note The vKVM does not function when the onboard VGA is disabled.</p>

Serial Configuration Parameters

Name	Description
Console Redirection set <code>ConsoleRedir</code>	Allows a serial port to be used for console redirection during POST and BIOS booting. After the BIOS has booted and the operating system is responsible for the server, console redirection is irrelevant and has no effect. This can be one of the following: <ul style="list-style-type: none"> • Disabled—No console redirection occurs during POST. • Enabled—Enables console redirection on serial port A during POST.
Terminal Type set <code>TerminalType</code>	What type of character formatting is used for console redirection. This can be one of the following: <ul style="list-style-type: none"> • PC-ANSI—The PC-ANSI terminal font is used. • VT100—A supported vt100 video terminal and its character set are used. • VT100+—A supported vt100-plus video terminal and its character set are used. • VT-UTF8—A video terminal with the UTF-8 character set is used. <p>Note This setting must match the setting on the remote terminal application.</p>

Name	Description
Bits per second set BaudRate	<p>What BAUD rate is used for the serial port transmission speed. If you disable Console Redirection, this option is not available. This can be one of the following:</p> <ul style="list-style-type: none"> • 9600—A 9,600 BAUD rate is used. • 19200—A 19,200 BAUD rate is used. • 38400—A 38,400 BAUD rate is used. • 57600—A 57,600 BAUD rate is used. • 115200—A 115,200 BAUD rate is used. <p>Note This setting must match the setting on the remote terminal application.</p>
Flow Control set FlowCtrl	<p>Whether a handshake protocol is used for flow control. Request to Send / Clear to Send (RTS/CTS) helps to reduce frame collisions that can be introduced by a hidden terminal problem. This can be one of the following:</p> <ul style="list-style-type: none"> • None—No flow control is used. • Hardware_RTS/CTS—RTS/CTS is used for flow control. <p>Note This setting must match the setting on the remote terminal application.</p>
Putty KeyPad set PuttyFunctionKeyPad	<p>Allows you to change the action of the PuTTY function keys and the top row of the numeric keypad. This can be one of the following:</p> <ul style="list-style-type: none"> • VT100—The function keys generate ESC OP through ESC O[. • LINUX—Mimics the Linux virtual console. Function keys F6 to F12 behave like the default mode, but F1 to F5 generate ESC [[A through ESC [[E. • XTERMR6—Function keys F5 to F12 behave like the default mode. Function keys F1 to F4 generate ESC OP through ESC OS, which are the sequences produced by the top row of the keypad on Digital terminals. • SCO—The function keys F1 to F12 generate ESC [M through ESC [X. The function and shift keys generate ESC [Y through ESC [j. The control and function keys generate ESC [k through ESC [v. The shift, control and function keys generate ESC [w through ESC [{}. • ESCN—The default mode. The function keys match the general behavior of Digital terminals. The function keys generate sequences such as ESC [11~ and ESC [12~. • VT400—The function keys behave like the default mode. The top row of the numeric keypad generates ESC OP through ESC OS.

Name	Description
Redirection After BIOS POST set <code>RedirectionAfterPOST</code>	Whether BIOS console redirection should be active after BIOS POST is complete and control given to the OS bootloader. This can be one of the following: <ul style="list-style-type: none"> • Always_Enable—BIOS Legacy console redirection is active during the OS boot and run time. • Bootloader—BIOS Legacy console redirection is disabled before giving control to the OS boot loader.
Out-of-Band Mgmt Port set <code>comSpcrEnable</code>	Allows you to configure the COM port 0 that can be used for Windows Emergency Management services. ACPI SPCR table is reported based on this setup option. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Configures the COM port 0 as a general purpose port for use with the Windows Operating System. • Enabled—Configures the COM port 0 as a remote management port for Windows Emergency Management services.

LOM and PCIe Slots Configuration Parameters

Name	Description
All Onboard LOM Ports set <code>AllLomPortControl</code>	Whether all LOM ports are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—All LOM ports are disabled. • Enabled—All LOM ports are enabled.
LOM Port <i>n</i> OptionROM set <code>LomOpromControlPort<i>n</i></code>	Whether Option ROM is available on the LOM port designated by <i>n</i> . This can be one of the following: <ul style="list-style-type: none"> • Disabled—Option ROM is not available on LOM port <i>n</i>. • Enabled—Option ROM is available on LOM port <i>n</i>. • UEFI_Only—The expansion slot <i>n</i> is available for UEFI only. • Legacy_Only—The expansion slot <i>n</i> is available for legacy only.

Name	Description
All PCIe Slots OptionROM set PcieOptionROMs	Whether the server can use Option ROM present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for all PCIe slots are not available. • Enabled—The Option ROMs for all the PCIe slots are available. • UEFI_Only—The Option ROMs for slot <i>n</i> are available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> are available for legacy only.
PCIe Slot:<i>n</i> OptionROM set Slot-<i>n</i>-ROM	Whether the server can use the Option ROMs present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
PCIe Slot:<i>n</i> Link Speed PCIe Slot:<i>n</i>LinkSpeed	This option allows you to restrict the maximum speed of an adapter card installed in PCIe slot <i>n</i> . This can be one of the following: <ul style="list-style-type: none"> • GEN1—2.5GT/s (gigatransfers per second) is the maximum speed allowed. • GEN2—5GT/s is the maximum speed allowed. • GEN3—8GT/s is the maximum speed allowed. • Disabled—The maximum speed is not restricted. <p>For example, if you have a 3rd generation adapter card in PCIe slot 2 that you want to run at a maximum of 5GT/s instead of the 8GT/s that card supports, set the PCIe Slot 2 Link Speed to GEN2. The system then ignores the card's supported maximum speed of 8GT/s and forces it to run at a maximum of 5 GT/s.</p>

Name	Description
CDN Support for LOM set CdnSupport	Whether the Ethernet Network naming convention is according to Consistent Device Naming (CDN) or the traditional way of naming conventions. This can be one of the following: <ul style="list-style-type: none"> • Disabled— OS Ethernet Networking Identifier is named in a default convention as ETH0, ETH1 and so on. By default, CDN option is disabled. • LOMS Only— OS Ethernet Network identifier is named in a consistent device naming (CDN) according to the physical LAN on Motherboard(LOM) port numbering; LOM Port 0, LOM Port 1 and so on. <p>Note CDN is enabled for LOM ports and works with Windows 2012 or the latest OS only.</p>
CDN Support for VIC set CdnEnable	Whether the Ethernet Network naming convention is according to Consistent Device Naming (CDN) or the traditional way of naming conventions. This can be one of the following: <ul style="list-style-type: none"> • Disabled— CDN support for VIC cards is disabled. • Enabled— CDN support is enabled for VIC cards. <p>Note CDN support for VIC cards work with Windows 2012 or the latest OS only.</p>

Server Management BIOS Parameters for C22 and C24 Servers

Name	Description
FRB-2 Timer set FRB-2	Whether the FRB2 timer is used by Cisco IMC to recover the system if it hangs during POST. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The FRB2 timer is not used. • Enabled—The FRB2 timer is started during POST and used to recover the system if necessary.

Name	Description
OS Watchdog Timer set OSBootWatchdogTimer	Whether the BIOS programs the watchdog timer with a specified timeout value. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The watchdog timer is not used to track how long the server takes to boot. • Enabled—The watchdog timer tracks how long the server takes to boot. If the server does not boot within the length of time specified by the set OSBootWatchdogTimerTimeout command, the Cisco IMC logs an error and takes the action specified by the set OSBootWatchdogTimerPolicy command.
OS Watchdog Timer Timeout set OSBootWatchdogTimerTimeOut	If OS does not boot within the specified time, OS watchdog timer expires and system takes action according to timer policy. This can be one of the following: <ul style="list-style-type: none"> • 5_Minutes—The OS watchdog timer expires 5 minutes after it begins to boot. • 10_Minutes—The OS watchdog timer expires 10 minutes after it begins to boot. • 15_Minutes—The OS watchdog timer expires 15 minutes after it begins to boot. • 20_Minutes—The OS watchdog timer expires 20 minutes after it begins to boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>
OS Watchdog Timer Policy set OSBootWatchdogTimerPolicy	What action the system takes if the watchdog timer expires. This can be one of the following: <ul style="list-style-type: none"> • Do_Nothing—The server takes no action if the watchdog timer expires during OS boot. • Power_Down—The server is powered off if the watchdog timer expires during OS boot. • Reset—The server is reset if the watchdog timer expires during OS boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>

C220 and C240 Servers

Main BIOS Parameters for C220 and C240 Servers

Name	Description
TPM Support set TPMAdminCtrl	<p>TPM (Trusted Platform Module) is a microchip designed to provide basic security-related functions primarily involving encryption keys. This option allows you to control the TPM Security Device support for the system. It can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The server does not use the TPM. • Enabled—The server uses the TPM. <p>Note We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Advanced BIOS Parameters for C220 and C240 Servers

Processor Configuration Parameters

Name	Description
Intel Hyper-Threading Technology set IntelHyperThread	<p>Whether the processor uses Intel Hyper-Threading Technology, which allows multithreaded software applications to execute threads in parallel within each processor. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit hyperthreading. • Enabled—The processor allows for the parallel execution of multiple threads. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Name	Description
Number of Enabled Cores set CoreMultiProcessing	<p>Allows you to disable one or more of the physical cores on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • All—Enables all physical cores. This also enables Hyper Threading on the associated logical processor cores. • 1 through n—Specifies the number of physical processor cores that can run on the server. Each physical core has an associated logical core. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Execute Disable set ExecuteDisable	<p>Classifies memory areas on the server to specify where application code can execute. As a result of this classification, the processor disables code execution if a malicious worm attempts to insert code in the buffer. This setting helps to prevent damage, worm propagation, and certain classes of malicious buffer overflow attacks. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not classify memory areas. • Enabled—The processor classifies memory areas. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Intel VT set IntelVT	<p>Whether the processor uses Intel Virtualization Technology (VT), which allows a platform to run multiple operating systems and applications in independent partitions. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit virtualization. • Enabled—The processor allows multiple operating systems in independent partitions. <p>Note If you change this option, you must power cycle the server before the setting takes effect.</p>
Intel VT-d set IntelVTD	<p>Whether the processor uses Intel Virtualization Technology for Directed I/O (VT-d). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not use virtualization technology. • Enabled—The processor uses virtualization technology.

Name	Description
Intel VT-d Coherency Support set CoherencySupport	Whether the processor supports Intel VT-d Coherency. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support coherency. • Enabled—The processor uses VT-d Coherency as required.
Intel VT-d ATS Support set ATS	Whether the processor supports Intel VT-d Address Translation Services (ATS). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support ATS. • Enabled—The processor uses VT-d ATS as required.
CPU Performance set CPUPerformance	Sets the CPU performance profile for the server. The performance profile consists of the following options: <ul style="list-style-type: none"> • DCU Streamer Prefetcher • DCU IP Prefetcher • Hardware Prefetcher • Adjacent Cache-Line Prefetch This can be one of the following: <ul style="list-style-type: none"> • Enterprise—All options are enabled. • High_Throughput—Only the DCU IP Prefetcher is enabled. The rest of the options are disabled. • HPC—All options are enabled. This setting is also known as high performance computing. • Custom—All performance profile options can be configured from the BIOS setup on the server. In addition, the Hardware Prefetcher and Adjacent Cache-Line Prefetch options can be configured in the fields below.
Hardware Prefetcher set HardwarePrefetch	Whether the processor allows the Intel hardware prefetcher to fetch streams of data and instruction from memory into the unified second-level cache when necessary. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The hardware prefetcher is not used. • Enabled—The processor uses the hardware prefetcher when cache issues are detected.

Name	Description
Adjacent Cache Line Prefetcher set AdjacentCacheLinePrefetch	Whether the processor fetches cache lines in even/odd pairs instead of fetching just the required line. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor only fetches the required line. • Enabled— The processor fetches both the required line and its paired line.
DCU Streamer Prefetch set DcuStreamerPrefetch	Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not try to anticipate cache read requirements and only fetches explicitly requested lines. • Enabled—The DCU prefetcher analyzes the cache read pattern and prefetches the next line in the cache if it determines that it may be needed.
DCU IP Prefetcher set DcuIpPrefetch	Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not preload any cache data. • Enabled—The DCU IP prefetcher preloads the L1 cache with the data it determines to be the most relevant.
Direct Cache Access Support set DirectCacheAccess	Allows processors to increase I/O performance by placing data from I/O devices directly into the processor cache. This setting helps to reduce cache misses. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Data from I/O devices is not placed directly into the processor cache. • Enabled—Data from I/O devices is placed directly into the processor cache.

Name	Description
<p>Power Technology set CPUPowerManagement</p>	<p>Enables you to configure the CPU power management settings for the following options:</p> <ul style="list-style-type: none"> • Enhanced Intel Speedstep Technology • Intel Turbo Boost Technology • Processor Power State C6 <p>Power Technology can be one of the following:</p> <ul style="list-style-type: none"> • Custom—The server uses the individual settings for the BIOS parameters mentioned above. You must select this option if you want to change any of these BIOS parameters. • Disabled—The server does not perform any CPU power management and any settings for the BIOS parameters mentioned above are ignored. • Energy_Efficient—The server determines the best settings for the BIOS parameters mentioned above and ignores the individual settings for these parameters.
<p>Enhanced Intel Speedstep Technology set EnhancedIntelSpeedStep</p>	<p>Whether the processor uses Enhanced Intel SpeedStep Technology, which allows the system to dynamically adjust processor voltage and core frequency. This technology can result in decreased average power consumption and decreased average heat production. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor never dynamically adjusts its voltage or frequency. • Enabled—The processor utilizes Enhanced Intel SpeedStep Technology and enables all supported processor sleep states to further conserve power. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p> <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>

Name	Description
Intel Turbo Boost Technology set IntelTurboBoostTech	<p>Whether the processor uses Intel Turbo Boost Technology, which allows the processor to automatically increase its frequency if it is running below power, temperature, or voltage specifications. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not increase its frequency automatically. • Enabled—The processor utilizes Turbo Boost Technology if required. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C6 set ProcessorC6Report	<p>Whether the BIOS sends the C6 report to the operating system. When the OS receives the report, it can transition the processor into the lower C6 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not send the C6 report. • Enabled—The BIOS sends the C6 report, allowing the OS to transition the processor to the C6 low power state. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C1 Enhanced set ProcessorC1EReport	<p>Whether the CPU transitions to its minimum frequency when entering the C1 state. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The CPU continues to run at its maximum frequency in C1 state. • Enabled—The CPU transitions to its minimum frequency. This option saves the maximum amount of power in C1 state.
Frequency Floor Override set CpuFreqFloor	<p>Whether the CPU is allowed to drop below the maximum non-turbo frequency when idle. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled— The CPU can drop below the maximum non-turbo frequency when idle. This option decreases power consumption but may reduce system performance. • Enabled— The CPU cannot drop below the maximum non-turbo frequency when idle. This option improves system performance but may increase power consumption.

Name	Description
<p>P-STATE Coordination set PsdCoordType</p>	<p>Allows you to define how BIOS communicates the P-state support model to the operating system. There are 3 models as defined by the Advanced Configuration and Power Interface (ACPI) specification.</p> <ul style="list-style-type: none"> • HW_ALL—The processor hardware is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package). • SW_ALL—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a physical package), and must initiate the transition on all of the logical processors. • SW_ANY—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package), and may initiate the transition on any of the logical processors in the domain. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
<p>Energy Performance set CpuEngPerfBias</p>	<p>Allows you to determine whether system performance or energy efficiency is more important on this server. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced_Energy • Balanced_Performance • Energy_Efficient • Performance

Memory Configuration Parameters

Name	Description
Select Memory RAS set SelectMemoryRAS	<p>How the memory reliability, availability, and serviceability (RAS) is configured for the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Maximum_Performance—System performance is optimized. • Mirroring—System reliability is optimized by using half the system memory as backup. • Lockstep—If the DIMM pairs in the server have an identical type, size, and organization and are populated across the SMI channels, you can enable lockstep mode to minimize memory access latency and provide better performance. This option offers better system performance than Mirroring and better reliability than Maximum Performance but lower reliability than Mirroring and lower system performance than Maximum Performance.
DRAM Clock Throttling set DRAMClockThrottling	<p>Allows you to tune the system settings between the memory bandwidth and power consumption. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced—DRAM clock throttling is reduced, providing a balance between performance and power. • Performance—DRAM clock throttling is disabled, providing increased memory bandwidth at the cost of additional power. • Energy_Efficient—DRAM clock throttling is increased to improve energy efficiency.
NUMA set NUMAOptimize	<p>Whether the BIOS supports Non-Uniform Memory Access (NUMA). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not support NUMA. • Enabled—The BIOS includes the ACPI tables that are required for NUMA-aware operating systems. If you enable this option, the system must disable Inter-Socket Memory interleaving on some platforms.

Name	Description
Low Voltage DDR Mode set LvDDRMode	Whether the system prioritizes low voltage or high frequency memory operations. This can be one of the following: <ul style="list-style-type: none"> • Power_Saving_Mode—The system prioritizes low voltage memory operations over high frequency memory operations. This mode may lower memory frequency in order to keep the voltage low. • Performance_Mode—The system prioritizes high frequency operations over low voltage operations.
DRAM Refresh rate set DramRefreshRate	Allows you to set the rate at which the DRAM cells are refreshed. This can be one of the following: <ul style="list-style-type: none"> • 1x—DRAM cells are refreshed every 64ms. • 2x—DRAM cells are refreshed every 32ms. • 3x—DRAM cells are refreshed every 21ms. • 4x—DRAM cells are refreshed every 16ms. • Auto—DRAM cells refresh rate is automatically chosen by the BIOS based on the system configuration. This is the recommended setting for this parameter.
Channel Interleaving set ChannelInterLeave	Whether the CPU divides memory blocks and spreads contiguous portions of data across interleaved channels to enable simultaneous read operations. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some channel interleaving is used. • 2_Way • 3_Way • 4_Way—The maximum amount of channel interleaving is used.

Name	Description
Rank Interleaving set RankInterLeave	<p>Whether the CPU interleaves physical ranks of memory so that one rank can be accessed while another is being refreshed. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some rank interleaving is used. • 2_Way • 4_Way • 8_Way—The maximum amount of rank interleaving is used.
Patrol Scrub set PatrolScrub	<p>Whether the system actively searches for, and corrects, single bit memory errors even in unused portions of the memory on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The system checks for memory ECC errors only when the CPU reads or writes a memory address. • Enabled—The system periodically reads and writes memory searching for ECC errors. If any errors are found, the system attempts to fix them. This option may correct single bit errors before they become multi-bit errors, but it may adversely affect performance when the patrol scrub is running.
Demand Scrub set DemandScrub	<p>Whether the system corrects single bit memory errors encountered when the CPU or I/O makes a demand read. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled— Single bit memory errors are not corrected. • Enabled— Single bit memory errors are corrected in memory and the corrected data is set in response to the demand read.

Name	Description
Altitude set Altitude	The approximate number of meters above sea level at which the physical server is installed. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines the physical elevation. • 300_M—The server is approximately 300 meters above sea level. • 900_M—The server is approximately 900 meters above sea level. • 1500_M—The server is approximately 1500 meters above sea level. • 3000_M—The server is approximately 3000 meters above sea level.

QPI Configuration Parameters

Name	Description
QPI Link Frequency Select set QPILinkFrequency	The Intel QuickPath Interconnect (QPI) link frequency, in gigatransfers per second (GT/s). This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines the QPI link frequency. • 6.4_GT/s • 7.2_GT/s • 8.0_GT/s

Name	Description
QPI Snoop Mode set QpiSnoopMode	<p>The Intel QuickPath Interconnect (QPI) snoop mode. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU automatically recognizes this as Early Snoop mode. • Early Snoop— The distributed cache ring stops can send a snoop probe or a request to another caching agent directly. This mode has lower latency and it is best for workloads that have shared data sets across threads and can benefit from a cache-to-cache transfer, or for workloads that are not NUMA optimized. • Home Snoop— The snoop is always spawned by the home agent (centralized ring stop) for the memory controller. This mode has a higher local latency than early snoop, but it provides extra resources for a larger number of outstanding transactions. • Home Directory Snoop— The home directory is an optional enabled feature that is implemented at both the HA and iMC logic in the processor. The goal of the directory is to filter snoops to the remote sockets and a node controller in scalable platforms and 2S and 4S configurations. • Home Directory Snoop with OSB— In the Opportunistic Snoop Broadcast (OSB) directory mode, the HA could choose to do speculative home snoop broadcast under very lightly loaded conditions even before the directory information has been collected and checked.

Onboard Storage Parameters

Name	Description
Onboard SCU Storage Support set DisableSCU	<p>Whether the onboard software RAID controller is available to the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The software RAID controller is not available. • Enabled—The software RAID controller is available.
Onboard SCU Storage SW Stack set PchScuOromSelect	<p>Allows you to choose a pre-boot software stack for an onboard SCU storage controller. This can be one of the following:</p> <ul style="list-style-type: none"> • Intel RSTe(1) • LSI SW RAID (0) <p>Note This configuration parameter is valid only for the C220 servers.</p>

USB Configuration Parameters

Name	Description
Legacy USB Support set LegacyUSBSupport	Whether the system supports legacy USB devices. This can be one of the following: <ul style="list-style-type: none"> • Disabled—USB devices are only available to EFI applications. • Enabled—Legacy USB support is always available. • Auto—Disables legacy USB support if no USB devices are connected.
Port 60/64 Emulation set UsbEmul6064	Whether the system supports 60h/64h emulation for complete USB keyboard legacy support. This can be one of the following: <ul style="list-style-type: none"> • Disabled—60h/64 emulation is not supported. • Enabled—60h/64 emulation is supported. You should select this option if you are using a non-USB aware operating system on the server.
All USB Devices set AllUsbDevices	Whether all physical and virtual USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—All USB devices are disabled. • Enabled—All USB devices are enabled.
USB Port: Rear set UsbPortRear	Whether the rear panel USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the rear panel USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the rear panel USB ports. Devices connected to these ports are detected by the BIOS and operating system.
USB Port: Front set UsbPortFront	Whether the front panel USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the front panel USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the front panel USB ports. Devices connected to these ports are detected by the BIOS and operating system.

Name	Description
USB Port: Internal set UsbPortInt	Whether the internal USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the internal USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the internal USB ports. Devices connected to these ports are detected by the BIOS and operating system.
USB Port: KVM set UsbPortKVM	Whether the KVM ports are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the KVM keyboard and/or mouse devices. Keyboard and/or mouse will not work in the KVM window. • Enabled—Enables the KVM keyboard and/or mouse devices.
USB Port: vMedia set UsbPortVMedia	Whether the virtual media devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the vMedia devices. • Enabled—Enables the vMedia devices.
USB Port: SD Card set UsbPortSdCard	Whether the SD card drives are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the SD card drives. The SD card drives are not detected by the BIOS and operating system. • Enabled—Enables the SD card drives.

PCI Configuration Parameters

Name	Description
Memory Mapped I/O Above 4GB set MemoryMappedIOAbove4GB	Whether to enable or disable MMIO above 4GB or not. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The server does not map I/O of 64-bit PCI devices to 4GB or greater address space. • Enabled—The server maps I/O of 64-bit PCI devices to 4GB or greater address space. <p>Note PCI devices that are 64-bit compliant but use a legacy option ROM may not function correctly with this setting enabled.</p>

Name	Description
MMCFG BASE set MmcfgBaseSelect	Sets the low base address for PCIe adapters within 4GB. This can be one of the following: <ul style="list-style-type: none"> • 1 GB • 2 GB • 2.5 GB • 3 GB • Auto—Automatically sets the low base address for PCIe adapters. <p>Note This is valid for C240 servers only.</p>
ASPM Support set ASPMSupport	Allows you to set the level of ASPM (Active Power State Management) support in the BIOS. This can be one of the following: <ul style="list-style-type: none"> • Disabled—ASPM support is disabled in the BIOS. • Force L0s—Force all links to L0 standby (L0s) state. • Auto—The CPU determines the power state.
VGA Priority set VgaPriority	Allows you to set the priority for VGA graphics devices if multiple VGA devices are found in the system. This can be one of the following: <ul style="list-style-type: none"> • Onboard—Priority is given to the onboard VGA device. BIOS post screen and OS boot are driven through the onboard VGA port. • Offboard—Priority is given to the PCIe Graphics adapter. BIOS post screen and OS boot are driven through the external graphics adapter port. • Onboard_VGA_Disabled—Priority is given to the PCIe Graphics adapter, and the onboard VGA device is disabled. <p>Note The vKVM does not function when the onboard VGA is disabled.</p>

Serial Configuration Parameters

Name	Description
Out-of-Band Mgmt Port set comSpcrEnable	<p>Allows you to configure the COM port 0 that can be used for Windows Emergency Management services. ACPI SPCR table is reported based on this setup option. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—Configures the COM port 0 as a general purpose port for use with the Windows Operating System. • Enabled—Configures the COM port 0 as a remote management port for Windows Emergency Management services.
Console Redirection set ConsoleRedir	<p>Allows a serial port to be used for console redirection during POST and BIOS booting. After the BIOS has booted and the operating system is responsible for the server, console redirection is irrelevant and has no effect. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—No console redirection occurs during POST. • COM_0—Enables console redirection on COM port 0 during POST. • COM_1—Enables console redirection on COM port 1 during POST.
Terminal Type set TerminalType	<p>What type of character formatting is used for console redirection. This can be one of the following:</p> <ul style="list-style-type: none"> • PC-ANSI—The PC-ANSI terminal font is used. • VT100—A supported vt100 video terminal and its character set are used. • VT100+—A supported vt100-plus video terminal and its character set are used. • VT-UTF8—A video terminal with the UTF-8 character set is used. <p>Note This setting must match the setting on the remote terminal application.</p>

Name	Description
Bits per second set BaudRate	<p>What BAUD rate is used for the serial port transmission speed. If you disable Console Redirection, this option is not available. This can be one of the following:</p> <ul style="list-style-type: none"> • 9600—A 9,600 BAUD rate is used. • 19200—A 19,200 BAUD rate is used. • 38400—A 38,400 BAUD rate is used. • 57600—A 57,600 BAUD rate is used. • 115200—A 115,200 BAUD rate is used. <p>Note This setting must match the setting on the remote terminal application.</p>
Flow Control set FlowCtrl	<p>Whether a handshake protocol is used for flow control. Request to Send / Clear to Send (RTS/CTS) helps to reduce frame collisions that can be introduced by a hidden terminal problem. This can be one of the following:</p> <ul style="list-style-type: none"> • None—No flow control is used. • Hardware_RTS/CTS—RTS/CTS is used for flow control. <p>Note This setting must match the setting on the remote terminal application.</p>
Putty KeyPad set PuttyFunctionKeyPad	<p>Allows you to change the action of the PuTTY function keys and the top row of the numeric keypad. This can be one of the following:</p> <ul style="list-style-type: none"> • VT100—The function keys generate ESC OP through ESC O[. • LINUX—Mimics the Linux virtual console. Function keys F6 to F12 behave like the default mode, but F1 to F5 generate ESC [[A through ESC [[E. • XTERMR6—Function keys F5 to F12 behave like the default mode. Function keys F1 to F4 generate ESC OP through ESC OS, which are the sequences produced by the top row of the keypad on Digital terminals. • SCO—The function keys F1 to F12 generate ESC [M through ESC [X. The function and shift keys generate ESC [Y through ESC [j. The control and function keys generate ESC [k through ESC [v. The shift, control and function keys generate ESC [w through ESC [{. • ESCN—The default mode. The function keys match the general behavior of Digital terminals. The function keys generate sequences such as ESC [11~ and ESC [12~. • VT400—The function keys behave like the default mode. The top row of the numeric keypad generates ESC OP through ESC OS.

Name	Description
Redirection After BIOS POST set RedirectionAfterPOST	Whether BIOS console redirection should be active after BIOS POST is complete and control given to the OS bootloader. This can be one of the following: <ul style="list-style-type: none"> • Always_Enabled—BIOS Legacy console redirection is active during the OS boot and run time. • Bootloader—BIOS Legacy console redirection is disabled before giving control to the OS boot loader.

LOM and PCIe Slots Configuration Parameters

Name	Description
CDN Support for LOM set CdnSupport	Whether the Ethernet Network naming convention is according to Consistent Device Naming (CDN) or the traditional way of naming conventions. This can be one of the following: <ul style="list-style-type: none"> • Disabled— OS Ethernet Networking Identifier is named in a default convention as ETH0, ETH1 and so on. By default, CDN option is disabled. • LOMS Only— OS Ethernet Network identifier is named in a consistent device naming (CDN) according to the physical LAN on Motherboard(LOM) port numbering; LOM Port 0, LOM Port 1 and so on. <p>Note CDN is enabled for LOM ports and works with Windows 2012 or the latest OS only.</p>
CDN Support for VIC set CdnEnable	Whether the Ethernet Network naming convention is according to Consistent Device Naming (CDN) or the traditional way of naming conventions. This can be one of the following: <ul style="list-style-type: none"> • Disabled— CDN support for VIC cards is disabled. • Enabled— CDN support is enabled for VIC cards. <p>Note CDN support for VIC cards work with Windows 2012 or the latest OS only.</p>
All Onboard LOM Ports set AllLomPortControl	Whether all LOM ports are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—All LOM ports are disabled. • Enabled—All LOM ports are enabled.

Name	Description
LOM Port <i>n</i> OptionROM set <code>LomOpromControlPort<i>n</i></code>	Whether Option ROM is available on the LOM port designated by <i>n</i> . This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
All PCIe Slots OptionROM set <code>PcieOptionROMs</code>	Whether the server can use Option ROM present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
PCIe Slot:<i>n</i> OptionROM set <code>PcieSlot<i>n</i>OptionROM</code>	Whether the server can use the Option ROMs present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
PCIe Mezzanine OptionROM set <code>PcieMezzOptionROM</code>	Whether the PCIe mezzanine slot expansion ROM is available to the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.

Name	Description
PCIe Slot:<i>n</i> Link Speed PCIe Slot:<i>n</i>LinkSpeed	<p>This option allows you to restrict the maximum speed of an adapter card installed in PCIe slot <i>n</i>. This can be one of the following:</p> <ul style="list-style-type: none"> • GEN1—2.5GT/s (gigatransfers per second) is the maximum speed allowed. • GEN2—5GT/s is the maximum speed allowed. • GEN3—8GT/s is the maximum speed allowed. • Disabled—The maximum speed is not restricted. <p>For example, if you have a 3rd generation adapter card in PCIe slot 2 that you want to run at a maximum of 5GT/s instead of the 8GT/s that card supports, set the PCIe Slot 2 Link Speed to GEN2. The system then ignores the card's supported maximum speed of 8GT/s and forces it to run at a maximum of 5 GT/s.</p>

Server Management BIOS Parameters for C220 and C240 Servers

Name	Description
FRB-2 Timer set FRB-2	<p>Whether the FRB2 timer is used by Cisco IMC to recover the system if it hangs during POST. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The FRB2 timer is not used. • Enabled—The FRB2 timer is started during POST and used to recover the system if necessary.
OS Watchdog Timer set OSBootWatchdogTimer	<p>Whether the BIOS programs the watchdog timer with a specified timeout value. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The watchdog timer is not used to track how long the server takes to boot. • Enabled—The watchdog timer tracks how long the server takes to boot. If the server does not boot within the length of time specified by the set OSBootWatchdogTimerTimeout command, the Cisco IMC logs an error and takes the action specified by the set OSBootWatchdogTimerPolicy command.

Name	Description
OS Watchdog Timer Timeout set OSBootWatchdogTimerTimeOut	<p>If OS does not boot within the specified time, OS watchdog timer expires and system takes action according to timer policy. This can be one of the following:</p> <ul style="list-style-type: none"> • 5_Minutes—The OS watchdog timer expires 5 minutes after it begins to boot. • 10_Minutes—The OS watchdog timer expires 10 minutes after it begins to boot. • 15_Minutes—The OS watchdog timer expires 15 minutes after it begins to boot. • 20_Minutes—The OS watchdog timer expires 20 minutes after it begins to boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>
OS Watchdog Timer Policy set OSBootWatchdogTimerPolicy	<p>What action the system takes if the watchdog timer expires. This can be one of the following:</p> <ul style="list-style-type: none"> • Do_Nothing—The server takes no action if the watchdog timer expires during OS boot. • Power_Down—The server is powered off if the watchdog timer expires during OS boot. • Reset—The server is reset if the watchdog timer expires during OS boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>

C460 Servers

Main BIOS Parameters for C460 Servers

Name	Description
POST Error Pause set POSTErrorPause	<p>What happens when the server encounters a critical error during POST. This can be one of the following:</p> <ul style="list-style-type: none"> • Enabled—The BIOS pauses the attempt to boot the server and opens the Error Manager when a critical error occurs during POST. • Disabled—The BIOS continues to attempt to boot the server.

Name	Description
Boot Option Retry set <code>BootOptionRetry</code>	Whether the BIOS retries NON-EFI based boot options without waiting for user input. This can be one of the following: <ul style="list-style-type: none"> • Enabled—Continually retries NON-EFI based boot options without waiting for user input. • Disabled—Waits for user input before retrying NON-EFI based boot options.

Advanced BIOS Parameters for C460 Servers

Processor Configuration Parameters

Name	Description
Intel Turbo Boost Technology set <code>IntelTurboBoostTech</code>	Whether the processor uses Intel Turbo Boost Technology, which allows the processor to automatically increase its frequency if it is running below power, temperature, or voltage specifications. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not increase its frequency automatically. • Enabled—The processor utilizes Turbo Boost Technology if required.
Enhanced Intel Speedstep Technology set <code>EnhancedIntelSpeedStep</code>	Whether the processor uses Enhanced Intel SpeedStep Technology, which allows the system to dynamically adjust processor voltage and core frequency. This technology can result in decreased average power consumption and decreased average heat production. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor never dynamically adjusts its voltage or frequency. • Enabled—The processor utilizes Enhanced Intel SpeedStep Technology and enables all supported processor sleep states to further conserve power. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Name	Description
Intel Hyper-Threading Technology set IntelHyperThread	<p>Whether the processor uses Intel Hyper-Threading Technology, which allows multithreaded software applications to execute threads in parallel within each processor. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit hyperthreading. • Enabled—The processor allows for the parallel execution of multiple threads. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Number of Enabled Cores set CoreMultiProcessing	<p>Allows you to disable one or more of the physical cores on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • All—Enables all physical cores. This also enables Hyper Threading on the associated logical processor cores. • 1 through n—Specifies the number of physical processor cores that can run on the server. Each physical core has an associated logical core. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Execute Disable set ExecuteDisable	<p>Classifies memory areas on the server to specify where application code can execute. As a result of this classification, the processor disables code execution if a malicious worm attempts to insert code in the buffer. This setting helps to prevent damage, worm propagation, and certain classes of malicious buffer overflow attacks. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not classify memory areas. • Enabled—The processor classifies memory areas. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Intel Virtualization Technology set IntelVT	<p>Whether the processor uses Intel Virtualization Technology (VT), which allows a platform to run multiple operating systems and applications in independent partitions. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit virtualization. • Enabled—The processor allows multiple operating systems in independent partitions. <p>Note If you change this option, you must power cycle the server before the setting takes effect.</p>

Name	Description
Intel VT for Directed IO set IntelVTD	Whether the processor uses Intel Virtualization Technology for Directed I/O (VT-d). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not use virtualization technology. • Enabled—The processor uses virtualization technology.
Intel VT-d Interrupt Remapping set InterruptRemap	Whether the processor supports Intel VT-d Interrupt Remapping. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support remapping. • Enabled—The processor uses VT-d Interrupt Remapping as required.
Intel VT-d Coherency Support set CoherencySupport	Whether the processor supports Intel VT-d Coherency. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support coherency. • Enabled—The processor uses VT-d Coherency as required.
Intel VT-d Address Translation Services set ATS	Whether the processor supports Intel VT-d Address Translation Services (ATS). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support ATS. • Enabled—The processor uses VT-d ATS as required.
Intel VT-d PassThrough DMA set PassThroughDMA	Whether the processor supports Intel VT-d Pass-through DMA. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support pass-through DMA. • Enabled—The processor uses VT-d Pass-through DMA as required.
Direct Cache Access set DirectCacheAccess	Allows processors to increase I/O performance by placing data from I/O devices directly into the processor cache. This setting helps to reduce cache misses. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Data from I/O devices is not placed directly into the processor cache. • Enabled—Data from I/O devices is placed directly into the processor cache.

Name	Description
Processor C3 Report set ProcessorC3Report	<p>Whether the BIOS sends the C3 report to the operating system. When the OS receives the report, it can transition the processor into the lower C3 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not send the C3 report. • ACPI_C2—The BIOS sends the C3 report using the ACPI C2 format, allowing the OS to transition the processor to the C3 low power state. • ACPI_C3—The BIOS sends the C3 report using the ACPI C3 format, allowing the OS to transition the processor to the C3 low power state.
Processor C6 Report set ProcessorC6Report	<p>Whether the BIOS sends the C6 report to the operating system. When the OS receives the report, it can transition the processor into the lower C6 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not send the C6 report. • Enabled—The BIOS sends the C6 report, allowing the OS to transition the processor to the C6 low power state.

Name	Description
<p>Package C State Limit set PackageCStateLimit</p>	<p>The amount of power available to the server components when they are idle. This can be one of the following:</p> <ul style="list-style-type: none"> • C0_state—The server provides all server components with full power at all times. This option maintains the highest level of performance and requires the greatest amount of power. • C1_state—When the CPU is idle, the system slightly reduces the power consumption. This option requires less power than C0 and allows the server to return quickly to high performance mode. • C3_state—When the CPU is idle, the system reduces the power consumption further than with the C1 option. This requires less power than C1 or C0, but it takes the server slightly longer to return to high performance mode. • C6_state—When the CPU is idle, the system reduces the power consumption further than with the C3 option. This option saves more power than C0, C1, or C3, but there may be performance issues until the server returns to full power. • C7_state—When the CPU is idle, the server makes a minimal amount of power available to the components. This option saves the maximum amount of power but it also requires the longest time for the server to return to high performance mode. • No_Limit—The server may enter any available C state. <p>Note This option is used only if CPU C State is enabled.</p>
<p>CPU C State set ProcessorCcxEnable</p>	<p>Whether the system can enter a power savings mode during idle periods. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The system remains in high performance state even when idle. • Enabled—The system can reduce power to system components such as the DIMMs and CPUs. The amount of power reduction is specified by the set PackageCStateLimit command.

Name	Description
C1E set ProcessorC1eEnable	Whether the CPU transitions to its minimum frequency when entering the C1 state. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The CPU continues to run at its maximum frequency in C1 state. • Enabled—The CPU transitions to its minimum frequency. This option saves the maximum amount of power in C1 state. <p>Note This option is used only if ProcessorCcxEnable is enabled.</p>

Memory Configuration Parameters

Name	Description
Select Memory RAS set SelectMemoryRAS	How the memory reliability, availability, and serviceability (RAS) is configured for the server. This can be one of the following: <ul style="list-style-type: none"> • Maximum_Performance—System performance is optimized. • Mirroring—System reliability is optimized by using half the system memory as backup. • Sparing—The system reserves some memory for use in the event a DIMM fails. If that happens, the server takes the DIMM offline and replaces it with the reserved memory. This option provides less redundancy than mirroring, but it leaves more of the memory available for programs running on the server.
NUMA Optimized set NUMAOptimize	Whether the BIOS supports Non-Uniform Memory Access (NUMA). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The BIOS does not support NUMA. • Enabled—The BIOS includes the ACPI tables that are required for NUMA-aware operating systems. If you enable this option, the system must disable Inter-Socket Memory interleaving on some platforms.

Name	Description
Sparing Mode set SparingMode	<p>The sparing mode used by the Cisco IMC. This can be one of the following:</p> <ul style="list-style-type: none"> • Rank_Sparing—The spared memory is allocated at the rank level. • DIMM Sparing—The spared memory is allocated at the DIMM level. <p>Note This option is used only if set SelectMemoryRAS is set to Sparing.</p>
Mirroring Mode set MirroringMode	<p>Mirroring is supported across Integrated Memory Controllers (IMCs) where one memory riser is mirrored with another. This can be one of the following:</p> <ul style="list-style-type: none"> • Intersocket—Each IMC is mirrored across two sockets. • Intrsocket—One IMC is mirrored with another IMC in the same socket. <p>Note This option is used only if SelectMemoryRAS is set to Mirroring.</p>
Patrol Scrub set PatrolScrub	<p>Whether the system actively searches for, and corrects, single bit memory errors even in unused portions of the memory on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The system checks for memory ECC errors only when the CPU reads or writes a memory address. • Enabled—The system periodically reads and writes memory searching for ECC errors. If any errors are found, the system attempts to fix them. This option may correct single bit errors before they become multi-bit errors, but it may adversely affect performance when the patrol scrub is running.
Patrol Scrub Interval set PatrolScrubDuration	<p>Controls the time interval between each patrol scrub memory access. A lower interval scrubs the memory more often but requires more memory bandwidth.</p> <p>Select a value between 5 and 23. The default value is 8.</p> <p>Note This option is used only if Patrol Scrub is enabled.</p>

Name	Description
CKE Low Policy set CkeLowPolicy	Controls the DIMM power savings mode policy. This can be one of the following: <ul style="list-style-type: none"> • Disabled—DIMMs do not enter power saving mode. • Slow—DIMMs can enter power saving mode, but the requirements are higher. Therefore, DIMMs enter power saving mode less frequently. • Fast—DIMMs enter power saving mode as often as possible. • Auto—The BIOS controls when a DIMM enters power saving mode based on the DIMM configuration.

Serial Port Configuration Parameters

Name	Description
Serial A Enable set Serial-PortA	Whether serial port A is enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The serial port is disabled. • Enabled—The serial port is enabled.

USB Configuration Parameters

Name	Description
Make Device Non-Bootable set MakeUSBDeviceNonBootable	Whether the server can boot from a USB device. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The server can boot from a USB device. • Enabled—The server cannot boot from a USB device.

PCI Configuration Parameters

Name	Description
Memory Mapped I/O Above 4GB set MemoryMappedIOAbove4GB	Whether to enable or disable MMIO above 4GB or not. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The server does not map I/O of 64-bit PCI devices to 4GB or greater address space. • Enabled—The server maps I/O of 64-bit PCI devices to 4GB or greater address space.
Onboard NIC <i>n</i> ROM set NIC-<i>n</i>-ROM	Whether the system loads the embedded PXE option ROM for the onboard NIC designated by <i>n</i> . This can be one of the following: <ul style="list-style-type: none"> • Disabled—PXE option ROM is not available for NIC <i>n</i>. • Enabled—PXE option ROM is available for NIC <i>n</i>.
PCIe OptionROMs set PciOptRomsDisable	Whether the server can use Option ROM present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for all PCIe slots are not available. • Enabled—The Option ROMs for all the PCIe slots are available. • UEFI_Only—The Option ROMs for slot <i>n</i> are available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> are available for legacy only.
PCIe Slot <i>n</i> ROM set Slot-<i>n</i>-ROM	Whether the server can use the Option ROMs present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
Onboard Gbit LOM set OnboardNic1	Whether Gbit LOM is enabled or disabled on the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Gbit LOM is not available. • Enabled—10Gbit LOM is available.

Name	Description
Onboard 10Gbit LOM set OnboardNic2	Whether 10Gbit LOM is enabled or disabled on the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—10Gbit LOM is not available. • Enabled—10Gbit LOM is available.
Sriov set SrIov	Whether SR-IOV (Single Root I/O Virtualization) is enabled or disabled on the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—SR-IOV is disabled. • Enabled—SR-IOV is enabled. <p>Note By default, SR-IOV option is enabled for C220, C240, C22 and C24 M3 servers.</p>
IOH Resource Allocation set IOHResource	Enables you to distribute 64KB of 16-bit IO resources between IOH0 and IOH1 as per system requirement. This can be one of the following: <ul style="list-style-type: none"> • IOH0 24k IOH1 40k— Allocates 24KB of 16-bit IO resources to IOH0 and 40KB of 16-bit IO resources to IOH1. • IOH0 32k IOH1 32k— Allocates 32KB of 16-bit IO resources to IOH0 and 32KB of 16-bit IO resources to IOH1. • IOH0 40k IOH1 24k— Allocates 40KB of 16-bit IO resources to IOH0 and 24KB of 16-bit IO resources to IOH1. • IOH0 48k IOH1 16k— Allocates 48KB of 16-bit IO resources to IOH0 and 16KB of 16-bit IO resources to IOH1. • IOH0 56k IOH1 8k— Allocates 56KB of 16-bit IO resources to IOH0 and 8KB of 16-bit IO resources to IOH1.

Server Management BIOS Parameters for C460 Servers

Name	Description
Assert NMI on SERR set AssertNMIONSERR	Whether the BIOS generates a non-maskable interrupt (NMI) and logs an error when a system error (SERR) occurs. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The BIOS does not generate an NMI or log an error when a SERR occurs. • Enabled—The BIOS generates an NMI and logs an error when a SERR occurs. You must enable this setting if you want to enable Assert_NMI_on_PERR.
Assert NMI on PERR set AssertNMIONPERR	Whether the BIOS generates a non-maskable interrupt (NMI) and logs an error when a processor bus parity error (PERR) occurs. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The BIOS does not generate an NMI or log an error when a PERR occurs. • Enabled—The BIOS generates an NMI and logs an error when a PERR occurs. You must enable Assert_NMI_on_SERR to use this setting.
Console Redirection set ConsoleRedir	Allows a serial port to be used for console redirection during POST and BIOS booting. After the BIOS has booted and the operating system is responsible for the server, console redirection is irrelevant and has no effect. This can be one of the following: <ul style="list-style-type: none"> • Disabled—No console redirection occurs during POST. • Serial_Port_A—Enables console redirection on serial port A during POST. <p>Note If you enable this option, you also disable the display of the Quiet Boot logo screen during POST.</p>
Flow Control set FlowCtrl	Whether a handshake protocol is used for flow control. Request to Send / Clear to Send (RTS/CTS) helps to reduce frame collisions that can be introduced by a hidden terminal problem. This can be one of the following: <ul style="list-style-type: none"> • None—No flow control is used. • RTS-CTS—RTS/CTS is used for flow control. <p>Note This setting must match the setting on the remote terminal application.</p>

Name	Description
Baud Rate set BaudRate	<p>What BAUD rate is used for the serial port transmission speed. If you disable Console Redirection, this option is not available. This can be one of the following:</p> <ul style="list-style-type: none"> • 9.6k—A 9600 BAUD rate is used. • 19.2k—A 19200 BAUD rate is used. • 38.4k—A 38400 BAUD rate is used. • 57.6k—A 57600 BAUD rate is used. • 115.2k—A 115200 BAUD rate is used. <p>Note This setting must match the setting on the remote terminal application.</p>
Terminal Type set TerminalType	<p>What type of character formatting is used for console redirection. This can be one of the following:</p> <ul style="list-style-type: none"> • PC-ANSI—The PC-ANSI terminal font is used. • VT100—A supported vt100 video terminal and its character set are used. • VT100-PLUS—A supported vt100-plus video terminal and its character set are used. • VT-UTF8—A video terminal with the UTF-8 character set is used. <p>Note This setting must match the setting on the remote terminal application.</p>
OS Boot Watchdog Timer Timeout set OSBootWatchdogTimerTimeOut	<p>If OS does not boot within the specified time, OS watchdog timer expires and system takes action according to timer policy. This can be one of the following:</p> <ul style="list-style-type: none"> • 5_Minutes—The OS watchdog timer expires 5 minutes after it begins to boot. • 10_Minutes—The OS watchdog timer expires 10 minutes after it begins to boot. • 15_Minutes—The OS watchdog timer expires 15 minutes after it begins to boot. • 20_Minutes—The OS watchdog timer expires 20 minutes after it begins to boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>

Name	Description
OS Boot Watchdog Policy set OSBootWatchdogTimerPolicy	What action the system takes if the watchdog timer expires. This can be one of the following: <ul style="list-style-type: none"> • Power_Off—The server is powered off if the watchdog timer expires during OS boot. • Reset—The server is reset if the watchdog timer expires during OS boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>
Legacy OS Redirection set LegacyOSRedir	Whether redirection from a legacy operating system, such as DOS, is enabled on the serial port. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The serial port enabled for console redirection is hidden from the legacy operating system. • Enabled—The serial port enabled for console redirection is visible to the legacy operating system.
OS Boot Watchdog Timer set OSBootWatchdogTimer	Whether the BIOS programs the watchdog timer with a specified timeout value. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The watchdog timer is not used to track how long the server takes to boot. • Enabled—The watchdog timer tracks how long the server takes to boot. If the server does not boot within the length of time specified by the set OSBootWatchdogTimerTimeout command, the Cisco IMC logs an error and takes the action specified by the set OSBootWatchdogTimerPolicy command.

C220 M4 and C240 M4 Servers

Main Tab for C220M4 and C240M4 Servers

Reboot Server Option

If you want your changes applied automatically after you click **Save Changes**, check the **Reboot Host Immediately** check box. Cisco IMC immediately reboots the server and applies your changes.

If you want to apply your changes at a later time, clear the **Reboot Host Immediately** check box. Cisco IMC stores the changes and applies them the next time the server reboots.

**Note**

If there are existing BIOS parameter changes pending, Cisco IMC automatically overwrites the stored values with the current settings when you click **Save Changes**.

Main BIOS Parameters

Name	Description
TPM Support set TPMAdminCtrl	<p>TPM (Trusted Platform Module) is a microchip designed to provide basic security-related functions primarily involving encryption keys. This option allows you to control the TPM Security Device support for the system. It can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The server does not use the TPM. • Enabled—The server uses the TPM. <p>Note We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

BIOS Configuration Dialog Box Button Bar**Important**

The buttons in this dialog box affect all BIOS parameters on all available tabs, not just the parameters on the tab that you are viewing.

Name	Description
Save Changes button	<p>Saves the settings for the BIOS parameters on all three tabs and closes the dialog box.</p> <p>If the Reboot Host Immediately check box is checked, the server is rebooted immediately and the new BIOS settings go into effect. Otherwise the changes are saved until the server is manually rebooted.</p>
Reset Values button	Restores the values for the BIOS parameters on all three tabs to the settings that were in effect when this dialog box was first opened.
Restore Defaults button	Sets the BIOS parameters on all three tabs to their default settings.
Cancel button	Closes the dialog box without making any changes.

Advanced Tab for C220M4 and C240M4 Servers

Reboot Server Option

If you want your changes applied automatically after you click **Save Changes**, check the **Reboot Host Immediately** check box. Cisco IMC immediately reboots the server and applies your changes.

If you want to apply your changes at a later time, clear the **Reboot Host Immediately** check box. Cisco IMC stores the changes and applies them the next time the server reboots.



Note

If there are existing BIOS parameter changes pending, Cisco IMC automatically overwrites the stored values with the current settings when you click **Save Changes**.

Processor Configuration Parameters

Name	Description
Intel Hyper-Threading Technology set <code>IntelHyperThread</code>	Whether the processor uses Intel Hyper-Threading Technology, which allows multithreaded software applications to execute threads in parallel within each processor. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not permit hyperthreading. • Enabled—The processor allows for the parallel execution of multiple threads. We recommend that you contact your operating system vendor to make sure the operating system supports this feature.
Number of Enabled Cores set <code>CoreMultiProcessing</code>	Allows you to disable one or more of the physical cores on the server. This can be one of the following: <ul style="list-style-type: none"> • All—Enables all physical cores. This also enables Hyper Threading on the associated logical processor cores. • 1 through n—Specifies the number of physical processor cores that can run on the server. Each physical core has an associated logical core. We recommend that you contact your operating system vendor to make sure the operating system supports this feature.

Name	Description
Execute Disable set ExecuteDisable	Classifies memory areas on the server to specify where application code can execute. As a result of this classification, the processor disables code execution if a malicious worm attempts to insert code in the buffer. This setting helps to prevent damage, worm propagation, and certain classes of malicious buffer overflow attacks. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not classify memory areas. • Enabled—The processor classifies memory areas. We recommend that you contact your operating system vendor to make sure the operating system supports this feature.
Intel VT set IntelVT	Whether the processor uses Intel Virtualization Technology (VT), which allows a platform to run multiple operating systems and applications in independent partitions. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not permit virtualization. • Enabled—The processor allows multiple operating systems in independent partitions. Note If you change this option, you must power cycle the server before the setting takes effect.
Intel VT-d set IntelVTD	Whether the processor uses Intel Virtualization Technology for Directed I/O (VT-d). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not use virtualization technology. • Enabled—The processor uses virtualization technology.
Intel VT-d Interrupt Remapping set InterruptRemap	Whether the processor supports Intel VT-d Interrupt Remapping. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support remapping. • Enabled—The processor uses VT-d Interrupt Remapping as required.
Intel VT-d PassThrough DMA set PassThroughDMA	Whether the processor supports Intel VT-d Pass-through DMA. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support pass-through DMA. • Enabled—The processor uses VT-d Pass-through DMA as required.

Name	Description
Intel VT-d Coherency Support set CoherencySupport	Whether the processor supports Intel VT-d Coherency. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support coherency. • Enabled—The processor uses VT-d Coherency as required.
Intel VT-d ATS Support set ATS	Whether the processor supports Intel VT-d Address Translation Services (ATS). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support ATS. • Enabled—The processor uses VT-d ATS as required.
CPU Performance set CPUPerformance	Sets the CPU performance profile for the server. The performance profile consists of the following options: <ul style="list-style-type: none"> • DCU Streamer Prefetcher • DCU IP Prefetcher • Hardware Prefetcher • Adjacent Cache-Line Prefetch This can be one of the following: <ul style="list-style-type: none"> • Enterprise—All options are enabled. • High_Throughput—Only the DCU IP Prefetcher is enabled. The rest of the options are disabled. • HPC—All options are enabled. This setting is also known as high performance computing. • Custom—All performance profile options can be configured from the BIOS setup on the server. In addition, the Hardware Prefetcher and Adjacent Cache-Line Prefetch options can be configured in the fields below.
Hardware Prefetcher set HardwarePrefetch	Whether the processor allows the Intel hardware prefetcher to fetch streams of data and instruction from memory into the unified second-level cache when necessary. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The hardware prefetcher is not used. • Enabled—The processor uses the hardware prefetcher when cache issues are detected.

Name	Description
Adjacent Cache Line Prefetcher set AdjacentCacheLinePrefetch	Whether the processor fetches cache lines in even/odd pairs instead of fetching just the required line. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor only fetches the required line. • Enabled— The processor fetches both the required line and its paired line.
DCU Streamer Prefetch set DcuStreamerPrefetch	Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not try to anticipate cache read requirements and only fetches explicitly requested lines. • Enabled—The DCU prefetcher analyzes the cache read pattern and prefetches the next line in the cache if it determines that it may be needed.
DCU IP Prefetcher set DcuIpPrefetch	Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not preload any cache data. • Enabled—The DCU IP prefetcher preloads the L1 cache with the data it determines to be the most relevant.
Direct Cache Access Support set DirectCacheAccess	Allows processors to increase I/O performance by placing data from I/O devices directly into the processor cache. This setting helps to reduce cache misses. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Data from I/O devices is not placed directly into the processor cache. • Enabled—Data from I/O devices is placed directly into the processor cache.

Name	Description
<p>Power Technology set CPUPowerManagement</p>	<p>Enables you to configure the CPU power management settings for the following options:</p> <ul style="list-style-type: none"> • Enhanced Intel Speedstep Technology • Intel Turbo Boost Technology • Processor Power State C6 <p>Power Technology can be one of the following:</p> <ul style="list-style-type: none"> • Custom—The server uses the individual settings for the BIOS parameters mentioned above. You must select this option if you want to change any of these BIOS parameters. • Disabled—The server does not perform any CPU power management and any settings for the BIOS parameters mentioned above are ignored. • Energy_Efficient—The server determines the best settings for the BIOS parameters mentioned above and ignores the individual settings for these parameters.
<p>Enhanced Intel Speedstep Technology set EnhancedIntelSpeedStep</p>	<p>Whether the processor uses Enhanced Intel SpeedStep Technology, which allows the system to dynamically adjust processor voltage and core frequency. This technology can result in decreased average power consumption and decreased average heat production. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor never dynamically adjusts its voltage or frequency. • Enabled—The processor utilizes Enhanced Intel SpeedStep Technology and enables all supported processor sleep states to further conserve power. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p> <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>

Name	Description
Intel Turbo Boost Technology set IntelTurboBoostTech	<p>Whether the processor uses Intel Turbo Boost Technology, which allows the processor to automatically increase its frequency if it is running below power, temperature, or voltage specifications. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not increase its frequency automatically. • Enabled—The processor utilizes Turbo Boost Technology if required. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor C3 Report set ProcessorC3Report	<p>Whether the BIOS sends the C3 report to the operating system. When the OS receives the report, it can transition the processor into the lower C3 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—BIOS does not send C3 report. • Enabled—BIOS sends the C3 report, allowing the OS to transition the processor to the C3 low power state. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor C6 Report set ProcessorC6Report	<p>Whether the BIOS sends the C6 report to the operating system. When the OS receives the report, it can transition the processor into the lower C6 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not send the C6 report. • Enabled—The BIOS sends the C6 report, allowing the OS to transition the processor to the C6 low power state. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C1 Enhanced set ProcessorC1EReport	<p>Whether the CPU transitions to its minimum frequency when entering the C1 state. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The CPU continues to run at its maximum frequency in C1 state. • Enabled—The CPU transitions to its minimum frequency. This option saves the maximum amount of power in C1 state.

Name	Description
P-STATE Coordination set PsdCoordType	<p>Allows you to define how BIOS communicates the P-state support model to the operating system. There are 3 models as defined by the Advanced Configuration and Power Interface (ACPI) specification.</p> <ul style="list-style-type: none"> • HW_ALL—The processor hardware is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package). • SW_ALL—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a physical package), and must initiate the transition on all of the logical processors. • SW_ANY—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package), and may initiate the transition on any of the logical processors in the domain. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Energy Performance Tuning set PwrPerfTuning	<p>Allows you to choose BIOS or Operating System for energy performance bias tuning. This can be one of the following:</p> <ul style="list-style-type: none"> • OS— Chooses OS for energy performance tuning. • BIOS— Chooses BIOS for energy performance tuning.
Energy Performance set CpuEngPerfBias	<p>Allows you to determine whether system performance or energy efficiency is more important on this server. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced_Energy • Balanced_Performance • Energy_Efficient • Performance

Name	Description
<p>Package C State Limit set PackageCStateLimit</p>	<p>The amount of power available to the server components when they are idle. This can be one of the following:</p> <ul style="list-style-type: none"> • C0_state—The server provides all server components with full power at all times. This option maintains the highest level of performance and requires the greatest amount of power. • C1_state—When the CPU is idle, the system slightly reduces the power consumption. This option requires less power than C0 and allows the server to return quickly to high performance mode. • C3_state—When the CPU is idle, the system reduces the power consumption further than with the C1 option. This requires less power than C1 or C0, but it takes the server slightly longer to return to high performance mode. • C6_state—When the CPU is idle, the system reduces the power consumption further than with the C3 option. This option saves more power than C0, C1, or C3, but there may be performance issues until the server returns to full power. • C7_state—When the CPU is idle, the server makes a minimal amount of power available to the components. This option saves the maximum amount of power but it also requires the longest time for the server to return to high performance mode. • No_Limit—The server may enter any available C state.
<p>Extended APIC set LocalX2Apic</p>	<p>Allows you to enable or disable extended APIC support. This can be one of the following:</p> <ul style="list-style-type: none"> • XAPIC—Enables APIC support. • X2APIC—Enables APIC and also enables Intel VT-d and Interrupt Remapping .
<p>Workload Configuration set WorkLdConfig</p>	<p>Allows you to set a parameter to optimize workload characterization. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced— Chooses balanced option for optimization. • I/O Sensitive— Chooses I/O sensitive option for optimization. <p>Note We recommend you to set the workload configuration to Balanced.</p>

Memory Configuration Parameters

Name	Description
Select Memory RAS set SelectMemoryRAS	<p>How the memory reliability, availability, and serviceability (RAS) is configured for the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Maximum_Performance—System performance is optimized. • Mirroring—System reliability is optimized by using half the system memory as backup. • Lockstep—If the DIMM pairs in the server have an identical type, size, and organization and are populated across the SMI channels, you can enable lockstep mode to minimize memory access latency and provide better performance. This option offers better system performance than Mirroring and better reliability than Maximum Performance but lower reliability than Mirroring and lower system performance than Maximum Performance.
NUMA set NUMAOptimize	<p>Whether the BIOS supports Non-Uniform Memory Access (NUMA). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not support NUMA. • Enabled—The BIOS includes the ACPI tables that are required for NUMA-aware operating systems. If you enable this option, the system must disable Inter-Socket Memory interleaving on some platforms.
Channel Interleaving set ChannelInterLeave	<p>Whether the CPU divides memory blocks and spreads contiguous portions of data across interleaved channels to enable simultaneous read operations. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some channel interleaving is used. • 2_Way • 3_Way • 4_Way—The maximum amount of channel interleaving is used.

Name	Description
Rank Interleaving set RankInterLeave	Whether the CPU interleaves physical ranks of memory so that one rank can be accessed while another is being refreshed. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some rank interleaving is used. • 2_Way • 4_Way • 8_Way—The maximum amount of rank interleaving is used.
Patrol Scrub set PatrolScrub	Whether the system actively searches for, and corrects, single bit memory errors even in unused portions of the memory on the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The system checks for memory ECC errors only when the CPU reads or writes a memory address. • Enabled—The system periodically reads and writes memory searching for ECC errors. If any errors are found, the system attempts to fix them. This option may correct single bit errors before they become multi-bit errors, but it may adversely affect performance when the patrol scrub is running.
Demand Scrub set DemandScrub	Whether the system corrects single bit memory errors encountered when the CPU or I/O makes a demand read. This can be one of the following: <ul style="list-style-type: none"> • Disabled— Single bit memory errors are not corrected. • Enabled— Single bit memory errors are corrected in memory and the corrected data is set in response to the demand read.

Name	Description
Altitude set Altitude	<p>The approximate number of meters above sea level at which the physical server is installed. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU determines the physical elevation. • 300_M—The server is approximately 300 meters above sea level. • 900_M—The server is approximately 900 meters above sea level. • 1500_M—The server is approximately 1500 meters above sea level. • 3000_M—The server is approximately 3000 meters above sea level.

QPI Configuration Parameters

Name	Description
QPI Link Frequency Select set QPILinkFrequency	<p>The Intel QuickPath Interconnect (QPI) link frequency, in gigatransfers per second (GT/s). This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU determines the QPI link frequency. • 6.4_GT/s • 7.2_GT/s • 8.0_GT/s
QPI Snoop Mode set QpiSnoopMode	<p>The Intel QuickPath Interconnect (QPI) snoop mode. This can be one of the following:</p> <ul style="list-style-type: none"> • Home Snoop—The snoop is always spawned by the home agent (centralized ring stop) for the memory controller. This mode has a higher local latency than early snoop, but it provides extra resources for a larger number of outstanding transactions. • Cluster on Die—Enables Cluster On Die. When enabled LLC is split into two parts with an independent caching agent for each. This helps increase the performance in some workloads. This mode is available only for processors that have 10 or more cores. It is the best mode for highly NUMA optimized workloads. • Early Snoop—The distributed cache ring stops can send a snoop probe or a request to another caching agent directly. This mode has lower latency and it is best for workloads that have shared data sets across threads and can benefit from a cache-to-cache transfer, or for workloads that are not NUMA optimized.

USB Configuration Parameters

Name	Description
Legacy USB Support set LegacyUSBSupport	Whether the system supports legacy USB devices. This can be one of the following: <ul style="list-style-type: none"> • Disabled—USB devices are only available to EFI applications. • Enabled—Legacy USB support is always available. • Auto—Disables legacy USB support if no USB devices are connected.
Port 60/64 Emulation set UsbEmul6064	Whether the system supports 60h/64h emulation for complete USB keyboard legacy support. This can be one of the following: <ul style="list-style-type: none"> • Disabled—60h/64 emulation is not supported. • Enabled—60h/64 emulation is supported. <p>You should select this option if you are using a non-USB aware operating system on the server.</p>
xHCI Mode set PchUsb30Mode	Whether the xHCI controller legacy support is enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the xHCI controller legacy support. • Enabled—Enables the xHCI controller legacy support.

PCI Configuration Parameters

Name	Description
Memory Mapped I/O Above 4GB set MemoryMappedIOAbove4GB	Whether to enable or disable MMIO above 4GB or not. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The server does not map I/O of 64-bit PCI devices to 4GB or greater address space. • Enabled—The server maps I/O of 64-bit PCI devices to 4GB or greater address space. <p>Note PCI devices that are 64-bit compliant but use a legacy option ROM may not function correctly with this setting enabled.</p>

Name	Description
Sriov set Sriov	Whether SR-IOV (Single Root I/O Virtualization) is enabled or disabled on the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—SR-IOV is disabled. • Enabled—SR-IOV is enabled.

Serial Configuration Parameters

Name	Description
Out-of-Band Mgmt Port set comSpcrEnable	Allows you to configure the COM port 0 that can be used for Windows Emergency Management services. ACPI SPCR table is reported based on this setup option. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Configures the COM port 0 as a general purpose port for use with the Windows Operating System. • Enabled—Configures the COM port 0 as a remote management port for Windows Emergency Management services.
Console Redirection set ConsoleRedir	Allows a serial port to be used for console redirection during POST and BIOS booting. After the BIOS has booted and the operating system is responsible for the server, console redirection is irrelevant and has no effect. This can be one of the following: <ul style="list-style-type: none"> • Disabled—No console redirection occurs during POST. • COM_0—Enables console redirection on COM port 0 during POST. • COM_1—Enables console redirection on COM port 1 during POST.
Terminal Type set TerminalType	What type of character formatting is used for console redirection. This can be one of the following: <ul style="list-style-type: none"> • PC-ANSI—The PC-ANSI terminal font is used. • VT100—A supported vt100 video terminal and its character set are used. • VT100+—A supported vt100-plus video terminal and its character set are used. • VT-UTF8—A video terminal with the UTF-8 character set is used. <p>Note This setting must match the setting on the remote terminal application.</p>

Name	Description
Bits per second set BaudRate	<p>What BAUD rate is used for the serial port transmission speed. If you disable Console Redirection, this option is not available. This can be one of the following:</p> <ul style="list-style-type: none"> • 9600—A 9,600 BAUD rate is used. • 19200—A 19,200 BAUD rate is used. • 38400—A 38,400 BAUD rate is used. • 57600—A 57,600 BAUD rate is used. • 115200—A 115,200 BAUD rate is used. <p>Note This setting must match the setting on the remote terminal application.</p>
Flow Control set FlowCtrl	<p>Whether a handshake protocol is used for flow control. Request to Send / Clear to Send (RTS/CTS) helps to reduce frame collisions that can be introduced by a hidden terminal problem. This can be one of the following:</p> <ul style="list-style-type: none"> • None—No flow control is used. • Hardware_RTS/CTS—RTS/CTS is used for flow control. <p>Note This setting must match the setting on the remote terminal application.</p>
Putty KeyPad set PuttyFunctionKeyPad	<p>Allows you to change the action of the PuTTY function keys and the top row of the numeric keypad. This can be one of the following:</p> <ul style="list-style-type: none"> • VT100—The function keys generate ESC OP through ESC O[. • LINUX—Mimics the Linux virtual console. Function keys F6 to F12 behave like the default mode, but F1 to F5 generate ESC [[A through ESC [[E. • XTERMR6—Function keys F5 to F12 behave like the default mode. Function keys F1 to F4 generate ESC OP through ESC OS, which are the sequences produced by the top row of the keypad on Digital terminals. • SCO—The function keys F1 to F12 generate ESC [M through ESC [X. The function and shift keys generate ESC [Y through ESC [j. The control and function keys generate ESC [k through ESC [v. The shift, control and function keys generate ESC [w through ESC [{. • ESCN—The default mode. The function keys match the general behavior of Digital terminals. The function keys generate sequences such as ESC [11~ and ESC [12~. • VT400—The function keys behave like the default mode. The top row of the numeric keypad generates ESC OP through ESC OS.

Name	Description
Redirection After BIOS POST set RedirectionAfterPOST	Whether BIOS console redirection should be active after BIOS POST is complete and control given to the OS bootloader. This can be one of the following: <ul style="list-style-type: none"> • Always_Enabled—BIOS Legacy console redirection is active during the OS boot and run time. • Bootloader—BIOS Legacy console redirection is disabled before giving control to the OS boot loader.

LOM and PCIe Slots Configuration Parameters

Name	Description
CDN Support for VIC set CdnEnable	Whether the Ethernet Network naming convention is according to Consistent Device Naming (CDN) or the traditional way of naming conventions. This can be one of the following: <ul style="list-style-type: none"> • Disabled— CDN support for VIC cards is disabled. • Enabled— CDN support is enabled for VIC cards. <p>Note CDN support for VIC cards work with Windows 2012 or the latest OS only.</p>
PCI ROM CLP set PciRomClp	PCI ROM Command Line Protocol (CLP) controls the execution of different Option ROMs such as PxE and iSCSI that are present in the card. By default, it is disabled. <ul style="list-style-type: none"> • Enabled— Enables you to configure execution of different option ROMs such as PxE and iSCSI for an individual ports separately. • Disabled—The default option. You cannot choose different option ROMs. A default option ROM is executed during PCI enumeration.
PCH SATA Mode set SataModeSelect	This options allows you to select the PCH SATA mode. This can be one of the following: <ul style="list-style-type: none"> • AHCI—Sets both SATA and sSATA controllers are set to AHCI mode. • Disabled—Disables both SATA and sSATA controllers. • LSI SW Raid— Sets both SATA and sSATA controllers to raid mode for LSI SW Raid

Name	Description
All Onboard LOM Ports set AllLomPortControl	Whether all LOM ports are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—All LOM ports are disabled. • Enabled—All LOM ports are enabled.
LOM Port <i>n</i> OptionROM set LomOpromControlPort <i>n</i>	Whether Option ROM is available on the LOM port designated by <i>n</i> . This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
All PCIe Slots OptionROM set PcieOptionROMs	Whether the server can use Option ROM present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
PCIe Slot:<i>n</i> OptionROM set PcieSlot <i>n</i> OptionROM	Whether the server can use the Option ROMs present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.

Name	Description
PCIe Slot:MLOM OptionROM set PcieSlotMLOMOptionROM	<p>This options allows you to control the Option ROM execution of the PCIe adapter connected to the MLOM slot. This can be one of the following:</p> <ul style="list-style-type: none"> • Enabled—Executes both legacy and UEFI Option ROM. • Disabled—Both legacy and UEFI Option ROM will not be executed. • UEFI Only—Executes only UEFI Option ROM. • Legacy Only—Executes only Legacy Option ROM.
PCIe Slot:HBA OptionROM set PcieSlotHBAOptionROM	<p>This options allows you to control the Option ROM execution of the PCIe adapter connected to the HBA slot. This can be one of the following:</p> <ul style="list-style-type: none"> • Enabled—Executes both legacy and UEFI Option ROM. • Disabled—Both legacy and UEFI Option ROM will not be executed. • UEFI Only—Executes only UEFI Option ROM. • Legacy Only—Executes only Legacy Option ROM.
PCIe Slot:N1 OptionROM set PcieSlotN1OptionROM	<p>This options allows you to control the Option ROM execution of the PCIe adapter connected to the SSD:NVMe1 slot. This can be one of the following:</p> <ul style="list-style-type: none"> • Enabled—Executes both legacy and UEFI Option ROM. • Disabled—Both legacy and UEFI Option ROM will not be executed. • UEFI Only—Executes only UEFI Option ROM. • Legacy Only—Executes only Legacy Option ROM.
PCIe Slot:N2 OptionROM set PcieSlotN2OptionROM	<p>This options allows you to control the Option ROM execution of the PCIe adapter connected to the SSD:NVMe2 slot. This can be one of the following:</p> <ul style="list-style-type: none"> • Enabled—Executes both legacy and UEFI Option ROM. • Disabled—Both legacy and UEFI Option ROM will not be executed. • UEFI Only—Executes only UEFI Option ROM. • Legacy Only—Executes only Legacy Option ROM.

Name	Description
PCIe Slot:N2 OptionROM set PcieSlotN2OptionROM	<p>This options allows you to control the Option ROM execution of the PCIe adapter connected to the SSD:NVMe2 slot. This can be one of the following:</p> <ul style="list-style-type: none"> • Enabled—Executes both legacy and UEFI Option ROM. • Disabled—Both legacy and UEFI Option ROM will not be executed. • UEFI Only—Executes only UEFI Option ROM. • Legacy Only—Executes only Legacy Option ROM.
PCIe Slot:HBA Link Speed PCIe SlotHBALinkSpeed	<p>This option allows you to restrict the maximum speed of an adapter card installed in PCIe HBA slot. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto— System selects the maximum speed allowed. • GEN1—2.5GT/s (gigatransfers per second) is the maximum speed allowed. • GEN2—5GT/s is the maximum speed allowed. • GEN3—8GT/s is the maximum speed allowed. • Disabled—The maximum speed is not restricted.

BIOS Configuration Dialog Box Button Bar



Important

The buttons in this dialog box affect all BIOS parameters on all available tabs, not just the parameters on the tab that you are viewing.

Name	Description
Save Changes button	<p>Saves the settings for the BIOS parameters on all three tabs and closes the dialog box.</p> <p>If the Reboot Host Immediately check box is checked, the server is rebooted immediately and the new BIOS settings go into effect. Otherwise the changes are saved until the server is manually rebooted.</p>
Reset Values button	Restores the values for the BIOS parameters on all three tabs to the settings that were in effect when this dialog box was first opened.
Restore Defaults button	Sets the BIOS parameters on all three tabs to their default settings.
Cancel button	Closes the dialog box without making any changes.

Server Management Tab for C220M4 and C240M4 Servers

Reboot Server Option

If you want your changes applied automatically after you click **Save Changes**, check the **Reboot Host Immediately** check box. Cisco IMC immediately reboots the server and applies your changes.

If you want to apply your changes at a later time, clear the **Reboot Host Immediately** check box. Cisco IMC stores the changes and applies them the next time the server reboots.



Note

If there are existing BIOS parameter changes pending, Cisco IMC automatically overwrites the stored values with the current settings when you click **Save Changes**.

Server Management BIOS Parameters

Name	Description
FRB-2 Timer set FRB-2	Whether the FRB2 timer is used by Cisco IMC to recover the system if it hangs during POST. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The FRB2 timer is not used. • Enabled—The FRB2 timer is started during POST and used to recover the system if necessary.
OS Watchdog Timer set OSBootWatchdogTimer	Whether the BIOS programs the watchdog timer with a specified timeout value. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The watchdog timer is not used to track how long the server takes to boot. • Enabled—The watchdog timer tracks how long the server takes to boot. If the server does not boot within the length of time specified by the set OSBootWatchdogTimerTimeout command, the Cisco IMC logs an error and takes the action specified by the set OSBootWatchdogTimerPolicy command.

Name	Description
OS Watchdog Timer Timeout set OSBootWatchdogTimerTimeOut	<p>If OS does not boot within the specified time, OS watchdog timer expires and system takes action according to timer policy. This can be one of the following:</p> <ul style="list-style-type: none"> • 5_Minutes—The OS watchdog timer expires 5 minutes after it begins to boot. • 10_Minutes—The OS watchdog timer expires 10 minutes after it begins to boot. • 15_Minutes—The OS watchdog timer expires 15 minutes after it begins to boot. • 20_Minutes—The OS watchdog timer expires 20 minutes after it begins to boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>
OS Watchdog Timer Policy set OSBootWatchdogTimerPolicy	<p>What action the system takes if the watchdog timer expires. This can be one of the following:</p> <ul style="list-style-type: none"> • Do_Nothing—The server takes no action if the watchdog timer expires during OS boot. • Power_Down—The server is powered off if the watchdog timer expires during OS boot. • Reset—The server is reset if the watchdog timer expires during OS boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>

BIOS Configuration Dialog Box Button Bar



Important The buttons in this dialog box affect all BIOS parameters on all available tabs, not just the parameters on the tab that you are viewing.

Name	Description
Save Changes button	<p>Saves the settings for the BIOS parameters on all three tabs and closes the dialog box.</p> <p>If the Reboot Host Immediately check box is checked, the server is rebooted immediately and the new BIOS settings go into effect. Otherwise the changes are saved until the server is manually rebooted.</p>
Reset Values button	Restores the values for the BIOS parameters on all three tabs to the settings that were in effect when this dialog box was first opened.

Name	Description
Restore Defaults button	Sets the BIOS parameters on all three tabs to their default settings.
Cancel button	Closes the dialog box without making any changes.

C3160 Servers

Main BIOS Parameters for C3160 Servers

Main BIOS Parameters

Name	Description
TPM Support set TPMAdminCtrl	<p>TPM (Trusted Platform Module) is a microchip designed to provide basic security-related functions primarily involving encryption keys. This option allows you to control the TPM Security Device support for the system. It can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The server does not use the TPM. • Enabled—The server uses the TPM. <p>Note We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Advanced BIOS Parameters for C3160 Servers

Processor Configuration Parameters

Name	Description
Intel Hyper-Threading Technology set IntelHyperThread	<p>Whether the processor uses Intel Hyper-Threading Technology, which allows multithreaded software applications to execute threads in parallel within each processor. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit hyperthreading. • Enabled—The processor allows for the parallel execution of multiple threads. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>

Name	Description
Number of Enabled Cores set CoreMultiProcessing	<p>Allows you to disable one or more of the physical cores on the server. This can be one of the following:</p> <ul style="list-style-type: none"> • All—Enables all physical cores. This also enables Hyper Threading on the associated logical processor cores. • 1 through <i>n</i>—Specifies the number of physical processor cores that can run on the server. Each physical core has an associated logical core. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Execute Disable set ExecuteDisable	<p>Classifies memory areas on the server to specify where application code can execute. As a result of this classification, the processor disables code execution if a malicious worm attempts to insert code in the buffer. This setting helps to prevent damage, worm propagation, and certain classes of malicious buffer overflow attacks. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not classify memory areas. • Enabled—The processor classifies memory areas. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p>
Intel VT set IntelVT	<p>Whether the processor uses Intel Virtualization Technology (VT), which allows a platform to run multiple operating systems and applications in independent partitions. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not permit virtualization. • Enabled—The processor allows multiple operating systems in independent partitions. <p>Note If you change this option, you must power cycle the server before the setting takes effect.</p>
Intel VT-d set IntelVTD	<p>Whether the processor uses Intel Virtualization Technology for Directed I/O (VT-d). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not use virtualization technology. • Enabled—The processor uses virtualization technology.

Name	Description
Intel VT-d Coherency Support set CoherencySupport	Whether the processor supports Intel VT-d Coherency. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support coherency. • Enabled—The processor uses VT-d Coherency as required.
Intel VT-d ATS Support set ATS	Whether the processor supports Intel VT-d Address Translation Services (ATS). This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not support ATS. • Enabled—The processor uses VT-d ATS as required.
CPU Performance set CPUPerformance	Sets the CPU performance profile for the server. The performance profile consists of the following options: <ul style="list-style-type: none"> • DCU Streamer Prefetcher • DCU IP Prefetcher • Hardware Prefetcher • Adjacent Cache-Line Prefetch This can be one of the following: <ul style="list-style-type: none"> • Enterprise—All options are enabled. • High_Throughput—Only the DCU IP Prefetcher is enabled. The rest of the options are disabled. • HPC—All options are enabled. This setting is also known as high performance computing. • Custom—All performance profile options can be configured from the BIOS setup on the server. In addition, the Hardware Prefetcher and Adjacent Cache-Line Prefetch options can be configured in the fields below.
Hardware Prefetcher set HardwarePrefetch	Whether the processor allows the Intel hardware prefetcher to fetch streams of data and instruction from memory into the unified second-level cache when necessary. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The hardware prefetcher is not used. • Enabled—The processor uses the hardware prefetcher when cache issues are detected.

Name	Description
Adjacent Cache Line Prefetcher set AdjacentCacheLinePrefetch	Whether the processor fetches cache lines in even/odd pairs instead of fetching just the required line. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor only fetches the required line. • Enabled— The processor fetches both the required line and its paired line.
DCU Streamer Prefetch set DcuStreamerPrefetch	Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not try to anticipate cache read requirements and only fetches explicitly requested lines. • Enabled—The DCU prefetcher analyzes the cache read pattern and prefetches the next line in the cache if it determines that it may be needed.
DCU IP Prefetcher set DcuIpPrefetch	Whether the processor uses the DCU IP Prefetch mechanism to analyze historical cache access patterns and preload the most relevant lines in the L1 cache. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The processor does not preload any cache data. • Enabled—The DCU IP prefetcher preloads the L1 cache with the data it determines to be the most relevant.
Direct Cache Access Support set DirectCacheAccess	Allows processors to increase I/O performance by placing data from I/O devices directly into the processor cache. This setting helps to reduce cache misses. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Data from I/O devices is not placed directly into the processor cache. • Enabled—Data from I/O devices is placed directly into the processor cache.

Name	Description
<p>Power Technology set CPUPowerManagement</p>	<p>Enables you to configure the CPU power management settings for the following options:</p> <ul style="list-style-type: none"> • Enhanced Intel Speedstep Technology • Intel Turbo Boost Technology • Processor Power State C6 <p>Power Technology can be one of the following:</p> <ul style="list-style-type: none"> • Custom—The server uses the individual settings for the BIOS parameters mentioned above. You must select this option if you want to change any of these BIOS parameters. • Disabled—The server does not perform any CPU power management and any settings for the BIOS parameters mentioned above are ignored. • Energy_Efficient—The server determines the best settings for the BIOS parameters mentioned above and ignores the individual settings for these parameters.
<p>Enhanced Intel Speedstep Technology set EnhancedIntelSpeedStep</p>	<p>Whether the processor uses Enhanced Intel SpeedStep Technology, which allows the system to dynamically adjust processor voltage and core frequency. This technology can result in decreased average power consumption and decreased average heat production. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor never dynamically adjusts its voltage or frequency. • Enabled—The processor utilizes Enhanced Intel SpeedStep Technology and enables all supported processor sleep states to further conserve power. <p>We recommend that you contact your operating system vendor to make sure the operating system supports this feature.</p> <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>

Name	Description
Intel Turbo Boost Technology set IntelTurboBoostTech	<p>Whether the processor uses Intel Turbo Boost Technology, which allows the processor to automatically increase its frequency if it is running below power, temperature, or voltage specifications. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The processor does not increase its frequency automatically. • Enabled—The processor utilizes Turbo Boost Technology if required. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C6 set ProcessorC6Report	<p>Whether the BIOS sends the C6 report to the operating system. When the OS receives the report, it can transition the processor into the lower C6 power state to decrease energy usage while maintaining optimal processor performance. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not send the C6 report. • Enabled—The BIOS sends the C6 report, allowing the OS to transition the processor to the C6 low power state. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Processor Power State C1 Enhanced set ProcessorC1EReport	<p>Whether the CPU transitions to its minimum frequency when entering the C1 state. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The CPU continues to run at its maximum frequency in C1 state. • Enabled—The CPU transitions to its minimum frequency. This option saves the maximum amount of power in C1 state.
Frequency Floor Override set CpuFreqFloor	<p>Whether the CPU is allowed to drop below the maximum non-turbo frequency when idle. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled— The CPU can drop below the maximum non-turbo frequency when idle. This option decreases power consumption but may reduce system performance. • Enabled— The CPU cannot drop below the maximum non-turbo frequency when idle. This option improves system performance but may increase power consumption.

Name	Description
P-STATE Coordination set PsdCoordType	<p>Allows you to define how BIOS communicates the P-state support model to the operating system. There are 3 models as defined by the Advanced Configuration and Power Interface (ACPI) specification.</p> <ul style="list-style-type: none"> • HW_ALL—The processor hardware is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package). • SW_ALL—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a physical package), and must initiate the transition on all of the logical processors. • SW_ANY—The OS Power Manager (OSPM) is responsible for coordinating the P-state among logical processors with dependencies (all logical processors in a package), and may initiate the transition on any of the logical processors in the domain. <p>Note CPUPowerManagement must be set to Custom or the server ignores the setting for this parameter.</p>
Energy Performance set CpuEngPerfBias	<p>Allows you to determine whether system performance or energy efficiency is more important on this server. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced_Energy • Balanced_Performance • Energy_Efficient • Performance

Memory Configuration Parameters

Name	Description
Select Memory RAS set SelectMemoryRAS	<p>How the memory reliability, availability, and serviceability (RAS) is configured for the server. This can be one of the following:</p> <ul style="list-style-type: none"> • Maximum_Performance—System performance is optimized. • Mirroring—System reliability is optimized by using half the system memory as backup. • Lockstep—If the DIMM pairs in the server have an identical type, size, and organization and are populated across the SMI channels, you can enable lockstep mode to minimize memory access latency and provide better performance. This option offers better system performance than Mirroring and better reliability than Maximum Performance but lower reliability than Mirroring and lower system performance than Maximum Performance.
DRAM Clock Throttling set DRAMClockThrottling	<p>Allows you to tune the system settings between the memory bandwidth and power consumption. This can be one of the following:</p> <ul style="list-style-type: none"> • Balanced—DRAM clock throttling is reduced, providing a balance between performance and power. • Performance—DRAM clock throttling is disabled, providing increased memory bandwidth at the cost of additional power. • Energy_Efficient—DRAM clock throttling is increased to improve energy efficiency.
NUMA set NUMAOptimize	<p>Whether the BIOS supports Non-Uniform Memory Access (NUMA). This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—The BIOS does not support NUMA. • Enabled—The BIOS includes the ACPI tables that are required for NUMA-aware operating systems. If you enable this option, the system must disable Inter-Socket Memory interleaving on some platforms.

Name	Description
<p>Low Voltage DDR Mode set LvDDRMode</p>	<p>Whether the system prioritizes low voltage or high frequency memory operations. This can be one of the following:</p> <ul style="list-style-type: none"> • Power_Saving_Mode—The system prioritizes low voltage memory operations over high frequency memory operations. This mode may lower memory frequency in order to keep the voltage low. • Performance_Mode—The system prioritizes high frequency operations over low voltage operations.
<p>DRAM Refresh rate set DramRefreshRate</p>	<p>Allows you to set the rate at which the DRAM cells are refreshed. This can be one of the following:</p> <ul style="list-style-type: none"> • 1x—DRAM cells are refreshed every 64ms. • 2x—DRAM cells are refreshed every 32ms. • 3x—DRAM cells are refreshed every 21ms. • 4x—DRAM cells are refreshed every 16ms. • Auto—DRAM cells refresh rate is automatically chosen by the BIOS based on the system configuration. This is the recommended setting for this parameter.
<p>Channel Interleaving set ChannelInterLeave</p>	<p>Whether the CPU divides memory blocks and spreads contiguous portions of data across interleaved channels to enable simultaneous read operations. This can be one of the following:</p> <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some channel interleaving is used. • 2_Way • 3_Way • 4_Way—The maximum amount of channel interleaving is used.

Name	Description
Rank Interleaving set RankInterLeave	Whether the CPU interleaves physical ranks of memory so that one rank can be accessed while another is being refreshed. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines what interleaving is done. • 1_Way—Some rank interleaving is used. • 2_Way • 4_Way • 8_Way—The maximum amount of rank interleaving is used.
Patrol Scrub set PatrolScrub	Whether the system actively searches for, and corrects, single bit memory errors even in unused portions of the memory on the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The system checks for memory ECC errors only when the CPU reads or writes a memory address. • Enabled—The system periodically reads and writes memory searching for ECC errors. If any errors are found, the system attempts to fix them. This option may correct single bit errors before they become multi-bit errors, but it may adversely affect performance when the patrol scrub is running.
Demand Scrub set DemandScrub	Whether the system corrects single bit memory errors encountered when the CPU or I/O makes a demand read. This can be one of the following: <ul style="list-style-type: none"> • Disabled— Single bit memory errors are not corrected. • Enabled— Single bit memory errors are corrected in memory and the corrected data is set in response to the demand read.

Name	Description
Altitude set Altitude	The approximate number of meters above sea level at which the physical server is installed. This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines the physical elevation. • 300_M—The server is approximately 300 meters above sea level. • 900_M—The server is approximately 900 meters above sea level. • 1500_M—The server is approximately 1500 meters above sea level. • 3000_M—The server is approximately 3000 meters above sea level.

QPI Configuration Parameters

Name	Description
QPI Link Frequency Select set QPILinkFrequency	The Intel QuickPath Interconnect (QPI) link frequency, in gigatransfers per second (GT/s). This can be one of the following: <ul style="list-style-type: none"> • Auto—The CPU determines the QPI link frequency. • 6.4_GT/s • 7.2_GT/s • 8.0_GT/s

SATA Configuration Parameters

Name	Description
SATA Mode set SataMode	Mode of operation of Serial Advanced Technology Attachment (SATA) Solid State Drives (SSD). <ul style="list-style-type: none"> • Disabled— All SATA ports is disabled, and drivers are not enumerated. • IDE Mode— Mode of operation follows previous hardware standards of Integrated Drive Electronics (IDE) interface • AHCI Mode—The default mode. Drives operate according to newer standard of Advance Host Controller Interface(AHCI).

USB Configuration Parameters

Name	Description
Legacy USB Support set LegacyUSBSupport	Whether the system supports legacy USB devices. This can be one of the following: <ul style="list-style-type: none"> • Disabled—USB devices are only available to EFI applications. • Enabled—Legacy USB support is always available. • Auto—Disables legacy USB support if no USB devices are connected.
Port 60/64 Emulation set UsbEmul6064	Whether the system supports 60h/64h emulation for complete USB keyboard legacy support. This can be one of the following: <ul style="list-style-type: none"> • Disabled—60h/64 emulation is not supported. • Enabled—60h/64 emulation is supported. You should select this option if you are using a non-USB aware operating system on the server.
All USB Devices set AllUsbDevices	Whether all physical and virtual USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—All USB devices are disabled. • Enabled—All USB devices are enabled.
USB Port: Rear set UsbPortRear	Whether the rear panel USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the rear panel USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the rear panel USB ports. Devices connected to these ports are detected by the BIOS and operating system.
USB Port: Internal set UsbPortInt	Whether the internal USB devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the internal USB ports. Devices connected to these ports are not detected by the BIOS and operating system. • Enabled—Enables the internal USB ports. Devices connected to these ports are detected by the BIOS and operating system.

Name	Description
USB Port: KVM set UsbPortKVM	Whether the KVM ports are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the KVM keyboard and/or mouse devices. Keyboard and/or mouse will not work in the KVM window. • Enabled—Enables the KVM keyboard and/or mouse devices.
USB Port: vMedia set UsbPortVMedia	Whether the virtual media devices are enabled or disabled. This can be one of the following: <ul style="list-style-type: none"> • Disabled—Disables the vMedia devices. • Enabled—Enables the vMedia devices.

PCI Configuration Parameters

Name	Description
PCI ROM CLP set PciRomClp	PCI ROM Command Line Protocol (CLP) controls the execution of different Option ROMs such as PxE and iSCSI that are present in the card. By default, it is disabled. <ul style="list-style-type: none"> • Enabled— Enables you to configure execution of different option ROMs such as PxE and iSCSI for an individual ports separately. • Disabled—The default option. You cannot choose different option ROMs. A default option ROM is executed during PCI enumeration.
ASPM Support set ASPMSupport	Allows you to set the level of ASPM (Active Power State Management) support in the BIOS. This can be one of the following: <ul style="list-style-type: none"> • Disabled—ASPM support is disabled in the BIOS. • Force L0s—Force all links to L0 standby (L0s) state. • Auto—The CPU determines the power state.

Serial Configuration Parameters

Name	Description
Out-of-Band Mgmt Port set comSpcrEnable	<p>Allows you to configure the COM port 0 that can be used for Windows Emergency Management services. ACPI SPCR table is reported based on this setup option. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—Configures the COM port 0 as a general purpose port for use with the Windows Operating System. • Enabled—Configures the COM port 0 as a remote management port for Windows Emergency Management services.
Console Redirection set ConsoleRedir	<p>Allows a serial port to be used for console redirection during POST and BIOS booting. After the BIOS has booted and the operating system is responsible for the server, console redirection is irrelevant and has no effect. This can be one of the following:</p> <ul style="list-style-type: none"> • Disabled—No console redirection occurs during POST. • COM_0—Enables console redirection on COM port 0 during POST. • COM_1—Enables console redirection on COM port 1 during POST.
Terminal Type set TerminalType	<p>What type of character formatting is used for console redirection. This can be one of the following:</p> <ul style="list-style-type: none"> • PC-ANSI—The PC-ANSI terminal font is used. • VT100—A supported vt100 video terminal and its character set are used. • VT100+—A supported vt100-plus video terminal and its character set are used. • VT-UTF8—A video terminal with the UTF-8 character set is used. <p>Note This setting must match the setting on the remote terminal application.</p>

Name	Description
Bits per second set BaudRate	<p>What BAUD rate is used for the serial port transmission speed. If you disable Console Redirection, this option is not available. This can be one of the following:</p> <ul style="list-style-type: none"> • 9600—A 9,600 BAUD rate is used. • 19200—A 19,200 BAUD rate is used. • 38400—A 38,400 BAUD rate is used. • 57600—A 57,600 BAUD rate is used. • 115200—A 115,200 BAUD rate is used. <p>Note This setting must match the setting on the remote terminal application.</p>
Flow Control set FlowCtrl	<p>Whether a handshake protocol is used for flow control. Request to Send / Clear to Send (RTS/CTS) helps to reduce frame collisions that can be introduced by a hidden terminal problem. This can be one of the following:</p> <ul style="list-style-type: none"> • None—No flow control is used. • Hardware_RTS/CTS—RTS/CTS is used for flow control. <p>Note This setting must match the setting on the remote terminal application.</p>
Putty KeyPad set PuttyFunctionKeyPad	<p>Allows you to change the action of the PuTTY function keys and the top row of the numeric keypad. This can be one of the following:</p> <ul style="list-style-type: none"> • VT100—The function keys generate ESC OP through ESC O[. • LINUX—Mimics the Linux virtual console. Function keys F6 to F12 behave like the default mode, but F1 to F5 generate ESC [[A through ESC [[E. • XTERMR6—Function keys F5 to F12 behave like the default mode. Function keys F1 to F4 generate ESC OP through ESC OS, which are the sequences produced by the top row of the keypad on Digital terminals. • SCO—The function keys F1 to F12 generate ESC [M through ESC [X. The function and shift keys generate ESC [Y through ESC [j. The control and function keys generate ESC [k through ESC [v. The shift, control and function keys generate ESC [w through ESC [{}. • ESCN—The default mode. The function keys match the general behavior of Digital terminals. The function keys generate sequences such as ESC [11~ and ESC [12~. • VT400—The function keys behave like the default mode. The top row of the numeric keypad generates ESC OP through ESC OS.

Name	Description
Redirection After BIOS POST set RedirectionAfterPOST	Whether BIOS console redirection should be active after BIOS POST is complete and control given to the OS bootloader. This can be one of the following: <ul style="list-style-type: none"> • Always_Enable—BIOS Legacy console redirection is active during the OS boot and run time. • Bootloader—BIOS Legacy console redirection is disabled before giving control to the OS boot loader.

LOM and PCIe Slots Configuration Parameters

Name	Description
CDN Support for VIC set CdnEnable	Whether the Ethernet Network naming convention is according to Consistent Device Naming (CDN) or the traditional way of naming conventions. This can be one of the following: <ul style="list-style-type: none"> • Disabled— OS Ethernet Networking Identifier is named in a default convention as ETH0, ETH1 and so on. By default, CDN option is disabled. • LOMS Only— OS Ethernet Network identifier is named in a consistent device naming (CDN) according to the physical LAN on Motherboard(LOM) port numbering; LOM Port 0, LOM Port 1 and so on. <p>Note CDN is enabled for LOM ports and works with Windows 2012 or the latest OS only.</p>
All PCIe Slots OptionROM set PcieOptionROMs	Whether the server can use Option ROM present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for all PCIe slots are not available. • Enabled—The Option ROMs for all the PCIe slots are available. • UEFI_Only—The Option ROMs for slot <i>n</i> are available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> are available for legacy only.

Name	Description
PCIe Slot:<i>n</i> OptionROM set PcieSlot<i>n</i>OptionROM	Whether the server can use the Option ROMs present in the PCIe Cards. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The Option ROM for slot <i>n</i> is not available. • Enabled—The Option ROM for slot <i>n</i> is available. • UEFI_Only—The Option ROM for slot <i>n</i> is available for UEFI only. • Legacy_Only—The Option ROM for slot <i>n</i> is available for legacy only.
PCIe Mezzanine OptionROM set PcieMezzOptionROM	Whether the PCIe mezzanine slot expansion ROM is available to the server. This can be one of the following: <ul style="list-style-type: none"> • Disabled— The Option ROM for slot <i>M</i> is not available. • Enabled— The Option ROM for slot <i>M</i> is available. • UEFI_Only—The Option ROM for slot <i>M</i> is available for UEFI only. • Legacy_Only—The expansion slot for slot <i>M</i> is available for legacy only.
SIOC1 Link Speed Set PcieSlot1LinkSpeed	System IO Controller 1 (SIOC1) add-on slot 1 link speed. <ul style="list-style-type: none"> • GEN1— Link speed can reach up to first generation. • GEN2—The default link speed. Link speed can reach up to second generation. • GEN3— Link speed can reach up to third generation. • Disabled—Slot is disabled, and the card is not enumerated.
SIOC2 Link Speed set PcieSlot2LinkSpeed	System IO Controller 2 (SIOC2) add-on slot 2 link speed. <ul style="list-style-type: none"> • GEN1— Link speed can reach up to first generation. • GEN2—The default link speed. Link speed can reach up to second generation. • GEN3— Link speed can reach up to third generation. • Disabled—Slot is disabled, and the card is not enumerated.

Name	Description
Mezz Link Speed set PcieSlotMLinkSpeed	Mezz link speed. This can be one of the following: <ul style="list-style-type: none"> • GEN 1— Link speed can reach up to first generation. • GEN 2— Link speed can reach up to second generation. • GEN 3—The default link speed. Link speed can reach up to third generation. • Disabled—Slot is disabled, and the card is not enumerated.

Server Management Tab for C3160 Servers

Name	Description
FRB-2 Timer set FRB-2	Whether the FRB2 timer is used by Cisco IMC to recover the system if it hangs during POST. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The FRB2 timer is not used. • Enabled—The FRB2 timer is started during POST and used to recover the system if necessary.
OS Watchdog Timer set OSBootWatchdogTimer	Whether the BIOS programs the watchdog timer with a specified timeout value. This can be one of the following: <ul style="list-style-type: none"> • Disabled—The watchdog timer is not used to track how long the server takes to boot. • Enabled—The watchdog timer tracks how long the server takes to boot. If the server does not boot within the length of time specified by the set OSBootWatchdogTimerTimeout command, the Cisco IMC logs an error and takes the action specified by the set OSBootWatchdogTimerPolicy command.

Name	Description
<p>OS Watchdog Timer Timeout set OSBootWatchdogTimerTimeOut</p>	<p>If OS does not boot within the specified time, OS watchdog timer expires and system takes action according to timer policy. This can be one of the following:</p> <ul style="list-style-type: none"> • 5_Minutes—The OS watchdog timer expires 5 minutes after it begins to boot. • 10_Minutes—The OS watchdog timer expires 10 minutes after it begins to boot. • 15_Minutes—The OS watchdog timer expires 15 minutes after it begins to boot. • 20_Minutes—The OS watchdog timer expires 20 minutes after it begins to boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>
<p>OS Watchdog Timer Policy set OSBootWatchdogTimerPolicy</p>	<p>What action the system takes if the watchdog timer expires. This can be one of the following:</p> <ul style="list-style-type: none"> • Do_Nothing—The server takes no action if the watchdog timer expires during OS boot. • Power_Down—The server is powered off if the watchdog timer expires during OS boot. • Reset—The server is reset if the watchdog timer expires during OS boot. <p>Note This option is only applicable if you enable the OS Boot Watchdog Timer.</p>

