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The following table lists the PCI BIOS settings that you can configure through a BIOS policy or the default BIOS settings:

Name	Description	Supported Attributes			
		Versions	Platforms	Values	Dependencies
ASPM Support	Allows you to set the level of ASPM (Active Power State Management) support in the BIOS.	4.0 (1), 4.0(2), 4.0(4), 4.1(1)	All M5 servers	Disabled, **Auto**, ForceL0 • **ForceL0**—Force all links to L0 standby (L0s) state.	
Memory Mapped IO above 4GB	Whether to enable or disable memory mapped I/O of 64-bitPCI devices to 4GB or greater address space. Legacy option ROMs are not able to access addresses above 4GB. PCI devices that are 64-bit compliant but use a legacy option ROM may not function correctly with this setting enabled.	4.0 (1), 4.0(2), 4.0(4), 4.1(1), 4.1(3)	All M5 servers and , C220 M7, C240 M7, X210c M7, X410c M7 servers	**Disabled**, Enabled • **Disabled**—This option is Disabled. • **Enable**—This options is enabled.	

Name	Description	Supported Attributes			
		Versions	Platforms	Values	Dependencies
VGA Priority	Allows you to set the priority for VGA graphics devices if multiple VGA devices are found in the system.	4.0(2), 4.0(4), 4.1(1)	C220 M5, C240 M5, C220 M7, C240 M7	Offboard, **Onboard**, Onboard VGA Disabled • **Onboard**—Priority is given to the onboard VGA device. BIOS post screen and OS boot are driven through the onboard VGA port • **Offboard**——Priority is given to thePCIE Graphics adapter. BIOS post screen and OS boot are driven through the external graphics adapter port. • **Onboard VGA Disabled** —Priority is given to the PCIE Graphics adapter, and the onboard VGA device is disabled **Note** The vKVM does not function when the onboard VGA is disabled.	