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<!DOCTYPE article PUBLIC "-//Boost//DTD BoostBook XML V1.0//EN"
"http://www.boost.org/tools/boostbook/dtd/boostbook.dtd">
<article id="copyright_test" last-revision="DEBUG MODE Date: 2000/12/20 12:00:00 $"
xmlns:xi="http://www.w3.org/2001/XInclude">
<title>Copyright Test</title>
<articleinfo>
```

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</copyright>
<copyright>
<year>2018</year> <holder>Joe Blow, John Coe</holder>
</copyright>
<copyright>
<year>1977</year> <year>1985</year> <holder>Someone else</holder>
</copyright>
</articleinfo>
</article>
This package was debianized by Vladimir Prus <ghost@cs.msu.su> on
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Copyright:

Wed, 17 July 2002, 19:27:00 +0400.

```
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From strings/apr_fnmatch.c, include/apr_fnmatch.h, misc/unix/getopt.c, file_io/unix/mktemp.c, strings/apr_strings.c:

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From network_io/unix/inet_ntop.c, network_io/unix/inet_pton.c:

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From dso/aix/dso.c:

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From strings/apr_strnatcmp.c, include/apr_strings.h:

strnatcmp.c -- Perform 'natural order' comparisons of strings in C.

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From strings/apr_snprintf.c:

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Written by: Philip Hazel <ph10@cam.ac.uk>

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X
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Apache HTTP Server Version 2.2
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<div class="up"><a href="./"><img title="<-" alt="<-" src="./images/left.gif" /></a></div>
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href="http://httpd.apache.org/docs/">Documentation</a> > <a href="./">Version 2.2</a></div><div id="page-
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class="warning"><strong>Notice:</strong><br/>obr />This is not a Q&A section. Comments placed here should be
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href="http://httpd.apache.org/lists.html">mailing lists</a>.</div>
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    s.src = 'https://comments.apache.org/show comments.lua?site=' + comments shortname + '&page=' +
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```

```
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Written by: Philip Hazel <ph10@cam.ac.uk>

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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:

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FlashPoint Driver Developer's Kit

Version 1.0

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```
<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice
```

That's all there is to it!

/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)

* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)

*

- * Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
- * http://www.hypermall.com/
- * 10/1/97 commented out CFG_PHYIE bit we don't care when the PHY
- * interrupts us (except possibly for removal/insertion of the cable?)
- * 10/4/97 began heavy inline documentation of the code. Corrected typos
- * and spelling mistakes.
- * 10/5/97 added code to handle PHY interrupts, disable PHY on
- * loss of link, and correctly re-enable PHY when link is
- * re-established. (put back CFG_PHYIE)

*

- * Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.
- * R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997
- * Linux driver for the IDT77201 NICStAR PCI ATM controller.

```
* PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
```

*

- * Implementing minimal-copy of received data:
- * IDT always receives data into a small buffer, then large buffers
- * as needed. This means that data must always be copied to create
- * the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
- * Fix is simple: make large buffers large enough to hold entire
- * SDU, and leave <small_buffer_data> bytes empty at the start. Then
- * copy small buffer contents to head of large buffer.
- * Trick is to avoid fragmenting Linux, due to need for a lot of large
- * buffers. This is done by 2 things:
- * 1) skb->destructor / skb->atm.recycle_buffer
- * combined, allow nicstar_free_rx_skb to be called to
- recycle large data buffers
- * 2) skb_clone of received buffers
- * See nicstar free rx skb and linearize buffer for implementation
- * details.

т ...

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*

* M. Welsh, 6 July 1996

*

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^{*} see init nicstar() for PHY initialization to change this. This driver

^{*} expects the Linux ATM stack to support scatter-gather lists

^{* (}skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.

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1.11 libuv 0.10.28 :1.el7

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1.13 logrotate 3.8.1

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- 47. [51] Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
- 48. [52]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
- 49. [53]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
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jpg "Clone me," says Dolly sheepishly.

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- 1. [1]Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller
- 2. [2]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
- 3. [3] Viraj Bais <vbais@mailman1.intel.com> and [4] Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
- 4. [5] Michael Barone <michael, barone@lmco.com> GPSVME fixes
- 5. [6]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
- 6. [7]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.

Open Source Used In Cisco Nexus 1000V Switch for VMware vSphere 5.2(1)SV3(2.5)

- 7. [8]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
- 8. [9]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support
- 9. [10]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in sntp
- 10. [11]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
- 11. [12]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
- 12. [13]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
- 13. [14]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
- 14. [15]Sven Dietrich <sven_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
- 15. [16]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port
- 16. [17]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
- 17. [18]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
- 18. [19]John Hay < jhay@icomtek.csir.co.za> IPv6 support and testing
- 19. [20]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite
- 20. [21]Claas Hilbrecht <neoclock4x@linum.com> NeoClock4X clock driver
- 21. [22]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
- 22. [23]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
- 23. [24]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
- 24. [25]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
- 25. [26]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or
 [27]<H.Lambermont@chello.nl> ntpsweep
- 26. [28]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
- 27. [29]Frank Kardel [30]kardel (at) ntp (dot) org> PARSE <GENERIC> driver (>14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
- 28. [31]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
- 29. [32]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
- 30. [33]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
- 31. [34]George Lindholm < lindholm@ucs.ubc.ca> SunOS 5.1 port
- 32. [35]Louis A. Mamakos < louie@ni.umd.edu> MD5-based authentication
- 33. [36]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
- 34. [37]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
- 35. [38]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics;

- audio clock drivers: CHU, WWV/H, IRIG
- 36. [39]Wolfgang Moeller < moeller@gwdgv1.dnet.gwdg.de> VMS port
- 37. [40]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
- 38. [41]Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port
- 39. [42]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
- 40. [43]Derek Mulcahy <derek@toybox.demon.co.uk> and [44]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
- 41. [45]Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code maintenance
- 42. [46]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling
- 43. [47] Dirce Richards < dirce@zk3.dec.com> Digital UNIX V4.0 port
- 44. [48]Wilfredo Sönchez <wsanchez@apple.com> added support for NetInfo
- 45. [49]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
- 46. [50]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
- 47. [51] Ray Schnitzler < schnitz@unipress.com > Unixware1 port
- 48. [52] Michael Shields < shields @tembel.org > USNO clock driver
- 49. [53]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
- 50. [54]Harlan Stenn harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
- 51. [55]Kenneth Stone < ken@sdd.hp.com> HP-UX port
- 52. [56]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
- 53. [57]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock driver
- 54. [58]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
- 55. [59]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD

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* @version 3.0 (December 2000)

*

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*

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- * @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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1.23 openssl 0.9.7l

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1.24 Perl 5.8.8

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1.26 rapidjson 0.11

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