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1.7 arp-scan 1.9-40-g69b2f70-1

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1.8 avahi 0.6.32

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"http://www.boost.org/tools/boostbook/dtd/boostbook.dtd">
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xmlns:xi="http://www.w3.org/2001/XInclude">
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<articleinfo>
 <copyright>
  <year>1963</year> <year>1964der>Jane Doe
 </copyright>
 <copyright>
  <year>2018</year> <holder>Joe Blow, John Coe</holder>
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This is an attempt to acknowledge early contributions to the garbage collector. Later contributions should instead be mentioned in README.changes.

HISTORY -

Early versions of this collector were developed as a part of research projects supported in part by the National Science Foundation and the Defense Advance Research Projects Agency.

The garbage collector originated as part of the run-time system for the Russell programming language implementation. The first version of the garbage collector was written primarily by Al Demers. It was then refined and mostly rewritten, primarily by Hans-J. Boehm, at Cornell U., the University of Washington, Rice University (where it was first used for C and assembly code), Xerox PARC, SGI, and HP Labs. However, significant contributions have also been made by many others.

Some other contributors:

More recent contributors are mentioned in the modification history in README.changes. My apologies for any omissions.

The SPARC specific code was originally contributed by Mark Weiser.

The Encore Multimax modifications were supplied by

Kevin Kenny (kenny@m.cs.uiuc.edu). The adaptation to the IBM PC/RT is largely due to Vernon Lee, on machines made available to Rice by IBM.

Much of the HP specific code and a number of good suggestions for improving the generic code are due to Walter Underwood.

Robert Brazile (brazile@diamond.bbn.com) originally supplied the ULTRIX code.

Al Dosser (dosser@src.dec.com) and Regis Cridlig (Regis.Cridlig@cl.cam.ac.uk)

subsequently provided updates and information on variation between ULTRIX

systems. Parag Patel (parag@netcom.com) supplied the A/UX code.

Jesper Peterson(jep@mtiame.mtia.oz.au), Michel Schinz, and

Martin Tauchmann (martintauchmann@bigfoot.com) supplied the Amiga port.

Thomas Funke (thf@zelator.in-berlin.de(?)) and

Brian D.Carlstrom (bdc@clark.lcs.mit.edu) supplied the NeXT ports.

Douglas Steel (doug@wg.icl.co.uk) provided ICL DRS6000 code.

 $Bill\ Janssen\ (janssen@parc.xerox.com)\ supplied\ the\ SunOS\ dynamic\ loader$

specific code. Manuel Serrano (serrano@cornas.inria.fr) supplied linux and

Sony News specific code. Al Dosser provided Alpha/OSF/1 code. He and

Dave Detlefs(detlefs@src.dec.com) also provided several generic bug fixes.

Alistair G. Crooks(agc@uts.amdahl.com) supplied the NetBSD and 386BSD ports.

Jeffrey Hsu (hsu@soda.berkeley.edu) provided the FreeBSD port.

Brent Benson (brent@jade.ssd.csd.harris.com) ported the collector to

a Motorola 88K processor running CX/UX (Harris NightHawk).

Ari Huttunen (Ari.Huttunen@hut.fi) generalized the OS/2 port to

nonIBM development environments (a nontrivial task).

Patrick Beard (beard@cs.ucdavis.edu) provided the initial MacOS port.

David Chase, then at Olivetti Research, suggested several improvements.

Scott Schwartz (schwartz@groucho.cse.psu.edu) supplied some of the code to save and print call stacks for leak detection on a SPARC.

Jesse Hull and John Ellis supplied the C++ interface code.

Zhong Shao performed much of the experimentation that led to the current typed allocation facility. (His dynamic type inference code hasn't made it into the released version of the collector, yet.)

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1.11 Chart.js 1.0.2

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1.12 classnames 2.1.5

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1.13 curl 7.61.1

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1.14 d3 3.5.5

1.15 datepicker_ui 1.11.1 :2014/8/27

1.15.1 Available under license:

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- * jQuery UI Datepicker 1.11.1
- * http://jqueryui.com

Ψ.

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1.16 ejdb 1.2.12

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1.17 es6-promise 3.0.2

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1.18 event-emitter 0.3.3

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1.19 FileSaver.js 1.1.0

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[1]: http://eligrey.com

1.20 Font Awesome 4.3.0

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1.21 heirloom-mailx(nail) 1.2.5

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```
/*
* imap_gssapi.c is partially derived from sample code in:
* GSS-API Programming Guide
* Part No: 816-1331-11
* Sun Microsystems, Inc. 4150 Network Circle Santa Clara, CA 95054 U.S.A.
*
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*/
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/>

* hmac.c is derived from:

Network Working Group
Request for Comments: 2104
Category: Informational
UCSD
R. Canetti
IBM

February 1997

HMAC: Keyed-Hashing for Message Authentication

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Appendix -- Sample Code

Parts of nss.c are derived from the Mozilla NSS 3.9.2 source, mozilla/security/nss/cmd/smimetools/cmsutil.c. Therefore:

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Version 1.1

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1.25 jquery-ui 1.11.2 :2014/11/17

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/*! ¡Query UI - v1.11.2 - 2014-10-16

- * http://jqueryui.com
- * Includes: core.js, widget.js, mouse.js, position.js, accordion.js, autocomplete.js, button.js, datepicker.js, dialog.js, draggable.js, droppable.js, effect.js, effect-blind.js, effect-bounce.js, effect-clip.js, effect-drop.js, effect-explode.js, effect-fade.js, effect-fold.js, effect-highlight.js, effect-pulf.js, effect-pulsate.js, effect-scale.js, effect-shake.js, effect-size.js, effect-slide.js, selectmenu.js, slider.js, sortable.js, spinner.js, tabs.js, tooltip.js
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1.26 jquery.flot.Stack 1.2

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/* Flot plugin for stacking data sets rather than overlyaing them.

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The plugin assumes the data is sorted on x (or y if stacking horizontally). For line charts, it is assumed that if a line has an undefined gap (from a null point), then the line above it should have the same gap - insert zeros instead of "null" if you want another behaviour. This also holds for the start and end of the chart. Note that stacking a mix of positive and negative values in most instances doesn't make sense (so it looks weird).

Two or more series are stacked when their "stack" attribute is set to the same key (which can be any number or string or just "true"). To specify the default stack, you can set the stack option like this:

```
series: {
     stack: null/false, true, or a key (number/string)
}
```

You can also specify it for a single series, like this:

```
$.plot( $("#placeholder"), [{
    data: [ ... ],
    stack: true
}])
```

The stacking order is determined by the order of the data series in the array (later series end up on top of the previous).

Internally, the plugin modifies the datapoints in each series, adding an offset to the y value. For line series, extra data points are inserted through interpolation. If there's a second y value, it's also adjusted (e.g for bar charts or filled areas).

*/

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1.27 jquery.flot.tooltip 0.6.2

1.27.1 Available under license:

/*
* jquery.flot.tooltip

*
* description: easy-to-use tooltips for Flot charts
* version: 0.6.2
* author: Krzysztof Urbas @krzysu [myviews.pl]

*
* build on 2013-09-30
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1.28 jquery.flot.valuelabels 2

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1.29 json-c 0.12.1

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1.30 jszip.js 2.4.0

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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1.33 libnetconf 0.10

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.

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- b) Use a suitable shared library mechanism for linking with the

Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

- c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
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- e) Verify that the user has already received a copy of these materials or that you have already sent this user a copy.

For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the materials to be distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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Linking with OpenSSL

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/* \$OpenBSD: blowfish.c,v 1.18 2004/11/02 17:23:26 hshoexer Exp \$ */

/*

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*

- * Implementation advice by David Mazieres <dm@lcs.mit.edu>.
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*/

1.35 libuuid 2.25.2

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(For example, a function in a library to compute square roots has a purpose that is entirely well-defined independent of the application. Therefore, Subsection 2d requires that any application-supplied function or table used by this function must be optional: if the application does not supply it, the square root function must still compute square roots.)

These requirements apply to the modified work as a whole. If identifiable sections of that work are not derived from the Library, and can be reasonably considered independent and separate works in themselves, then this License, and its terms, do not apply to those sections when you distribute them as separate works. But when you distribute the same sections as part of a whole which is a work based on the Library, the distribution of the whole must be on the terms of this License, whose permissions for other licensees extend to the entire whole, and thus to each and every part regardless of who wrote it.

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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

If distribution of object code is made by offering access to copy from a designated place, then offering equivalent access to copy the source code from the same place satisfies the requirement to distribute the source code, even though third parties are not compelled to copy the source along with the object code.

5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

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Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

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Initialize empty image f1c9645dbc14efddc7d8a322685f26eb bsd.img Create new DOS partition table 57e721e38d1266c2df055067c18f2cf9 bsd.img

---layout-----

Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors

Units: sectors of 1 * 512 = 512 bytes

Sector size (logical/physical): 512 bytes / 512 bytes I/O size (minimum/optimal): 512 bytes / 512 bytes

Disklabel type: dos

Disk identifier: <removed>

Create 1st primary partition ada64ace122978d00d1d1c0e5ee45d26 bsd.img

---layout-----

Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors

Units: sectors of 1 * 512 = 512 bytes

Sector size (logical/physical): 512 bytes / 512 bytes I/O size (minimum/optimal): 512 bytes / 512 bytes

Disklabel type: dos

Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type <removed>1 2048 4095 2048 1M 83 Linux

Create 2st primary partition 1bebf87248e05d6e4e62b749da65d023 bsd.img Set 2nd partition type 2d8e8dff51a88a045db233418dd73fbe bsd.img

---layout-----

Disk <removed>: 10 MiB, 10485760 bytes, 20480 sectors

Units: sectors of 1 * 512 = 512 bytes

Sector size (logical/physical): 512 bytes / 512 bytes I/O size (minimum/optimal): 512 bytes / 512 bytes

Disklabel type: dos

Disk identifier: <removed>

Device Boot Start End Sectors Size Id Type <removed>1 2048 4095 2048 1M 83 Linux <removed>2 4096 20479 16384 8M a5 FreeBSD

Create default BSD

2e1cee529cb59c9341afef0443f196a1 bsd.img

---layout-----

Welcome to fdisk <removed>.

Changes will remain in memory only, until you decide to write them.

Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors

Geometry: 255 heads, 63 sectors/track, 1 cylinders

Units: sectors of 1 * 512 = 512 bytes

Sector size (logical/physical): 512 bytes / 512 bytes I/O size (minimum/optimal): 512 bytes / 512 bytes

Disklabel type: bsd

partitions: 4 Slice Start End Sectors Size Type Fsize Bsize Cpg 4096 20479 16384 8M unused 0 0 0 0 0 d 0 16064 16065 7.9M unused Partition table entries are not in disk order. Command (m for help): Leaving nested disklabel. Command (m for help): ----b5c121c2091b2ff26b880551feac7112 bsd.img ---layout-----Welcome to fdisk <removed>. Changes will remain in memory only, until you decide to write them. Be careful before using the write command. Command (m for help): Entering nested BSD disklabel. Command (m for help): Disk <removed>2: 10 MiB, 10485760 bytes, 20480 sectors Geometry: 255 heads, 63 sectors/track, 1 cylinders Units: sectors of 1 * 512 = 512 bytes Sector size (logical/physical): 512 bytes / 512 bytes I/O size (minimum/optimal): 512 bytes / 512 bytes Disklabel type: bsd partitions: 4 Slice Start End Sectors Size Type Fsize Bsize Cpg 4096 6144 2049 1M 4.2BSD С 4096 20479 16384 8M unused 0 0 0 0 16064 16065 7.9M unused 0 0 0 d

Partition table entries are not in disk order.

Command (m for help): Leaving nested disklabel.

Command (m for help):

Changes will remain in memory only, until you decide to write them.

Be careful before using the write command.

Command (m for help): Entering nested BSD disklabel.

Command (m for help):

```
0 unused
             5 4.1BSD
                           9 4.4LFS
                                         d boot
1 swap
            6 Eighth Edition a unknown
                                           e ADOS
             7 4.2BSD
2 Version 6
                            b HPFS
                                         f HFS
3 Version 7
              8 MS-DOS
                             c ISO-9660
                                           10 AdvFS
4 System V
```

Command (m for help):

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```
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```

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- test-server/*.h
- lwsws/*

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1.37 libxml2 2.9.8

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1.38 linux 3.10.70 :2015_T1.1p10

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```
* ahci_mv.c - Marvell AHCI SATA platform support
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* based on the AHCI SATA platform driver by Jeff Garzik and Anton Vorontsov
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* along with this program; if not, write to the Free Software
* Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
#include linux/kernel.h>
#include linux/gfp.h>
#include linux/module.h>
#include linux/pm.h>
#include linux/init.h>
#include linux/interrupt.h>
#include linux/device.h>
#include linux/platform device.h>
#include linux/libata.h>
#include linux/ahci platform.h>
#include linux/ata_platform.h>
#include linux/mbus.h>
#include "ahci.h"
#define AHCI_WINDOW_CTRL(win) (0x60 + ((win) << 4))
#define AHCI_WINDOW_BASE(win) (0x64 + ((win) << 4))
#define AHCI_WINDOW_SIZE(win) (0x68 + ((win) << 4))
#define VENDOR_SPECIFIC_0_ADDR 0xa0
```

```
#define VENDOR_SPECIFIC_0_DATA 0xa4
static void ahci_mv_windows_config(struct ahci_host_priv *hpriv,
   const struct mbus_dram_target_info *dram)
{
int i;
for (i = 0; i < 4; i++) {
 writel(0, hpriv->mmio + AHCI_WINDOW_CTRL(i));
 writel(0, hpriv->mmio + AHCI_WINDOW_BASE(i));
 writel(0, hpriv->mmio + AHCI_WINDOW_SIZE(i));
for (i = 0; i < dram - num_cs; i++) {
 const struct mbus_dram_window *cs = dram->cs + i;
 writel((cs->mbus_attr << 8) |
     (dram->mbus dram target id << 4) | 1,
     hpriv->mmio + AHCI_WINDOW_CTRL(i));
 writel(cs->base >> 16, hpriv->mmio + AHCI_WINDOW_BASE(i));
 writel(((cs->size - 1) & 0xffff0000),
     hpriv->mmio + AHCI_WINDOW_SIZE(i));
}
}
static void ahci_mv_host_stop(struct ata_host *host);
static struct ata_port_operations ahci_mv_ops = {
.inherits = &ahci_ops,
.host_stop = ahci_mv_host_stop,
};
static const struct ata_port_info ahci_mv_port_info = {
.flags = AHCI_FLAG_COMMON,
.pio_{mask} = ATA_{PIO4},
.udma_mask = ATA\_UDMA6,
.port_ops = &ahci_mv_ops,
};
static struct scsi_host_template ahci_mv_platform_sht = {
AHCI_SHT("ahci_mv_platform"),
};
static const struct of_device_id ahci_mv_of_match[] = {
{ .compatible = "marvell,ahci-sata", },
{},
```

MODULE_DEVICE_TABLE(of, ahci_of_match);

```
static int ahci_mv_probe(struct platform_device *pdev)
struct device *dev = &pdev->dev;
const struct mbus_dram_target_info *dram;
struct ata_port_info pi = ahci_mv_port_info;
const struct ata_port_info *ppi[] = { &pi, NULL };
struct ahci_host_priv *hpriv;
struct ata_host *host;
struct resource *mem:
int irq;
int n_ports;
int i:
int rc;
mem = platform_get_resource(pdev, IORESOURCE_MEM, 0);
if (!mem) {
dev_err(dev, "no mmio space\n");
return -EINVAL;
}
irq = platform_get_irq(pdev, 0);
if (irq <= 0) {
dev_err(dev, "no irq\n");
return -EINVAL;
}
hpriv = devm_kzalloc(dev, sizeof(*hpriv), GFP_KERNEL);
if (!hpriv) {
dev_err(dev, "can't alloc ahci_host_priv\n");
return -ENOMEM;
}
hpriv->clk = devm_clk_get(&pdev->dev, NULL);
if (IS_ERR(hpriv->clk)) {
dev_err(dev, "can't get clock\n");
return PTR_ERR(hpriv->clk);
}
rc = clk_prepare_enable(hpriv->clk);
if (rc < 0) {
dev_err(dev, "can't enable clock\n");
 return rc;
}
hpriv->flags |= (unsigned long)pi.private_data;
hpriv->mmio = devm_request_and_ioremap(dev, mem);
```

```
if (!hpriv->mmio) {
dev_err(dev, "can't map %pR\n", mem);
clk_disable_unprepare(hpriv->clk);
return -ENOMEM;
}
* (Re-)program MBUS remapping windows if we are asked to.
dram = mv_mbus_dram_info();
if (dram)
ahci_mv_windows_config(hpriv, dram);
ahci_save_initial_config(dev, hpriv, 0, 0);
/* prepare host */
if (hpriv->cap & HOST_CAP_NCQ)
pi.flags |= ATA_FLAG_NCQ;
if (hpriv->cap & HOST_CAP_PMP)
pi.flags |= ATA_FLAG_PMP;
ahci_set_em_messages(hpriv, &pi);
* CAP.NP sometimes indicate the index of the last enabled
* port, at other times, that of the last possible port, so
* determining the maximum port number requires looking at
* both CAP.NP and port_map.
n_ports = max(ahci_nr_ports(hpriv->cap), fls(hpriv->port_map));
host = ata_host_alloc_pinfo(dev, ppi, n_ports);
if (!host) {
clk_disable_unprepare(hpriv->clk);
return -ENOMEM;
}
host->private_data = hpriv;
if (!(hpriv->cap & HOST_CAP_SSS) || ahci_ignore_sss)
host->flags |= ATA_HOST_PARALLEL_SCAN;
else
printk(KERN_INFO "ahci: SSS flag set, parallel bus scan disabled\n");
if (pi.flags & ATA_FLAG_EM)
ahci_reset_em(host);
```

```
for (i = 0; i < host->n_ports; i++) {
 struct ata_port *ap = host->ports[i];
 ata_port_desc(ap, "mmio %pR", mem);
 ata_port_desc(ap, "port 0x\%x", 0x100 + ap->port_no * 0x80);
 /* set enclosure management message type */
 if (ap->flags & ATA_FLAG_EM)
 ap->em_message_type = hpriv->em_msg_type;
 /* disabled/not-implemented port */
 if (!(hpriv->port_map & (1 << i)))
 ap->ops = &ata_dummy_port_ops;
}
/* Enabling regret bit to enables the SATA unit to regret
a request that didn't receive an acknowlegde and avoid a deadlock */
writel(0x4, hpriv->mmio + VENDOR_SPECIFIC_0_ADDR);
writel (0x80, hpriv-> mmio + VENDOR\_SPECIFIC\_0\_DATA);
rc = ahci_reset_controller(host);
if (rc) {
 clk_disable_unprepare(hpriv->clk);
 return rc;
}
ahci_init_controller(host);
ahci_print_info(host, "platform");
rc = ata_host_activate(host, irq, ahci_interrupt, IRQF_SHARED,
      &ahci_mv_platform_sht);
if (rc) {
 clk_disable_unprepare(hpriv->clk);
 return rc;
return 0;
static void ahci_mv_host_stop(struct ata_host *host)
struct ahci_host_priv *hpriv = host->private_data;
clk_disable_unprepare(hpriv->clk);
#ifdef CONFIG_PM_SLEEP
static int ahci_mv_suspend(struct device *dev)
```

```
struct ata_host *host = dev_get_drvdata(dev);
struct ahci_host_priv *hpriv = host->private_data;
void __iomem *mmio = hpriv->mmio;
u32 ctl;
int rc;
if (hpriv->flags & AHCI_HFLAG_NO_SUSPEND) {
 dev_err(dev, "firmware update required for suspend/resume\n");
return -EIO;
}
 * AHCI spec rev1.1 section 8.3.3:
 * Software must disable interrupts prior to requesting a
 * transition of the HBA to D3 state.
ctl = readl(mmio + HOST CTL);
ctl &= ~HOST_IRQ_EN;
writel(ctl, mmio + HOST_CTL);
readl(mmio + HOST_CTL); /* flush */
rc = ata_host_suspend(host, PMSG_SUSPEND);
if (rc)
return rc;
return 0;
}
static int ahci_mv_resume(struct device *dev)
struct ata_host *host = dev_get_drvdata(dev);
struct ahci_host_priv *hpriv = host->private_data;
const struct mbus_dram_target_info *dram;
int rc;
dram = mv_mbus_dram_info();
if (dram)
ahci_mv_windows_config(hpriv, dram);
if (dev->power_state.event == PM_EVENT_SUSPEND) {
 rc = ahci_reset_controller(host);
 if (rc)
 return rc;
 ahci_init_controller(host);
}
```

```
ata_host_resume(host);
return 0;
#endif
static SIMPLE_DEV_PM_OPS(ahci_mv_pm_ops, ahci_mv_suspend, ahci_mv_resume);
static struct platform_driver ahci_mv_driver = {
.probe = ahci mv probe,
.remove = ata_platform_remove_one,
.driver = {
 .name = "ahci mv",
 .owner = THIS_MODULE,
 .of_match_table = ahci_mv_of_match,
 .pm = &ahci_mv_pm_ops,
},
};
module_platform_driver(ahci_mv_driver);
MODULE_DESCRIPTION("Marvell AHCI SATA platform driver");
MODULE_AUTHOR("Marcin Wojtas <mw@semihalf.com>");
MODULE LICENSE("GPL");
MODULE_ALIAS("platform:ahci_mv");
```

1.39 lodash 3.7.0

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1.41 moment 2.10.6

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1.42 net-snmp 5.7.3-1ubuntu4.2

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1.44 ngDialog 0.4.0

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1.45 nginx 1.10.1

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1.46 nmap 6.47

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Version 1.1

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On all platforms, Nmap is normally linked with:
o libpcap: Network packet capture library. Distributed with Nmap in the
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http://www.tcpdump.org/

o libdnet: Networking library, used for low-level tasks such as sending ethernet frames. A modified version is distributed with Nmap in the libdnet-stripped subdirectory. A summary of Nmap-local modifications is in the file NMAP MODIFICATIONS.

http://code.google.com/p/libdnet/

o PCRE: Perl-compatible regular expressions. PCRE is part of Nmap's version detection and is also made available as an NSE library.

Distributed with Nmap in the libpere subdirectory.

http://www.pcre.org/

o liblua: Lua programming language. Lua is the implementation language of NSE, the Nmap Scripting Engine. Nmap links with liblua to allow running Lua programs inside Nmap. Distributed with Nmap in the liblua subdirectory. liblua can be omitted by configuring with the --without-liblua configuration directive.

http://www.lua.org/

o OpenSSL: Cryptographic library. OpenSSL is used by service detection and by NSE to connect to SSL services. NSE also provides access to OpenSSL functions such as encryption and digest calculation. OpenSSL can be disabled by configuring with the --without-openssl configuration directive.

http://www.openssl.org/

o LIBLINEAR. Used for IPv6 OS classifiction. http://www.csie.ntu.edu.tw/~cjlin/liblinear/

o libsvn. The Subversion library, used by the updater program nmap-update. http://subversion.apache.org/

o APR (Apache Portable Runtime). Used by libsvn. http://apr.apache.org/

On Windows only, Nmap uses:

o WinPcap: libpcap for Windows. The libpcap license applies to WinPcap, and it also has its own license. A binary copy of the library is distributed with Nmap in the subdirectory mswin32/winpcap. http://www.winpcap.org/

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Zenmap and Ndiff require:

o Python. The binary distributions of Nmap include a Python interpreter and various libraries, built using either py2exe or py2app. http://www.python.org/

The Windows and Mac OS X binary packages include bundled versions of: o GLib, GTK+, ATK, Pango: These libraries are licensed under the GNU LGPL 2, a copy of which is in the file licenses/LGPL-2. http://www.gtk.org/

o Cairo: graphics library. Dual-licensed under the GNU LGPL 2.1 and the MPL 1.1. See the files licenses/LGPL-2.1 and licenses/MPL-1.1.

http://www.cairographics.org/

- o libgailutil: Accessibility support for GTK+.
- o libpng, jpeg, LibTIFF: Image file format libraries used by GTK+.

http://www.libpng.org/ http://www.ijg.org/ http://www.libtiff.org/

o zlib and bzip2: compression libraries.

http://www.zlib.org/ http://www.bzip.org/

o Expat: XML parser library. http://expat.sourceforge.net/

o fontconfig: font configuration library.

http://www.fontconfig.org/

o FreeType: font rendering library.

http://www.freetype.org/

o libicony. Under the GNU LGPL 2.

http://www.gnu.org/software/libiconv/

o libintl: gettext translation facilities. Under the GNU LGPL 2 and 2.1.

http://www.gnu.org/software/gettext/

o libxml2: XML parsing library.

http://xmlsoft.org/

o SQLite.

http://www.sqlite.org/

o PyGTK, PyGObject: bindings for Python. Licensed under the GNU LGPL

2.1.

http://www.pygtk.org/

o PyCairo: bindings for Python. Dual-licensed under the GNU LGPL 2.1 and the MPL 1.1.

The Mac OS binary packages also include:

o Various X.org libraries. These were built using MacPorts.

http://www.x.org/ http://www.macports.org/

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THE BASIC LIBRARY FUNCTIONS

Written by: Philip Hazel
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- * @version 3.0 (December 2000)
- *
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*

- * @author Vincent Rijmen < vincent.rijmen@esat.kuleuven.ac.be>
- * @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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1.48 OpenSSL 1.0.2m

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If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

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1.51 Polar-SSL 0.10

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1.52 react 16.0.0

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1.53 react-select 1.0.0-rc.4

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1.54 react-tabs 2.1.0

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1.55 reflux 0.2.7

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1.56 RequireJS 2.1.18

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1.57 sarissa 0.9.9.6

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1.59 tinyxml 2.6.2

1.59.1 Available under license:

/*

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*/

1.60 u-boot 2013_Q3_eng_drop_2

1.60.1 Available under license:

 $From\ d223f6b3f1be839e23c5b21aa4a266f84a75acec\ Mon\ Sep\ 17\ 00:00:00\ 2001$

From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 09:07:29 +0300

Subject: [PATCH 0631/2009] fix: common: update copyright header

use include to mvCopyright.h file instead of an old copyright header

Change-Id: I0e25072e7e163c750bbfecd50958a194c78e6c68
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3035
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
--board/mv_ebu/common/common/mvList.c | 17 +++-----board/mv_ebu/common/common/mvStack.c | 17 +++-----board/mv_ebu/common/common/mvStack.c | 17 +++------4 files changed, 12 insertions(+), 56 deletions(-)

```
diff --git a/board/mv_ebu/common/common/mvList.c b/board/mv_ebu/common/common/mvList.c
index 5d7928f..541b190 100644
--- a/board/mv ebu/common/common/mvList.c
+++ b/board/mv ebu/common/common/mvList.c
@@ -1,17 +1,6 @@
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-* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL).
+#include "mvCopyright.h"
* mvList.c - Implementation File for Linked List.
* DESCRIPTION:
diff --git a/board/mv ebu/common/common/mvList.h b/board/mv ebu/common/common/mvList.h
index 9050b9e..ad26655 100644
--- a/board/mv ebu/common/common/mvList.h
+++ b/board/mv ebu/common/common/mvList.h
@@ -1.17 +1.6 @@
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-* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL).
+#include "mvCopyright.h"
```

```
* mvList.h - Header File for Linked List.
* DESCRIPTION:
diff --git a/board/mv_ebu/common/common/mvStack.c b/board/mv_ebu/common/common/mvStack.c
index 696cb06..a2a0b0b 100644
--- a/board/mv ebu/common/common/mvStack.c
+++ b/board/mv_ebu/common/common/mvStack.c
@@ -1.17 +1.6 @@
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-* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL).
+#include "mvCopyright.h"
* mvQueue.c
* FILENAME: $Workfile: mvStack.c $
diff --git a/board/mv_ebu/common/common/mvStack.h b/board/mv_ebu/common/common/mvStack.h
index e7ff74b..353d52a 100644
--- a/board/mv_ebu/common/common/mvStack.h
+++ b/board/mv ebu/common/common/mvStack.h
@@ -1,17 +1,6 @@
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-* (MJKK), MARVELL SEMICONDUCTOR ISRAEL LTD (MSIL).
+#include "mvCopyright.h"
```

```
* mvStack.h - Header File for :
* FILENAME: $Workfile: mvStack.h $
1.7.9.5
From 0adbe3af131850060daafb19644ddb1742beb9af Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 10:28:42 +0300
Subject: [PATCH 0636/2009] fix: cpu: update copyright header
   use include to mvCopyright.h file instead of GPL header
Change-Id: Idc0e1117289627feb86e93f9911d9599d87b8116
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3036
Tested-by: Star Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
.../common/mv hal/cpu/feroceon/mvFeroceonCntrs.c | 16 +-----
board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c | 16 +-----
board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c | 16 +-----
board/mv ebu/common/mv hal/cpu/pj4/mvPJ4Cntrs.c | 16 +-----
4 files changed, 4 insertions(+), 60 deletions(-)
diff --git a/board/mv ebu/common/mv hal/cpu/feroceon/mvFeroceonCntrs.c
b/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c
index 5b9a091..db2268f 100644
--- a/board/mv ebu/common/mv hal/cpu/feroceon/mvFeroceonCntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/feroceon/mvFeroceonCntrs.c
@@ -1,18 +1,4 @@
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- * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
- */
+#include "mvCopyright.h"
```

```
#include "../mvCpuCntrs.h"
diff --git a/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c b/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c
index 33f8c06..f78bc0b 100644
--- a/board/mv_ebu/common/mv_hal/cpu/mvCpuCntrs.c
+++ b/board/mv ebu/common/mv hal/cpu/mvCpuCntrs.c
@@ -1.18 +1.4 @@
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- * Foundation, Inc., 59 Temple Place, Suite 330, Boston, MA 02111-1307 USA
+#include "mvCopyright.h"
#include "mvOs.h"
#include "mvCpuCntrs.h"
diff --git a/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c
b/board/mv ebu/common/mv hal/cpu/mvCpuL2Cntrs.c
index 18120be..19460af 100644
--- a/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/mvCpuL2Cntrs.c
@@ -1,18 +1,4 @@
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```

#include "myOs.h"

```
+#include "mvCopyright.h"
#include "mvOs.h"
#include "mvCpuL2Cntrs.h"
diff --git a/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
b/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
index 0b38381..836f267 100644
--- a/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
+++ b/board/mv_ebu/common/mv_hal/cpu/pj4/mvPJ4Cntrs.c
@@ -1.18 +1.4 @@
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- */
+#include "mvCopyright.h"
#include "mvOs.h"
#include "../mvCpuCntrs.h"
1.7.9.5
From 61c56e2b35786fa69e425f3e73d4cbd8e54e8e18 Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Tue, 9 Jul 2013 16:16:38 +0300
Subject: [PATCH 0531/2009] Marvell copyright header
This copyright file is replacing the current Marvell Triple
license header. each file with Marvell triple copyright,
should add include to this h file in the beginning of the file.
This is relevant to files that are common to more than one OSs.
This license header include Marvell Quad header, include the
current license headers - GPL, BSD & Marvell Commercial License, and
addition of a new license header -
General Public License FreeRTOS Exception.
Change-Id: Ie8b6f03bfab41273575b451e7c741507b8cca17d
```

Open Source Used In 250, 350, 350X, and 550X Series Switches 2.5.0.x

Reviewed-on: http://vgitil04.il.marvell.com:8080/2463

Signed-off-by: Neta Zur <neta@marvell.com>

```
Reviewed-by: Maen Suleiman <maen@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
Tested-by: Nadav Haklai <nadavh@marvell.com>
1 file changed, 97 insertions(+)
create mode 100644 board/mv ebu/common/common/mvCopyright.h
diff --git a/board/mv_ebu/common/common/mvCopyright.h b/board/mv_ebu/common/common/mvCopyright.h
new file mode 100644
index 0000000..458562b
--- /dev/null
+++ b/board/mv ebu/common/common/mvCopyright.h
@@ -0,0 +1,97 @@
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1.7.9.5
From 3119d3409bd752808772589578e525a1294935dc Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Tue, 1 Oct 2013 11:01:52 +0200
Subject: [PATCH 0671/2009] fix: qd-dsdt: update copyright header
use qd-dsdt copyright header file instead of proprietary header
Change-Id: Id745d6cb389de29a86b5762e9d15316bf7d08dcc
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/3619
Tested-by: Star_Automation <star@marvell.com>
Reviewed-by: Nadav Haklai <nadavh@marvell.com>
.../mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h | 15 ++------
.../common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h | 11 +-----
.../common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h | 15 ++-----
.../common/mv_hal/qd-dsdt/Include/msApiTypes.h | 11 +-----
4 files changed, 6 insertions(+), 46 deletions(-)
diff --git a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
index 4fd3f94..ca5b8e7 100644
--- a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
+++ b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/h/msApi/gtVct.h
@@ -1,17 +1,6 @@
+#include <Copyright.h>
_*
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* DESCRIPTION:

index a2ef122..ee1b364 100644

--- a/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h

+++ b/board/mv_ebu/common/mv_hal/qd-dsdt-3.3/Include/msApiTypes.h

@@ -1,13 +1,4 @@

-/*

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```
- */
+#include <Copyright.h>
```

* FILENAME: \$Workfile: mv_types.h \$

index 1c58a1b..ded5c5d 100755

- --- a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h
- +++ b/board/mv_ebu/common/mv_hal/qd-dsdt/Include/h/msApi/gtVct.h

@@ -1,17 +1,6 @@

+#include <Copyright.h>

-

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* gtPhy.h

*

* DESCRIPTION:

 $diff --git \ a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h \ b/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h$

index 3afd0c7..5e29ab6 100755

- --- a/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h
- +++ b/board/mv_ebu/common/mv_hal/qd-dsdt/Include/msApiTypes.h

@@ -1,13 +1,4 @@

-/*

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- */

+#include <Copyright.h>

/*

* FILENAME: \$Workfile: mv_types.h \$

--. _

1.7.9.5

From d94585ce826b7e523b124e24ab8887517f159640 Mon Sep 17 00:00:00 2001

From: Neta Zur <neta@marvell.com>
Date: Thu, 15 Aug 2013 10:31:30 +0300

Subject: [PATCH 0638/2009] fix: neta: update copyright header

use Marvell triple license header instead of GPL header

Change-Id: Ibf7ffa3abf58354f7c9cdc705258ff04af69c930

Signed-off-by: Neta Zur <neta@marvell.com>

Reviewed-on: http://vgitil04.il.marvell.com:8080/3037 Reviewed-by: Dmitri Epshtein <dima@marvell.com> Reviewed-by: Nadav Haklai <nadavh@marvell.com> Tested-by: Nadav Haklai <nadavh@marvell.com>

```
1 file changed, 36 insertions(+)
diff --git a/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
b/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
index 232bb6f..8260963 100644
--- a/board/mv ebu/common/mv hal/neta/pnc/mvPncAging.c
+++ b/board/mv_ebu/common/mv_hal/neta/pnc/mvPncAging.c
@@ -9,6 +9,12 @@ introductory statement regarding license alternatives, (ii) delete the two
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***********************************
#include "mvOs.h"
1.7.9.5
From b607bda07a2440b3778130b0c6bb5a9ba91b04c0 Mon Sep 17 00:00:00 2001
From: Neta Zur <neta@marvell.com>
Date: Mon. 1 Jul 2013 15:01:52 +0300
Subject: [PATCH 0497/2009] Add Marvell copyright to VANIR driver
Add Marvell copyright and Marvell GPL license to the VANIR driver files.
This is Marvell driver that was originated from Freescale driver.
Change-Id: I1dc2893307d608591bfa9e64b18ed9e84969ef59
Signed-off-by: Neta Zur <neta@marvell.com>
Reviewed-on: http://vgitil04.il.marvell.com:8080/2376
Reviewed-by: Star_Automation <star@marvell.com>
Tested-by: Omri Itach <omrii@marvell.com>
Reviewed-by: Omri Itach <omrii@marvell.com>
include/mv94xx scsi.h
                    | 18 ++++++++++++++
2 files changed, 36 insertions(+)
diff --git a/drivers/block/mv94xx_scsi.c b/drivers/block/mv94xx_scsi.c
index 8090c04..f3f0d89 100644
--- a/drivers/block/mv94xx scsi.c
+++ b/drivers/block/mv94xx scsi.c
@@ -1.3 +1.21 @@
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* Author: Jason Jin<Jason.jin@freescale.com>
diff --git a/include/mv94xx_scsi.h b/include/mv94xx_scsi.h
index 16fcb34..6f1c058 100644
--- a/include/mv94xx scsi.h
+++ b/include/mv94xx scsi.h
@@ -1,3 +1,21 @@
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* Copyright (C) Freescale Semiconductor, Inc. 2006.
* Author: Jason Jin<Jason.jin@freescale.com>
1.7.9.5
```

1.61 zlib 1.2.8

1.61.1 Available under license:

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/* zlib.h -- interface of the 'zlib' general purpose compression library version 1.2.8, April 28th, 2013

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Jean-loup Gailly Mark Adler

jloup@gzip.org madler@alumni.caltech.edu

The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files http://tools.ietf.org/html/rfc1950 (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

*/

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