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From network_io/unix/inet_ntop.c, network_io/unix/inet_pton.c:

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From strings/apr_strnatcmp.c, include/apr_strings.h:

strnatcmp.c -- Perform 'natural order' comparisons of strings in C.

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Sidebar - Default font size" />
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href="http://httpd.apache.org/docs/">Documentation</a> > <a href="./">Version 2.2</a></div><div id="page-
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    s.type = 'text/javascript';
    s.async = true;
    s.src = 'https://comments.apache.org/show comments.lua?site=' + comments shortname + '&page=' +
comments_identifier;
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```

```
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Written by Adam Twiss (adam@zeus.co.uk). March 1996

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- b) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.
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For an executable, the required form of the "work that uses the Library" must include any data and utility programs needed for reproducing the executable from it. However, as a special exception, the source code distributed need not include anything that is normally distributed (in either source or binary form) with the major components (compiler, kernel, and so on) of the operating system on which the executable runs, unless that component itself accompanies the executable.

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```
<signature of Ty Coon>, 1 April 1990
Ty Coon, President of Vice
```

That's all there is to it!

/* nicstar.c v0.22 Jawaid Bazyar (bazyar@hypermall.com)

* nicstar.c, M. Welsh (matt.welsh@cl.cam.ac.uk)

*

- * Hacked October, 1997 by Jawaid Bazyar, Interlink Advertising Services Inc.
- * http://www.hypermall.com/
- * 10/1/97 commented out CFG_PHYIE bit we don't care when the PHY
- * interrupts us (except possibly for removal/insertion of the cable?)
- * 10/4/97 began heavy inline documentation of the code. Corrected typos
- * and spelling mistakes.
- * 10/5/97 added code to handle PHY interrupts, disable PHY on
- * loss of link, and correctly re-enable PHY when link is
- * re-established. (put back CFG_PHYIE)

*

* Modified to work with the IDT7721 nicstar -- AAL5 (tested) only.

* R. D. Rechenmacher <ron@fnal.gov>, Aug. 6, 1997

* Linux driver for the IDT77201 NICStAR PCI ATM controller.

Open Source Used In Cisco Nexus 1000V for VMware vSphere Release 5.2(1)SV3(3.15)

```
\ensuremath{^{*}} PHY component is expected to be 155 Mbps S/UNI-Lite or IDT 77155;
```

* (skb->atm.iovcnt != 0) for Rx skb's passed to vcc->push.

*

- * Implementing minimal-copy of received data:
- * IDT always receives data into a small buffer, then large buffers
- * as needed. This means that data must always be copied to create
- * the linear buffer needed by most non-ATM protocol stacks (e.g. IP)
- * Fix is simple: make large buffers large enough to hold entire
- * SDU, and leave <small_buffer_data> bytes empty at the start. Then
- * copy small buffer contents to head of large buffer.
- * Trick is to avoid fragmenting Linux, due to need for a lot of large
- * buffers. This is done by 2 things:
- * 1) skb->destructor / skb->atm.recycle_buffer
- combined, allow nicstar_free_rx_skb to be called to
- * recycle large data buffers
- * 2) skb clone of received buffers
- * See nicstar free rx skb and linearize buffer for implementation
- * details.

本

*

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*

* M. Welsh, 6 July 1996

*

*/

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^{*} see init_nicstar() for PHY initialization to change this. This driver

^{*} expects the Linux ATM stack to support scatter-gather lists

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1.8 libaudiofile 0.2.4 : 1.0.0.0501961

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1.9 libaudiofile - programs 0.2.4 :1.0.0.0501961

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1.11 logrotate 3.8.1

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1.14 ntp 4.2.8p2

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Version 3, 29 June 2007

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- 1. [1]Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller
- 2. [2]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
- 3. [3] Viraj Bais <vbais@mailman1.intel.com> and [4] Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
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- 7. [8]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
- 8. [9]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support
- 9. [10]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in sntp
- 10. [11]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support
- 11. [12]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
- 12. [13]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
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- 16. [17]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
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- 18. [19]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
- 19. [20]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite
- 20. [21] Claas Hilbrecht < neoclock4x@linum.com> NeoClock4X clock driver
- 21. [22]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
- 22. [23]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
- 23. [24]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
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- 25. [26]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or
 [27]<H.Lambermont@chello.nl> ntpsweep
- 26. [28]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
- 27. [29]Frank Kardel [30]kardel (at) ntp (dot) org> PARSE <GENERIC> driver (>14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
- 28. [31]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
- 29. [32]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
- 30. [33]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
- 31. [34]George Lindholm < lindholm@ucs.ubc.ca> SunOS 5.1 port
- 32. [35]Louis A. Mamakos < louie@ni.umd.edu> MD5-based authentication
- 33. [36]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
- 34. [37]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
- 35. [38]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics;

- audio clock drivers: CHU, WWV/H, IRIG
- 36. [39]Wolfgang Moeller < moeller@gwdgv1.dnet.gwdg.de> VMS port
- 37. [40]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
- 38. [41]Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port
- 39. [42]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
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- 47. [51] Ray Schnitzler < schnitz@unipress.com > Unixware1 port
- 48. [52] Michael Shields < shields @tembel.org > USNO clock driver
- 49. [53]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
- 50. [54]Harlan Stenn harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
- 51. [55]Kenneth Stone <ken@sdd.hp.com> HP-UX port
- 52. [56] Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
- 53. [57]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock driver
- 54. [58]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
- 55. [59]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD

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