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## Release 4.0.1

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1. [1]Mark Andrews <mark\_andrews@isc.org> Leitch atomic clock controller
2. [2]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
3. [3]Viraj Bais <vbais@mailman1.intel.com> and [4]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5
4. [5]Michael Barone <michael,barone@lmco.com> GPSVME fixes
5. [6]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca>IPv6 support
6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
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12. [13]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
13. [14]Sven Dietrich <sven\_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
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16. [17]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
17. [18]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing
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21. [22]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
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23. [25]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
24. [26]Frank Kardel [27]<Frank.Kardel@informatik.uni-erlangen.de> PARSE <GENERIC> driver (14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup
25. [28]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications
26. [29]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
27. [30]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox

- GPS clock driver
- 28. [31]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
  - 29. [32]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
  - 30. [33]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
  - 31. [34]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance
  - 32. [35]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG
  - 33. [36]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
  - 34. [37]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
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  - 36. [39]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
  - 37. [40]Derek Mulcahy <derek@toybox.demon.co.uk> and [41]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
  - 38. [42]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling
  - 39. [43]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
  - 40. [44]Wilfredo S?nchez <wsanchez@apple.com> added support for NetInfo
  - 41. [45]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
  - 42. [46]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
  - 43. [47]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
  - 44. [48]Michael Shields <shields@tembel.org> USNO clock driver
  - 45. [49]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
  - 46. [50]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
  - 47. [51]Kenneth Stone <ken@sdd.hp.com> HP-UX port
  - 48. [52]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support
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  - 50. [54]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
  - 51. [55]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD
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principal author, although it includes many contributions from others.

In 1995, Guido continued his work on Python at the Corporation for  
National Research Initiatives (CNRI, see <http://www.cnri.reston.va.us>)  
in Reston, Virginia where he released several versions of the  
software.

In May 2000, Guido and the Python core development team moved to  
BeOpen.com to form the BeOpen PythonLabs team. In October of the same  
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